






# Catalyst-free synthesis of sub-5 nm silicon nanowire arrays with massive lattice contraction and wide bandgap

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The need for miniaturized and high-performance devices has attracted enormous attention to the development of quantum silicon nanowires. However, the preparation of abundant quantities of silicon nanowires with the effective quantum-confined dimension remains challenging. Here, we prepare highly dense and vertically aligned sub-5 nm silicon nanowires with length/diameter aspect ratios greater than 10,000 by developing a catalyst-free chemical vapor etching process. We observe an unusual lattice reduction of up to 20% within ultra-narrow silicon nanowires and good oxidation stability in air compared to conventional silicon. Moreover, the material exhibits a direct optical bandgap of 4.16 eV and quasi-particle bandgap of 4.75 eV with the large exciton binding energy of 0.59 eV, indicating the significant phonon and electronic confinement. The results may provide an opportunity to investigate the chemistry and physics of highly confined silicon quantum nanostructures and may explore their potential uses in nanoelectronics, optoelectronics, and energy systems.

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Silicon nanowires (SiNWs) have been actively researched in the past decades for a wide variety of topics, including nanoelectronics, optoelectronics, sensing/detection, biotechnology, and energy systems<sup>1–9</sup>. Notably, as the electronic devices' physical size becomes petite, the quantum confinement effect on electronic characteristics emerges significantly<sup>10</sup>. Theoretical and experimental studies have confirmed that the indirect bandgap of silicon could be tailored to a direct bandgap as the diameter of SiNW approaches the carrier de Broglie wavelength (1 nm for electron)<sup>11–13</sup>. The bandgap of these small nanowires can be increased several eVs above the bulk value ( $E_g = 1.12$  eV), placing its photoluminescence in the visible range<sup>14,15</sup>.

The current SiNW synthesis methods use catalyst-nanoparticle-assisted vapor–liquid–solid (VLS) growth<sup>16–22</sup> or wet chemical etching processes<sup>23–25</sup>, whose diameters are constrained by the size of the catalyst nanoparticle<sup>26,27</sup>. As a result, the typical diameters of formed SiNWs are quite large (10–100 nm)<sup>25,28</sup>, where the effects of 1D confinement can at best be partial. While there were valuable successes in fabricating further reduced diameter of SiNWs through the VLS growth with small catalyst nanoparticles<sup>27,29,30</sup>, etching oxide sheath of core-shell SiNWs<sup>12</sup>, or supercritical solution-phase growth with Au nanoclusters<sup>14</sup>, most of these synthesis methods still use catalyst nanoparticles requiring complex purifying treatments to remove them, and which in the process damage and dope SiNWs. Moreover, the as-produced SiNWs are not aligned and in low growth density<sup>31,32</sup>. This might be one of the key reasons why much less is experimentally known about crystalline 1D silicon at sub-5 nm quantum-confined dimensions.

Here, we report a chemical vapor etching (CVE) process that enables the formation of highly dense and vertically aligned ultra-narrow SiNWs. Crystalline sub-5 nm SiNWs up to a few tens of micrometers in length (aspect ratios greater than 10,000) can form directly in Si wafers without any catalyst nanoparticles representing the formation mechanism is different from catalyst-aided growth and etching approaches. SiNWs produced via the CVE process reported herein exhibit extraordinary lattice reduction up to 20% and excellent oxidation stability over conventional silicon. We also report rich quantum confinement properties within a strong 1D confinement of ultra-narrow silicon nanowires.

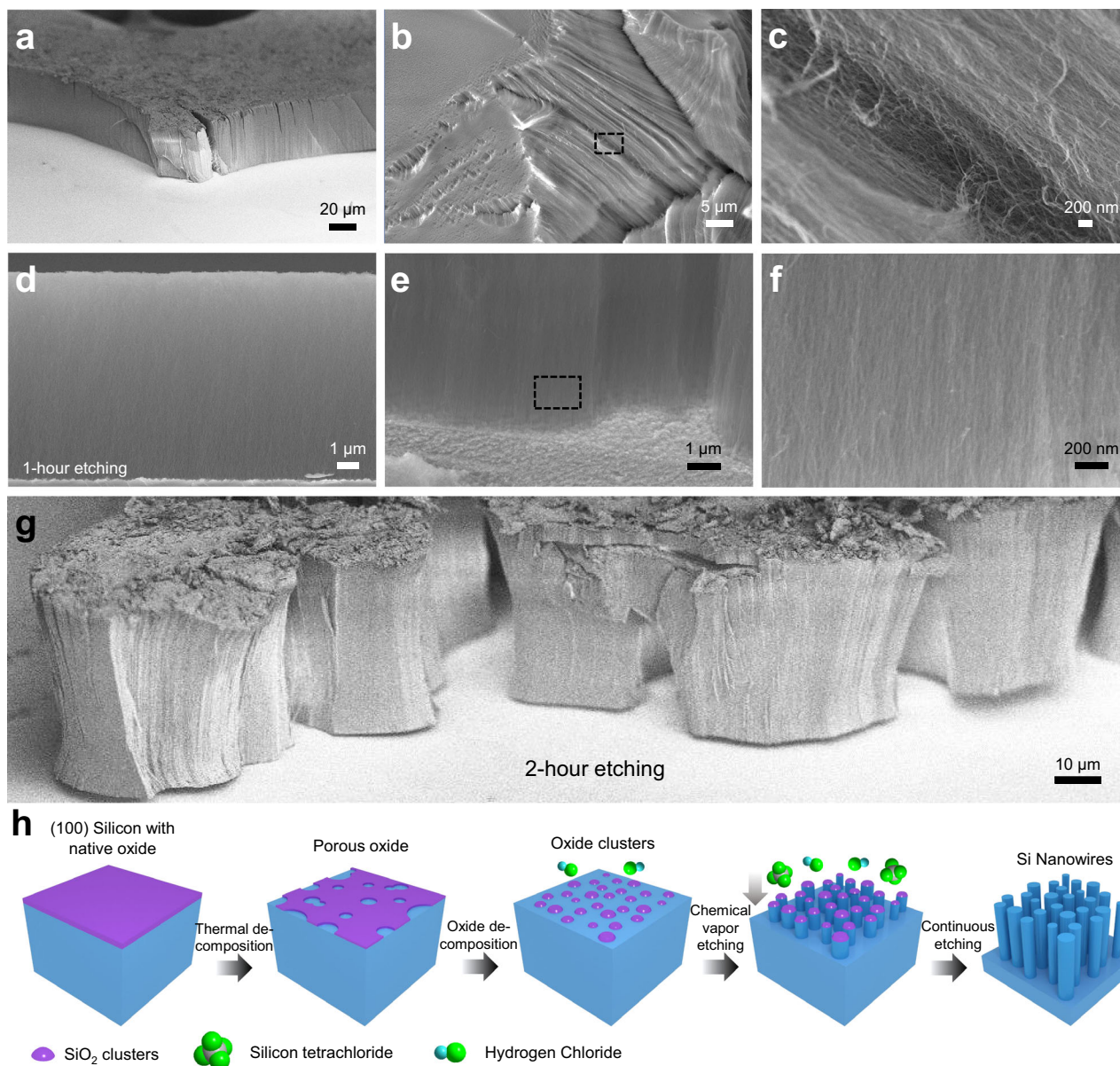
## Results and discussion

**Morphology and formation mechanism.** Scanning electron microscopy (SEM) images in Fig. 1a–c demonstrate striking examples of ultra-dense and vertically aligned SiNWs where formed arrays closely resemble high-density and vertically aligned single-walled carbon nanotube forest. The surface-directed SiNW forest is fabricated directly from the Si wafer, etching vertically on the flat surface with a 9  $\mu\text{m}$  height after 1 h high-temperature CVE using  $\text{SiCl}_4$  gas in a highly controlled Ar and  $\text{H}_2$  environment (Fig. 1d and Supplementary Fig. 1). A close examination (Fig. 1e, f) at the bottom of the SiNW array shows that very narrow nanowires are densely packed with high uniformity. After 2 h etching, the highly dense and well-aligned nanowires could be further extended to a 37  $\mu\text{m}$  height (Fig. 1g). These SiNWs can be dispersed in solvents such as ethanol, and isolated nanowires can be obtained through a simple sonication (Supplementary Fig. 2). Figure 1h illustrates the oxide-induced CVE process synthesizing the ultra-narrow SiNWs. According to equilibrium concentration calculations, the dominating etchants responsible for the etching of Si at 1400 K in atmospheric pressure are  $\text{SiCl}_4$  and HCl (Supplementary Note 1 and Supplementary Tables 1, 2). The HCl vapor etchant is first generated from  $\text{SiCl}_4$  decomposition with the involvement of  $\text{H}_2$  at around 1400 K, along with other gas

compounds ( $\text{SiCl}_3$ ,  $\text{SiCl}_2$ , and  $\text{SiHCl}_3$ ). The initial silicon substrate had a native oxide layer of 2–2.5 nm thickness (Supplementary Fig. 3a, d). As the temperature increased to 1150 °C in the Ar atmosphere, we observed the thermal desorption of the native oxide layer into 1–2 nm thickness (Supplementary Fig. 3b, e)<sup>33,34</sup>. When the  $\text{SiCl}_4$  was introduced at 1150 °C to the CVE reactor, the oxide layer was further inhomogeneously etched by HCl to a highly porous and high-density clustered oxide structure, which acted as a mask to allow a controlled number of etchants to diffuse into the Si (Supplementary Fig. 3c, f and Supplementary Table 3). From a large representative set of cross-sectional TEM images, we extract the diameter distributions (violin plots) of pores (2.55 nm on average) and oxide clusters (1.92 nm on average) (Supplementary Fig. 3g).

The Si substrate is then etched into nanowire structures by the two major anisotropic etchants,  $\text{SiCl}_4$  and HCl vapors, using porous Si oxide as a mask, producing byproducts  $\text{SiCl}_2$ ,  $\text{SiHCl}_3$ , and  $\text{H}_2$  at the same time. The continuation of the above CVE process leads to the elongation of well-aligned nanowires. We speculate that with the increase of oxidant concentration (>5.5 ppm), the oxidant gases will oxidize and passivate the surface of SiNWs, facilitating nanowires' survival from the etchant vapors. The emergence of high-density nanowires will decrease the concentration of etchants penetrating the nanowire array and alleviate the lateral etching of the nanowires, resulting in the formation of an extended nanowire array (Supplementary Fig. 4a). However, when the concentration of oxidant gas is above the threshold, the whole Si surface will be oxidized, leading to a continuous dense oxide film resistant to etching (Supplementary Fig. 4b). Furthermore, we found that the dominant etching of Si without nanowire formation occurred under very low  $\text{O}_2/\text{H}_2\text{O}$  concentration (<5 ppm) (Supplementary Fig. 4c).

**Crystal structure analysis.** High-resolution transmission electron microscopy (HRTEM) was used to obtain direct evidence of the crystalline structure of the vertically aligned SiNWs with sub-5 nm diameters and lengths up to several micrometers. An HRTEM image of the SiNW bundle in Fig. 2a shows clear lattice fringes of the SiNWs, reflecting the highly crystalline nature without significant oxidation on the nanowires' surface (Supplementary Note 2 and Supplementary Fig. 5). The nanowires' structural features have been further studied by the selected area electron diffraction (SAED) pattern. The diffraction rings from randomly-dispersed SiNWs can be used to index the structure, and each radius corresponds exactly to the interplanar distance  $d_{hkl}$ . The observed three diffraction rings at about 2.50, 1.53, and 1.31 Å reveal the presence of {111}, {220}, and {311} planes of the diamond cubic lattice (space group  $Fd\bar{3}m$ )<sup>35</sup>. The calculated lattice parameter is 4.33 Å, which is 79.7% of that for bulk Si (5.43 Å). The inordinately isotropic large compression of the diamond cubic Si lattice under atmospheric pressure and room temperature is surprising. By analyzing a large representative set of HRTEM images (Fig. 2b), we extract the diameter distribution (violin plot) of nanowires in the range of 2–5 nm (3.44 nm on average) with narrow diameter size distribution (relative standard deviation  $\sim$ 20.7%). The majority of the nanowires show a diameter comparable to or smaller than the exciton Bohr radius ( $\sim$ 5 nm)<sup>36,37</sup>, a size range of quantum confinement effects for tunable electronic and optical properties. To corroborate the extraordinary lattice reduction, we have performed X-ray diffraction (XRD) analysis on the SiNW sample. XRD pattern of the vertically aligned SiNWs on (100) Si substrate shows the  $2\theta$  shift of {111} plane from 28.45° (bulk Si, JCPDS card No. 65–1060) to 32.96°, as well as that of (220) plane from 47.31° to 54.99° (Fig. 2c). The strong (400) peak at 69.12° arises from the (100)



**Fig. 1 Morphology of the vertically aligned high-density SiNWs formed on the (100) silicon substrate.** **a** Tilted, **b** low, and **c** high magnification planar views of the vertically aligned SiNWs. **d** SEM image of the vertically aligned SiNWs on the (100) Si wafer after 1 h etching. **e** High magnification SEM image of the bottom interface between SiNWs and etched Si substrate. **f** Enlarged image of the dashed rectangular area in **(e)**. **g** Tilted views of the vertically aligned SiNWs after 2 h etching. **h** Schematic illustration of the oxide-induced etching process to fabricate ultra-narrow Si nanowires.

silicon wafer substrate. The corresponding lattice constant of SiNWs is around 4.70 Å, reconfirming the lattice reduction (13.4%).

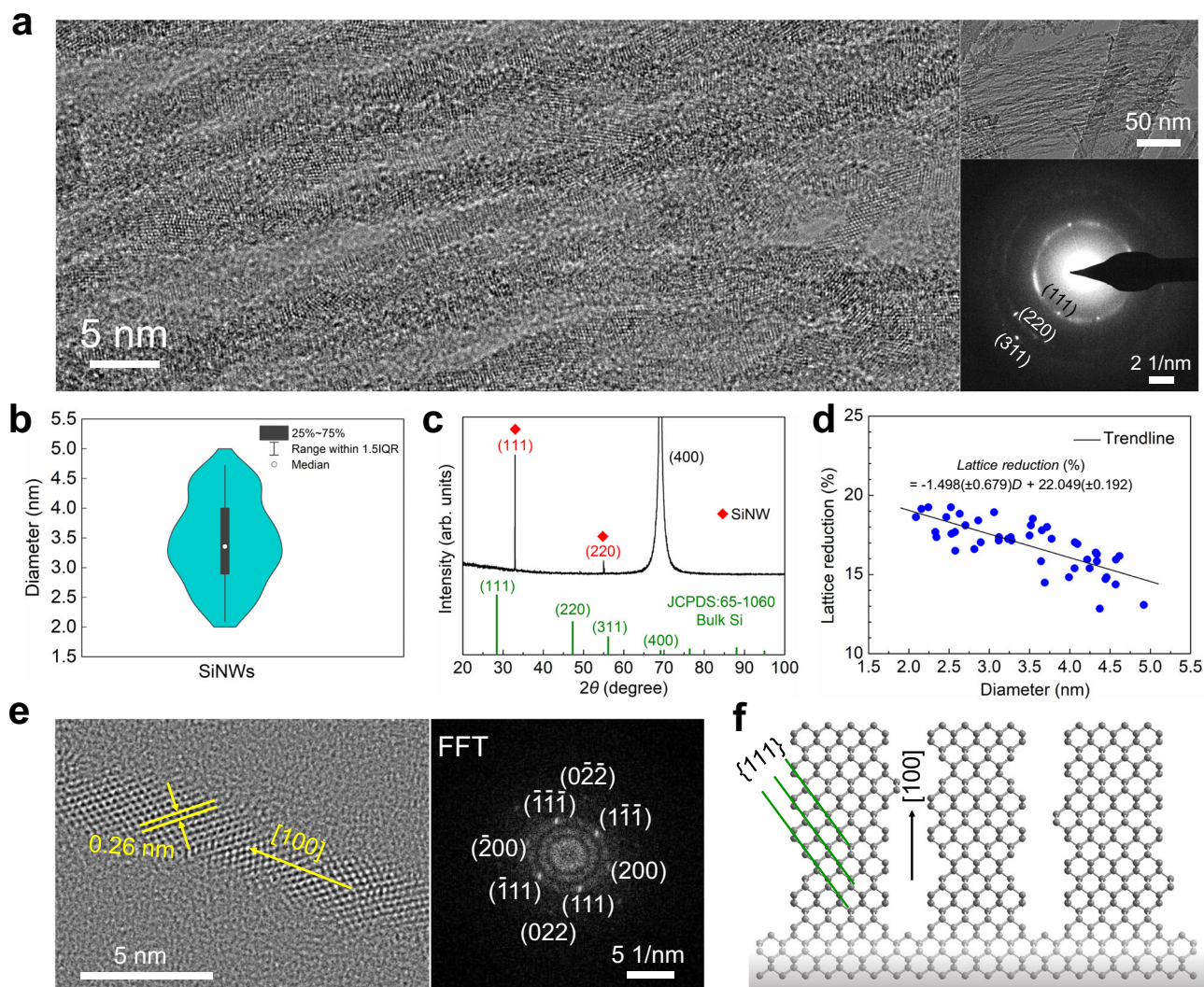
Past studies have shown that etching rates in silicon wafers are sensitive to local strain<sup>38</sup>. Specifically, tensile strains result in significantly faster etching rates<sup>38</sup>. We hypothesize that sustained nanowires formation following initial etching, therefore, requires exposed surfaces with tensile strains, that are stabilized by lattice contraction of the bulk lattice. The large lattice contraction we observe can arise due to the increasing need for tensile strained surfaces as the nanowire size decreases. For low-dimensional crystals, surface stresses and related surface reconstructions (e.g., dimerization in silicon) can lead to residual strains that can become significant as the size approaches a few nanometers<sup>39–42</sup>. In extreme cases, they can result in non-linear elastic deformations, superelasticity, and even lattice phase transformations<sup>43–45</sup>. Size dependence of the lattice deformations is a signature of these

effects, and to see if the contraction we observe is correlated with the nanowire size, we analyze several HRTEM images of SiNWs along their length. Figure 2d shows that the lattice reduction (%) increases with the decrease of local nanowire diameter,  $D$ , and the increase is approximately linear, that is

$$\text{Lattice reduction (\%)} = -1.498 (\pm 0.679)D + 22.049 (\pm 0.192) \quad (1)$$

with the net reduction in the range of 13–20%. Note that there is a considerable fluctuation in the measured lattice contraction at a fixed diameter, which may be attributed to the wiggled morphology of the randomly-positioned nanowires. In addition, the etchants aggressively attack and remove the surface Si atoms, which causes a slightly non-uniform diameter of the nanowire and also contributes to the complexity in evaluation. The size dependence is suggestive of a surface effect as the origin of the stabilization of the lattice contraction, suitably aided by a stress-



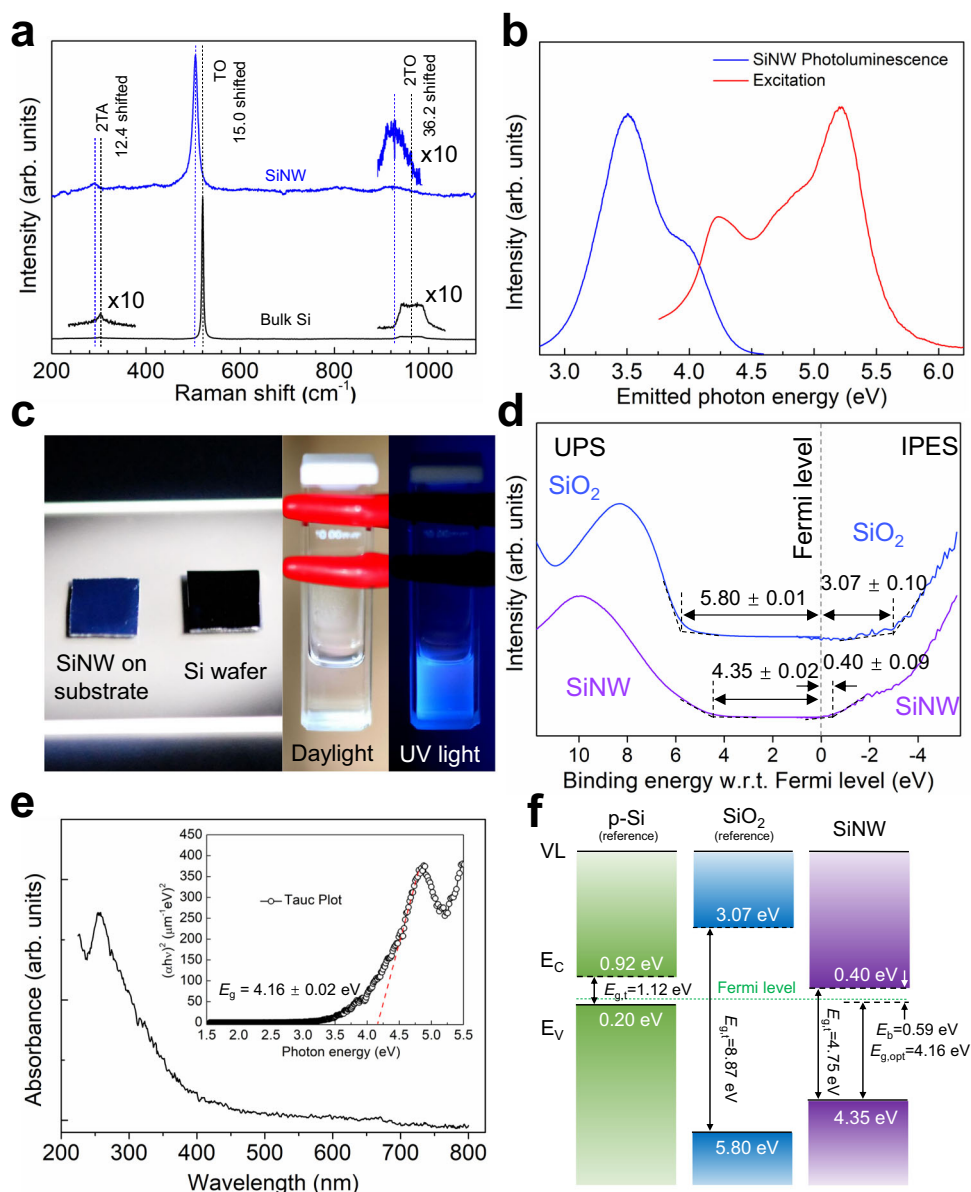


**Fig. 2** Crystal structure analysis of the vertically aligned high-density SiNWs. **a** HRTEM images and SAED pattern of multiple crystalline SiNWs. **b** Violin plot showing diameter distribution as determined from HRTEM. **c** X-ray diffraction pattern of the vertically aligned SiNWs on (100) Si substrate. The strong (400) peak is from the (100) silicon wafer substrate. **d** The relationship between lattice reduction and the diameter of the SiNWs. **e** HRTEM and corresponding FFT images of an individual Si nanowire. **f** Schematic illustration of the crystal orientation of vertically aligned (100) SiNWs on (100) Si substrate observed from <110> direction with a relatively rough edge.

induced etching pathway for the formation of Si surface nanowire clusters and atomically thin Si suboxide sheath that is amplified as nanowire diameter approaches a few nanometers<sup>46,47</sup> (Supplementary Fig. 5). The combination of XRD characterization, TEM measurements of lattice fringes along with the axial and radial directions, and size dependence of the contraction show that the nanowire unit cell remains cubic upon contraction. The nanowire is therefore uniformly compressed along with both axial and radial (and therefore also azimuthal) directions, with the resultant strains scaled by the Poisson's ratios. We attribute this hydrostatic stress state to the etching reaction chemistry, likely stabilized by the oxide layer that forms in the wake of the surface reactions, and delegate a more detailed analysis of the compressed nanowire stability to a later study.

To probe further, the HRTEM images recorded along the [110] zone axis of crystalline SiNWs and perpendicular to the nanowire's long axis reveal fringes from {111} planes. HRTEM of multiple individual nanowires shows the vertically aligned SiNWs with corresponding interplanar spacing  $d_{111}$  of around 0.26 nm (Fig. 2e and Supplementary Fig. 6). The corresponding

fast Fourier transform (FFT) images reveal the nanowire formation direction to be [100], which is the same orientation of the initial silicon wafer. This etching technique shows small dependence upon the doping characteristics of the Si substrates, i.e., SiNWs can be derived from n-type, p-type, and highly doped p-type Si wafers (Supplementary Fig. 7). As shown in Fig. 2e, the atomic-scale roughness of the surface of SiNW is commonly observed. The rough morphology of the SiNW sidewalls may cause the scattering of electrons and/or phonons<sup>48</sup>. The anisotropic etching of the Si surface depends on the removal rates of Si atoms associated with the back-bond strength theory<sup>49,50</sup>. Specifically, the {100} surfaces have only two bonds connected to the substrate while the other surfaces, such as {111} surfaces, have three. The weakening effect of the Si back bonds due to the bonding of the surface Si atoms to  $\text{Cl}^-$  is more substantial for {100} surfaces. In addition, the surface energies of Si {100}, {110} and {111} are 1.99, 1.41 and 1.15  $\text{J cm}^{-2}$ , respectively<sup>51</sup>. Si {100} plane with higher surface energy and surface bond density ( $1.36 \times 10^{15} \text{ cm}^{-2}$ ) provides more reacting points, causing a much faster Si atom removal rate and the anisotropic <100> etching (Fig. 2f).



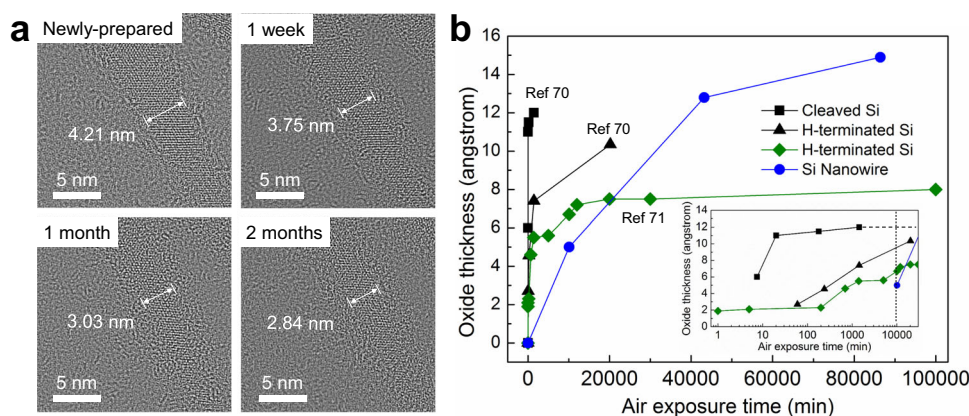
**Fig. 3 Optical properties and bandgap of the vertically aligned ultra-narrow SiNWs.** **a** Raman characterization result showing clear SiNW peaks with redshift. **b** Photoluminescence spectrum of ultra-narrow dispersed in ethanol with excitation energy of 5.17 eV. **c** Photographs of the as-fabricated SiNWs on Si and SiNWs dispersion in ethanol under UV light. **d** Combined UPS and IPES spectra show the quasi-particle bandgap of SiNWs with the conduction/valence band offsets. **e** UV-vis absorption spectrum of SiNWs. Tauc plot indicates the direct bandgap transition for SiNWs. **f** Energy-level diagram of the SiNWs derived from UPS, IPES, and PL.  $E_{g,t}$ ,  $E_{g,opt}$  and  $E_b$  are transport bandgap, optical bandgap, and binding energy, respectively.

**Optical properties and bandgap.** Raman spectroscopy with an excitation wavelength of 532 nm was used to distinguish the structural fingerprint for ultra-narrow SiNWs. All measurements were performed at low laser power and room temperature to eliminate the heat effects (Supplementary Figs. 8, 9 and Supplementary Tables 4, 5). Figure 3a compares the Raman spectra of bulk Si and SiNWs characterized at the same excitation energy but with different exposure times. The bulk crystalline Si shows a representative Raman peak at  $520\text{ cm}^{-1}$  due to the scattering of the first-order optical phonons<sup>52</sup>. However, in our SiNWs, the corresponding Raman peak was redshifted by almost  $15\text{ cm}^{-1}$  compared to bulk Si. Its line-width broadened (FWHM of  $12.4\text{ cm}^{-1}$ ), and the line shape became asymmetric<sup>52–55</sup>. The redshift of the second-order spectra of transverse acoustical phonon mode (2TA, from  $302$  to  $290\text{ cm}^{-1}$ ) and transverse optical phonon mode (2TO, from  $969$  to  $933\text{ cm}^{-1}$ ) have also

been observed in SiNWs. Such wavenumber downshifts of Raman peaks can be mainly attributed to the phonon confinement effects dominated by diameter reduction<sup>52–54,56</sup>. When the crystal size is reduced to the nanoscale, the momentum conservation rule is relaxed, and the phonon scattering is not limited to the center of the Brillouin zone, while phonon dispersion near the center will also be considered<sup>56</sup>. The smaller the crystal size is, the bigger the wavenumber downshifts, and the more asymmetric and broader the Raman peak becomes<sup>53</sup>.

It is known that as the diameter of SiNWs approaches the carrier de Broglie wavelength, the bandgap of SiNW is renormalized due to quantum confinement effects<sup>11,57</sup>. It is also expected that sub-critical diameter SiNWs exhibit a direct bandgap<sup>58</sup>, which increases as the nanowire diameter decreases regardless of surface termination<sup>13</sup>. As the diameter narrows, the nanowire’s bandgap gradually widens and deviates from that of





**Fig. 4** Stability against oxidation of the vertically aligned ultra-narrow SiNWs. **a** HRTEM images showing the stability of the ultra-narrow Si nanowire subject to air exposure at room temperature. Note that the identical SiNW was observed in HRTEM for different time durations. The sizes of the crystalline Si cores are measured. **b** Oxide thickness as a function of exposure time to ambient air<sup>70,71</sup>.

bulk Si<sup>12</sup>. As shown in Fig. 3b, using a 240 nm excitation wavelength (5.17 eV) at room temperature, the ultra-narrow nanowires show strong photoluminescence (PL) peak-centered at 3.50 eV and a weak shoulder peak at 3.8 eV. With respect to the bulk crystalline Si indirect bandgap of 1.12 eV<sup>59</sup>, the PL peak has been significantly blue-shifted, which can evidence the renormalized bandgap of SiNWs. In Fig. 3c, it is confirmed that the blue light emissions under UV light (4.88 eV) of both as-fabricated SiNWs on the Si substrate and SiNWs dispersed in ethanol corresponds to the emission energy of the PL spectrum.

Ultraviolet photoemission spectroscopy (UPS) and inverse photoemission spectroscopy (IPES) were employed to investigate the density of state (DOS) of the ultra-narrow SiNWs and their energetic position of valence band maximum (VBM) and the conduction band minimum (CBM). To identify the feature of the DOS of ultra-narrow SiNWs and their surface oxidation, native SiO<sub>2</sub> was measured as a reference. In both cases, apparent O 2*p* valence features were observed around 7–9 eV for UPS and –3 ~ –4 eV for IPES in Fig. 3d. From that, the DOS of inside SiNWs was obtained, and their level onsets centered around the Fermi level were found at the binding energy of 4.35 eV for the VBM and 0.40 eV for the CBM, indicating a quasi-particle bandgap of 4.75 eV for the ultra-narrow SiNWs. The combination of UPS and IPES spectra reconfirm bandgap renormalization as it directly indicates the DOS of SiNW. Moreover, we couldn't observe noticeable features between VBM and CBM onset, possibly suggesting the very low density or absence of in-gap state stems from the defect or impurities within the margin of experimental error<sup>60–63</sup>. However, further investigation should be followed for a comprehensive understanding of defects and impurities within SiNWs. To determine the optical bandgap energy, the Tauc plot (Fig. 3e, Supplementary Note 3) has been obtained from the UV–vis absorption spectrum. A direct optical bandgap of 4.16 eV has been extracted for the SiNWs by extrapolating the linear region to the abscissa of photon energy. Besides, in Fig. 3f, the large exciton binding energy of 0.59 eV, which is one of the fingerprints of quantum confinement effects, was estimated by comparing the optical bandgap of SiNWs derived from the absorption spectrum with the quasi-particle bandgap ( $E_b = E_{g,t} - E_{g,opt}$ ). Surprisingly it is ca. 100 times higher than that of bulk Si (0.0055 eV)<sup>64</sup>. We assume that these largely increased quasi-particle bandgap of 4.75 eV, and exciton binding energy of 0.59 eV are might be due to a combination of different factors, including the highly effective quantum confinement dimension<sup>11,57</sup>, extraordinary lattice reduction (energy gap

increase due to interatomic distance reduction)<sup>65–68</sup>, and dielectric screen effect from Si/SiO<sub>x</sub> core-shell structure of SiNW<sup>65,66</sup>.

**Stability against oxidation.** To investigate the stability of SiNWs in the air, SiNWs were exposed to ambient air (room temperature 22 °C, relative humidity 40–50%) for up to two months without any surface modification/termination, and HRTEM images of SiNWs have been recorded accordingly (Fig. 4a). The newly prepared SiNWs show clear lattice fringes without a noticeable amorphous oxide shell. After 7, 30, and 60 days in the air, the same SiNW surface was slowly oxidized, and the estimated oxide thickness (and corresponding measured silicon core diameter) were 5.0 Å (3.75 nm), 12.8 Å (3.03 nm), and 14.9 Å (2.84 nm), respectively. As shown in Fig. 4b, the surface oxidation rate of SiNWs decreases with time, possibly due to a self-limiting oxidation effect caused by the Si/SiO<sub>x</sub> interface<sup>46,47,69</sup>. It has been reported that when bulk Si was cleaved in and exposed to air (25 °C, relative humidity 30–50%), the surface of silicon was oxidized immediately, forming 11–13 Å thickness of silicon oxide within 24 h<sup>70</sup>. Further, hydrogen passivated silicon also showed surface oxidation up to 7.6 Å after 24 h and 11 Å within two weeks in the air<sup>70</sup>. During the first seven days in ambient air, the average oxide growth rate of our ultra-narrow SiNWs was around 0.7 Å per day, which is at least 58%<sup>70</sup> and 25–43%<sup>70,71</sup> lower than the oxidation rate of bulk and hydrogen-passivated Si, respectively.

In this study, we demonstrated a catalyst-free synthesis of high-density, aligned, and sub-5 nm SiNWs by developing a vapor phase silicon etching process. The SiNWs are oriented along [100] direction, with a significant lattice reduction of 13–20%, which possibly enhanced the nanowires' stability against etching and oxidation. These sub-5 nm SiNWs with extraordinary lattice reduction manifest significant phonon and electronic confinement effects, which might be beneficial for potential applications in nanoelectronics and optoelectronics, such as transistors<sup>72,73</sup> and biosensors<sup>3,74</sup>. Furthermore, macroscopic films containing billions of such small SiNWs in a highly aligned fashion can also be a promising material system for gas/chemical sensors<sup>75</sup>, anodes in lithium-ion<sup>5,76</sup> and lithium–sulfur batteries<sup>77</sup>, and solar cells<sup>78–82</sup>, where strong quantum confinement, as well as ultra-high surface area of silicon, is desirable.

## Methods

**Materials preparation.** The fabrication of high density and well-aligned SiNWs with the sub-5 nm diameters and up to a few tens of micrometers in length was

accomplished on Si wafers by developing a chemical vapor silicon etching process that uses silicon tetrachloride ( $\text{SiCl}_4$ ) gas in a highly controlled argon (Ar) and hydrogen ( $\text{H}_2$ ) environment at 1000–1150 °C. The Si wafer was located at the center of a furnace. In a typical fabrication process, the reaction chamber was pumped down to a base pressure at the order of  $10^{-2}$  Torr and backfilled with ultra-high purity Ar gas (500 sccm) until atmospheric pressure. When the chamber's temperature reaches 1000–1150 °C,  $\text{SiCl}_4$  vapor was introduced to the chamber by bubbling 20 sccm of 5–10%  $\text{H}_2$ -balanced Ar gas through a glass bubbler. After etching, 500 sccm flow of ultra-high purity Ar gas was streamed through the chamber to help cool the chamber down.

**Materials characterizations.** The characterization of SiNW morphology was conducted using a thermal field emission-scanning electron microscope (Supra 25 FE-SEM, Zeiss). For the structural analysis, an aberration-corrected transmission electron microscope (TEM, FEI Titan Themis 300) was used to obtain a direct crystalline silicon structure. The nanowires' chemical composition was determined by the energy dispersive X-ray spectrometer (EDS) attached to TEM. SiNWs were dispersed in ethanol (Reagent Alcohol, anhydrous,  $\leq 0.005\%$  water, Sigma-Aldrich) and dropped on an ultra-thin carbon layer (3 nm) supported by lacey carbon and copper grid (Ted Pella, inc.). The TEM cross section samples were prepared by using SEM/focused ion beam (FIB) dualbeam system with Ga ion beam. To protect the top surface of the cross section sample, a 200 nm-thick layer of Pt was deposited by electron beam first and followed by a 2  $\mu\text{m}$ -thick layer of ion beam Pt deposition. 30 kV Ga ion beam was used to make the rough cut and cleaning cross section patterns. 5 kV Ga ion beam was used for the final cross section polishing. High-resolution TEM (HRTEM) images were taken at 300 kV, and EDS mappings were performed in STEM mode. To investigate nanowires' oxidation resistance in air, HRTEM images were recorded on the same nanowires after exposing the above grid to ambient air for different periods from 1 week to 2 months. ImageJ was used for background noise reduction of the HRTEM images. The XRD spectrum of SiNWs on Si substrate was recorded by a high-resolution X-ray diffractometer (Rigaku SmartLab) with  $\text{Cu K}\alpha$  radiation ( $\lambda = 1.54 \text{ \AA}$ ) using theta-2theta scans. X-ray photoelectron spectroscopy (XPS) was performed on the nanowires with an XPS spectrometer (Thermo Fisher Scientific K-Alpha<sup>+</sup>). SiNWs were first dispersed in ethanol and then dropped on a highly oriented pyrolytic graphite (HOPG) substrate. The optical measurements were performed using a Raman spectroscopy (Jobin Yvon HR800, Horiba) and photoluminescence (Hitachi F7000 fluorescence spectrophotometer). For the Raman sample, a stainless-steel razor blade was chosen as the substrate, which does not have any strong peaks near typical Si peaks at around 300, 520, and 960  $\text{cm}^{-1}$ . Then SiNWs were collected from the surface of the etched Si substrate, dispersed inside of ethanol using a weak sonication process, and placed on the substrate for Raman measurement. To reduce the effects of heating by the laser Raman scattering data were collected in the region of low laser flux  $P = 1.87 \text{ mW}$  where the shape of the band is independent of  $P$ . For the PL sample, SiNWs were dispersed in ethanol by sonication and excited at a wavelength of 240 nm. The light-emitting examination was carried out on SiNW dispersion in a UV quartz cuvette under a UV light source (UV lamp, 254 nm) in a dark room. SiNWs on the Si substrate and SiNW dispersion in ethanol solvent were both used as samples, with Si wafer as a reference. UPS measurements were conducted using PHOIBOS 150 hemisphere (SPECS GmbH) with a He I ( $h\nu = 21.22 \text{ eV}$ ) discharging lamp as an excitation source. The IPES was performed in the isochromatic mode using a low-energy electron gun with a BaO cathode and a band-pass filter of 9.5 eV ( $\text{SrF}_2 + \text{NaCl}$ ). The resolution of UPS of 100 meV and IPES of 750 meV were determined by measuring the Fermi edge of a clean Au film. The binding energy of the presented spectra was calibrated with respect to the Fermi level. To avoid sample charging, the SiNWs were prepared on top of a HOPG substrate by spin-coating methods via ethanol dispersion. The transmittance of the nanowires was measured by the JASCO V-770 UV-vis spectrophotometer. To prepare the sample, a thin SiNWs film was fabricated by drop-casting ethanol dispersion on a quartz substrate, the signal of which was later subtracted from the spectrum.

## Data availability

The data that support the findings of this study are available from the manuscript, its supplementary information, or from the corresponding author upon request. Source data are provided with this paper.

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## References

- Shao, M., Ma, D. D. D. & Lee, S.-T. Silicon nanowires—synthesis, properties, and applications. *Eur. J. Inorg. Chem.* **2010**, 4264–4278 (2010).
- Cui, Y., Zhong, Z., Wang, D., Wang, W. U. & Lieber, C. M. High performance silicon nanowire field effect transistors. *Nano Lett.* **3**, 149–152 (2003).
- Cui, Y., Wei, Q., Park, H. & Lieber, C. M. Nanowire nanosensors for highly sensitive and selective detection of biological and chemical species. *Science* **293**, 1289 (2001).
- Garnett, E. C. & Yang, P. Silicon nanowire radial p–n junction solar cells. *J. Am. Chem. Soc.* **130**, 9224–9225 (2008).
- Chan, C. K. et al. High-performance lithium battery anodes using silicon nanowires. *Nat. Nanotechnol.* **3**, 31–35 (2008).
- Qu, Y. et al. Electrically conductive and optically active porous silicon nanowires. *Nano Lett.* **9**, 4539–4543 (2009).
- Kim, W., Ng, J. K., Kunitake, M. E., Conklin, B. R. & Yang, P. Interfacing silicon nanowires with mammalian cells. *J. Am. Chem. Soc.* **129**, 7228–7229 (2007).
- Wang, N. et al. Si nanowires grown from silicon oxide. *Chem. Phys. Lett.* **299**, 237–242 (1999).
- Gole, J. L., Stout, J. D., Rauch, W. L. & Wang, Z. L. Direct synthesis of silicon nanowires, silica nanospheres, and wire-like nanosphere agglomerates. *Appl. Phys. Lett.* **76**, 2346–2348 (2000).
- Peng, K.-Q. & Lee, S.-T. Silicon nanowires for photovoltaic solar energy conversion. *Adv. Mater.* **23**, 198–215 (2011).
- Read, A. J. et al. First-principles calculations of the electronic properties of silicon quantum wires. *Phys. Rev. Lett.* **69**, 1232–1235 (1992).
- Ma, D. D. D., Lee, C. S., Au, F. C. K., Tong, S. Y. & Lee, S. T. Small-diameter silicon nanowire surfaces. *Science* **299**, 1874 (2003).
- Nolan, M., O'Callaghan, S., Fagas, G., Greer, J. C. & Frauenheim, T. Silicon nanowire band gap modification. *Nano Lett.* **7**, 34–38 (2007).
- Holmes, J. D., Johnston, K. P., Doty, R. C. & Korgel, B. A. Control of thickness and orientation of solution-grown silicon nanowires. *Science* **287**, 1471 (2000).
- Zhang, Y. F. et al. Diameter modification of silicon nanowires by ambient gas. *Appl. Phys. Lett.* **75**, 1842–1844 (1999).
- Wagner, R. S. & Ellis, W. C. Vapor–liquid–solid mechanism of single crystal growth. *Appl. Phys. Lett.* **4**, 89–90 (1964).
- Wu, Y. et al. Controlled growth and structures of molecular-scale silicon nanowires. *Nano Lett.* **4**, 433–436 (2004).
- Hofmann, S. et al. Ledge-flow-controlled catalyst interface dynamics during si nanowire growth. *Nat. Mater.* **7**, 372–375 (2008).
- Garnett, E. C., Liang, W. & Yang, P. Growth and electrical characteristics of platinum-nanoparticle-catalyzed silicon nanowires. *Adv. Mater.* **19**, 2946–2950 (2007).
- Ross, F. M., Tersoff, J. & Reuter, M. C. Sawtooth faceting in silicon nanowires. *Phys. Rev. Lett.* **95**, 146104 (2005).
- Hannon, J. B., Kodambaka, S., Ross, F. M. & Tromp, R. M. The influence of the surface migration of gold on the growth of silicon nanowires. *Nature* **440**, 69–71 (2006).
- Wen, C. Y., Reuter, M. C., Tersoff, J., Stach, E. A. & Ross, F. M. Structure, growth kinetics, and ledge flow during vapor–solid–solid growth of copper-catalyzed silicon nanowires. *Nano Lett.* **10**, 514–519 (2010).
- Huang, Z., Geyer, N., Werner, P., de Boor, J. & Gösele, U. Metal-assisted chemical etching of silicon: A review. *Adv. Mater.* **23**, 285–308 (2011).
- Peng, K. et al. Metal-particle-induced, highly localized site-specific etching of si and formation of single-crystalline Si nanowires in aqueous fluoride solution. *Chem. Eur. J.* **12**, 7942–7947 (2006).
- Huang, Z., Fang, H. & Zhu, J. Fabrication of silicon nanowire arrays with controlled diameter, length, and density. *Adv. Mater.* **19**, 744–748 (2007).
- Yang, P. Chemistry and physics of silicon nanowire. *Dalton Trans.* **33**, 4387–4391 (2008).
- Cui, Y., Lauhon, L. J., Gudiksen, M. S., Wang, J. & Lieber, C. M. Diameter-controlled synthesis of single-crystal silicon nanowires. *Appl. Phys. Lett.* **78**, 2214–2216 (2001).
- Shimizu, T. et al. Synthesis of vertical high-density epitaxial si(100) nanowire arrays on a Si(100) substrate using an anodic aluminum oxide template. *Adv. Mater.* **19**, 917–920 (2007).
- Tan, Z. et al. Fabrication of ultra-thin silicon nanowire arrays using ion beam assisted chemical etching. *Nanoscale* **7**, 17268–17273 (2015).
- Burchak, O. et al. Scalable chemical synthesis of doped silicon nanowires for energy applications. *Nanoscale* **11**, 22504–22514 (2019).
- Ishiyama, T., Nakagawa, S. & Wakamatsu, T. Growth of epitaxial silicon nanowires on a si substrate by a metal-catalyst-free process. *Sci. Rep.* **6**, 30608 (2016).
- Dong, Y. et al. Low-temperature molten-salt production of silicon nanowires by the electrochemical reduction of casio3. *Angew. Chem. Int. Ed.* **56**, 14453–14457 (2017).
- Kobayashi, Y., Shinoda, Y. & Sugii, K. Thermal desorption from si(111) surfaces with native oxides formed during chemical treatments. *Jpn. J. Appl. Phys.* **29**, 1004–1008 (1990).
- Kobayashi, Y. & Sugii, K. Thermal decomposition of very thin oxide layers on si(111). *J. Vac. Sci. Technol. A* **10**, 2308–2313 (1992).
- Viera, G., Mikikian, M., Bertran, E., Cabarrocas, P. R. I. & Boufendi, L. Atomic structure of the nanocrystalline si particles appearing in nanostructured si thin films produced in low-temperature radiofrequency plasmas. *J. Appl. Phys.* **92**, 4684–4694 (2002).

36. Yu, D. P. et al. Nanoscale silicon wires synthesized using simple physical evaporation. *Appl. Phys. Lett.* **72**, 3458–3460 (1998).
37. Yogi, P. et al. Spectral anomaly in Raman scattering from p-type silicon nanowires. *J. Phys. Chem. C* **121**, 5372–5378 (2017).
38. Langner, T., Sieber, T. & Acker, J. Etching shapes the topography of silicon wafers: Lattice-strain enhanced chemical reactivity of silicon for efficient solar cells. *ACS Appl. Nano Mater.* **1**, 4135–4144 (2018).
39. Cammarata, R. C. Surface and interface stress effects in thin films. *Prog. Surf. Sci.* **46**, 1–38 (1994).
40. Miller, R. E. & Shenoy, V. B. Size-dependent elastic properties of nanosized structural elements. *Nanotechnology* **11**, 139–147 (2000).
41. Diao, J., Gall, K. & Dunn, L. M. Atomistic simulation of the structure and elastic properties of gold nanowires. *J. Mech. Phys. Solids* **52**, 1935–1962 (2004).
42. Lee, B. & Rudd, R. E. First-principles study of the Young's modulus of si <001> nanowires. *Phys. Rev. B* **75**, 041305 (2007).
43. Liang, H., Upmanyu, M. & Huang, H. Size-dependent elasticity of nanowires: Nonlinear effects. *Phys. Rev. B* **71**, 241403 (2005).
44. Park, H. S., Gall, K. & Zimmerman, J. A. Shape memory and pseudoelasticity in metal nanowires. *Phys. Rev. Lett.* **95**, 255504 (2005).
45. Diao, J., Gall, K. & Dunn, M. L. Surface-stress-induced phase transformation in metal nanowires. *Nat. Mater.* **2**, 656–660 (2003).
46. Cui, H., Wang, C. X. & Yang, G. W. Origin of self-limiting oxidation of si nanowires. *Nano Lett.* **8**, 2731–2737 (2008).
47. Büttner, C. C. & Zacharias, M. Retarded oxidation of si nanowires. *Appl. Phys. Lett.* **89**, 263106 (2006).
48. Tao, C., Cullen, W. G. & Williams, E. D. Visualizing the electron scattering force in nanostructures. *Science* **328**, 736–740 (2010).
49. Peng, K., Lu, A., Zhang, R. & Lee, S.-T. Motility of metal nanoparticles in silicon and induced anisotropic silicon etching. *Adv. Funct. Mater.* **18**, 3026–3035 (2008).
50. Chen, C.-Y., Wu, C.-S., Chou, C.-J. & Yen, T.-J. Morphological control of single-crystalline silicon nanowire arrays near room temperature. *Adv. Mater.* **20**, 3811–3815 (2008).
51. Hesketh, P. J., Ju, C., Gowda, S., Zanolari, E. & Danyluk, S. Surface free energy model of silicon anisotropic etching. *J. Electrochem. Soc.* **140**, 1080–1085 (1993).
52. Viera, G., Huet, S. & Boufendi, L. Crystal size and temperature measurements in nanostructured silicon using Raman spectroscopy. *J. Appl. Phys.* **90**, 4175–4183 (2001).
53. Li, B., Yu, D. & Zhang, S.-L. Raman spectral study of silicon nanowires. *Phys. Rev. B* **59**, 1645–1648 (1999).
54. Zi, J. et al. Raman shifts in si nanocrystals. *Appl. Phys. Lett.* **69**, 200–202 (1996).
55. Prades, J. D., Arbiol, J., Cirera, A., Morante, J. R. & Fontcuberta i Morral, A. Concerning the 506  $\text{cm}^{-1}$  band in the Raman spectrum of silicon nanowires. *Appl. Phys. Lett.* **91**, 123107 (2007).
56. Wang, R.-p et al. Raman spectral study of silicon nanowires: High-order scattering and phonon confinement effects. *Phys. Rev. B* **61**, 16827–16832 (2000).
57. Shen, M.-Y. & Zhang, S.-L. Band gap of a silicon quantum wire. *Phys. Lett. A* **176**, 254–258 (1993).
58. Jensen, I. J. T., Ulyashin, A. G. & Løvrik, O. M. Direct-to-indirect bandgap transitions in <110> silicon nanowires. *J. Appl. Phys.* **119**, 015702 (2016).
59. Hao, L. et al. Electrical and photovoltaic characteristics of  $\text{MoS}_2/\text{si}$  p-n junctions. *J. Appl. Phys.* **117**, 114502 (2015).
60. Rawool, S. A., Yadav, K. K. & Polshettiwar, V. Defective  $\text{TiO}_2$  for photocatalytic  $\text{CO}_2$  conversion to fuels and chemicals. *Chem. Sci.* **12**, 4267–4299 (2021).
61. Park, S. et al. Demonstration of the key substrate-dependent charge transfer mechanisms between monolayer  $\text{MoS}_2$  and molecular dopants. *Commun. Phys.* **2**, 109 (2019).
62. Park, S. et al. Direct determination of monolayer  $\text{MoS}_2$  and  $\text{WSe}_2$  exciton binding energies on insulating and metallic substrates. *2d Mater.* **5**, 025003 (2018).
63. Park, S. et al. The Schottky–Mott rule expanded for two-dimensional semiconductors: Influence of substrate dielectric screening. *ACS Nano* **15**, 14794–14803 (2021).
64. Macfarlane, G. G., McLean, T. P., Quarrington, J. E. & Roberts, V. Exciton and phonon effects in the absorption spectra of germanium and silicon. *J. Phys. Chem. Solids* **8**, 388–392 (1959).
65. Ashcroft, N. W. & Mermin, N. D. *Solid State Physics* (Saunders College, Philadelphia, 1976).
66. Kittel, C., McEuen, P. & McEuen, P. *Introduction to Solid State Physics* (Wiley New York, 1996).
67. Hong, K.-H., Kim, J., Lee, S.-H. & Shin, J. K. Strain-driven electronic band structure modulation of si nanowires. *Nano Lett.* **8**, 1335–1340 (2008).
68. Niquet, Y.-M., Delerue, C. & Krzeminski, C. Effects of strain on the carrier mobility in silicon nanowires. *Nano Lett.* **12**, 3545–3550 (2012).
69. Liu, H. I., Biegelsen, D. K., Ponce, F. A., Johnson, N. M. & Pease, R. F. W. Self-limiting oxidation for fabricating sub-5 nm silicon nanowires. *Appl. Phys. Lett.* **64**, 1383–1385 (1994).
70. Raider, S. I., Flitsch, R. & Palmer, M. J. Oxide growth on etched silicon in air at room temperature. *J. Electrochem. Soc.* **122**, 413–418 (1975).
71. Morita, M., Ohmi, T., Hasegawa, E., Kawakami, M. & Suma, K. Control factor of native oxide growth on silicon in air or in ultrapure water. *Appl. Phys. Lett.* **55**, 562–564 (1989).
72. Cui, Y. & Lieber, C. M. Functional nanoscale electronic devices assembled using silicon nanowire building blocks. *Science* **291**, 851 (2001).
73. Colinge, J.-P. et al. Nanowire transistors without junctions. *Nat. Nanotechnol.* **5**, 225–229 (2010).
74. Duan, X. et al. Quantification of the affinities and kinetics of protein interactions using silicon nanowire biosensors. *Nat. Nanotechnol.* **7**, 401–407 (2012).
75. Peng, K.-Q., Wang, X. & Lee, S.-T. Gas sensing properties of single crystalline porous silicon nanowires. *Appl. Phys. Lett.* **95**, 243112 (2009).
76. Yang, Y. et al. A review on silicon nanowire-based anodes for next-generation high-performance lithium-ion batteries from a material-based perspective. *Sustain. Energy Fuels* **4**, 1577–1594 (2020).
77. Krause, A. et al. High area capacity lithium–sulfur full-cell battery with prelithiated silicon nanowire-carbon anodes for long cycling stability. *Sci. Rep.* **6**, 27982 (2016).
78. Law, M., Greene, L. E., Johnson, J. C., Saykally, R. & Yang, P. Nanowire dye-sensitized solar cells. *Nat. Mater.* **4**, 455–459 (2005).
79. Tian, B. et al. Coaxial silicon nanowires as solar cells and nanoelectronic power sources. *Nature* **449**, 885–889 (2007).
80. Cao, L. et al. Engineering light absorption in semiconductor nanowire devices. *Nat. Mater.* **8**, 643–647 (2009).
81. Mahmoud, A. H. K. et al. Surface roughness effect on characteristics of si nanowire solar cell. *J. Photonics Energy* **10**, 045502 (2020).
82. Kalita, G. et al. Silicon nanowire array/polymer hybrid solar cell incorporating carbon nanotubes. *J. Phys. D: Appl. Phys.* **42**, 115104 (2009).

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## Author contributions

Y.J.J., S.G., S.H., H.Y.J., and S.G.L. conceived the idea and designed the experiments. S.H., S.G., and J.S. performed the experiments and fabricated the silicon nanowires. S.G., S.H., and W.L. performed the TEM and EDS characterizations. S.P., K.K., and Y.Y. performed the UPS and IPES characterizations. Y.L. and C.W.A. conducted the PL and XPS characterizations. S.G. and J.Y.B. performed the equilibrium calculations. Y.J.J., S.G., S.H., W.L., H.T., H.Y.J., S.P., Y.L., H.K., H.W., and M.U. analyzed the data. Y.J.J. and S.G. wrote the original draft. All authors contributed to discussions on the experiment and preparation of the manuscript.

## Competing interests

The invention described in this paper is the subject of a patent (US 9840774 B2) on December 12, 2017. The authors declare no further competing interests.

## Additional information

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