#### **ORIGINAL ARTICLE**



# Three-level inverter-fed model predictive torque control of a permanent magnet synchronous motor with discrete space vector modulation and simplified neutral point voltage balancing

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#### Abstract

In this study, discrete space vector modulation (DSVM) is used to achieve the flux and torque ripple reduction of finite set model predictive torque control (FS-MPTC) fed by a three-level neutral point clamped inverter (3L-NPCI). In the proposed DSVM, the synthesis of voltage vectors (VVs) can be controlled and implemented online, thereby eliminating the need for a VV lookup table. In addition, a simple and efficient preselection VV strategy is presented to reduce the number of candidate VVs in the prediction stage. The problem of capacitor voltage imbalance in 3L-NPCI is easily solved without including it as a control objective in the cost function. Thus, the complexity of tuning multiple weighting factors can be reduced. Hence, the proposed PTC-DSVM algorithm is simple and is capable of minimizing torque/flux ripples. It can also achieve a fast torque dynamic performance while maintaining balanced capacitor voltages. The effectiveness of the proposed method is verified through experimental results.

**Keywords** Discrete space vector modulation  $\cdot$  Permanent magnet synchronous motor  $\cdot$  Predictive torque control  $\cdot$  Three-level neutral point clamped inverter

# 1 Introduction

Finite set model predictive torque control (FS-MPTC) has emerged as a promising derivative structure from direct torque control (DTC) owing to its ease of implementation, fast torque response, and simple inclusion of constraints and nonlinearities [1–6]. However, some inherent issues are associated with FS-MPTC because of the finite number of switching states. In general, the application of a single switching state throughout the control cycle increases flux/ torque ripples with a variable switching frequency [7]. Several strategies have been introduced to reduce the flux/torque ripple of the FS-MPTC scheme [8].

Over the past decades, the demand for multilevel power converters (ML-PCs) has increased across industries and research communities [9-15], particularly in high-power applications [13]. One of the most attractive ML-PC

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To decrease the calculation effort of MPC owing to the large number of feasible voltage vectors (VVs), scholars have introduced various solutions [16–18]. The main aim of these predictive torque control (PTC) algorithms is to reduce the number of predicted VVs and/or switching sequences. The issue of weighting factor tuning in MPC has been completely eliminated or reduced in previous studies [19–21]. However, the difficulty with 3L-NPCIs is the neutral point (NP) imbalance owing to the deviation of the capacitor voltage. This issue causes the degradation of output currents and imposes large stress on switching devices. Consequently, several studies have focused on addressing this problem

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using the opposite effect of redundant switching states on NP potential and alterations in various modulation techniques [11, 22, 23].

Some solutions have been proposed to ensure minimal torque errors using FS-MPTC. One of these solutions is duty cycle control by inserting one or two VVs per sampling interval [24]. Another torque/flux reduction strategy involves applying discrete space vector modulation (DSVM), which synthesizes a large number of virtual VVs [25]. The application of DSVM to MPC results in a relatively high computational burden in actual implementation owing to the large number of virtual VVs. This issue can be solved by applying deadbeat control [13] and using lookup tables [25–27].

This study proposes a simple FS-MPTC with DSVM (PTC-DSVM) for a 3L-NPCI-fed permanent magnet synchronous motor (PMSM) while considering the NP voltage deviation. The proposed method generates feasible VVs online using an effective preselection strategy on the basis of flux orientation and torque error. To address the imbalance issue, this study proposes a simple offset voltage to be injected on the best switching VV after the cost function evaluation. In this manner, the control complexity and calculation load are minimized by reducing the number of candidate VVs and control objectives. The feasibility of the proposed method is validated using experimental results.

#### 2 Basic 3L-NPCI-fed PTC of PMSM

### 2.1 Model of 3L-NPCI

Figure 1 shows the topology of the 3L-NPCI for PMSM. Each phase has four switching devices:  $S_{x1}$ ,  $S_{x2}$ ,  $S_{x3}$ , and



The 3L-NPCI consists of 19 VVs with a combination of 27 switching states. The VVs include three zero VVs (ZVV)  $\vec{v}_{0,1,2}$ ; six small VVs (SVVs)  $\vec{v}_{3},...,\vec{v}_{8}$ ; six medium VVs (MVVs)  $\vec{v}_{9},...,\vec{v}_{14}$ ; and six large VVs (LVVs)  $\vec{v}_{15},...,\vec{v}_{20}$ .

Each SVV has two switching states with two opposite connections to the top capacitor voltage  $(V_c^T)$  or bottom capacitor voltage  $(V_c^B)$ . The voltage switching states are characterized by *P*, *O*, and *N*, which represent a link to "+" DC voltage bus, NP, and "–" DC bus, respectively. On the basis of the size of the VV, the space VVs are divided into four groups: large  $(\vec{v}_L)$ , medium  $(\vec{v}_M)$ , small  $(\vec{v}_S)$ , and zero  $(\vec{v}_Z)$ . To ensure a balanced 3L-NPCI drive system, the voltages for the top and bottom capacitors should be equally distributed.

#### 2.2 Traditional FS-MPTC

The equations for the FS-MPTC of the PMSM in the stationary reference frame are as follows:

$$\vec{\psi}_s(\mathbf{k}) = L_s \cdot \vec{i}_s(k) + \vec{\psi}_{pm}(k), \tag{1}$$

$$T_e = \frac{3}{2} p \bullet (\vec{\psi}(\mathbf{k}) \bullet \vec{i}_s(k)), \qquad (2)$$

where  $\vec{v}_s$ ,  $\vec{i}_s$ , and  $\vec{\psi}_s$  represent the stator voltage, stator current, and stator flux vectors, respectively.  $T_e$  represents the electrical torque.  $R_s$  and  $L_s$  represent the stator resistance and stator self-inductance, respectively.  $\vec{\psi}_{pm}$  represents



Fig.2 Space vector diagram of conventional FS-MPTC fed by 3L-NPCI



Fig. 1 Circuit structure of 3L-NPCI of PMSM

the permanent magnet flux, and *P* represents the number of pole pairs.

The predicted quantities of stator flux vector and stator current vector can be, respectively, expressed as follows:

$$\vec{\psi}_s(k+1) = \vec{\psi}_s(k) + T_s \cdot \vec{v}_s(k) + T_s R_s \cdot \vec{i}_s(k) + T_s \cdot Y \cdot \omega_r(k),$$
(3)

$$\vec{i}_s(k+1) = \Lambda \cdot \vec{i}_s(k) + \mathbf{M} \cdot \vec{v}_s(k) + \Gamma,$$
(4)

where

$$\vec{i}_{s} = \begin{bmatrix} i_{s\alpha} \\ i_{s\beta} \end{bmatrix}, \ \vec{v}_{s} = \begin{bmatrix} v_{s\alpha} \\ v_{s\beta} \end{bmatrix}, \ Y = \begin{bmatrix} \psi_{s\beta} \\ -\psi_{s\alpha} \end{bmatrix},$$
$$\Lambda = \begin{bmatrix} 1 - R_{s} \cdot T_{s}/L_{s} \ L_{s} \cdot T_{s} \cdot \omega_{r}(k) \\ -L_{s}.T_{s} \cdot \omega_{r}(k) \ 1 - R_{s} \cdot T_{s}/L_{s} \end{bmatrix},$$
$$\mathbf{M} = \begin{bmatrix} T_{s}/L_{s} \ 0 \\ 0 \ T_{s}/L_{s} \end{bmatrix}, \ \Gamma = \begin{bmatrix} 0 \\ -\vec{\psi}_{pm} \cdot \omega_{r} \cdot T_{s}/L_{s} \end{bmatrix},$$

where  $\omega_r$  represents the motor rotor speed and k and k + l denote the present and predicted sampling discrete steps, respectively.  $T_s$  represents the sampling time.

On the basis of the predicted values of (3) and (4), the motor torque can be predicted as follows:

$$T_e(k+1) = 6\frac{3}{2}p.(\vec{\psi}_s(k+1) \cdot \vec{i}_s(k+1)).$$
(5)

The 3L-NPCI structure consists of 19 VVs, as illustrated in Fig. 2. A cost function is formed as follows to determine the optimal VV among the admissible VVs:

$$g = |T_{ref} - T_e(k+1)| + Y_{\psi} |\psi_{ref} - |\vec{\psi}_s(k+1)||,$$
(6)

where  $T_{ref}$  represents the torque reference and  $\psi_{ref}$  represents the flux linkage reference at the  $k^{th}$  sampling time.  $Y_{\psi}$  represents the weighting factor that defines the relative importance of the flux control objective.  $Y_{\psi}$  is found herein as the ratio of rated torque and rated flux  $(T_{rated}/\psi_{rated})$ . Note that the obtained value of the weighting factor still needs further fine-tuning during the experimental implementation.

## 3 Proposed PTC-DSVM fed by 3L-NPCI

The proposed PTC method uses a DSVM to synthesize VVs. The aim of this study is to achieve a reduction in torque and flux ripple such that the method can be easily applied with reduced complexity and the problem of NP balance for 3L-NPCI systems can be resolved.

#### 3.1 Synthesis of online-generated VVs

Although the conventional PTC fed by 3L-NPC uses only 19 VVs defined by switching states for prediction, the proposed PTC based on the DSVM strategy can extend the prediction to a large number of synthesized virtual VVs to achieve further waveform ripple reductions in steady-state operations.

Figure 3a shows the general voltage space voltage diagram (VSVD), which was initially developed by [24]. This DSVM method consists of 12 VSVD regions and *K* hexagons of various sizes and numbers of VVs; it is expressed as follows:

$$O(m) = 12 \bullet m, \{ [m = 1 : K] : O(m) = 1 \},$$
(7)

where *O* is the total number of VVs. In this DSVM, the VSVD is divided into 12 regions (i.e.,  $R_I - R_{12}$ ). In each VSVD region, VVs are synthesized upon invoking on the



Fig. 3 Coordinate diagram for a General VSVD; b VV values of R4 when K=2

basis of (7). The symmetry of the VSVD regions according to the  $\alpha - \beta$  frame is an attractive feature of the DSVM method. Thus, three mathematical expressions for each group of VSVD regions can be generated online and are expressed as follows:

$$(v_{\alpha}, v_{\beta})^{(x,y)} = \left\langle \delta \cdot V_{DC} / _{6K} (x + 3y), \sqrt{3} \cdot \xi \cdot V_{DC} / _{6K} (-x + y) \right\rangle,$$
(8)

$$(v_{\alpha}, v_{\beta})^{(x,y)} = \left\langle \delta \cdot V_{DC} / _{6K}((-x+3y)), \sqrt{3} \cdot \xi \cdot V_{DC} / _{6K}(x+y) \right\rangle,$$
(9)

$$(v_{\alpha}, v_{\beta})^{(x,y)} = \left\langle \delta \cdot V_{DC} / _{3K}(x), \xi \cdot V_{DC} / _{3K} \left( \sqrt{3} \cdot y \right) \right\rangle,$$
(10)

where x and y represent the quantities for each coordinate VSVD region from 0 to K.  $\delta$  and  $\xi$  represent the coefficients based on the VSVD regions, as listed in Table 1. Thus, the VSVD region sets ( $R_3$ ,  $R_4$ ,  $R_8$ , and  $R_{10}$ ), ( $R_2$ ,  $R_5$ ,  $R_8$ , and  $R_{11}$ ), and ( $R_1$ ,  $R_6$ ,  $R_7$ , and  $R_{12}$ ) correspond to (8), (9), and (10), respectively. On the basis of (7), the number of predicted VVs depends on parameter K of the hexagonal diagrams. Thus, in this study, K can be freely set online, and it can control the number of VVs in the prediction stage.

Figure 3b shows the coordinate labels of  $\alpha - \beta$  VVs in the VSVD region  $R_4$  when K is set to 2 and the coefficients  $\delta$  and  $\xi$  are identified to be + 1. Apparently, the same sets of voltage labels in the (x, y) coordinate plane  $\{v_{\alpha\beta}^{(0,0)}, v_{\alpha\beta}^{(0,0)}, v_{\alpha\beta}^{(0,1)}, v_{\alpha\beta}^{(0,2)}, v_{\alpha\beta}^{(1,2)}, v_{\alpha\beta}^{(2,2)}\}$  are employed upon initiation for all VSVD regions. However, the value for each voltage label also depends on the corresponding equations, i.e., (8) to (10), and the coefficients in Table 1.

#### 3.2 Proposed preselection strategy

Considering all the VVs for future torque and flux prediction is known to significantly increase the calculation time. Hence, such a strategy is unsuitable for a real control system. Moreover, storing VVs in predefined lookup tables increases system complexity, as in [25]. In addition, the preselection technique of the conventional DSVM-based PTC [25] can affect torque performance because this method needs to regulate the flux and torque simultaneously. Hence, the candidate VVs in the selected

Table 1 Coefficient values according to VSVD regions

Coefficient	VSVD regions				
	$\overline{R_1 - R_3}$	$R_4 - R_6$	$R_{7} - R_{9}$	$R_0 - R_{12}$	
δ	+1	+1	-1	-1	
ξ	-1	+1	+1	-1	

DSVM region for the increase or decrease of flux during the torque transient could have a slow torque dynamic response.

In this study, a new preselection strategy based on flux orientation and torque error is proposed to determine the optimal VSVD region per control cycle. Upon the selection of the VSVD region, the corresponding candidate VVs are generated online for the cost function calculation using (8)–(10) and the coefficients in Table 1.

To find the admissible VVs, the proposed PTC-DSVM applies active VVs that ensure the circular motion of the stator flux on the basis of the stator flux orientation ( $\phi_s$ ) and torque error ( $T_{err} = T_{ref} - T_e$ ) for the flux direction. The flux position is determined as follows:

$$\phi_s = \arctan(\psi_\beta / \psi_\alpha). \tag{11}$$

Thus, the variation in stator flux is obviously controlled by the VV, as in the DTC algorithm, and is proportional to the direction of the applied VV during  $T_s$ .

The designed preselection strategy applies the DSVM region with VVs, which ensure fast toque responses while maintaining good flux regulation.

As the 12 SVD regions in Fig. 3 are strictly symmetrical, the  $\alpha - \beta$  plane of flux rotation is divided into 12 sectors, as defined by

$$(Z-1) \bullet \pi/6 \le S_{\psi}(Z) \le Z \bullet \pi/6, \tag{12}$$

where  $S_w$  represents the flux sector and Z = 1, ..., 12.

In this manner, each flux sector corresponds to one VSVD region, whose VVs are the closest to the circular trajectory of the stator flux in the anticlockwise direction for an increasing torque (TI) and in the clockwise direction for a decreasing torque (TD). Figure 4 shows an example in which the stator flux  $\vec{\psi}_s$  is located at  $S_{\psi}(1)$ . The VSVD region  $R_6$  is the optimum region in the anticlockwise direction, and region  $R_1$  is the optimal VSVD region in the clockwise direction. Table 2 lists the optimum VSVD region, the VVs are initiated online. The prediction process begins with the evaluation of these VVs using the cost function in (6) to obtain the optimal VV ( $v^{opt}$ ).

#### 3.3 Proposed neutral point voltage balance

The 3L-NPCI consists of two DC link capacitors linked in series. Although good performance can be achieved with PTC-DSVM, this topology can cause NP voltage deviation, which leads to increased voltage stress on the switching device. For good motor drive performance, the top and bottom voltages of the in-series-linked DC link capacitors should be balanced. A common practice is to treat the



Fig. 4 Illustration for the VSVD region selection for both flux rotation at  $S\psi(1)$ 

Table 2	Optimal VSVD region
for each	flux sector

	Torque increase (TI)	Torque decrease (TD)
$S_w(1)$	<i>R</i> <sub>7</sub>	$R_1$
$S_{\psi}(2)$	$R_8$	$R_2$
$S_{\psi}(3)$	$R_9$	$R_3$
$S_{\psi}(4)$	$R_{10}$	$R_4$
$S_{\psi}(5)$	<i>R</i> <sub>11</sub>	$R_5$
$S_{\psi}(6)$	<i>R</i> <sub>12</sub>	$R_6$
$S_{\psi}(7)$	$R_1$	$R_7$
$S_{\psi}(8)$	$R_2$	$R_8$
$S_{\psi}(9)$	$R_3$	$R_9$
$S_{\psi}(10)$	$R_4$	$R_{10}$
$S_{\psi}(11)$	$R_5$	$R_{11}$
$S_{\psi}(12)$	<i>R</i> <sub>6</sub>	<i>R</i> <sub>12</sub>

problem of capacitor voltage balancing by using a separate cost function; this approach requires an additional weighting factor [19–21]. However, this practice involves tedious tuning of the cost function weighting factors, thereby leading to high control complexity.

In this study, the proposed balancing technique involves a simple voltage offset based on the maximum and minimum values of the selected VV  $v^{opt}$ . The main advantage of this proposed capacitor voltage balancing scheme is that it is simple and easily employed, thus eliminating the need for a weighting factor or cost function evaluation. In addition, the

proposed method does not consider the redundant switching states of small VVs. The proposed NP voltage balancing scheme applies a compensated voltage offset ( $v_{off,cv}$ ) on the basis of the top capacitor voltage  $V_c^T$  and bottom capacitor voltage  $V_c^B$  in the linear modulation range; it is expressed as follows:

$$\begin{cases} v_{off,cv} = \frac{V_{dc}}{2} - v_{max}, & (V_C^T \ge V_C^B) \\ v_{off,cv} = -\frac{V_{dc}}{2} - v_{min}, & (V_C^T < V_C^B) \end{cases},$$
(13)

where  $v_{max}$  and  $v_{min}$  are the maximum and minimum values of best switching vector  $v^{opt}$ , respectively. Then, the resultant voltage output ( $v_{res}$ ) can be obtained by injecting the optimum VV obtained from the cost evaluation using the offset voltage in (13) as follows:

$$v_{res} = v^{opt} + v_{off,cv.} \tag{14}$$

For convenience, the resultant VV  $v_{res}$  is implemented on a carrier-based pulse-width modulator to produce switching signals. The entire control algorithm for the proposed method is illustrated in Fig. 5.

#### 3.4 Implementation and experimental results

The performance of the proposed PTC-DSVM for the 3L-NPCI-fed PMSM coupled with an induction machine (load) was verified experimentally by using a functional DSP (TMS320C28346). The specifications of the PMSM parameters are listed in Table 3. The nominal flux and weighting factor were set at 0.58 Wb and 150, respectively.



Fig. 5 Control diagram for the proposed PTC-DSVM

#### Table 3 PMSM parameters

Parameter	Value	
Torque rating	60 Nm	
Power rating	11 kW	
Current rating	19.9 A	
Speed rating	1750 rpm	
Permanent magnet flux	0.554 Wł	
Number of poles	6	
Stator resistance	0.349 Ω	
Stator inductance	15.6 mH	

The DC link voltage was 300 V. Therefore, each capacitor voltage was 150 V.

Figure 6 shows the proposed NP voltage balancing control for the PTC-DSVM when the motor operates at 100 rpm and is loaded with 5 Nm. Before the NP voltage balancing control, a large voltage deviation between the capacitor voltages (i.e.,  $V_c^T$  and  $V_c^B$ ) was clearly observed. After the proposed offset voltage was applied to the best switching VV obtained from the cost function evaluation, the capacitor voltage deviations immediately converged to 150 V.

The performance of the proposed PTC-DSVM was evaluated by comparing it with the traditional PTC-DSVM [25] at different motor operations under the same number of VVs (i.e., 37 VVs) and the same weighting factor.

Figure 7 shows the steady-state performance at 600 rpm with a load of 10 Nm for the proposed PTC-DSVM and the traditional PTC-DSVM [25]. From top to bottom, the waveforms shown in Fig. 7 are the flux amplitude, electromagnetic torque, top and bottom capacitor voltages, line-to-line voltage  $V_{ab}$ , and A-phase stator current  $i_a$ . The torque and flux ripples were calculated online using the equation in [26]. Figure 7b clearly shows that the proposed PTC-DSVM



Fig. 6 Experimental result for the control of capacitor voltages balancing in the proposed PTC-DSVM



Fig. 7 Experimental steady-state response at 600 rpm for: a traditional PTC-DSVM [25]; (b) proposed PTC-DSVM

improved the steady-state performance with minimal torque and flux ripples while maintaining excellent capacitor voltage balancing. A feature worth mentioning is that the proposed balancing control is applied to the PTC-DSVM methods. In addition, the line-to-line voltage of the proposed PTC-DSVM algorithm had the best shape with reduced voltage spikes. Figures 7a and 7b show that the traditional PTC-DSVM suffered from large torque spikes, which resulted in undesirable output current surges.

Figure 8 shows the steady-state performance of the PTC-DSVM methods at 100 rpm with a load of 10 Nm. The proposed method showed an excellent reduction in terms of flux/torque ripples, whereas the conventional PTC-DVSM presented torque spikes and current surges. Relative to the traditional PTC-DSVM, the proposed technique had reduced voltage spikes for the line-to-line voltage. Both methods maintained excellent capacitor voltage balancing using the proposed offset voltage.

Figure 9 illustrates the torque transient performance from 3 to 30 Nm for the proposed and traditional PTC-DSVM methods when the PMSM ran at 300 rpm. Notably, the proposed method demonstrated excellent torque



Fig.8 Experimental steady-state response at 100 rpm for: a traditional PTC-DSVM [25]; (b) proposed PTC-DSVM

transient performance relative to the traditional method. The poor torque transient performance of the traditional PTC-DSVM [25] was attributed to the selected region of the VVs and the location of the flux position. Notably, the fast torque response depended on the selected VV during the torque transient [28]. Thus, the selected VV region could have inappropriate candidate VVs to achieve a fast torque performance.

# 4 Conclusion

A DSVM-based PTC algorithm was proposed in this study to enhance the performance of a 3L-NPCI-driven PMSM. The study also achieved NP voltage balancing using a simple offset voltage, which was injected after the cost function calculation.

The proposed PTC-DSVM technique significantly improved the performance of the PMSM fed by 3L-NPCI in terms of a reduction in flux, current, and torque ripples while maintaining NP voltage balancing. The main advantage of the proposed PTC-DSVM algorithm is its simplicity.



**Fig. 9** Experimental dynamic torque response for: **a** traditional PTC-DSVM [25]; (**b**) proposed PTC-DSVM

It is also capable of minimizing the torque/flux ripples and achieving a fast torque dynamic performance.

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