REVIEW PAPER

Status, challenges and trends of data‑intensive supercomputing

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Abstract

Supercomputing technology has been supporting the solution of cutting-edge scientifc and complex engineering problems since its inception—serving as a comprehensive representation of the most advanced computer hardware and software technologies over a period of time. Over the course of nearly 80 years of development, supercomputing has progressed from being oriented towards computationally intensive tasks, to being oriented towards a hybrid of computationally and data-intensive tasks. Driven by the continuous development of high performance data analytics (HPDA) applications—such as big data, deep learning, and other intelligent tasks—supercomputing storage systems are facing challenges such as a sudden increase in data volume for computational processing tasks, increased and diversifed computing power of supercomputing systems, and higher reliability and availability requirements. Based on this, data-intensive supercomputing, which is deeply integrated with data centers and smart computing centers, aims to solve the problems of complex data type optimization, mixed-load optimization, multi-protocol support, and interoperability on the storage system—thereby becoming the main protagonist of research and development today and for some time in the future. This paper frst introduces key concepts in HPDA and data-intensive computing, and then illustrates the extent to which existing platforms support data-intensive applications by analyzing the most representative supercomputing platforms today (Fugaku, Summit, Sunway TaihuLight, and Tianhe 2A). This is followed by an illustration of the actual demand for data-intensive applications in today's mainstream scientifc and industrial communities from the perspectives of both scientifc and commercial applications. Next, we provide an outlook on future trends and potential challenges data-intensive supercomputing is facing. In a word, this paper provides researchers and practitioners with a quick overview of the key concepts and developments in supercomputing, and captures the current and future data-intensive supercomputing research hotspots and key issues that need to be addressed.

Keywords Data-intensive supercomputing · I/O intensive supercomputing · High performance data analytics · Parallel processing systems · Supercomputing storage

1 Introduction

Supercomputing, or High performance computing (HPC), has been an important engine of progress for human society since its inception in the middle of the last century. Its development is a comprehensive refection of the highestend computer hardware and software technologies, and is also the frontier track for countries around the world to compete in science and technology. From the birth of the frst

 \boxtimes Longxiang Wang wlx419@xjtu.edu.cn electronic computer in 1946, mankind entered the era of supercomputing. HPC served as a high-end computing infrastructure for solving computationally intensive scientifc and engineering problems. The core problem to be solved by supercomputing is how to efficiently utilize the evolving parallel processing units as well as parallel architecture to serve scientifc computing tasks such as high energy physics, astronomy, bioinformatics, dynamics and fuid dynamics. The traditional supercomputing aims to increase the number of foating point operations per second, focusing on computational performance, programmability, scalability, energy efficiency and other issues, and is dedicated to handling computationally intensive tasks.

With the technological development of hardware and software for supercomputing and the change in demand for

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supercomputing applications caused by social progress, the role played by supercomputing is also changing (Lu et al. [2018](#page-18-0)). The data generated by typical HPC applications such as earthquake prediction, oil exploration, and climate analysis are growing explosively, and are characterized by volume, velocity, and variability, which are typical big data scenarios. On the other hand, AI technology is being used in various HPC scenarios—such as deep learning-based climate analysis and earthquake simulations—and has even won a Gordon Bell Award. Joseph II, Executive Director of IDC HPC User Forum, pointed out that the advent of the Big Data era has made HPDA (High performance data analytics) applications the next hot spot for HPC, with 67% of HPC resources currently devoted to HPDA—of which AI is a typical application. Zheng Weimin, an academician of the Chinese Academy of Engineering, also pointed out that the future trend of supercomputing is $AI +$ scientific computing at the 2019 HPC China Conference. Consequently, data-intensive tasks are becoming a mainstream application of supercomputing.

Figure [1](#page-1-0) shows the industry application distribution from the 2018 China HPC TOP 100. The applications are distributed across eight major areas, with the largest number of applications used for big data processing at 27%; the rest are cloud computing (20%), scientifc computing (17%), supercomputing centers (13%), government telecommunications (6%), security (4%), industrial manufacturing (4%), and energy and oil (2%). This indicates that HPC applications are becoming more data-intensive. The design goals of supercomputing have also shifted from simply pursuing computational power in terms of FLOPS, to pursuing both computational and data processing power. In recent years, the benchmark for assessing supercomputing capabilities has been supplemented by the I/O 500 and GRAPH 500 benchmarks, which have shifted from purely assessing compute-intensive capabilities to more data-intensive processing capabilities. GRAPH 500 focuses on a newly designed benchmark based on graph search and shortest path problems, with a focus on the ability to handle data-intensive applications.

There are currently two main academic perspectives that distinguish between data-intensive and compute-intensive tasks. One considers the amount of data and computation required by the application itself; and the other considers the most signifcant challenging problems that exist for the application.

From the perspective of the amount of data and the computations required, AM Middleton considers data-intensive computing tasks as a class of parallel computing tasks that use data-parallel methods to process large amounts of data, which are typically terabytes or petabytes in size and are often referred to as big data. Computing tasks that spend most of their execution time on computational requirements are considered computationally intensive, while computing tasks that require large amounts of data and spend most of their time on I/O and data processing are considered data intensive(Middleton [2016](#page-18-1)).

In terms of the most signifcant challenging problems of the application, Kleppmann argues that if data problems including the amount of data, the complexity of the data, and the frequency of data exchange—are the main challenges for a task, then the task is considered as data intensive. Conversely, if computing is the main challenge, then the task is computationally intensive (Kleppmann [2017](#page-18-2)).

Both defnitions are widely accepted by the industry. We argue that these two defnitions are equivalent. When the time spent on processing data accounts for a large portion of the overall completion time in a task, the problems associated with data processing must become a bottleneck for the entire task, and the bottleneck device is a key limiting factor in improving the performance of the overall system (Harchol-Balter [2013](#page-17-0)). Therefore, this paper argues that data-intensive tasks are a class of applications where the size of data is massive, the variety of data is rich, the

data operations are frequent, and where the data processing becomes a critical bottleneck.

The evolution of applications into a data-driven paradigm requires supercomputing platforms to make adaptive developments. The future development trend of supercomputing platforms is focused on the explosive increasing volume of data for computational processing tasks, diversifcation of computing power, higher reliability and availability requirements, and deeper integration of supercomputing centers with data and intelligent computing centers.

First, the amount of data processed by supercomputing platforms increases signifcantly. Further, data types are becoming more diverse. In addition to the structured data commonly used in traditional supercomputers, unstructured data, such as image, video, and voice data, are increasingly being used. The ultimate goal of supercomputing is to better serve academia and industry (Hager and Wellein [2010](#page-17-1)). Since 1980, supercomputing applications have started to enter the commercial area, and typical applications such as gene sequencing (Schmidt and Hildebrandt [2017](#page-18-3)), earth simulation (Wang and Yuan [2020](#page-19-0)), engineering design (Henz et al. [2018\)](#page-17-2), and drug development (Puertas-Martín et al. [2020\)](#page-18-4) have an intense demand for accessing large-scale data. In particular, in recent years, the rapid development of deep learning, reinforcement learning, and other related AI technologies has greatly contributed to performance breakthroughs in image recognition, speech recognition, time series prediction, and other applications. AI technologies support the rapid development of smart cities, autonomous driving, precision medicine, and other applications (Zhong et al. [2019\)](#page-19-1). In addition, traditional compute-intensive tasks on HPC, such as weather prediction, are gradually being transformed to incorporate new AI technologies, which are more data-intensive. AI techniques often beneft from ftting accurate models to large-scale data. For example, the ResNeXt WSL (Orhan [2019\)](#page-18-5), the most powerful pre-training model for image classifcation to date, developed by Kaiming and he's team, has 800 million parameters and uses 940 million images for training. The development of AI technology has brought huge demands and challenges for storage capacity. According to the forecast data of the global HPC market growth rate in 2020 by Hyperion Research, an international authoritative analyst frm, the storage market is growing at the frst rate, much higher than the overall market average growth rate. In addition, supercomputing systems are increasingly demanding the performance of metadata. The VPIC, a representative scientifc computing application, needs to save the particle track into a fle for each process. This requires storage systems to have the ability to handle millions of fle creation, writes, and other operations simultaneously.

Second, the computing power of supercomputing platforms has increased significantly and has diversified.

Existing supercomputing platforms can often provide hundreds of Peta FLOPS of computing power; and in recent years, they will break through Exa FLOPS of computing power. Therefore, a few single tasks can consume all the cluster computing power, and most scenarios are multi-task concurrent, such as the Shanghai Jiaotong University Supercomputing Center with 100 PFLOPS of computing power. Further, the average number of concurrent tasks is approximately 50. These tasks have diferent storage requirements, and some have high bandwidth requirements, and some have high IOPS requirements. At the same time, the dramatic increase in computing power has also led to an explosion in the size of data output from scientifc computing—requiring HPC storage systems to be able to handle a variety of diferent load capacities. With the rise of diverse computing power such as GPUs, FPGAs, and various AI chips, efficient data transfer from the storage side to the accelerated computing device side has become a serious bottleneck in traditional HPC storage architectures.

Third, ever-rich applications have become more demanding in terms of the reliability and availability of supercomputing platforms. Traditional supercomputers were mainly used for scientifc research, which had a high tolerance for failures and could accept occasional machine failures as long as the correct results could be obtained. Currently, supercomputers are more often used in production systems, which have high requirements for the reliability and availability of results and processes, and have a very low tolerance for data loss.

Fourth, supercomputing centers, data centers, and AI computing centers are gradually integrating to provide more diversifed services; such as AI computing, cloud computing, big data, virtualization, and disaster recovery. For example, Tianhe 2A is no longer limited to providing scientifc computing services, but also supports big data analysis services (Orion big data analysis platform). The biggest problem they face is the fow of large amounts of data. According to a study by the Internet Data Center (IDC), the total amount of global data reached 44 ZB in 2020 (1 ZB = 1 billion TB $= 1$ trillion GB), while the amount of data generated by China alone reaches 8 ZB, accounting for approximately one-ffth of the total global data volume. China's total data volume is expected to grow to 48.6 ZB by 2025, accounting for 27.8% of the world's total data volume. These data include fle storage for supercomputing applications, block storage for virtualization, object storage for AI, HDFS storage for big data, and archival storage of supercomputers, all of which are fragmented. Further, accessing the data fow is the biggest challenge today.

The change in supercomputing platforms presents a signifcant challenge for storage systems. The increasing volume and richness of data types, the balance of bandwidth and I/O capabilities, the guarantee of processing and results reliability, and data fow are the challenges faced by the supercomputing industry. These four challenges place higher demands on storage systems. Therefore, storage systems have become a key bottleneck in data analysis. We argue that future supercomputing platforms need to balance computing-intensive tasks and data-intensive tasks, as the contradiction between growing data processing needs, and relatively poor storage capacity deepens.

Through analysis of the characteristics of data-intensive applications and the development of supercomputing, we argue that storage systems for supercomputers need to focus on the following key issues.

- Complex data-type supporting problems. HPDA applications have complex data types, including structured data, semi-structured data, and unstructured data. The datasets may consist of large fles, or many small fles. How should the platform handle diferent types and sizes of data to fully utilize the platform's computational potential is a key challenge.
- Mixed-load optimization problems. How to ensure that the storage system performs well under various HPDA application workloads.
- Multiprotocol support and interoperability issues. The storage system should simultaneously support multiple data access protocols that HPDA applications may use, such as NFS, HDF, POSIX, SMB protocol, and Amazon S3, etc.

This paper frst briefy introduces the key concepts of dataintensive supercomputing. We then introduce the typical data-intensive application requirements through both commercial and scientifc supercomputing applications, and reveal the problems of data silos in the next generation of supercomputing in the face of multiple computing power paradigms in complex application scenarios. Next, we introduce the most representative supercomputers in the TOP 500 and IO 500 lists. Finally, we present the challenges faced by data-intensive supercomputers, such as data transfer bottlenecks, data silos, resource seizure, and data management.

2 Basic Concept

2.1 Supercomputers and Clusters

In most computing contexts, a supercomputer refers to a computer capable of performing high-speed operations that cannot be handled by ordinary personal computers, with specifcations and performance much more powerful than those of personal computers, and in this context supercomputers and high-performance computing systems are diferent expressions of a unifed concept. Supercomputer can usually be divided into fve categories: parallel vector processors (PVP), symmetric multiprocessors (SMP), massively parallel processors (MPP), distributed shared storage multiprocessors (DSM) and clusters (Rajak [2018\)](#page-18-6). The only three massively parallel processors left are the "Fugaku"(Shimizu [2020](#page-18-7)) supercomputer developed by Japan's RIKEN and Fujitsu, the "Sunway TaihuLight"(Gao et al. [2021\)](#page-17-3) supercomputer developed by China's Wuxi National Supercomputing Centre and the "Perlmutter" supercomputer developed by American National Energy Research Scientifc Computing Centre.

In summary, clusters are one type of supercomputer and have taken over the mainstream of supercomputers. A cluster is an inexpensive, easy to build, and highly scalable parallel computer system. It consists of multiple homogeneous or heterogeneous independent computers interconnected by a high-performance network or local area network to collaborate on a specifc parallel computing task. Nowadays, many enterprises' data centers or cloud computing centers are implemented in the form of computer clusters, such as Huawei Cloud Computing Centre, Baidu Cloud Computing Centre, Ali Cloud Computing Centre, Google Data Centre, Amazon Cloud Computing Centre, etc.

2.2 High Performance Data Analysis

In recent years, high-resolution simulations, observations from advanced instruments, and a vast network of sensors and edge components have provided a vast and constant fow of data—driving a variety of new scientifc discoveries and technological advances. In such an evolving environment, where the scale, velocity, and complexity of data continues to expand, scientifc advances rely on the practical ability of analytical tools to efectively extract and process valuable information from the sea of data. For example, real-time, high-frequency stock trading, as well as highly complex analytical problems found in scientifc research, all require the dual needs of both high-volume and high-speed processing. Therefore, there is an urgent need to develop new methods and strategies to deal with such extreme scale data-centric scenarios.

HPDA has been created to solve the above problems. High-performance data analytics combines supercomputing with data analysis. It uses the parallel processing of supercomputers to run powerful analysis software at speeds in excess of a trillion foating point operations, which allows large datasets to be examined quickly, and valuable information to be found.

The development of high-performance data analytics has been attributed to technological advances in supercomputing, but also to the development of data management techniques and tools designed to address the challenges of big data applications. To leverage high-performance data analytics for scientifc development and commercial applications, both supercomputing and data-intensive analytics research are considered to be two of the most fundamental and complementary aspects of high-performance data analytics (Usman et al. [2020\)](#page-19-2).

3 Support of Mainstream Supercomputer Systems for Data Intensive Applications

3.1 Fugaku

The Japanese ARM-based Fugaku (Shimizu [2020](#page-18-7); Kodama et al. [2020](#page-18-8)) supercomputers are recognized as the most powerful machines in the world. As of June 2020, the system held the 1st rank in the TOP 500 list, which lists the world's 500 most powerful supercomputers. Fugaku emphasizes multiple capabilities, such as computation and storage, and also performs well on other benchmarks—such as Graph500 (Nakao et al. [2020](#page-18-9)) and HPL-AI (Kudo et al. [2020\)](#page-18-10).

Fugaku uses a A64FX CPU based on the Armv8.2- A architecture (Odajima et al. [2020;](#page-18-11) Zhang et al. [2020](#page-19-3)). TheA64FX is a general-purpose processor with a high computational performance and memory bandwidth. Figure [2](#page-4-0) illustrates the architecture of A64FX with 48 compute cores, and two or four auxiliary cores. The auxiliary core handle interrupts from the operating system and communications. The users can select either 2.0 or 2.2 GHz as the CPU frequency for a given job. The A64FX has four core memory groups (CMGs) consisting of 12+1 cores and HBM2 (8 GiB, 256 GB/s). Because the four CMGs are connected by a network-on-chip (NOC), the number of processes per node should be a divisor of four for general applications.

Fugaku uses Tofu Interconnect(Schmidt and Hildebrandt [2017](#page-18-3)) for network interconnection, whose topology is a sixdimensional grid/circle. The locations of the nodes can be specifed using the XYZ abc axes. The ac axes are grid topologies consisting of only two nodes, and the other axes are circular topologies. The dimensions of the abc axes are fixed at $(a, b, c) = (2, 3, 2)$, and the dimensions of the XYZ axes depend on the system. Because $(X, Y, Z) = (24, 23, 24)$ for Fugaku, the total number of nodes is 158, 976. Fig. [3](#page-4-1) also

Fig. 2 The Architecture of Fugaku

Fig. 3 The Architecture of A64FX

shows that the XY Zb axes use two ports, and the ac axes use one port. A64FX has six tofu network interfaces (TNIs) and is capable of communicating at 6.8 GB/s in six directions simultaneously.

Fugaku's storage system is divided into three tiers, as shown in Fig. [4](#page-4-2). The frst tier serves as a dedicated highperformance area for job execution. The second tier provides a large-capacity shared area for users and jobs. The third tier provides a commercial cloud storage.

The main problems of Fugaku are insufficient multi-protocol support, poor metadata performance, and the inability to support million-scale fle operations simultaneously. Moreover, it cannot support multiple computing power.

3.2 Summit

The Summit (Kahle et al. [2019](#page-17-4); Wang et al. [2020;](#page-19-4) Luo et al. [2020](#page-18-12); Hernández et al. [2020](#page-17-5); Womble et al. [2019](#page-19-5)) consists of 256 racks with 18 IBM Power System AC922 computing nodes. Figure [5](#page-5-0) shows a node architecture consisting of two IBM POWER9 CPUs and six NVIDIA V100 GPUs connected using the NVLink 2.0. The CPUs and GPUs are

Fig. 4 The Storage Architecture of Fugaku

Fig. 5 The Computing Node Architecture of Summit

connected to over 5 TB of addressable memory through a high-bandwidth NVLink. The Merano enhanced data rate (EDR) and infnite bandwidth (IB) link the nodes in a nonblocking fat tree topology. The intra-node CPU bandwidth is up to 64 GB/s and the GPU bandwidth is up to 50 GB/s. Each node has 1.6 TB of local high-speed temporary storage (NVMe) with 6.0 GB/s read bandwidth, and 2.1 GB/s write bandwidth. A large, high-performance, global temporary fle system is provided as a fully centric GPFS.

Summit uses the IBM Spectrum Scale platform to provide storage capacity. Spectrum Scale offers excellent scalability, fash based acceleration performance, and automatic policy-based storage tiering (from fash, disk to tape). The Spectrum Scale provides a single fle namespace for all data, Swift/S3 object services, and interfaces to storage services such as Openstack and Big Data. It is a high-performance, highly scalable storage software for file, object, block, and big data analytics, with the ability to store, run, and access data anywhere. Spectrum Scale has added Hadoop big data and objects to GPFS, and enhanced storage capabilities with rich features such as cache acceleration, lifecycle management, unifed namespace, and multi-site. It also supports features such as encryption, hardware, and software decoupled deployment—and provides rich access interfaces such as fle interfaces (POSIX, NFS, CIFS),

3.3 Sunway TaihuLight

The Sunway (Gao et al. [2021;](#page-17-3) Fu et al. [2016\)](#page-17-6) is a converged architecture. One part is oriented toward highspeed computing systems for traditional supercomputing, and this part favors computational power. The other part is oriented toward auxiliary computing systems for emerging

applications such as big data, and this part takes storage into account. However, in general, Sunway still focuses on computing power, and the rest of it exists only as an auxiliary computing system. The system architecture is shown in Fig. [6.](#page-5-1)

The high-speed computing part of the system is composed of the SW26010 processor with an autonomous Sunway instruction set, which uses a heterogeneous multicore architecture, a cluster of on-chip computing arrays, and a combination of parallel shared storage. The architecture diagram is shown in Fig. [7.](#page-5-2)

Sunway's storage system (Chen et al. [2020](#page-17-7)) consists of online storage system and near-line storage system. The online storage system consists of storage service nodes with SSDs, high-performance dual-controller fiber serial SCSI disk arrays, and metadata service nodes, which are responsible for providing users with high-speed and reliable online data storage access services with an I/O aggregation bandwidth of 220 GB/s. The near-line storage system consists of metadata service nodes, storage service nodes, and high-capacity fber storage area networks, which are responsible for providing cloud-oriented and user-service storage services, as shown in Fig. [8.](#page-6-0)

The storage scale and bandwidth provided by Sunway is much smaller than other systems such as Summit and Fugaku. It only supports POSIX interfaces and does not optimize the storage performance for multiple computing power scenarios. Therefore, it is difficult to adapt to dataintensive application requirements.

Fig. 6 Architecture of Sunway

Fig. 7 Architecture of SW26010

Fig. 8 Storage Architecture of Sunway

Fig. 9 The Node Architecture of Tianhe-2A

3.4 Tianhe‑2A

The Tianhe-2A (Lu et al. [2020](#page-18-13)) consists of two parts: a computational processing module (CPM) and an accelerated processor unit (APU) module. The CPM module has four CPUs, and the APU is equipped with four Matrix-2000 accelerators. The logical architecture of Tianhe-2A is shown in Fig. [9.](#page-6-1)

The two CPUs are connected by an Intel QPI, each with four memory channels and eight memory slots. They are connected to dedicated NICs and Matrix-2000 accelerators with eight memory channels each by x16 PCI Express 3.0. In a compute node, the CPU is equipped with 64 Gb DDR3 memory, and the accelerator is equipped with 128 Gb DDR4 memory.

Matrix-2000 (Fig. [10](#page-6-2)) is configured with four super nodes (SNs) that are connected by a scalable on-chip

Fig. 10 The Architecture of Matrix-2000 accelerator

Fig. 11 On-chip network topology

communication network. Each SN has 32 compute cores, meets cache coherence requirements, supports eight DDR4- 2400 channels, and integrates x16 PCI Express 3.0 endpoint ports. The compute core is an 8 to 12 level reduced instruction set computer (RISC) pipeline that extends the 256-bit vector instruction set architecture (ISA).

The network-on-chip (NoC) topology within an SN is a 4 x 2 grid (Fig. [11\)](#page-6-3). The NoC has a total of eight routers, each connected to a cluster consisting of four compute cores, and a direct control unit (DCU). Each SN has three FIT link ports, and each port is connected to other SNs. In Matrix-2000, four SNs were connected by a point-to-point interconnect network.

Tianhe-2A has improved bandwidth and multi-protocol interface support to better face data-intensive application requirements. However, there are still shortcomings in the optimization of multiple computing power and metadata performance under massive fle operations.

3.5 Perlmutter

Perlmutter is a next-generation supercomputer deployed by the National Energy Research Scientifc Computing Center at Lawrence Berkeley National Laboratory. The compute nodes use AMD EPYC 7763 processors and NVDIA A100 SXM4 GPUs, with a total of 706,304 processor cores. The storage system uses the Lustre fle system with 35 petabytes of available capacity and an all-NVMe connection, which improves read and write bandwidth by up to 15 times and 9 times respectively compared with the previous generation Cori system.

In its frst participation in the TOP500 ranking in June 2021, Perlmutter reached the top 10 of both the TOP500, GREEN500 and HPCG rankings, achieving nearly 15% of its peak processing power using no more than 10% of the processor cores of the Fugaku supercomputer.

Perlmutter features extremely high bandwidth and IOPS due to its 35PB all-NVMe fle system based on HPE Cray E1000(as shown in Fig.[12\)](#page-7-0), which supports Lustre, Grid-RAID, Idiskfs, Slingshot and LNet multi-track transport. Meanwhile, Perlmutter uses the most advanced NVDIA A100 GPU available as an accelerator with a custom tensor processing unit to shine in data-intensive tasks such as AI and HPDA.

3.6 Exascale supercomputers

Frontier is a new generation of exascale supercomputers with peak FLOPS exceeding 1.5 EFLOPS, which was developed by Cray and AMD with the permission of the U.S. Department of Energy. The storage system of the frontier is similar to Summit, still using Luster as the parallel fle system, but has 2-4 times the storage performance and capacity than Summit.

The current Chinese prototypes of exascale supercomputers mainly include the Sunway exascale prototype, the Dawning exascale, and the Tianhe-3 exascale prototype. All three completed their development and deployment in 2018.

Among them, the Tianhe-3 exascale prototype was ranked frst in the latest GRAPH500 SSSP ranking in June 2021, demonstrating its strong ability to support data-intensive applications.

Similar to the Tianhe-3 exascale prototype, the main changes in the Sunway exascale prototype and the Dawning exascale supercomputers are the increase in computing power, with little change in the storage architecture.

4 Representative Parallel File Systems

Parallel fle systems are widely used for supercomputing storage. It is able to support multiple simultaneous accesses by multiple threads, mapping multiple fles and directories to many devices at the same time. We present the current development of parallel fle systems using the representative BeeGFS(Abramson et al. [2020](#page-16-0)), CephFS(Zhang et al. [2019b](#page-19-6)), IBM Spectrum Scale(Iannone et al. [2019](#page-17-8)), and Lustre(Braam and Zahir [2001](#page-17-9)) parallel fle systems, which are widely used in supercomputing, as examples.

4.1 BeeGFS

BeeGFS is a parallel cluster fle system with POSIX interface designed for all performance-oriented application scenarios, including HPC, AI and deep learning, life sciences, and oil.

As shown in Fig. [13](#page-7-1), the BeeGFS architecture consists of metadata servers, storage servers, and management servers. BeeGFS implements the separation of object data and metadata. Object data is the data that the users want to store and

Fig. 12 Architecture of E1000 **Fig. 13** Architecture of BeeGFS

is maintained by the storage servers. Metadata is the data about data, including access permission, fle size and storage location, managed by metadata servers. The most important part of metadata is how to fnd the specifc fle from multiple fle servers. In this way, the clients can directly talk to the storage servers to retrieve information after obtaining the metadata of the specifc fle or directory. Compared to Lustre, BeeGFS is easier to use and requires less maintenance, and the number of storage and metadata servers can be fexible and scalable.

4.2 CephFS

The Ceph File System (CephFS) is a POSIX-compliant distributed fle system that provides access to fles on the Ceph storage cluster and uses the Ceph storage cluster to store user data.CephFS stores data and metadata separately, providing excellent performance, reliability and scalability for upperlevel applications.

As shown in Fig. [14,](#page-8-0) Ceph's system architecture is divided into three parts: Clients client, Metadata server cluster (MDS) and RADOS distributed object storage. MDS caches and synchronizes distributed metadata, which allows clients to securely store metadata about the fle system and maintain consistency. RADOS stores data and metadata as object, performing other critical functions. CephFS is built on RADOS and inherits the fault tolerance and scalability of RADOS, supporting redundant copies and high data reliability. Clients query and update the corresponding metadata of MDS through RPC, and then get the fle data directly from RADOS based on object information. Compared with

Lustre, which is suitable for reading and writing large fles, CephFS is more suitable for unifed small fle storage.

4.3 IBM Spectrum Scale

Spectrum Scale is a distributed, shared, parallel cluster fle system developed by IBM. It ensures that all nodes within a resource group can access the entire fle system in parallel. The fle system operation provided by Spectrum Scale supports both parallel and serial applications, allowing parallel applications on any node to access the same fle or diferent fles at the same time, and providing a unifed naming interface.

As shown in Fig. [15](#page-8-1), the Spectrum Scale architecture is divided into three layers: storage layer, server layer, and client layer. The storage layer divides storage to server nodes, which confgure and format these storage disks to form the Spectrum Scale parallel fle system format. Finally, the server node communicates with the client nodes through Ethernet. NSD is a storage device visible to Spectrum Scale cluster for creating fle systems, and NSD server is capable of providing I/O servers for specifc NSD access. Spectrum Scale can span across all hosts within a cluster, providing better system performance and data consistency, and is a highly available fle system.

4.4 Lustre

Lustre is a cluster-oriented storage system, which is an open source, global single namespace, POSIX-compliant distributed parallel fle system. Lustre is highly scalable and highperformance, supporting tens of thousands of client systems, petabytes of storage capacity, and hundreds of GB of I/O throughput.

Fig. 14 Architecture of CephFS **Fig. 15** Architecture of IBM Spectrum Scale

Fig. 16 Architecture of Lustre

As shown in Fig. [16,](#page-9-0) the Lustre fle system architecture is a distributed, object-based scalable storage platform built on a computer network. The architecture consists of the following components: The Metadata Server (MDS) and the Object Storage Server (OSS), which provide namespace operations and I/O services with large amounts of data respectively. The Management Server (MGS) is a global registry of confguration information and is functionally independent of any one Lustre instance. The Client provides an interface for Lustre interaction between the application and the Lustre service. The software on the client provides a consistent POSIX interface for the end-user applications. The network protocol LNet provides a communication framework for binding services together.

5 Analysis of Typical Data‑Intensive Application Requirements

With the rapid development of IoT technology, the amount of data that needs to be processed by supercomputing systems has exploded. Benefting from the increasing capabilities of scientifc observation instruments, millions of sensors around the world are observing and recording cosmic, meteorological, biological, physical, and chemical processes in real time—and are generating large amounts of observational data. In addition, large amounts of computing equipment around the world are running various scientifc simulations that generate huge amounts of scientifc data. The U.S. Department of Energy (DOE) studies on scientifc data management indicate that "we are entering an informationled era. The ability to ride the information wave will be a marker that distinguishes whether it is the most suitable for science, business, and national security."

Data-intensive computing heralds not only the evolution of informatics, but also a revolution in the way researchers collect and process information. Data-intensive computing

has become a common research problem for industry and academia at a time when large amounts of data and associated applications are emerging for individuals, enterprises, and industries. Researchers are driving the focus of dataintensive computing by focusing only on large datasets for broader problem domains (Lin et al. [2017;](#page-18-14) Dong et al. [2018](#page-17-10)), and are more involved in all aspects of real life (Seal et al. [2020](#page-18-15); Hohman et al. [2019\)](#page-17-11). Typical data-intensive computing applications are summarized in the following subsections.

5.1 Scientifc and Engineering Applications

Scientific computing is superior in modern scientific research and engineering technology, where a large number of complex computation problems are encountered. Scientifc computing is the process of applying computers to mathematical computation problems, which has gradually evolved into an important research area in the current data explosion era. It is undeniable that computing ability has become one of the most important symbols for measuring the level of national scientifc and technological development. Data-intensive applications have become a necessary tool for scientists to discover new problems in frontier research areas.

Traditional applications, such as earthquake prediction, petroleum exploration, climate and environment prediction, and astrophysics are often simulated by dividing them into grids. With the continuous breakthrough in data collection and modeling technology, the grid is becoming increasingly more refned, which puts higher requirements on the calculation of numerical resolution and the processing ability of huge data in the process of scientifc research. The scale of artifcial intelligence and big data applications is so large that traditional supercomputing can no longer meet the demand for data storage and calculation (Table [1\)](#page-10-0).

In the climate and environment feld, several research areas are involved; such as meteorology, environmental science, and physical chemistry (Zhang et al. [2019a;](#page-19-7) Torbicki [2018](#page-19-8); Shuman et al. [2001;](#page-18-16) Anh Khoa et al. [2021;](#page-17-12) Guo et al. [2020](#page-17-13); Kappe et al. [2019](#page-17-14); Belair et al. [2019](#page-17-15); Massonnet et al. [2016;](#page-18-17) Kurth et al. [2018](#page-18-18)). Many meteorological satellites, weather sensors, and weather stations have been built in various countries to collect climate data, which has signifcantly increased the speed of collection, and also generated a large amount of information—leading to high storage capacity requirements. At the same time, the search time for useful data is inevitably prolonged, making it difficult to meet the standards for the access speed of weather data in storage systems. Berkeley National Laboratory's research, which applies machine learning methods to extreme weather events, is gaining international attention. In 2018, a team led by Prabhat proposed using AI to predict how extreme

Application name	Exascale deep learn- ing climate analysis	Practical evolutionary earth- Genomics-based quake urban problem solver	analysis of drug addiction	Quantum dynamics simulation
Storage Scale	3.5 TB	100TB to 1PB	10TB	TB
IOPS Requirement	1500 images per node	132665 degrees of freedom	2.36 billion	20 peta
AI Library support	Required	Required	Required	Required
Big Data Interface Support	Not required	Not required	Required	Not required
Storage Bandwidth Requirement	11.98 GB/s	10.2 GB/s	900 GB/s	975 GB/s
Data Reliability Requirement	High	High	High	High
Data Security Requirement	Medium	Medium	High	Medium
Real-time Requirement	High	Low	Low	Low
Fundamental Alogithms	$DeepLabv3+$	Artificial Neural Networks	Markov Clustering	Lattice Gauge Theory

Table 1 Analysis of typical scientifc application requirements(Lin et al. [2017;](#page-18-14) Dong et al. [2018;](#page-17-10) Seal et al. [2020;](#page-18-15) Hohman et al. [2019](#page-17-11))

weather will change in the future; that is, using variants of Tiramisu and DeepLabv3+ neural networks with supercomputers to identify extreme weather patterns from highresolution climate simulations. Based on 3.5 TB of weather data, by making large-scale use of the dedicated tensor kernel built into Summit's NVIDIA GPUs, the team achieved a peak performance of 1.13 Exa FLOPS. It is the fastest deep-learning algorithm reported to date (Kurth et al. [2018](#page-18-18)).

In the feld of earthquake prediction, seismic data are stored in large blocks of data and are developing toward refnement (Allen [2012](#page-17-16); Minson et al. [2018;](#page-18-19) Mapar et al. [2012;](#page-18-20) Luo and Paal [2021](#page-18-21); Yu et al. [2021](#page-19-9); Amin and Ahn [2021;](#page-17-17) Hu et al. [2019](#page-17-18); Ichimura et al. [2018](#page-17-19)). Scientists are creating more accurate fnite element grids to represent the geological properties of the earth's crust, and to simulate the effects of geological disturbances and the probability of earthquakes. Tsumura Ichimura's team at the University of Tokyo proposed the application of Artifcial Neural Networks and hybrid precision algorithms to accelerate the simulation of seismic physics in urban environments. A peak performance of 14.7% was achieved using 4096 nodes in Summit, and coupled ground and urban structure vibrations during large earthquakes into a unifed simulation—showing a high scalability. As historical seismic data and newly observed data continue to increase and new models are being expanded, traditional supercomputing is facing new challenges. By integrating analytical methods of data-intensive computing and large-scale storage systems, we can dig deeper into historical data and explore new research methods—so as to accurately predict the locations of possible geological disasters, respond in time, and reduce economic losses.

In the feld of life science, with the expansion of biological data, researchers need a unifed platform for data storage and processing. Existing systems such as Dryad can provide efficient data retrieval, but there is no efficient data management service (Mazzucco et al. [2017;](#page-18-22) Chien et al. [2015](#page-17-20); Kumar et al. [2008](#page-18-23); Lv et al. [2020;](#page-18-24) Sejdic et al. [2018](#page-18-25); Joubert et al. [2018\)](#page-17-21). With the help of supercomputers, storage systems can be developed for the integration and analysis of complex data. Many classical bioinformatics algorithms and processes can be targeted and optimized in combination with machine learning and other techniques. In 2018, the ORNL team developed a new "CoMet" algorithm (Joubert et al. [2018\)](#page-17-21). They use Custom Correlation Coefficient network topologies to make dense vector similarity calculation and use Markov clustering and principal component analysis of the Laplacian transform of a network adjacency matrix to create SNP set. This algorithm allowed supercomputers to process large amounts of genetic data—four to five times larger than state-of-the-art technology—and to identify genes and efective treatments for greater vulnerability to pain and opioid addiction, achieving a peak throughput of 2.36 Exa FLOPS. In addition to processing genetic information, CoMet is currently being used in bioenergy and clinical genomics. Furthermore, the team designed an artifcial intelligence system based on this algorithm to utilize 25,200 of Summits NVIDIA Volta GPUs. This algorithm automatically designs an optimal deep learning network to extract structural information from raw atomic resolution microscopy data, enabling a major step forward in the feld of material and genetic research. These applications are scalable and, to some extent, pose new challenges to the reliability and security of supercomputers.

In the feld of quantum physics, accurate testing of the Standard Model in the nuclear environment requires a quantitative understanding of quantum chromodynamics, to which machine learning methods are being extensively applied (Hush [2017](#page-17-22); Musser [2020;](#page-18-26) Gao et al. [2018,](#page-17-23) [2020](#page-17-24); Li et al. [2019;](#page-18-27) Liu et al. [2019;](#page-18-28) Zhang et al. [2016;](#page-19-10) Duan et al. [2018](#page-17-25); Chang et al. [2018](#page-17-26)). The importance of genetic algorithms makes them a benchmark for theoretical determination. The LLNL and LBNL teams used the Department of Energy's newest supercomputers—LLNL's Sierra and Oak Ridge's Summit—to develop an improved algorithm that could determine neutron lifetimes more accurately. They use Lattice Gauge Theory and calculate nuclear observables such as neutron lifetime and develop an improved algorithm (utilize GPU Direct RDMA for inter-node transfers, Communication Autotuning, Management and Backflling of Tasks) that expoentially decreases the time-to-solution. The team was able to achieve a sustained performance of approximately 20 peta FLOPS, which accounts for approximately 15% of Sierra's peak performance. Researchers say that just one percent more accuracy is what is driving physics forward. As a result, the high demand for arithmetic power and data storage management is driving the development of supercomputers to be more data-intensive.

In the field of oil exploration, the increasing difficulty and cost of extraction has become a serious challenge, and we have to seek innovations and breakthroughs in directional and geosteering drilling, deepwater drilling, enhanced recovery and the booming development of unconventional oil and gas. Seismic exploration is fundamental to oil and gas exploration. Shipilova et al. [\(2020](#page-18-29)) proposed the Matrioshka OMP algorithm, which uses homogenous seismic acquisition to construct subsurface images without actually penetrating the earth's crust. In addition, optical remote sensing has become a key discipline for analysing large-scale exploration areas. Gianinetto et al. ([2016\)](#page-17-27) proposed that remote sensing can be used to detect direct (hydrocarbon) and indirect (variable) seepage signals, and that the large microseepage signals identifed are considered spectral anomalies that can verify the presence of hydrocarbon accumulation and sedimentation centres. Due to the high mixture and complexity of ground source data, how to efficiently analyse noise effects and accurately image them, and how to maximise the efectiveness of real-time monitoring data, requires data-intensive supercomputing to provide efficient arithmetic power and stability support.

5.2 Commercial Applications

High-performance computing applications are gradually being widely used in the commercial field. HPDA is a typical data-intensive application, which mainly involves smart cities, fnancial analysis, intelligent transportation, and many other felds. These applications require the collection and maintenance of massive datasets and intensive data analysis (Table [2](#page-11-0)). As the technology for collecting and storing data becomes more widespread and economical, an increasing number of data-intensive computing problems are emerging, and the need for HPDA applications is emerging in various industries.

In the feld of credit card fraud detection (Anbuvizhi and Balakumar [2016;](#page-17-28) Dai et al. [2016](#page-17-29); Li et al. [2020;](#page-18-30) Koppers et al. [2017\)](#page-18-31), credit card fraud seriously afects normal transactions. Therefore, it is necessary to use credit card fraud detection technology to detect fraudulent transactions and guarantee the safety of the credit card business. With the development of e-commerce, credit card transactions have been growing explosively, showing the characteristics of massive volume and high frequency. Its real-time transaction records are up to millions per second, and the size of historical transaction data stored in the system reaches the PB or even the EB level. The massive data storage requirements and high-frequency real-time detection bring unprecedented performance challenges to credit card fraud detection. For unbalanced and asymmetric credit data, the use of HPDA technology can provide a more objective and fair judgment and prediction ability than human subjective judgment, greatly reducing the occurrence of fraud and breach of contract in transaction activities. Forough and Momtazi [\(2021](#page-17-30)) use an ensemble model based on sequential modeling of data using deep recurrent neural networks and a novel voting mechanism based on artifcial neural network to detect fraudulent actions.

In the feld of animation production (Hong et al. [2016](#page-17-31); Zhang et al. [2017\)](#page-19-11), animation rendering is the core part of animation production, which is a typical HPDA application that transforms animation design into specifc images mainly through the combination of model, light, material, shadow, and other elements. The core idea is to adopt and fne-tune multiple algorithms based on machine learning approaches, such as Random Forest (RF), Gradient Boosting Tree (GBT), Support V ector Machine (SVM), Deep Learning(DL). The rendering data generated by the 3D production software have high data-intensive characteristics. A highly fault-tolerant distributed cloud fle system is typically used to achieve efficient storage and retrieval operations for large image frame data. It uses a cluster of thousands of machines to manage data and supports large datasets of GB to TB level for a single file—thus achieving efficient storage operations for large image frame data and producing richer and more realistic animation. The cloud fle system contains name nodes that can manage the metadata of the entire fle system, and data nodes that can store the actual application data fles, thus providing a large data throughput.

In the feld of smart city construction (Wang et al. [2021](#page-19-12); de Assuncao et al. [2018](#page-17-32); Mehmood et al. [2017;](#page-18-32) Tang et al. [2017](#page-19-13)), the data center integrated with IoT, data communication, and other technologies is the core of building smart cities, involving petabyte data volumes, while requiring maximum security of data and business. Methods of machine learning are widely adopted among this group of studies, such as support vector machines (SVMs), C-means fuzzy clustering, K-means clustering, hierarchical clustering, ant colony algorithm, etc. The storage and analysis of massive data information can efectively promote the construction of smart cities. Through HPDA technology, valuable information can be mined from massive data, which in turn can provide help and guidance for smart city construction and solve the existing problems. By building a big data center to strengthen data processing and interaction efects, it can promote the sharing of resources among diferent smart platforms and improve the city management efect. For building traffic models, predicting disease outbreaks, and reasonably allocating education resources, etc.; massive information can be integrated together, and can be fully refned and mined by HPDA to help build a smart city.

In the field of internet applications(Jun et al. [2018](#page-17-33); Sapountzi and Psannis [2018](#page-18-33); Stavrinides and Karatza [2017](#page-18-34)), the data handled by the internet are characterized by being massive, fast updating, diverse, and heterogenous. As the largest database in the world, it provides various types of data and contains diverse applications. The existing mainstream application models of the internet are web applications and SaaS applications; which provide services including e-government, e-commerce, web information retrieval applications (search engines, knowledge sharing, internet news, etc.), web communication and interaction applications (instant messages, social networks, etc.), and web entertainment applications (videos, online games, etc.). They always use map-reduced based distributed parallel data mining algorithms. With the increase in data volume and the change in data characteristics, traditional data storage and indexing technologies can no longer handle these massive, dynamic, and distributed data. Therefore, HPDA becomes a key technology to drive the further development of Internet applications.

5.3 Summary

Data-intensive supercomputing opens up new possibilities for both scientifc and commercial applications. With the development of information technology, the cost of hardware has gradually decreased. At the same time, with the increase in data volume, the applications put higher demands on the storage capacity, arithmetic power, and network bandwidth of supercomputing. More efficient data exchange between servers, higher reliability, and security requirements are required. Storage and computing uncoupling technology use distributed storage and supercomputing, fexible confguration of computing and storage units in the hardware architecture to meet the expansion needs, and has a wide prospect of use—becoming the key to solving the aforementioned problems.

6 Trends in Data‑Intensive Supercomputing

Data-intensive supercomputing uses multiple computing power and EB-level storage to enable the processing of massive amounts of data. Data-intensive supercomputers are mainly oriented toward data-intensive tasks, aiming to solve data I/O, load balancing, data fow, data reliability, and other data storage-related problems during the execution of data-intensive tasks, with the goal of maximizing user productivity (rather than maximizing computational resource utilization). We argue that the data-intensive supercomputers should have the following characteristics.

6.1 Multiple Computing Power

According to a report by the IDC, the AI server market in China alone will be \$3.196 billion by 2020, which refects the importance of computing power for all industries. And with nearly 20 ZB of new data currently being generated each year, the demand for AI computing power doubles every 3.5 months. Similarly, OpenAI published research shows that the demand for computing power has increased by an average of 10 times per year between 2012 and 2018, respectively. In the future, with the continuous development of 5G and big data technology, the volume of data will explode, and the application scenarios will become more diverse—with the scale of data rising to tens of millions. This development trend will make it difficult for traditional CPU-based computing power to meet development requirements. In addition, the linear development model of a traditional supercomputer is difficult to adapt to new environments. Computing architecture and computing power will be diversifed in the future.

To achieve the convergence of multiple computing powers, it is necessary to break away from traditional architecture design, and solve the compatibility and efficiency problems caused by multiple computing devices with different architectures. It should be able to achieve efficient aggregation and on-demand defnition of multiple computing powers, implement storage and computing decoupling technology to improve fexibility, realize the combination of computing and data to break the upper limit of storage, and to achieve scalability through open and interconnected network technology. Supercomputers should adopt an open network chip with an open architecture so that it can support current mainstream network chips. It should have an open network operating system to improve the fexibility of computing power and to meet the needs of cloud computing in terms of interfaces, software defnitions, and rapid iterations of the network.

6.2 EB Level Storage

Traditional supercomputers treat increasing computing power as their fundamental goal. From 1976 to the present, the computing power of the most powerful supercomputer has increased by a factor of 4.21 billion. However, the storage capability has received insufficient attention and has become a new bottleneck. Therefore, HPC should focus on storage capability. Big data, AI, and other technologies are gradually merging with HPC, allowing valuable data to be exploited. The data being analyzed is gradually changing from cold data, to a large amount of dynamic and constantly changing hot data. IDC predicts that by 2021, the global market for HPC storage will reach \$14.8 billion. The emerging HPDA and HPC-based AI scenarios are predicted to grow rapidly at an annualized rate of 17% and 29.5%.

As data volumes continue to explode, the industry is evolving from compute-intensive HPC, to data-intensive HPDA. HPC faces changes in three areas. First, the expansion of massive data volumes requires storage to evolve to the EB level as soon as possible, and application scenarios are becoming more complex. To meet these challenges, dataintensive supercomputers should adopt more efficient highdensity technologies, and provide total cost of ownership (TCO) optimization. The main challenge for data storage is maintaining more valuable data per byte; and high-density

technology will be the best solution. In addition, dataintensive supercomputers will have to focus on optimizing software algorithms—such as compression algorithms—to improve the efficiency of storage. Workloads will become more diverse, and move towards mixed loads. For example, in oil and gas exploration, seismic data requires a higher bandwidth for processing and a higher IOPS for interpretation, each with a diferent load, which requires a storage design that can support multiple loads simultaneously. Dataintensive supercomputers should also support more data types to accelerate the transfer of data.

In order to cope with the increasing demand for storage capacity, data-intensive supercomputer architects need to design efficient storage management methods—in particular, when the number of objects to be managed is as high as ten billion or even a trillion. When the data size of the mass storage reaches the EB level, the cluster fle system needs to manage more than ten billion objects. In particular, when the data are stored in small fles, the metadata to be managed can reach trillions in size.

6.3 Enhanced Bandwidth and I/O Performance

High bandwidth, low latency, and high IOPS are important performance indicators for traditional supercomputers, and are also important for data-intensive supercomputers. Bandwidth, latency, and IOPS are key concerns for supercomputers. A higher bandwidth means that the computer can have more processing power and can perform more jobs. Latency is also an important indicator of computer performance. The lower the latency, the better the overall performance.

For data-intensive computing architectures, meeting the bandwidth requirements between computing and storage systems has become a major challenge in handling large amounts of data. In traditional supercomputers, data must pass through a relatively long I/O path from memory to the computing devices. To complete a portion of the computational process, a series of read and write operations are required. When the volume of data exceeds one petabyte, the overhead spent on the storage system will be very high, and it is difficult to meet the needs of large-scale data processing. Therefore, traditional computers tend to use coupled computing and storage architectures. This architecture can efectively shorten the read/write path between computation and storage, reduce system overhead, provide fast data access, and achieve a storage balance between computation and read/write operations. In a storage-computing-coupled architecture, data are distributed on each node responsible for computation and storage, which signifcantly reduces the system overhead of data movement. However, while this architecture optimizes the overhead between the computing and storage systems, it reduces the overall fexibility of the system, and makes it difficult to solve the problems of complex data types. Moreover, this architecture is not suitable for all applications, and many aspects need to be optimized and redesigned.

In the case of data-intensive computing, customers and data centers usually interoperate with each other over the internet. Their communication protocol is usually HTTP based. Owing to network bandwidth and stability limitations, and the fact that the requested service usually requires a large amount of data analysis, data-intensive supercomputing should try to achieve a high-speed response and incremental processing of data to meet application requirements.

Data-intensive supercomputing is still cluster-based, and data analysis is very important, so cluster-based supercomputers need to consider how to solve the transmission bottleneck problem. The transfer of large amounts of data between diferent processors and between diferent nodes, as well as the detection and processing of errors in parallel processing by the computing system, are issues that require a more robust I/O performance to address. To support efficient data-intensive computing, supercomputers will continue to require more intensive research in hardware, software systems, and algorithms to improve their IOPS.

How to meet the diferent demands for I/O performance for various scientifc computing applications and big data applications should also be one of the issues to be considered in the development of data-intensive supercomputers. In some scientifc research, the data processing and computation modes are very demanding in terms of computational performance—modes such as WRF, ARP, and others. The amount of communication between individual. The CPUs when running these software packages are also very high. In addition, because of the large number of users involved and the large number of fle read and write operations, this places high demands on the IOPS of the supercomputer.

6.4 Efcient Data Analysis and Management Capabilities

Collecting and maintaining data should be the capability provided by the platform rather than the user. Data-intensive supercomputing should provide efficient data analysis and management capabilities for data-intensive tasks with large data volumes, and high demands on data processing.

Moreover, data-intensive supercomputers must provide efficient data processing and analysis capabilities. The data processing methods for big data are complex, with the most frequently used data processing methods being graph and batch processing—therefore, good cooperation between diferent fles and systems must be obtained. This shows that for data-intensive supercomputers, a single-fle system does not meet the requirements for supercomputer operation. Therefore, multiple fle systems should be combined for supercomputer design.

On the other hand, a data-intensive supercomputer should provide as rich, comprehensive, and adaptable data management capabilities as possible—such as supporting users to query or update the programs and data they need to use according to their own authority and content; providing functions such as adding, deleting, updating, and checking based on database software; organizing, visualizing, and analyzing the data processed by users in a simple way. Further, the data management functions should be optimized using indexing, striping, and intelligent aggregation technologies. This enables the transfer of complex and additional data processing management and analysis operations to be performed outside the platform by users of traditional supercomputing platforms. And this is to be completed within the platform.

6.5 More Convenient Programming Models and Heterogeneous Programming

Traditional supercomputers tend to use low-level programming primitives to maximize the usage of system resources—often requiring the use of assembly or C languages to manipulate the underlying build the components of supercomputers such as processor cores, memory, and registers. Achieving high performance requires time-consuming and tedious optimization, and requires the user to have very high programming skills and a good understanding of the underlying supercomputing platform.

In the current era of big data, the programming model should meet the basic principles of simplicity and ease of use. In addition, it should improve performance as much as possible to ensure that the programmer's work is faster and smoother. In this process, the programmer does not need to be concerned with the state or condition of the program, but only solve problems that arise.

Big data applications also pose new challenges for programming models. These challenges arise mainly from conficts between programming simplicity and performance. For the sake of programming simplicity, the programming model should provide developers with a clean programming method that allows them to easily write programs that can execute in parallel on a cluster, where they only need to focus on the problem to be solved and not on the specifcs of how the program will run on a large cluster. This can include how the data is distributed, and how it is scheduled. The programming model should minimize the programming burden on developers by hiding specifc details from them, making the code easier to understand, reuse, and maintain. As for the performance optimization of the programming model, the programming model usually manages the resources of the entire cluster to ensure that the application can run efficiently on a large-scale cluster, but also enables multiple jobs to run concurrently and share resources to improve the resource utilization and job throughput of the entire cluster. Therefore, designing a simple, efficient, and reliable parallel programming model for big data applications is a major challenge.

With the development of data-intensive applications represented by intelligent applications, existing intelligent algorithm implementations often use the latest high-level languages such as Python3, Java, Julia, R, and Go. They often hide the underlying device interfaces from users and provide rich APIs and simple programming environments to meet the diverse algorithmic needs of users. Therefore, data-intensive supercomputers need to support these latest high-level programming languages to efficiently map the functionality implemented in their programs to the machine. At the same time, high-level programming languages can support more complex data structures such as 'dictionaries', 'tensors', etc., which are useful in AI tasks, and support for complex data structures is a must for data-intensive supercomputing.

In addition, we must ensure that diferent types of jobs and tasks can run concurrently on the cluster, which can greatly increase the efective utilization of resources within the cluster. However, with this model, we also face many new problems, such as how to deal efectively with heterogeneity. To improve efficiency, supercomputers generally choose to apply a parallel programming model, which not only improves the efficiency of the workforce, but also contributes to the efective implementation of big data applications to a large extent.

The programming language widely used today for supercomputing is still the C/C++ language invented more than 40 years ago, and still mainly uses the CPU for computational tasks. In recent years, however, supercomputers have been using GPU-assisted computing, which can handle several similar computational tasks simultaneously. Dataintensive supercomputers should therefore also consider how heterogeneous programming can be implemented better.

Heterogeneous computing refers to the collaboration between CPUs and GPUs or other devices (e.g., FPGAs). For example, the computing power of CPUs, GPUs, and even APUs (fusion of CPUs and GPUs) can be combined to increase the computational speed of a system. The use of heterogeneous systems is becoming increasingly common.

Therefore, designing a programming framework for heterogeneous computing should become an important issue in the design of data-intensive supercomputers. Some current heterogeneous programming frameworks, such as CUDA, OpenCL, and OpenACC, are based on the C/C++ language and take more time to program; and it is also difficult for beginners to become profcient in using these programming frameworks in a short time. In addition, existing parallel programming methods make it more difficult to analyze and verify parallel programs, and the uncertainty caused by

the cross-execution of parallel tasks in the program greatly reduces the reliability and predictability of the system.

6.6 Intelligent Scheduling

Existing data-intensive applications use data in increasingly complex ways, including data queries, data updates, simple numerical computations (data pre-processing, normalization, etc.), and complex numerical computations (calculation of neural network weights and reinforcement learning exploration processes). However, data-intensive supercomputers should also provide interactive access. A large number of existing supercomputers still mainly use batch mode to run user-submitted tasks. Although this approach can ensure that the task can be completed even after the terminal is closed or the logging node is down, the user can only obtain the task processing result after the batch task is completed and cannot make corresponding adjustments according to the intermediate outputs of the computation task. Data-intensive supercomputers should also provide an interactive task submission mode, ensuring that users can interact with the program at any point in time by writing code, and should also provide inherent interactive task support—including simple data processing and data visualization.

6.7 High Reliability and High Availability Mechanisms

The availability of supercomputers is an instantaneous concept, defned as a property of a system that indicates that it is ready to be used immediately. In other words, a highly available system works in a timely manner at any given moment. On the other hand, the reliability of a platform is a continuous concept, meaning that the system can continue to operate without failure. For dataintensive supercomputers, both reliability and availability must be guaranteed. Current supercomputers provide reliability primarily by periodically checking the status of programs, and rolling back to the nearest checkpoint when a supercomputer encounters an error. This is an inefficient, nonscalable mechanism that is not suitable for interactive use. Instead, this paper argues that dataintensive supercomputers should employ an uninterrupted reliability mechanism in which all raw and intermediate data are stored in redundant form, and selective recomputation can be performed in the event of component or data failure. Furthermore, the machine should automatically diagnose and disable faulty components, and these should only be replaced if they accumulate enough to impair system performance. Supercomputers should be available all year round, and hardware and software replacements and upgrades should be performed on a running system. This more distributed and less monolithic architecture is more suitable for use in data-intensive applications than traditional supercomputers.

6.8 Data Privacy and Security Mechanisms

The security and privacy of data are involved in a wide range of applications, such as education, healthcare, and state security. To a certain extent, these data involve private information about individuals and society. If leaked, it could cause great damage to individuals and society. Traditional supercomputers provide technical means to address privacy protection in a closed application environment. With the development of internet technology, the disclosure of information on sensitive data sets poses a signifcant challenge to privacy security, and will also have an impact on data security in databases and outsourced servers. For this reason, data-intensive supercomputers should provide better data security mechanisms to ensure data privacy, data security, data completeness, and data availability. Most traditional data security technologies use passive defense methods, such as frewalls and encryption. However, with the emergence of new open environments, more active and passive techniques need to be designed to defend against data attacks by unscrupulous people. Dataintensive supercomputers should analyze huge amounts of data, use artifcial intelligence to detect data attacks, screen out spam, and provide a more secure and reliable network and data storage base.

6.9 Multi‑Protocol Access Interface

Data-intensive supercomputers should provide richer business capabilities, this however also results in a greater variety of data interfaces. Too many data interfaces result in signifcant conversion overhead and wasted resources. For example, Spectrum Scale provides a large number of interfaces, such as fle interfaces (POSIX, NFS, CIFS), object interfaces (S3, SWIFT), big data interfaces, Openstack interfaces, storage service interfaces, etc., to provide rich business access. Traditional supercomputers have complex structures in terms of fle management and network structure, and the interfaces are not uniform. For example, the front-end may adopt an FC interface, the storage may adopt an FC interface, and the computing network adopts an Infniband interface. The complex communication network and various interfaces significantly reduce the conversion efficiency of data, resulting in a large protocol conversion overhead, a serious rate mismatch, a signifcant reduction in fexibility, an inability to meet the diverse changes in business, and a bottleneck in data transmission and conversion.

6.10 Better Support for Artifcial Intelligence Applications

At present, in the felds of life sciences, smart city management, oil surveying, and disaster warning, the combination of artifcial intelligence techniques, machine learning, and other technologies with supercomputers, will make traditional industries take on a new luster. Therefore, data-intensive supercomputers need to be considered to provide better performance services for AI-related models and achieve a better combination of AI models and supercomputing.

AI applications will become increasingly integrated with supercomputers. Because the demands of AI far exceed the computing power that a single chip can provide, it must be supported using a distributed architecture with many accelerator chips working in tandem. The actual performance of distributed training is highly dependent on the efficiency of the underlying hardware used.

While traditional supercomputers have the computing power and environment to support AI training, the energy consumption and cost efectiveness are not satisfactory. Applying supercomputers to AI requires innovations in architecture and software optimization.

7 Conclusions and Prospects

As supercomputing technology continues to grow in popularity and become more commonplace, it is no longer limited to scientifc computing applications only, but is also being widely used in more civil felds. The scale of data that needs to be processed by supercomputers has exploded in both traditional scientifc computing and civil felds, such as smart cities. HPC applications are gradually changing from compute-intensive to data-intensive supercomputing. The traditional supercomputing architecture is mainly designed for compute-intensive applications and is inadequate in terms of I/O bandwidth, IOPS, and reliability—and cannot support data-intensive applications well. Therefore, the development of a new generation of secure and reliable data-intensive supercomputers with decoupled storage and computation, and the design of application-optimized unifed storage systems for massive data, are key to the sustainable development of data-intensive supercomputers in the future.

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