

# Growth of Single-crystalline Transition Metal Dichalcogenides Monolayers with Large-size

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**Abstract** Two-dimensional(2D) transition metal dichalcogenides(TMDCs) semiconductors, such as monolayers of molybdenum disulfide(MoS<sub>2</sub>) and tungsten disulfide(WS<sub>2</sub>) can potentially serve as ultrathin channel materials for building short channel field-effect transistors(FETs) to further extend Moore's Law. It is essential to develop controllable approaches for the synthesis of large single crystals of these 2D semiconductors to promote their practical applications in future electronics. In this short review, we summarized the recent advances on the chemical vapor deposition(CVD) of single crystalline semiconducting 2D TMDCs with a large size. We first discussed the driving force and urgent demands on developing controllable approaches for the growth of large 2D TMDCs single crystals and then summarized the current strategies and representative studies on the CVD growth of large 2D single crystals. Finally, we discussed the challenges and future directions in this topic.

**Keywords** Two dimensional; Single crystal; Chemical vapor deposition; Field effect transistor

## 1 Introduction

In recent years, the further scaling of silicon-based transistors confronts great challenges<sup>[1–3]</sup>. Although silicon based semiconductors still have a strong vitality<sup>[4,5]</sup>, it is extremely urgent to explore new semiconducting materials. In the past two decades, low-dimensional semiconducting materials, such as single-walled carbon nanotubes(SWNTs) and two-dimensional(2D) semiconductors, such as several transition metal dichalcogenides(TMDCs) have attracted extensive attentions, showing great potentials in the application of electronic devices in the future.

2D semiconductors, such as MoS<sub>2</sub> are highly ordered single crystals without danging bonds on the surface, showing a band gap between 0.2 eV and 2.1 eV<sup>[6,7]</sup> and more importantly, they are ultra thin semiconductors with a thickness of down to *ca.* 0.65 nm, and therefore, are expected to overcome the short-channel effect<sup>[8–14]</sup>, which limited the further scaling of silicon-based transistors. In addition, 2D TMDCs semiconductors have various chemical compositions and structures, thus showing abundant and unique properties<sup>[15–19]</sup>. The band structures and electrical characteristics of such materials can be modulated by engineering the number of layers<sup>[20]</sup>, phases<sup>[21]</sup>, defects<sup>[22]</sup>, and so on, and be allowed for the construction of electronic devices with these tunable 2D building blocks<sup>[23–26]</sup>. For example, MoS<sub>2</sub> in 1T' phase<sup>[21]</sup> shows metallic properties, which is totally different from its semiconducting counterparts

of 2H phase MoS<sub>2</sub>, and 2D PtSe<sub>2</sub> exhibits obviously layer-dependent electrical properties and catalytic activity<sup>[27]</sup>. However, for device applications, it is essential to achieve the controlled synthesis of such ultra thin 2D single crystals.

## 2 Potential Applications of TMDCs in Electronic Devices and Integrated Circuits

### 2.1 Application of 2D TMDCs in Field-effect Transistors

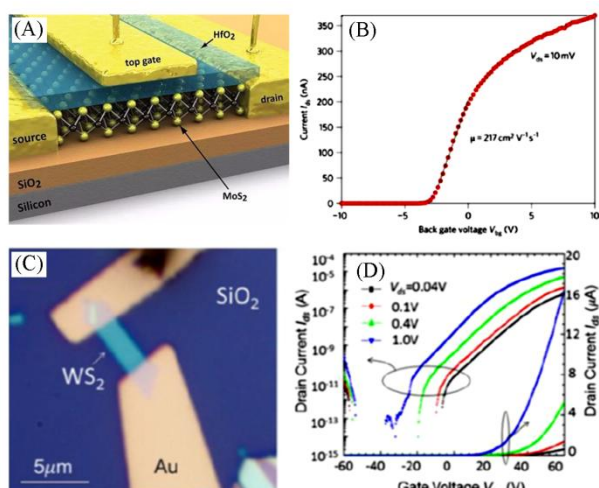
Semiconducting 2D TMDCs, such as MoS<sub>2</sub>, MoSe<sub>2</sub>, WS<sub>2</sub> and WSe<sub>2</sub>, owning wide band gaps, ultra-thin 2D planar structures, excellent electrical properties, and low dielectric constants, are ideal channel materials for constructing field-effect transistors(FETs)<sup>[28–31]</sup>. Compared with silicon-based materials, the thickness of 2D TMDCs can be reduced to sub-nanometer<sup>[26]</sup>, showing the potential to further extend Moore' Law. In 2011, Kis *et al.*<sup>[32]</sup> successfully fabricated top-gated FETs by using hafnium dioxide(HfO<sub>2</sub>) as the dielectrics layer and mechanically exfoliated single-layered MoS<sub>2</sub> as the channel material. The obtained FETs exhibited an on/off current ratio of *ca.* 10<sup>8</sup> and a subthreshold slope of *ca.* 70 mV/decade[Fig.1(A) and (B)]. Subsequently, Wei *et al.*<sup>[33]</sup> fabricated FETs with exfoliated WS<sub>2</sub> as channel material and the mobility at room temperature could reach up to 200 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>

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Received June 15, 2020; accepted July 8, 2020.

Supported by the National Natural Science Foundation of China(Nos.21875127, 21925504) and the Tsinghua University Initiative Scientific Research Program, China.

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**Fig.1** Schematics of a top-gated FET with single-layered MoS<sub>2</sub> as the channel material(A)<sup>[32]</sup>, typical transfer characteristic curve of the top-gated MoS<sub>2</sub> FET(B)<sup>[32]</sup>, optical image of an FET fabricated with exfoliated multilayered WS<sub>2</sub>(C)<sup>[33]</sup> and transfer characteristics curves of the FET shown in (C)(D)<sup>[33]</sup>

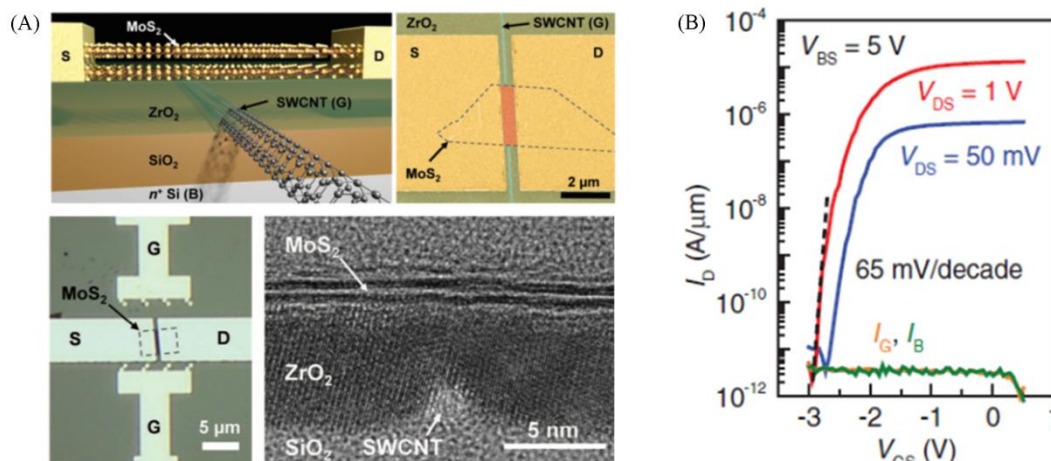
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[Fig.1(C) and (D)]. Chai *et al.*<sup>[34]</sup> fabricated FETs with the channel material of monolayer PtSe<sub>2</sub> exfoliated from bulk crystals grown by the chemical vapor transport(CVT) method,

showing an intrinsic high-mobility of *ca.* 210 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>. And they realized the carrier type control in WSe<sub>2</sub>-FETs by thickness modulation, obtained p-type, ambipolar and n-type with the thickness below 4 nm, *ca.* 6 nm and over 15 nm, respectively<sup>[35]</sup>. These works and more subsequent researches<sup>[36–41]</sup> opened up the intensive studies on the construction and optimization of 2D TMDCs based FETs in recent years and demonstrated the promise of 2D semiconductors in high performance field effect devices.

## 2.2 2D TMDCs for the Construction of Short-channel Devices

As the main driving force for the studies on 2D semiconductors is to further decrease the channel length of FETs, Javey *et al.*<sup>[42]</sup> demonstrated the fabrication of FETs with a gate length of only 1 nm by using a double-layered MoS<sub>2</sub> as the channel, a single-wall carbon nanotube with a diameter of 1 nm as the gate, and a 5-nm thick zirconia(ZrO<sub>2</sub>) as the dielectrics, as shown in Fig.2(A). The on/off current ratio of the as-prepared transistors reached *ca.* 10<sup>6</sup>, and the subthreshold slope reached 65 mV/decade[Fig.2(B)], which was close to the theoretical limit. These results confirmed that MoS<sub>2</sub> had a good immunity to short channel effect, which was consistent with the theoretical calculations<sup>[43,44]</sup>. Although the device prepared by this method is still at the prototype stage, it fully demonstrates the feasibility of using MoS<sub>2</sub> and other 2D TMDCs to construct electronic devices in nanoscale.



**Fig.2** FETs with MoS<sub>2</sub> as channel and single-wall carbon nanotubes as gate(A) and its  $I_D$ - $V_{GS}$  transfer characteristic curves(B)<sup>[42]</sup>

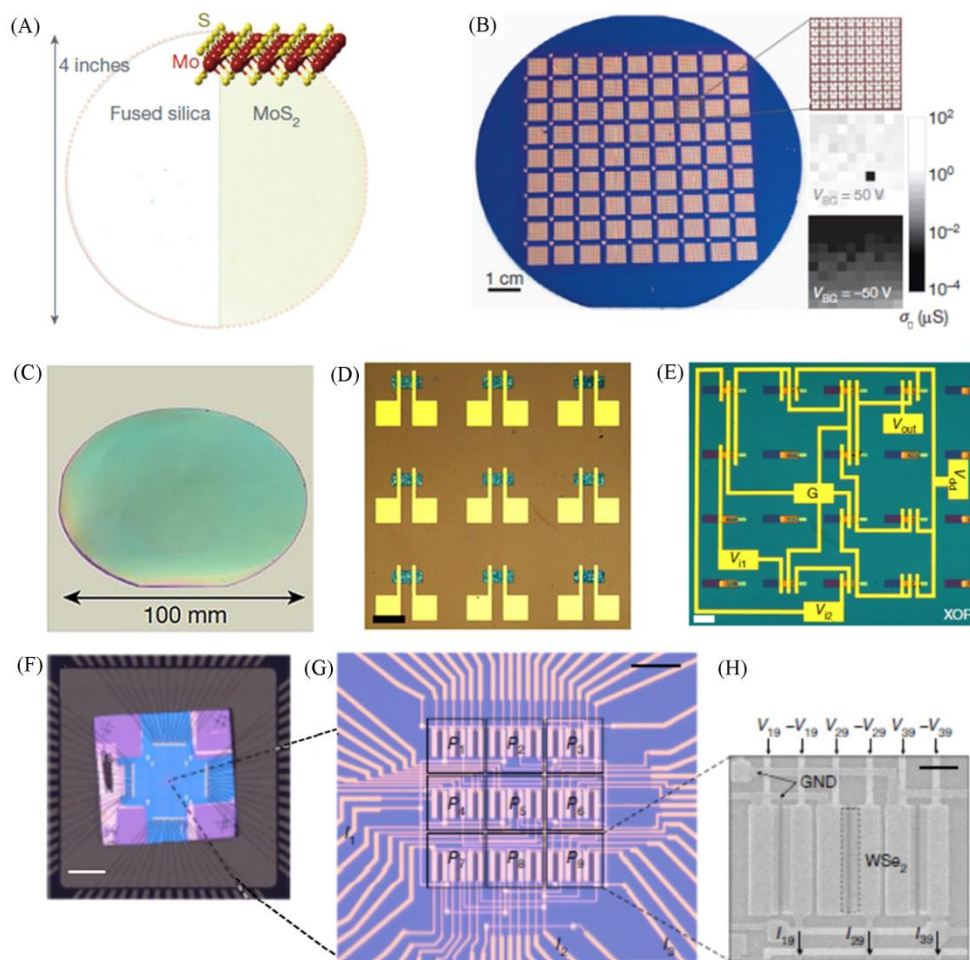
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## 2.3 Challenges of 2D TMDCs for Device Integration

Currently, due to the size limitation of the obtained single crystals of 2D TMDCs, the fabrication of large arrays of electronic devices with 2D semiconductors are mainly based on polycrystalline films. Park *et al.*<sup>[45]</sup> synthesized polycrystalline MoS<sub>2</sub> film *via* metal-organic chemical vapor deposition (MOCVD) on a 4-inch silicon wafer, and prepared a large FETs array based on this polycrystalline film[Fig.3(A) and (B)], in which *ca.* 99% of the devices worked normally. Duan *et al.*<sup>[46]</sup>

fabricated large arrays of FETs on polycrystalline MoS<sub>2</sub> film prepared by solution phase exfoliation, and further constructed logic circuits[Fig.3(C)—(E)]. Mueller *et al.*<sup>[47]</sup> prepared photodiode arrays over a large area with few-layered WSe<sub>2</sub> [Fig.3(F)—(H)] and realized rapid image recognition. Although the channel materials are not single crystals, these results have shown the great potential of 2D TMDCs in device integration.

Both experimental studies and theoretical calculations suggested that the abundant grain boundaries and defects in polycrystalline 2D TMDCs dramatically limited their electrical transport capabilities<sup>[48]</sup>. Therefore, it is critical and urgent to develop controllable synthesis approaches to enlarge the size



**Fig.3 Devices arrays fabricated with 2D TMDCs polycrystalline films**

(A) Optical image of the as-grown MoS<sub>2</sub> polycrystalline film; (B) the FETs arrays fabricated on the film shown in (A)<sup>[45]</sup>; (C) MoS<sub>2</sub> polycrystalline film prepared by liquid phase method; (D) the obtained transistor array; (E) the logic circuit, scale bars in (D) and (E): 100 μm<sup>[46]</sup>; (F)—(H) images of the device arrays using WSe<sub>2</sub> as channel material at different magnifications<sup>[47]</sup>, scale bars in (F)—(H): 2 mm, 15 μm and 3 μm, respectively.

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of the single crystals of 2D semiconductors to promote their practical applications in electronic devices.

### 3 Recent Advances on the CVD Growth of Large Single-crystal of 2D TMDCs

Among various preparation methods, some solution-phase preparation approaches are capable of producing TMDCs monolayers with size of over 100 μm<sup>[49,50]</sup> and hold the promise for large scale production. While 2D TMDCs synthesized by vapor phase deposition approaches, such as chemical vapor deposition(CVD), physical vapor deposition(PVD)<sup>[51]</sup>, chemical vapor transport(CVT)<sup>[15,52]</sup> and molecular beam epitaxy(MBE)<sup>[53]</sup> are suitable for applications in electronic devices as the growth is conducted on the gas-solid interfaces and can avoid the surface contamination of the obtained 2D materials to the largest extent. CVD method is widely used for the controlled synthesis of low dimensional materials and is capable of growing high quality single crystals of 2D TMDCs and moreover, this approach holds the promise of commercialization and therefore, we will focus on the growth of single

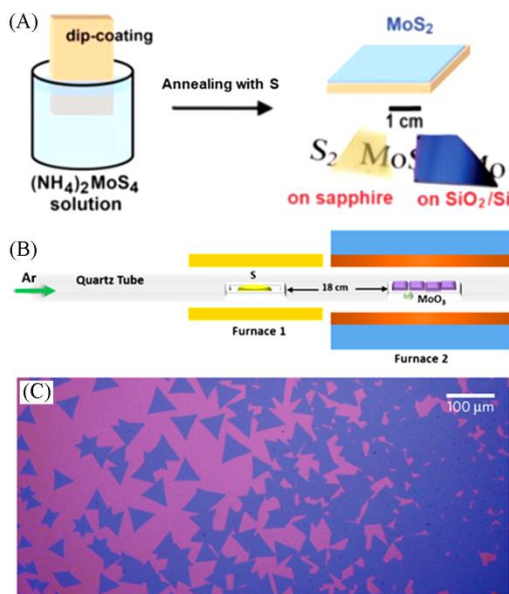
crystals of TMDCs monolayers with CVD approach in this review.

#### 3.1 Conventional CVD Growth of 2D TMDCs

In 2009, Ruoff *et al.*<sup>[54]</sup> synthesized monolayers of graphene on Cu foils by CVD method. Afterwards, CVD method was applied to the synthesis of 2D TMDCs. The method of *in-situ* synthesis of MoS<sub>2</sub> was first performed by thermal decomposition of (NH<sub>4</sub>)<sub>2</sub>MoS<sub>4</sub> thin film. Li *et al.*<sup>[55]</sup> soaked the substrate in (NH<sub>4</sub>)<sub>2</sub>MoS<sub>4</sub> solution, and heated it at 500 °C in Ar/H<sub>2</sub> atmosphere for 1 h. Then the atmosphere was adjusted to Ar or Ar/S for 30 min at 1000 °C, and obtained polycrystalline film on sapphire and SiO<sub>2</sub>/Si substrates, respectively[Fig.4(A)]. Another synthesis strategy of 2D MoS<sub>2</sub> is based on the surface growth of atomic layers of MoS<sub>2</sub> by using MoO<sub>3</sub> and S as growth precursors as shown in Fig.4(B). MoO<sub>3</sub> and S were vaporized in the CVD furnace and reacted in the gas phase at a high temperature to form MoS<sub>2</sub> or the intermediate products of MoS<sub>2-x</sub>O<sub>x</sub> and then the products deposited on the growth substrates as nucleuses and grew up into triangular flakes with a side length of a few micrometers to *ca.* 100 μm[Fig.4(C)]<sup>[56,57]</sup>.



The obtained monolayered MoS<sub>2</sub> flakes were fabricated into FETs and showed electron mobility of 1–8 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup>, comparable to the mechanically exfoliated samples. Compared with the *in-situ* conversion method, the surface growth mode is more suitable for the synthesis of single crystals of 2D TMDCs.



**Fig.4 CVD growth of 2D TMDCs**

(A) Schematics for the *in-situ* synthesis of MoS<sub>2</sub> by thermal decomposition of (NH<sub>4</sub>)<sub>2</sub>MoS<sub>4</sub> and optical images of the product<sup>[55]</sup>; (B) schematics for the typical set-up of the CVD growth of MoS<sub>2</sub> by using MoO<sub>3</sub> and S as the growth precursors<sup>[56]</sup>; (C) typical optical image of MoS<sub>2</sub> flakes grown on SiO<sub>2</sub>/Si substrate<sup>[57]</sup>.

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### 3.2 Large-size Growth of 2D TMDCs Single Crystals

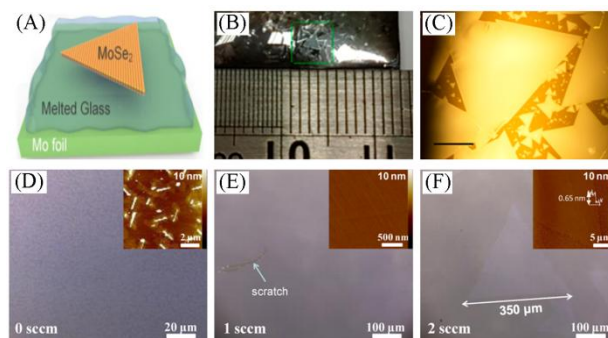
The surface growth of 2D TMDCs by CVD allows for the synthesis of highly crystalline MoS<sub>2</sub> and other 2D TMDCs on various substrates. For practical applications of 2D TMDCs in integrated circuits, the sizes of the samples should reach up to wafer scale. However, currently, the sizes of the obtained 2D single crystals are usually below 100 μm. As many growth parameters, such as the growth precursors, substrates, growth temperatures, pressures, flow rates of the carrier gas and so on can affect the growth results and the detailed growth mechanism is still unclear at the current stage, it is still challenging to further increase the size of the 2D single crystals. And a lot of related studies are still in progress.

Here we defined ‘large-size’ as above 100 μm and summarized the recent studies on growing 2D TMDCs single crystals larger than 100 μm by CVD method roughly from the following aspects: nucleation control, increase the growth rates and surface engineering.

#### 3.2.1 Nucleation Control

For the growth of the large single crystals of 2D TMDCs, it is straight forward to decrease the nucleation density and ideally, form only one nucleus on the substrates to allow for the growth of the largest single crystal on the substrate. During the growth of 2D materials, nucleuses are usually formed at a high

energy site on the substrate surface<sup>[58]</sup>. In the absence of effective control, the distribution and orientation of crystal nucleuses on the substrate are often random, which will lead to a large number of grain boundaries when different crystal domains grow up and merge together. Therefore, the suppression of nucleation is an effective strategy to realize high quality 2D single crystal growth from mono-nucleus. Loh *et al.*<sup>[59]</sup> employed a molten glass as substrate, which produced smooth surfaces with less defects and few nucleation sites at high temperature, and synthesized millimeter sized monolayer MoSe<sub>2</sub> single crystals with mobility of up to *ca.* 100 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup> [Fig.5(A)–(C)]. Another effective approach to reduce the density of nucleation is based on the etching of partially unstable MoS<sub>2</sub> nucleation points with O<sub>2</sub> during MoS<sub>2</sub> growth. With this approach, monolayered MoS<sub>2</sub> single crystals with the single domain size of *ca.* 350 μm and a mobility up to 90 cm<sup>2</sup>·V<sup>-1</sup>·s<sup>-1</sup> were obtained on sapphire substrate [Fig.5(D)–(F)]<sup>[60]</sup>. Single-crystalline MoS<sub>2</sub> monolayer over 300 μm was also synthesized by suppressing the nuclei density by modulating the distance between growth substrate and precursors<sup>[61]</sup>. In addition, the formation process of MoS<sub>2</sub> flakes was observed by *in situ* transmission electron microscope (TEM)<sup>[62]</sup>, which is important for understanding the detailed growth mechanism.



**Fig.5 Schematics(A), photograph(B) and optical images(C) of MoSe<sub>2</sub> crystals grown on molten glass(scale bar in 500 μm)<sup>[59]</sup>, optical and AFM images of MoS<sub>2</sub> grown on sapphire substrates with 0 sccm(D), 1 sccm(E), and 2 sccm(F) O<sub>2</sub>, respectively<sup>[60]</sup>**

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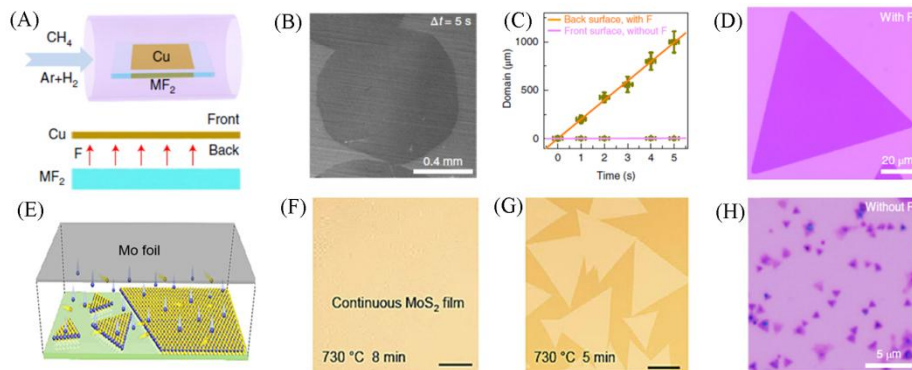
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#### 3.2.2 Accelerate the Growth Rates

In the growth process of 2D TMDCs on surface, the precursor molecules or the intermediate products in the gas phase are adsorbed by nucleation sites on the substrates, and then grow up into single crystals through epitaxial growth. So the growth rate significantly affects the size of the obtained 2D single crystals and it is feasible to increase the size of the 2D TMDCs single crystals by simply accelerating the growth rate. Liu *et al.*<sup>[63]</sup> employed fluorine to promote the growth dynamics during the synthesis of graphene and WS<sub>2</sub> by CVD, and the growth rate of graphene can be increased to *ca.* 200 μm/s[Fig.6(A)–(C)]. Particularly, the size of the domains of WS<sub>2</sub> single crystals increased from *ca.* 1 μm without fluoride to *ca.* 100 μm with fluoride, and the growth rate increased from 0.2 μm/min to 20 μm/min[Fig.6(D) and (H)]. Zhang *et al.*<sup>[64]</sup>

utilized molybdenum foil as the precursor and sodium-calcium glass as the growth substrate, and obtained monolayered MoS<sub>2</sub> polycrystalline film[Fig.6(E)—(G)] and large single domains of over 400 μm, with a growth rate of *ca.* 50 μm/min. They attributed the rapid growth of MoS<sub>2</sub> to the introduction of

sodium from the growth substrates. These promoters were involved in the gas-phase reactions and lowered the growth energy barriers and therefore, dramatically increased the growth rates and enlarged the size of the obtained single crystals.

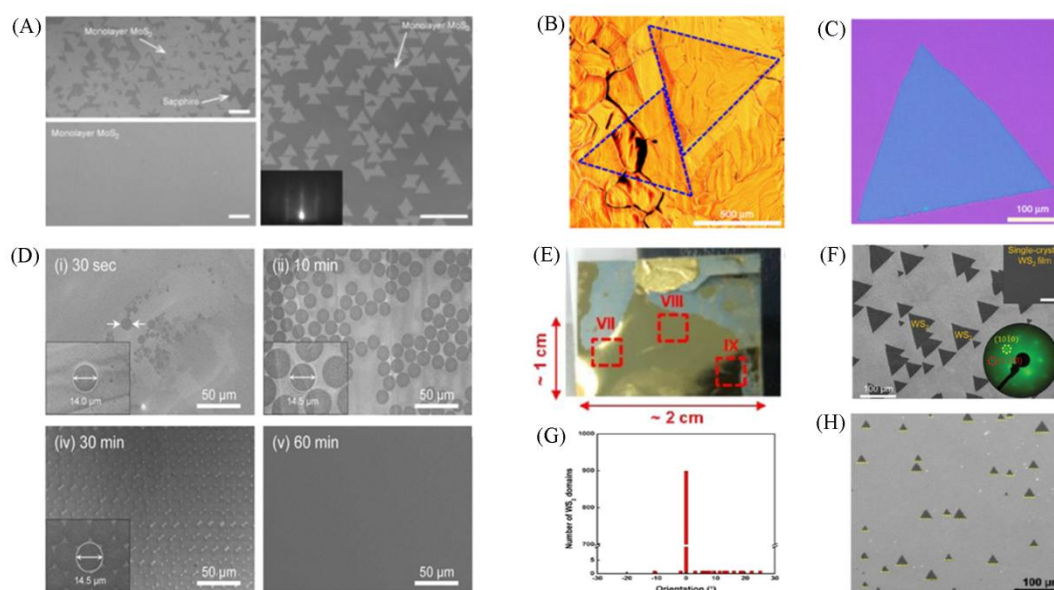


**Fig.6** Schematics for the CVD growth of graphene with fluoride(A) and the optical image of the obtained graphene(B), effect of fluoride on the fast growth of graphene(C)<sup>[63]</sup>, optical images of WS<sub>2</sub> grown with(D) and without(H) fluoride<sup>[63]</sup>, schematic diagram of MoS<sub>2</sub> grown on sodium-calcium glass(E) and optical images of MoS<sub>2</sub> grown after different growth durations(F, G)<sup>[64]</sup>  
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### 3.2.3 Orientation Alignment by Surface Engineering

In addition to the growth of a single crystal over a large area on the growth substrates, an alternative way to the growth of large 2D single crystals is based on merging different single crystalline domains with the same orientation to eliminate the formation of grain boundaries in the final products. The orientations of 2D TMDCs can be tuned by the symmetry of the substrate surface. Kis *et al.*<sup>[65]</sup> synthesized MoS<sub>2</sub> triangular monolayers with orientations of 0° and 60° on sapphire substrate as the results of the lattice matching of sapphire with MoS<sub>2</sub>[Fig.7(A)]. Gao *et al.*<sup>[66]</sup> synthesized a homogeneous

large-area monolayer WS<sub>2</sub> film on gold substrate, and the single domain size of the as-grown WS<sub>2</sub> was close to millimeter scale[Fig.7(B) and (C)]. They attributed the successful growth of large WS<sub>2</sub> to the gold substrate inhibiting the growth of multi-layer WS<sub>2</sub> and reducing the reaction energy barrier. Using liquid metal can break the symmetry of the substrate surface, as the symmetry of the substrate surface mainly comes from periodic lattice arrangement. For example, Lee *et al.*<sup>[67]</sup> first synthesized a large single crystals of *h*-BN on the liquid gold substrate[Fig.7(D)], and then synthesized WS<sub>2</sub> and MoS<sub>2</sub> single crystals using the obtained *h*-BN as the substrate. On this



**Fig.7** SEM images of MoS<sub>2</sub> grown on sapphire substrates(A)<sup>[65]</sup>, optical images of millimeter scale WS<sub>2</sub> grown on gold substrate(B) and transferred to silicon substrate(C)<sup>[66]</sup>, SEM images of *h*-BN grown on liquid gold substrate(D)<sup>[67]</sup> and optical images(E) and SEM images(F) of WS<sub>2</sub> grown on as-grown *h*-BN substrate, crystal domain orientation distributions of WS<sub>2</sub> grown on *h*-BN(G)<sup>[67]</sup> and optical image of MoS<sub>2</sub> grown on *h*-BN substrate(H)<sup>[67]</sup>  
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*h*-BN substrate, the obtained triangular WS<sub>2</sub> flakes exhibited almost the same orientation[Fig.7(F) and (G)], and by prolonging the growth time, the individual WS<sub>2</sub> merged together and formed large single crystal WS<sub>2</sub> film[Fig.7(E)]. They also realized the synthesis of large-area single crystal MoS<sub>2</sub> monolayer on *h*-BN grown on liquid gold substrate[Fig.7(H)].

## 4 Conclusions

In summary, although some encouraging progresses have been made on the growth of large 2D single crystals of semi-conducting TMDCs, related studies are still at an early stage. The size of CVD grown single crystalline graphene has reached up to meters' scale with very fast growth rates. However, the size of single crystals domains of MoS<sub>2</sub> is still within one millimeter. It is urgent to explore the detailed growth mechanism of these 2D TMDCs by combining experimental studies with theoretical calculations to guide the rational design of new growth methods. In addition, more efforts should be made on designing new growth set-up, searching for possible catalysts and promoters, testing appropriate growth substrate, synthesizing new precursors, and carefully optimizing the CVD growth parameters. Meanwhile, subsequent studies with these 2D single crystals should be conducted, such as the nondestructive transfer, patterning and device design, and integration strategies with these high quality ultrathin semiconductors to accelerate their practical applications in high performance devices.

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