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# **Concurrent fault localization using ANN**

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**Abstract** The software is becoming more capable of providing better solutions to our day-to-day activities. In order to increase performance, concurrent programs are always preferred. But the concurrency produces obstacles to diferent phases of software development, including testing and debugging. Finding faults in a concurrent program is always a challenging task due to the presence of many threads overlapping each other, problems in sharing memories, etc. In this paper, we have proposed a Back propagation neural network (BPNN) to generate ranks for each class of a given program. These ranks indicate the probability of a fault being present in each class. The model is trained using test case coverage data, and it is tested using virtual test cases. In all three case studies, the fault localization technique assigned the highest rank to the classes where the actual faults were implanted. By using BPNN and training the model with test case coverage data, the technique shows promising results in identifying the classes where faults are likely to occur. This can greatly aid in the testing and debugging processes of concurrent programs, improving their overall performance.

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# **1 Introduction**

Software fault localization is the process of identifying the specifc location or locations in the source code of a software program where a fault or error is likely to have occurred. This process is an important step in software debugging, as it helps developers narrow down the root cause of a problem and fix it more efficiently. Localizing faults is a more tedious task than testing (Chauhan [2010\)](#page-8-0). Traditional fault localization techniques, as found in the literature, perform well for sequential programs. Traditional fault localization techniques are commonly used in software development and debugging to identify the location of faults in software programs. These techniques do not rely on advanced statistical analysis or machine learning algorithms but rather use heuristics or manual methods to identify suspicious code locations (Zakari et al. [2020](#page-8-1); Shaikh et al. [2023](#page-8-2)). A few modern techniques have been introduced to improve the performance of testing techniques (Shaikh et al. [2021,](#page-8-3) [2022,](#page-8-4) [2021](#page-8-5); Almomani et al. [2015](#page-7-0)).

Spectrum-based fault localization (SBFL) is a widely used technique for identifying the location of faults in software programs (Sarhan and Beszédes [2022;](#page-8-6) Jafarzadeh et al. [2022\)](#page-8-7). It is based on the idea that the behavior of a faulty program during execution, as refected in the outcomes of test cases, is diferent from the behavior of a correct program. Spectrum-based fault localization techniques use information about the outcomes of program executions, such as passing or failing test cases, to identify suspicious code locations that are likely to contain the fault. The performance



of existing fault localization techniques is not satisfactory for concurrent programs (Shaikh et al. [2020\)](#page-8-8).

In concurrent programs, identifying the location of bugs or faults is more difficult as the faults occur during rum time (Bianchi et al. [2017](#page-8-9)). Fault detection in concurrent programs can be a challenging task due to the inherent complexity of concurrent execution. Concurrent programs are designed to run multiple threads or processes concurrently, often sharing resources and interacting with each other, which can lead to various types of faults or errors. Though the Spectrum-based Fault Localization (SBFL) technique can work for fnding concurrent faults, the results are not satisfying (Ghosh and Singh [2020\)](#page-8-10). Hence, there is a need for a new fault localization technique that can locate the faults or bugs present in concurrent programs.

In this paper, we have focused on identifying the faults present in concurrent program. Due to the uncertainty present in the concurrent program such as atomicity violations, thread interleaving, memory access patterns, etc., identifying the faults at the statement level of the concurrent program is very challenging. Therefore, we have tried to consider the function or branch level in our proposed approach to locating the fault in the program. In our approach, We have considered one test case at a time for the given concurrent program, then fnd the result of the test case i.e., pass or fail, and the function or branch coverage information against that particular test case. These information are considered as inputs for the back-propagation neural network. After tuning, the model generates a suspiciousness score for each branch or function. The branch or function with a higher suspiciousness score is more likely to be faulty. Hence, the proposed technique helps the programmer to fnd the faulty branch so that only the statements of the faulty branch need to be examined.

The other sections of this paper are organised as follows: In Sect. [2](#page-1-0), we discuss some basic concepts that are required to understand the proposed technique. Closely related works are described in Sect. [3.](#page-2-0) Section [4,](#page-3-0) represents our proposed technique, and implementation details are presented in Sect. [5](#page-5-0). Finally, Sect. [7](#page-7-1) concludes the paper.

# <span id="page-1-0"></span>**2 Basic concepts**

In this section, we explain the basics of Concurrency, faults in concurrent programs, and a brief idea of an artifcial neural network and its advantages in fault localization.

#### **2.1 Concurrency in programming**

Concurrency in programming refers to the ability of a program to execute multiple tasks simultaneously, or concurrently, rather than sequentially (Roscoe [1998\)](#page-8-11).

It allows a program to make progress on multiple tasks at the same time, even on a single processor system, by interleaving the execution of tasks. Concurrency is an important concept in modern programming as it enables efficient utilization of system resources, improves performance, and enhances responsiveness in concurrent or parallel environments. Concurrency introduces challenges such as race conditions, deadlocks, and synchronization issues that need to be carefully managed to ensure the correct behavior of concurrent programs. Techniques like locks, semaphores, and atomic operations can be used to synchronize access to shared resources and prevent data races. Properly designed concurrent programs can signifcantly improve performance and responsiveness, leading to more efficient and scalable software systems (Singh and Mohapatra [2018](#page-8-12)).

#### *2.1.1 Advantages of concurrency*

- 1. Now a days, CPU's are multi-core. Any single-threaded program utilises only one core. So it doesn't afect CPU performance. But multi-threaded program use multiple cores of the CPU, so the performance of CPU utilization increases.
- 2. Multi-threaded programs allow a series of program to run concurrently. Hence, it increases the response speed of the program.

#### *2.1.2 Disadvantages of concurrency*

- 1. Concurrent program increase context switching, which has adverse efects on CPU execution.
- 2. Multi-threaded program may cause deadlock and starvation.
- 3. Concurrent programs may limit resources after execution.

# **2.2 Concurrency issues in software testing**

Concurrency can introduce several challenges in software testing, as the concurrent execution of multiple tasks can impact the behavior and correctness of a software system. Faults or bugs in concurrent programs can occur during the thread interleaving in run-time (Angus et al. [1990\)](#page-7-2). Faults in concurrent programs not only occur during normal scenarios of computation but also due to thread interleaving, concurrent data access, and shared memory access. Hence, fnding the faults present in a concurrent program is a more challenging task compared to sequential programs (Tu et al. [2019](#page-8-13)).

# **2.3 Artifcial neural network**

An artifcial neural network (ANN) is a type of computational model that is inspired by the structure and function of the human brain (Yegnanarayana [2009\)](#page-8-14). It is a type of machine learning algorithm that is used for tasks such as pattern recognition, classifcation, regression, and optimization. ANNs are a key component of deep learning, which is a subset of machine learning that focuses on neural networks with multiple hidden layers, enabling them to learn complex representations of data. ANNs continue to be an active area of research, with ongoing eforts to improve their performance, interpretability, and robustness for various realworld applications. ANNs consist of interconnected nodes or



<span id="page-2-2"></span>

neurons organized in layers that receive input data, process it through activation functions, and produce output predictions. An ANN specifcally uses a learning algorithm that trains the dataset and modifes the weight of each neuron based on the error rate between actual and target output. After the model is trained, it can predict the new set of data (Guillod et al. [2020](#page-8-15)).

#### *2.3.1 Back propagation neural network*

Backpropagation, short for "backward propagation of errors," is a widely used supervised learning algorithm used in artifcial neural networks (ANNs) for training the network to make accurate predictions (Singh and Sahoo [2011](#page-8-16)). It is a method used to adjust the weights of connections between neurons in an ANN during the training process. Proper tuning of the networks decreases the rate of error and, hence, increases the reliability of the model. Figure [1](#page-2-1) shows the working principle of the back propagation technique.

# <span id="page-2-0"></span>**3 Literature review**

In this section, we review some of the fault localization techniques present in the literature. We have reviewed some works related to the application of Machine learning for software fault localization and some works related to fault localization techniques in concurrent programs. A few related research works are presented in Table [1](#page-2-2).

<span id="page-2-1"></span>You et al.  $(2013)$  $(2013)$  focused on the importance of the **Fig. 1** Back propagation weights of failing and passing test cases to the similarity



coefficients in fault localization. They have used modified similarity coefficients and analyzed how failing test cases can have a greater contribution than passing test cases. Their study shows that modifying the weight values of failing test cases to similarity coefficients is able to locate the faults more accurately. Wong et al. [\(2016](#page-8-24)) provided a brief study on diferent fault localization techniques. According to their analysis, static and dynamic slicing techniques were popular from 2004 to 2007. But, since 2008, spectrum-based techniques have become more popular, as they have made a big contribution to automated fault localization. Their study also shows that diferent evaluation metrics such as EXAM score, T-Score, P-Score, N-Score, etc can be applied to identify the accurate faulty statement.

Chakraborty et al. ([2018](#page-8-18)) proposed a new fault localization approach called EnSpec i.e., they have introduced a new code entropy to the spectrum-based technique. The code entropy is related to the faulty lines executed by the failing test cases. Hence, it makes fault localization more robust and effective. Kim et al. [\(2016\)](#page-8-25) applied 32 different spectrumbased formulas, and based on the results, they clustered the similar categories into three diferent groups. To avoid the limitations of each group, a test case optimization technique is implemented. This technique increases the performance of the fault localization technique.

Wong et al. [\(2011](#page-8-19)) proposed a Radial Basis Function (RBF) based fault localization technique. The network is trained with coverage information and the results of the test cases. The output of the network shows the suspiciousness of each statement. They have also implemented diferent benchmark programs to evaluate the accuracy of their proposed technique. Zheng et al. ([2016\)](#page-8-20) proposed a deep learning based fault localization technique. The statement coverage information and the results of the test cases are used as input for the network. The output shows the suspiciousness of each statement. They have implemented diferent benchmark programs, and the results show that only 10% statements need to be examined to fnd the buggy statement.

Al Qasem and Akour [\(2019\)](#page-7-3) proposed a software fault prediction technique on the basis of Multi-layer perceptrons (MLPs) and Convolutional Neural Network (CNN). They have compared the MLP and CNN, and the result shows that CNN outperforms the MLP. They have experimented with NASA datasets to check the accuracy of the proposed technique. Asadollah et al. [\(2016\)](#page-7-5) provided a brief study based on concurrency and non-concurrency bugs. Their study shows that the concurrent bugs are diferent in terms of severity and fxing time. The study helps to develop debugging and testing related tools for concurrent software.

Park et al. ([2010](#page-8-21)) developed a new dynamic fault localization technique, Falcon. The tool focuses on the dataaccess pattern among thread interleaving in concurrent programs. The tool generates a suspicious score for each data access pattern. Park et al. ([2015\)](#page-8-22) proposed a unifed fault localization tool, UNICORN. It is an automated pattern detection based tool for fnding non-deadlock concurrency bugs. It monitors the memory access patterns and generates a suspicious score for each pattern. Based on the score, it generates ranks of the most important nondeadlock bugs.

Koca et al. ([2013\)](#page-8-23) developed the tool SCRUF which uses Spectrum-Based fault localization techniques to fnd concurrency bugs. The tool instruments the diferent versions of the program that run on a particular thread interleaving pattern. Then it applies the SBFL technique to calculate the suspicious score of each pattern. Alves et al. [\(2017\)](#page-7-4) proposed a technique that fnds concurrent faults by using bounded model checking and code transformation process. The technique is implemented in Concurrent C programs or POSIX programs. The result shows that the tool can locate 84% of the faults present in the benchmark programs. Yu et al. [\(2016](#page-8-26)) presented testability testing for concurrent programs. A testability model was prepared by using test suite metrics and static metrics to predict the testability of concurrent programs.

Movassagh et al. ([2021](#page-8-27)) presented an article to improve the precision of the perceptron neural network as well as train the neural network utilising meta-heuristic methods. The primary objective of this presentation is to extract as much useful information as possible from the UCI data in order to improve accuracy. In this research, the authors used meta-heuristic methods, to identify better NN coefficients. Alzubi et al. ([2022](#page-7-6)) presented a novel strategy for detecting Android malware using machine learning. The Support Vector Machine (SVM) classifer and the Harris Hawks Optimisation (HHO) algorithm are the two components that make up the technique that has been suggested. To be more explicit, the function of the HHO method is to optimise the hyperparameters of the SVM classifer, while the SVM is responsible for doing the classifcation of malware based on the model that was determined to be the best ft, as well as producing the best possible result for how the features should be weighted.

# <span id="page-3-0"></span>**4 Proposed approach**

Locating faults in concurrent programs is more challenging than fnding faults in sequential program. In concurrent programs, examining the source code at the statement level is not possible, as the execution sequence in concurrent program depends on thread interleaving during runtime. In this section, we discuss our proposed approach for identifying the faults present at branch or class level in concurrent program based on back propagation neural networks.



<span id="page-4-0"></span>

```
publi c c lass MainThread {
public static void main (String [] args) {
    System.out.println (" Starting thread_1...");
    // create a thread class instanceThread thread 1 = new ThreadClass (16);// start the thread thread 1
    thread 1. start ();
    try {
        //wait for thread 1 to die
       thread_1. join ();
    } catch ( InterruptedException e ) {
       System.out.println ("Thread interrupted.");
    }
System.out.println ("Starting thread.2...");
    Thread thread 2 = new ThreadClass(6);// start thread_2thread 2 . start ();
    System.out.println("main() is ending...");}
```
<span id="page-4-1"></span>**Fig. 3** Example program

*}*

### **4.1 Sample experiment**

In this section we have taken one concurrent program as an example program for evaluating the accuracy of the proposed approach. For concurrent programs, we consider branch coverage of each class due to thread interleaving during runtime. We have considered a multithreaded program as a sample program. The program consists of two classes, presented in Figs. [2](#page-4-0) and [3](#page-4-1). In Fig. [2](#page-4-0) ThreadClass executes the code for the thread and prints the number till the specifed number is reached, starting from 10. It increments the counter, whereas MainThread creates a thread class instance and starts the thread as shown in Fig. [3](#page-4-1).

<span id="page-4-2"></span>**Table 2** Branch coverage of each test cases for sample program



<span id="page-4-3"></span>

Table [2](#page-4-2) presents the coverage matrix for an example program, where the number of rows equals the number of test cases and the number of columns equals the number of classes or branches. The last column denotes the results of each test case, i.e., either pass or fail. The value of each row indicates the frequency of execution for a specifc class or branch in the corresponding test case. Now, each class is covered by the test cases, either pass or fail. As example, MainThread is covered by two failed test cases, i.e., T1 and T6. A single row presents the coverage information for a particular test case, as shown in Table [2](#page-4-2).

Now, we consider a set of virtual test cases  $v_1,...v_n$  as shown in Table [3](#page-4-3). The speciality of a virtual test case is that it executes only one statement, which in reality is not possible, so it is called a virtual test case. Each row of this matrix is fed to the backpropagation network, and the model gives the suspiciousness score of each corresponding class or method as output.

# **4.2 Framework overview**

Figure [4](#page-5-1) represents the block diagram of the proposed fault localization technique.

Firstly, we generate the test cases for the concurrent program under test. We have considered a test suit to 6 test cases. Next, we fnd function or branch coverage and the result of the test cases, i.e., pass or fail after executing all the test cases.

Next, a backpropagation network is created with two input neurons, one output layer, and one hidden layer. Each class or branch works as input for the network. As our example



<span id="page-5-1"></span>**Fig. 4** Block diagram of proposed approach

program consists of two classes, we have two input layers. The collected coverage data and test case results are collectively used as input for the back-propagation network. The network is trained with collected coverage data. The frst input for our example program is 1, 1 with 0 as the expected output. Fail test cases are represented as 0, and pass test cases are represented as 1. If the actual output is not matched with the expected output, then the weight of the network is modifed, and the updated weight is considered for the next input. Hence, to train the network, the weight is continuously updated until the error is minimum, i.e., approx 0.001.

After training the Back-propagation network, the test data or virtual test cases are fed to the network. The generated output gives a suspiciousness score for each branch or function. Based on the suspiciousness score, the rank of each branch or function is generated. The branch with a higher suspicious score and a lower rank is more faulty.

In the example program, we found the suspiciousness score of two classes, i.e., MainThread and TreadClass as presented in Table [4](#page-5-2). The suspiciousness score of Thread-Class is higher than that of MainTread. Hence, we can say that ThreadClass contains a faulty statement.

<span id="page-5-2"></span>

# <span id="page-5-0"></span>**5 Implementation and result analysis**

In this section, we present the implementation details of the proposed framework for fnding the faults present in concurrent program. We discuss the detailed case studies considered for the experiment. Finally, we compare our fault localization approach with other closely related fault localization techniques.

#### **5.1 Experimental setup**

To execute the proposed approach, we have used a Linux based system with 32 GB of RAM and a 3 GHz Intel (R) i5 processor. An Eclipse IDE with JDK version 12 is used to develop an automated fault localization technique for concurrent programs.

# <span id="page-5-4"></span>**5.2 Case study**

In this section, we have used three Java open source benchmark programs to evaluate the accuracy of the proposed technique. The benchmark programs are downloaded from the SIR repository (Do et al. [2005](#page-8-28)). The programs are Account, Piper and Producer\_Consumer having 66, 74 and 99 LOCs, respectively. The detailed information on the benchmark programs is presented in Table [5.](#page-5-3) We have introduced faults manually in each benchmark programs to check the accuracy of the proposed approach. Firstly, we have seeded a fault in Account class in our frst case study, i.e., Account program. Likewise, we have introduced a fault manually to the Piper class and the Bufer class in the second and third case studies, i.e., the Piper program and Producer Consumer program respectively.

# **5.3 Result analysis**

In this section, we present the experimental results of the proposed approach. The suspiciousness scores of each class for each benchmark program are presented in Tables [6,](#page-6-0) [7](#page-6-1) and [8](#page-6-2).

<span id="page-5-3"></span>**Table 5** Detail information of Benchmark Programs

Programs	LOCs.	Classes	Types of concurrency
Account	66		Deadlock, race
Piper	74		<b>Deadlock</b>
Producer Consumer	99		Race

<span id="page-6-0"></span>**Table 6** Suspiciousness score of each class of account program



 $0.8$ Main Manage\_Account **Account**  $0.6$ Suspiciousness Score  $0.4$  $0<sub>2</sub>$  $\Omega$ Main, ManageAccount , Account Account Program

**Suspiciousness Score for Account Program** 

<span id="page-6-3"></span>**Fig. 5** Score of account programs

From Table [6](#page-6-0) and Fig. [5,](#page-6-3) we find that the suspiciousness score of the account class is the highest. As mentioned in Sect. [5.2,](#page-5-4) we have seeded a fault in the Account class, and from the result, we can verify that the Account class contains the fault. Likewise, for the Piper program, from Table [7](#page-6-1) and Fig. [6](#page-6-4), we can confrm that the class Piper has the highest probability of containing faults, as faults were seeded manually into the Piper class initially. From Table [8](#page-6-2) and Fig. [7,](#page-6-5) we can verify that the buffer class has the most suspicious score of being a faulty statement, and the HaltException class stands in second.

From the result analysis, we can say that the proposed model gives satisfactory results in fault fnding. To verify the accuracy of our model, we have initially introduced faults in a class of each benchmark program, and after training, the model generates the highest suspiciousness score for that particular class. Hence, the programmer needs to examine only the suspected class to locate the fault that occurred during concurrency. So, the proposed approach makes the debugging process easier and saves time in fnding faults in concurrent programs.

### **5.4 Comparison with related work**

We have compared our proposed technique with the widely used spectrum-based metric, D-Star. We have executed our benchmark programs with D-Star. As a result, we fnd D-Star generates a suspiciousness score of 4.8 for all three

<span id="page-6-1"></span>**Table 7** Suspiciousness score of each class of piper program

Class	Score	Rank
IBM AirlinesProducer	0.27	3
Piper	0.57	
IBM Airlines.Consumer	0.31	2
<b>IBM</b> Airlines	0.24	4



<span id="page-6-4"></span>**Fig. 6** Score of piper programs

<span id="page-6-2"></span>**Table 8** Suspiciousness score of each class of Producer\_Consumer program





<span id="page-6-5"></span>**Fig. 7** Score of Producer\_Consumer programs

<span id="page-7-7"></span>**Table 9** Account program

Class	D-Star score	Proposed approach score
Account	4.8	0.22
Main	4.8	0.34
ManageAccount	4.8	0.66

<span id="page-7-8"></span>**Table 10** Piper program



classes for our frst case study, i.e., the Account program, as shown in Table [9](#page-7-7). Hence, it is very difficult to find the exact faulty class, whereas our proposed approach assigns rank 1 to the Account class, which contains the actual fault.

In the same way, D-star generates a suspicious score of 5.2 for all four classes for the second case study, i.e., Piper program, as presented in Table [10](#page-7-8). But, the proposed technique assigns rank 1 to the faulty class Piper. Similarly, for our third case study, D-Star generates an 8.3 suspiciousness score for the Buffer class, Consumer class, HaltException class and Produce\_consumer class and 0 for the rest of the classes. But, the proposed technique is able to identify the faulty class, i.e., Buffer and assign rank as 1. Hence, we can say that our proposed technique gives a more accurate result compared to the widely used D-Star technique.

# **6 Threats to validity**

Here, we present some of the limitations of our tool-

- 1. In this work, we have considered Back-propagation technique to fnd concurrent faults that occur during program execution. The efficiency of the proposed technique needs to be evaluated for advanced machine learning models.
- 2. We have experimented with each concurrent program having 3 to 7 classes. For large-scale concurrent programs, the efficiency of the proposed technique needs to be evaluated.

# <span id="page-7-1"></span>**7 Conclusion**

In this paper, a fault localization technique is specifcally designed for concurrent programs. Locating faults at the statement level in concurrent programs is challenging, therefore, we focus on identifying faulty classes using a back-propagation technique. The technique involves training a model using coverage data obtained from executing test cases. The model is then fne-tuned, and it generates suspiciousness scores for each class as its output. Classes with higher suspiciousness scores are more likely to contain faults. This approach narrows down the search space for the programmer, allowing them to focus their examination on the suspicious classes to identify the faulty class. The results from the case studies conducted in the paper demonstrate the efectiveness of the proposed technique. In the frst case study, the programmer only needs to examine 33% of the code to fnd the fault. Similarly, for the second and third case studies, the examination efort is signifcantly reduced to 0.25% and 0.12% of the code, respectively. In the future, we plan to apply the proposed technique to large real-world programs, which will further validate its efectiveness. Additionally, we intend to explore advanced machine learning techniques or deep learning models to enhance the accuracy of the fault localization tool.

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#### **Declarations**

**Conflict of interest** The authors affirm that they have no conflicts of interest and have addressed all ethical concerns, including those involving human or animal engagement. Such consent is not relevant.

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