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Breakdown Voltage Enhancement of Al_{0.1}Ga_{0.9} N Channel HEMT with Recessed Floating Field Plate

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Abstract

In this paper, electrical and microwave characteristics of $Al_{0.1}Ga_{0.9}$ N channel HEMTs was reported. The device performance were evaluated for conventional gate, field plate gate, and recessed floating field plate with Silicon nitride (SiN)/Hafnium oxide (HfO₂) passivation. The recessed floating field plate HEMT with gate length $L_G = 0.8 \mu m$, gate to drain distance $L_{GD} = 1 \mu m$, and HfO₂ (SiN) passivation HEMT reports peak drain current density (I_{DS}) of 0.282(0.288) A/mm at V_{GS} = 0 V, three terminal off-state breakdown voltage (V_{BR}) of 677 (617) V, 6.38 Ω .mm of ON-resistance (R_{ON}), transconductance (g_{m,max}) of 93(95) mS/mm, and F_T/F_{MAX} of 11.4/49 (12/22) GHz. The HfO₂ (SiN) passivation device demonstrated the Johnson figure of merit (JFoM)) of 7.71 (7.404) THz.V and F_{MAX} x V_{BR} product of 33.173 (13.574) THz.V. The high JFoM along with high F_{MAX} x V_{BR} indicates the potential of the ultrawide bandgap AlGaN HEMTs for future power switching and high-power microwave applications. The breakdown voltage (V_{BR}) of the floating field plate HEMT is improved 54 % from conventional HEMT and 31 % improvement from gate field plate HEMT.

Keywords Floating Field plate · breakdown voltage · electric field · high power applications · HEMTs

1 Introduction

Group III-nitride based wide-bandgap semiconductors are used in high power microwave and switching applications. Owing the unique combination of high electron density, higher mobility, and high critical electic field of conventional GaN channel-based high electron mobility transistors enables high power and high frequency operation for the past two decades [1–4] and GaN based based power switches, RF amplifiers, and power diodes are commercially available in the market from multiple vendor [5]. Moreover, GaN-HEMTs are widely used in low-noise microwave applications due to its excellent noise performance [6-8]. On other hand, AlGaN ternary channel-based HEMTs are also an interesting devices alternate to GaN-channel HEMTs interms of high breakdown voltage due to ultrawide bandgap features of AlGaN material. AlGaN channel based HEMTs had proven its potential for fast-switching and low switching loss applications,

particularly in high temperature and high radiation environments [9–29]. Existence of large critical electrical field (> 3.3 MV/cm) and on par with GaN saturation velocity [30], the AlGaN channel based HEMTs are recognized as the most optimistics candidates future high-power switching as well as microwave applications in harsh environments [31].

After the first successful demonstartion of AlGaN channel based HEMT for high power and microwave application [10], several research groups reported the high performance of AlGaN channel HEMTs. T. Nanjo et.al. investigated the HEMT on AIN buffer and the device shown enrich V_{BR} by suppressing the drain leakage current [11]. Takuma Nanjo et al. reported the ternary channel (AlGaN) HEMT on sapphire, the breakdown voltage of 1700 V for L_{GD} of 10 μ m [12]. Sanyam Bajaj et al. reported Al_{0.75}Ga_{0.25} N channel HEMT with reduce source/drain contact resistances by n + AlGaN region [13]. The 20 nm Al_2O_3 gate dielectric MISFETs demonstrated 14 mS/mm of gm max and 224 V of breakdown voltage (V_{BR}). Albert G. Baca et al. demonstrated a circular HEMT with a V_{BR} of 810 V for 2.0 μm gate length and 314 µm circumference [14]. SakibMuhtadi et al. revealed high Al-content AlGaN channel HEMTs on AlN buffer and the device shown better thermal conduction. Operated up to 40 V at 0.25 A/mm without current collapse [15]. A high breakdown voltage of 2200 V was attained for 22 µm gate to drain

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distance device by adopting ohmic/Schottky-hybrid drain contacts [16]. Ming Xiao et al. reported GaN/ Al_{0.35}Ga_{0.65} N/AlN/graded channel HEMT and the device shown enhanced I_{DS} and high I_{on}/I_{off} ratio with improved breakdown voltage [17]. The first RF performance on AlGaN channel based HEMT was reported byAlbert G. Baca et.al. An 80 nm gate length HEMT yields I_{DS} of 0.16 A/mm, and 24 mS/mm of g_{m,max}, and having F_T/ F_{MAX} of 28.4/18.5 GHz [18]. An RF simulation study on AlGaN HEMT channel based HEMTs reported its potential for large signal RF application as well as power switching applications [24]. Zhang et al. proposed AlGaN double channel HEMTs for improving carrier transport and 2DEG density [27], however the large negative threshold voltage of the device may result in off-state power loss.

The bandgap of the $Al_xGa_{1-x}N$ tailoring by varying the Al composition (0 < x < 1). The high critical breakdown field and low on resistance (R_{ON}) are key parameters for power switching applications. Lateral Figure of Merit (LFOM)is used to estimate the potential of a material for power switching [32];

$$LFOM = \frac{V_B^2}{R_{on}} = q\mu_{ch}n_s E_C^2 \tag{1}$$

The LFOM of a material depends on the sheet charge density (n_s), critical electric field (E_c), and mobility of the channel (μ_{ch}). Since the critical electric field of AlGaN channel is higher ($E_c \sim E_g^n$, 2 < n < 2.5) than the GaN channel, Al_xGa_{1-x}N channel offers significant improvement in V_{BR} even at high temperature over GaN. The JFOM measures the ability of the materials for high power microwave applications;

$$JFOM = V_B \cdot F_T = \frac{E_c \nu_{sat}}{2\pi} \tag{2}$$

The JFOM of material systems is the product of F_T and V_{BR} . The critical electric field (E_c), and electron saturation velocity (ν_{sat}) influences the JFOM of the HEMT. Since the low Al composition Al_xGa_{1-x}N channel saturation velocity is on par with GaN channel, along with enhanced critical field improves the JFOM of AlGaN channel HEMTs than GaN-based HEMTs. The high V_{BR} is achieved for long channel L_G, long L_{GD} HEMTs, along with Al-richAlGaN channel [9–29]. Whereas, the smaller L_G,L_{GD}, and low Al composition of AlGaN channel results in improved cut-off frequency with the suppressed V_{BR} and hence, there is a trade-off between V_{BR} and device speed (cut-off frequency) of HEMTs.

In this work, we proposed the recessed floating field plate, Al_{0.1}Ga_{0.9} N channel HEMT for improve the V_{BR} of the device with satisfactory RF performance. $L_G = 0.8 \mu m$, and L_{GD} = 1 μm Al_{0.31}Ga_{0.69} N/Al_{0.1}Ga_{0.9} N HEMT on sapphire substrate is investigated using Silvaco ATLAS TCAD numerical simulation for SiN and HfO₂ passivation.

The HfO_2 passivation device shown remarkable improvement in breakdown voltage than SiN passivation. The organization of this work as follows; Device structure description for conventional gate HEMT (Device A), Gate field plate HEMT (Device B), and recessed floating field plate HEMTs (Device C) is discussed in Sec. 2. The physics-based simulation models are described in Sec. 3. The DC and microwave characteristic of proposed HEMT with experimental validation is discussed in Sec. 4 with concluding remarks.

2 Device Structure Description

The Al_{0.1}Ga_{0.9} N channel geometry of conventional gate HEMT (Device A), Gate field plate HEMT (Device B), and recessed floating field plate HEMTs (Device C) are displayed in Fig. 1(a), (b), and (c) respectively. The device grown on sapphire substrate and consists of 23 nm Al_{0.31}Ga_{0.69} N barrier $(E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ N channel } (E_{g} \sim 3.989 \text{ eV}), 100 \text{ nm Al}_{0.1}Ga_{0.9} \text{ eV})$ 3.563 eV), and 2.2 µm buffer. The device DC and RF performance are evaluated for 100 nm SiN and HfO₂ passivation. High Al composition in the Al_xGa_{1-x}N channel attenuates the mobility of the device [30] and also a very low Al composition in the Al_xGa_{1-x}N channel degrades the breakdown voltage of the device. Therefore, we have used the optimum value of Al composition to balance the mobility as well as breakdown voltage of the HEMT. The model device used in simulation has a 0.8 µm gate length, 1 µm gate to drain distance, and 0.8 µm gate to source distance. The source and drain ohmic contacts are realized by setting the work function of the source/drain electrode to 3.4 eV and Schottky contact was made by setting the gate electrode work function as 5.2 eV for the TCAD simulation. The conduction band offset along with interface charge details of the heterostructure shown in Fig. 2. At room temperature TCAD simulation exhibited 1.01×10^{13} cm⁻² of electron density and 810 cm²V⁻¹ s⁻¹ of carrier mobility in the 2DEG.

3 Simulation Models

The proposed HEMTis analyse during several device physics models in TCAD simulation including mobility model, recombination models, carrier transport, and polarization models.

Polarization charge at the heterostructure interface is as follows [33];

Fig. 1 Structure of Al_{0.1}Ga_{0.9} N channels (**a**) conventional HEMT (Device A), (**b**) Gate field plate (Device B), (**c**) Recessed floating field plate (Device C)



$$P_{total} = [P_{PE}(bottom) + P_{SP}(bottom)] - [P_{PE}(top) + P_{SP}(top),]$$
(3)

The P_{total} at the top/bottom heterointerface depends on the spontaneous (P_{SP}) and piezoelectric polarization (P_{PE}) of the materials. The temperature dependent mobility model $\mu_0(T,N)$ describes as following form [30];

$$\mu_0(T,N) = \mu_{\min\left(\frac{T}{300}\right)}^{\beta_1} + \frac{(\mu_{max} - \mu_{min})\left(\frac{T}{300}\right)^{\beta_2}}{1 + \left[\frac{N}{N_{ref}\left(\frac{T}{300}\right)^{\beta_3}}\right]^{\alpha \left(\frac{T}{300}\right)^{\beta_4}}}$$
(4)



Fig. 2 Conduction band and interface charge diagram of $Al_{0.31}Ga_{0.69}$ N/ $Al_{0.1}Ga_{0.9}$ N heterostructure



Fig. 3 Transfer characteristics of recessed floating gate HEMT (Device $\ensuremath{\mathrm{C}}\xspace)$

The capture of phonon transition within the forbidden bandgap due to trap states, Shockley-Read-Hall (SRH) recombination model is used. The SRH model expresses as a function of electron life time τ_n and holes life time τ_p , temperature, trap energy level E_{trap} [34];

$$R_{net}^{SRH} = \frac{n - n_{ie}^2}{\tau_p \left[p + n_{ie} \exp\left(\frac{-E_{trap}}{KT}\right) \right] + \tau_n \left[p + n_{ie} \exp\left(\frac{-E_{trap}}{KT}\right) \right]'},$$
(5)

The Selberherr's impact ionization model considered for device breakdown simulation [35] and the impact ionization carrier generation rate described as follows:

$$G = \left(\alpha_n \left| J_p \right| + a_p \left| J_p \right| \right) / q \tag{6}$$



Fig. 4 I - V characteristics of recessed floating gate HEMT (Device C)



Fig. 5 Transfer characteristics for $L_G = 0.8 \ \mu m$ of $Al_{0.1}Ga_{0.9} \ N$ channel HEMT (Device C) and experimental work [27]

Where, *E* is the electric field, electrons ionization rate $\alpha_n = A_n exp(-B_n)/|E|$ and holes ionization rate $\alpha_p = A_p exp(-B_p)/|E|$. The fitting parameters A_n , A_p , B_n , and B_p values are taken from [34] for the simulation.

4 Results and Discussions

The proposed recessed floating gate HEMT (Device C) transfer characteristic is depicted in Fig. 3 for $V_{DS} = 10$ V and V_{GS} swept from – 6 to 0 V. The conventional and gate field plate HEMTs also exhibited similar response. Al_{0.1}Ga_{0.9} N HEMT with L_G = 0.8 µm has reached the maximum output current (I_{DS}) of 0.28 A/mm and g_{m,max} of 95 mS/mm. The threshold voltage (V_{th}) of the HEMT is extracted as -3 V. The V_{th} of the



Fig. 6 Output characteristics for $L_G = 0.8 \ \mu m$ of $Al_{0.1}Ga_{0.9}$ N channel HEMT (Device C) and experimental work [27]

Fig. 7 E-field distribution for SiN passivation of (**a**) Device A (**b**) Device B (**c**) Device C









Fig. 9 Off-state breakdown characteristics of L_{G} = 0.8 μm $Al_{0.1}Ga_{0.9}$ N channel HEMTs

HEMT obtained from VGS intercepted of the linear extrapolation in the active region. The V-I characteristics of the proposed Device C is shown in Fig. 4 for $V_{GS} = -3$ to 0 V and V_{DS} swept from 0 V to 10 V. Device A and Device B also exhibited similar output characteristics. The extracted ONresistance (R_{ON}) of the device from the V-I characteristics $(V_{GS} = 0 \text{ V})$ is 6.38 Ω .mm. The R_{on} resistance of the device extracted from the output characteristics at $V_{GS} = 0$ V By taking the slope (1/Ron = I_{DS}/V_{DS}) corresponding to $\frac{1}{4}$ th of maximum drain current (I_{DS, max}). The device is perfectly pinched-off at V_{GS} = -3 V. The proposed device R_{ON} resistance is comparatively lower than the reported works [10-28]. The transfer and output characteristics of simulation result of conventional Al_{0.1}Ga_{0.9} N channel HEMT is validated with experiment result of [27] and it is shown in Figs. 5 and 6 respectively. The simulated results were well correlated with the reported experimental work.

The HEMT breakdown simulation was carried out at offstate condition ($V_{GS} = -8$ V). The electric field (E-filed) distributions for SiN and HfO₂ passivation of (a) Device A, (b) Device B and (c) Device C are depicted in Figs. 7 and 8 respectively. The permittivity (ε_r) and thickness (t) of the insulator (passivation) modulates the E-field. High permittivity HfO₂ (ε_r =25) passivation smoothening the E-filed at gate and drain edge [35–38].

From Fig. 7(a) (SiN passivation) and Fig. 8(a) (HfO₂ passivation), it is observed that a peak E-field exist at the gate edge, which lower the V_{BR} of the device. Therefore, sinking the field distribution becomes the alternate solution to enhance the V_{BR} . In general, the gate field plate (FPs), source field plate (SFPs), and drain connected field plate techniques are used to suppress the electric field [39]. The gate field plate techniques

demonstarted the improved breakdown voltage by alleviating high E-field as shown in Fig. 7(b) (SiN passivation) and Fig. 8(b) (HfO₂ passivation) and it reshaping the field distribution.

In this work, a recessed floating field plate structure is considered for further, to enhance the breakdown voltage of the HEMTs. The electic field distribution of proposed HEMTs are shown in Fig. 7(c) (SiN passivation) and Fig. 8(c) (HfO₂ passivation).

The introduction of floating field plate, reshaped the electric field and suppressed the E-field near the gate edge effectively and a peak electric field found at the drain side edge of the floating field plate because of much closer with drain electrode and also the recessed field plate is very closer with 2DEG channel than gate field plate results in better surface field distribution, which enhances the breakdown voltage of the HEMTs. The E-field engineering soley depends on field plate length, passivation permittivity, thickness of the passivation, and recess depth.

For the proposed Device C dimensions, the off-state breakdown voltage characteristics of HEMTs are plotted in Fig. 9. The breakdown voltage of the HEMT extracted from I_D - V_D curves at the intersection of the extrapolated saturation segment (an V_D saturated and a sudden increase in I_D). The recessed floating field plate HEMT shown remarkable V_{BR} of 677 (617) V for HfO₂ (SiN) passivation. However, the conventional HEMT shown V_{BR} of 307(297) V and gate filed plate shown V_{BR} of 462 (428) V. From this analysis of off state breakdown voltage simulation, the floating field plate demonstared 54 % improvement in V_{BR} than conventional HEMT, and 31 % higher than the gate field plate HEMT.

The Figs. 10 and 11 shows the simulation results of electron concentration distribution of HEMTs at breakdown condition. The larger depletion region of floating field plate shown in Figs. 10(c) and 11(c), suppressed E-field near the drain side of gate edge, and high critical field of $Al_{0.1}Ga_{0.9}$ N channel are the major reason for improving the breakdown voltage of the proposed HEMTs.

The microwave performance of the SiN and HfO₂ passivated Device C are depicted in Figs. 12 and 13 respectively. The F_T/F_{MAX} of the device extracted from current gain and power gain when it reaches 0 dB respectively. The conventional HEMTs are showing better F_T than field plated HEMTs. Due to the introduction of additional field plate in the device structure, increases the parasitic capacitance (C_{GS} and C_{GD}) which limits the speed of the HEMTs as given in the Eqs. (9);

$$P_{RF} == \frac{I_{max}[V_{br} - V_{knee}]}{8},\tag{7}$$



Fig. 10 Electron concentration distribution of SiN passivation (a) Device A, (b) Device B and (c) Device C at breakdown voltage condition



Fig. 11 Electron concentration distribution of HfO2 passivation (a) Device A, (b) Device B and (c) Device C at breakdown voltage condition



Fig. 12 Microwave performance of SiN passivation HEMTs

$$F_T = \frac{G_m}{2\pi (c_{Gs} + c_{GD})},\tag{8}$$

$$F_{MAX} = \frac{F_T}{\sqrt{(R_i + R_s + R_g)g_0 + (2\pi F_T)R_G C_{GD}}},$$
(9)

The I_{DS} and V_{BR} of a transistor expected to be very high for delivering high RF output power density as shown in Eq. (8). The proposed recessed floating field plate HEMT shown a F_T/F_{MAX} of 11.4/49 GHz for HfO2 passivation and 12/22 GHz for SiN passivation. The improved power performance along with the satisfactory cut-off frequencyof the proposed HEMTs demonstrated its capability for future RF and power switching applications. Further the small signal RF charcateristics can be enhanced by scale down the device dimensions.



Fig. 13 Microwave performance of HfO₂ passivation HEMTs

Table. 1 State of art AlGaN chant	nel parameter and	d performance					
Author, Year	Reference No	Gate length (L _G)	Gate to drain distance (L _{GD})	Drain current (I _{DS})	Breakdown voltage (V _{BR})	Cut-off frequency $(F_{\rm T}/F_{\rm MAX})$	JFoM (THz.V)
V. Adivarahan et al., 2008	[10]	1 µm	3 µm	0.11 A/mm	463 V		
T. Nanjo et al., 2009	[11]	1 µm	2 µm	0.14 A/mm	350 V		
Takuma Nanjo et al., 2013	[12]	1 µm	3 µm	0.18 A/mm	500 V		
Sanyam Bajaj et al., 2016	[13]	0.7 µm	1.1 µm	0.05 A/mm	224 V		
Albert G. Baca et al., 2016	[14]	2 µm	10 µm		810 V		
W. Zhang et al., 2018	[16]	3 µm	6 µm		500 V		ı
Ming Xiao et al., 2018	[17]	1 µm	5 µm	0.3 A/mm	408 V		1
Albert G. Baca et al., 2018	[18]	80 nm	0.5 µm	0.1 A/mm	92 V	28.4/18.5 GHz	2.6128
Andrew M. Armstrong et al., 2019	[19]	3 µm	2.7 µm	0.1 A/mm	620 V		1
HaoXue et al., 2019	[20]	160 nm	0.6 µm	0.42 A/mm	170 V	20/40 GHz	3.4
Yachao Zhang et al., 2020	[27]	0.8 µm	1 µm	0.24 A/mm	143 V		
Shahadat H. Sohel et al., 2020	[28]	0.7 µm	3.5 µm	0.3 A/mm	125 V	14/22 GHz	1.75
This work (SiN Passivation)		0.8 µm	1 µm	0.288 A/mm	617 V	12/22 GHz	7.4
This work (HfO2 Passivation)		0.8 µm	1 µm	0.282 A/mm	677 V	11.4/49 GHz	7.718

The comparison of state of art AlGaN channel based HEMTs parameter and performance along with proposed Device C are tabulated in Table 1. The proposed Device C shows improved drain current, breakdown volatge, and RF perfomance among the reported work for L_{GD} (1 μ m) and $L_G(0.8 \ \mu$ m).

5 Conclusions

Breakdown voltage performance analysis of Al_{0.1}Ga_{0.9} N channel HEMT is reported with floating field plate and AlGaN buffer. A Physics-based Technology Computer Aided Design(TCAD) simulation results shows that the peak electric field near the drain-side of gate edge is majorly reduced by recessed floating filed plate technique, which further elevated the breakdown performance of the AlGaN channel HEMTs. The insertion of floating filed plate reshaped the Efield distribution in the access region effectively and reduced the E-field near the drain side of gate edge and thus enhanced the breakdown voltage. The proposed recessed floating field plate HEMT yields V_{BR} of 677 Vand F_T/F_{max}11.4/49 GHz for a high-k HfO₂ passivation. The breakdown voltage (V_{BR}) of the HfO₂ passivation floating field plate HEMT is improved 54 % from conventional HEMT and 31 % improvement from gate field plate HEMT. The excellent breakdown power performances along with RF characteristics of the ultrawide bandgap AlGaN channel based HEMTs are attractive alternate devices for next geneartion power electronics and RF applications.

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Authors' Contributions All the works reported in this paper is original and have done with equal contribution in all the sections by Ramkumar Natarajan and Eswaran Parthasarathy.

Data Availability Not Applicable.

Code Availability Not Applicable.

Declarations

Manuscripts is prepared strictly adhering to the norm of the journal, contains 5 Sections and no sub sections.

Ethics Approval Not Applicable.

Consent to Participate Not Applicable.

Conflicts of Interest/Competing Interests The authors declare that there is no conflict of interest reported in this paper.

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Research Involving Human Participants and/or Animals Not Applicable.

Informed Consent Not Applicable.

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