

Synthesis of uniform two-dimensional MoS₂ films via thermal evaporation

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Received: 3 July 2023 / Revised: 17 August 2023 / Accepted: 20 August 2023

ABSTRACT

Two-dimensional (2D) molybdenum disulfide (MoS₂) holds great potential for various applications such as electronic devices, catalysis, lubrication, anti-corrosion and so on. Thermal evaporation is a versatile thin film deposition technique, however, the conventional thermal evaporation techniques face challenges in producing uniform thin films of MoS₂ due to its high melting temperature of 1375 °C. As a result, only thick and rough MoS₂ films can be obtained using these methods. To address this issue, we have designed a vacuum thermal evaporation system specifically for large-scale preparation of MoS₂ thin films. By using K₂MoS₄ as the precursor, we achieved reliable deposition of uniform polycrystalline MoS₂ thin films with a size of 50 mm × 50 mm and controllable thickness ranging from 0.8 to 2.4 nm. This approach also allows for patterned deposition of MoS₂ using shadow masks and sequential deposition of MoS₂ and tungsten disulfide (WS₂), similar to conventional thermal evaporation techniques. Moreover, we have demonstrated the potential applications of the obtained MoS₂ thin films in field effect transistors (FETs), memristors and electrocatalysts for hydrogen evolution reaction (HER).

KEYWORDS

thermal evaporation, molybdenum disulfide, two-dimensional, memristors, hydrogen evolution reaction

1 Introduction

Two-dimensional (2D) transition metal dichalcogenides (TMDCs) thin films, such as molybdenum disulfide (MoS₂) have sparked tremendous research interest owing to their promising applications in electronics [1, 2], optoelectronics [3], lubrication [4], catalysis [5–7], anti-corrosion [8, 9] and so on. Chemical vapor deposition (CVD) has proved to be a successful method for synthesizing high quality 2D MoS₂ and other TMDCs on various substrates [10]. However, most of the CVD growth of 2D MoS₂ has been carried out in horizontal tube furnaces, which significantly limited the size of the resulting films and the scalability of the growth process. Compared to CVD growth in tube furnaces, thermal evaporation offers several advantages. Firstly, it allows for the production of highly uniform thin films over large areas. Secondly, it provides reliable control over the deposition rate and thickness of the films. Additionally, thermal evaporation enables patterned deposition using masks and the deposition of alternative materials. These advantages make thermal evaporation a desirable method for the fabrication of 2D films, such as MoS₂. However, the high melting temperature of MoS₂ (1375 °C) poses a significant challenge in achieving uniform thin films through conventional thermal evaporation techniques. Previous attempts have resulted in the production of thick (~ 250 nm) and rough MoS₂ films with nanosphere morphology [11–13].

To address this challenge, we designed a vacuum thermal evaporation system for the preparation of 2D MoS₂ thin films. The large, vertical chamber and a face-to-face precursor supply configuration together with a rotatable sample holder allowed for

the large area and uniform deposition of 2D MoS₂ thin films with a size of up to 50 mm × 50 mm. By optimizing the parameters such as the amount of precursors, deposition time, and the distance between substrates and precursors, we were able to achieve precise control over the thickness of MoS₂ thin films, ranging from 0.8 to 2.4 nm. Similar to conventional thermal deposition of metals, we also achieved the patterned deposition of MoS₂ with a shadow mask and the layer by layer deposition of MoS₂ and tungsten disulfide (WS₂). In addition to planar substrates, the thermal deposition of MoS₂ can also be performed on curved substrates, such as carbon cloth. Field-effect transistors (FETs) fabricated on the obtained 2D MoS₂ thin films exhibited on/off current ratios on the order of 10⁵. Besides FETs, these samples are also suitable for fabricating memristors and catalysts for hydrogen evolution reaction (HER). Our work demonstrates the great potential of thermal evaporation technique in the preparation of uniform 2D MoS₂ thin films with large area and controllable thickness.

2 Results and discussion

The thermal deposition of MoS₂ thin films was conducted in a homemade vacuum thermal evaporation system as schematically shown in Fig. 1(a). To achieve the large area and uniform deposition of 2D MoS₂, the sample holder and the heating stage were vertically mounted in a large chamber (~ 27 dm³) to ensure the uniform diffusion of gas phase precursors [14]. Additionally, the sample holder was designed to be rotatable, further enhancing

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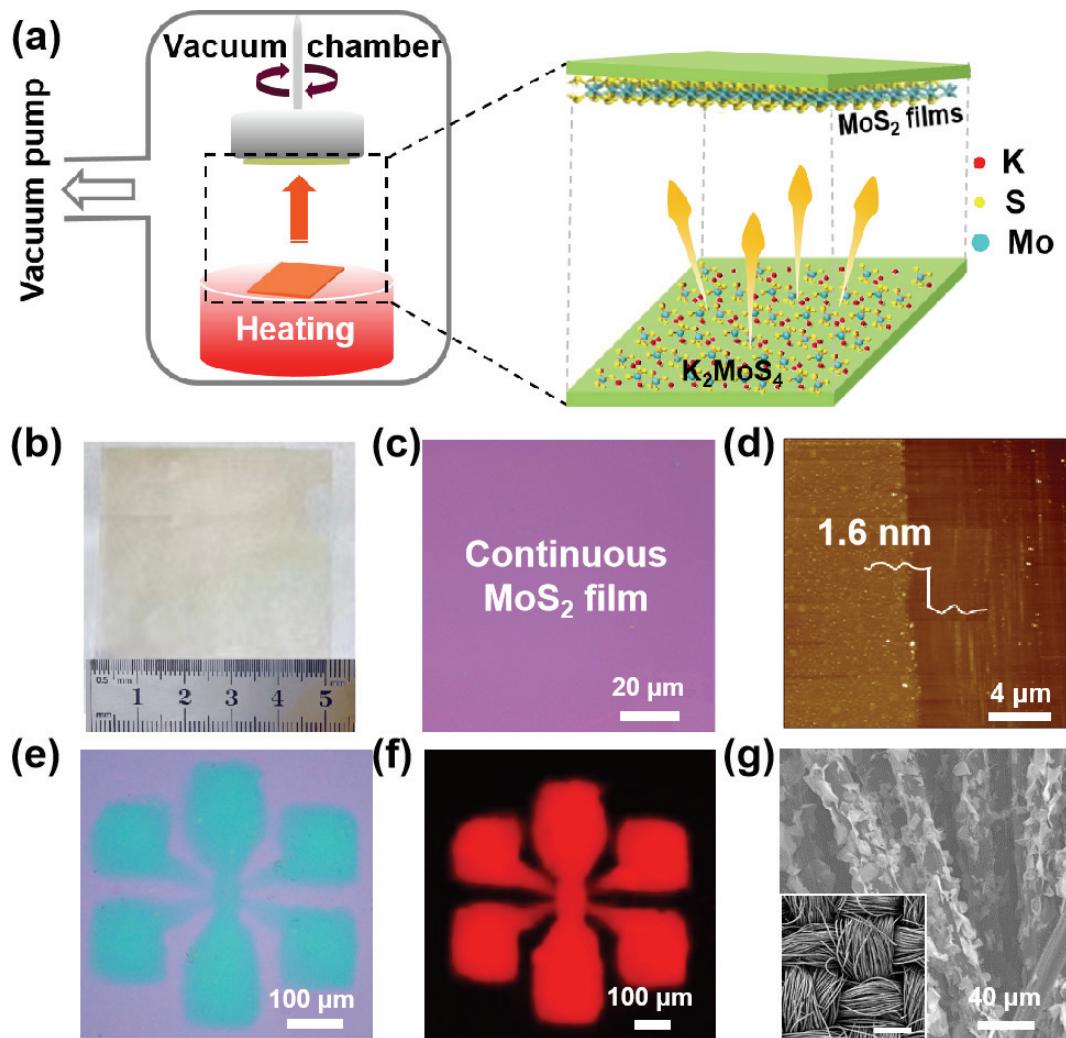


Figure 1 Thermal deposition of MoS₂ thin films in our homemade thermal evaporation system. (a) Schematic of the set-up used for the deposition of MoS₂ thin films. (b) Photograph showing a bilayer MoS₂ film grown on a mica substrate. (c) Optical image of the bilayer MoS₂ thin film transferred to a SiO₂/Si substrate, exhibiting uniform contrast indicative of homogeneous thickness. (d) AFM image of the bilayer MoS₂ thin film with a thickness of ~1.6 nm. (e) Optical image of a MoS₂ pattern transferred to a SiO₂/Si substrate. (f) Raman mapping image of the patterned MoS₂, demonstrating the uniformity of the deposited material. (g) SEM image of the MoS₂ flakes grown on carbon cloth. Inset: the low-magnification SEM image of the carbon cloth. Scale bar: 1 mm (inset of (g)).

the uniformity of precursor distribution between the source and growth substrates. The optimal rotational speed of the sample holder was set at tens of revolutions per minute (RPM). To lower the deposition temperature of MoS₂, we utilized potassium tetrathiomolybdate (K₂MoS₄), which decomposes into MoS₂ at 600 °C, as the MoS₂ source, as shown in the following reaction equation [15]



To further improve the uniformity of MoS₂ supply, K₂MoS₄ was uniformly dispersed on a mica substrate of the same size as the growth substrate, allowing for face-to-face source supply (Fig. S1 in the Electronic Supplementary Material (ESM)). The precise control of MoS₂ thin film thickness was achieved by adjusting the amount of precursors, deposition time, and the distance between the growth substrates and precursors. The resulting MoS₂ films had thicknesses of approximately 0.8, 1.6, and 2.4 nm, corresponding to 1, 2, and 3 L, respectively (Fig. S2 in the ESM), and their surfaces were relatively smooth over the entire films (Fig. S7 in the ESM).

By optimizing growth conditions such as the amount of precursors, temperature, the selection of substrates (Fig. S3 in the ESM), we successfully obtained MoS₂ thin films with a large size of up to 50 mm × 50 mm on mica substrates. Specifically, for the

deposition of bilayer MoS₂ thin films, a thermal deposition process was carried out at a heating temperature of 900 °C (with a substrate temperature of 600 °C) for 20 min using ~0.5 mg of K₂MoS₄ as the precursor. The distance between the substrate and the precursor was maintained at ~14 mm, while the chamber pressure during the deposition process was kept at around 10⁻³ mbar.

The MoS₂ thin films obtained through the thermal deposition process were subsequently transferred to a SiO₂/Si substrate using water-assisted peeling [5] for further characterization. The optical image of the sample revealed a uniform contrast, indicating a consistent thickness across the entire MoS₂ thin film (Fig. 1(c)). Atomic force microscopy (AFM) measurements showed a film thickness of ~1.6 nm, confirming the bilayer structure of the MoS₂ film (Fig. 1(d)). Additionally, by adjusting the amount of the precursor and the deposition time, we were able to synthesize single-layer and trilayer MoS₂ films (Fig. S2 in the ESM).

The vertical deposition of MoS₂ in our approach enables the patterned deposition of MoS₂ using shadow masks (Fig. 1(e)) and Fig. S4 in the ESM). The Raman mapping image of the transferred MoS₂ (Fig. 1(f)) confirms the uniformity of the MoS₂ pattern. We extended our approach to deposit thin films of other TMDCs. A WS₂ thin film with a thickness of ~0.8 nm was successfully grown on a mica substrate using the same deposition method and subsequently transferred to a SiO₂/Si substrate (Fig. S6(a)) in the

ESM). Additionally, by employing this technique, we obtained MoS₂-WS₂ vertical heterojunction by sequentially depositing MoS₂ and WS₂ on a mica substrate, followed by transfer onto a SiO₂/Si substrate (Fig. S6(b) in the ESM). The presence of distinct Raman peaks corresponding to MoS₂ (383.7 and 405.3 cm⁻¹) and WS₂ (350.5 and 417.5 cm⁻¹) confirmed the successful formation of MoS₂-WS₂ vertical heterojunction (Fig. S6(c) in the ESM). In addition to planar substrates, carbon cloth was also used as the substrate for the deposition of MoS₂. The scanning electron microscope (SEM) image of the MoS₂ deposited on carbon cloth clearly showed the morphology of the as-deposited MoS₂ flakes (Fig. 1(g)). These flakes exhibited an average size of tens of micrometers and a thickness in the nanometer range.

To investigate the crystallinity of the obtained MoS₂ films, we imaged the obtained samples with transmission electron microscope (TEM) by transferring the obtained MoS₂ samples to holey carbon TEM grids via water-assisted peeling (Fig. 2(a)). The high-resolution TEM image revealed the hexagonal lattice of the obtained MoS₂ thin film (Fig. 2(b)). The selected-area electron diffraction (SAED) patterns acquired at two different positions of MoS₂ films with an aperture size of ~130 nm showed hexagonal symmetry for the MoS₂ structure (Figs. 2(c) and 2(d)). We also collected SAED patterns at multiple locations on the films and observed consistent hexagonal patterns with the same orientations. This indicates that the MoS₂ films possess a single crystalline domain size well above 130 nm, which is significantly larger than that of MoS₂ produced by the thermal evaporation technique (≤ 25 nm) as reported previously [11].

Raman spectroscopy was employed to characterize the as-deposited MoS₂ thin films. Two characteristic Raman peaks at 383.7 and 405.3 cm⁻¹ were observed on MoS₂ films which were assigned to the in-plane (E_{2g}¹) mode and the out-of-plane (A_{1g}) mode of MoS₂, respectively (Fig. 3(c)). The photoluminescence (PL) spectrum of MoS₂ thin films exhibited two peaks at ~625 and ~672 nm (Fig. 3(d)), which were similar to the values previously reported for bilayer and trilayer MoS₂ [16]. To evaluate

the uniformity of MoS₂ thin films over a large area, we performed Raman line scan over a length of 10 mm with a step size of 0.8 μ m, as shown in the Raman mapping image (Fig. 3(a)). The obtained MoS₂ thin films exhibited uniform peak intensities and frequency difference (~21.6 cm⁻¹), indicating the high uniformity of the films over a large area. Raman mapping was performed over an area of 35 μ m × 40 μ m and the Raman mapping image with the intensity of the A_{1g} mode displayed a uniform color distribution. This observation also indicates the high spatial uniformity of the MoS₂ thin films (Fig. 3(b)).

To study the chemical state and elemental composition of the obtained MoS₂ thin films, X-ray photoelectron spectroscopy (XPS) was employed for surface analysis. According to the XPS data, the Mo 3d_{5/2} (~229.9 eV) and Mo 3d_{3/2} (~233.0 eV) peaks were in line with Mo⁺ (Fig. 3(e)), and S 2p_{3/2} (~162.5 eV) and S 2p_{1/2} (~163.7 eV) peaks were assigned to S²⁻ (Fig. 3(f)). The binding energies for both Mo and S elements measured on the obtained samples with XPS were in agreement with the values of MoS₂ in 2H phase [17, 18]. The atomic ratio of Mo:S was estimated to be ~1:2, and no K⁺ was detected on the water-rinsed MoS₂ thin films, providing further evidence of the high purity of the obtained MoS₂ thin films.

To evaluate the electrical performance of the as-grown MoS₂ films, large arrays of back-gated FETs were fabricated on MoS₂ thin films transferred on 300 nm SiO₂/Si substrates with 10 nm In/50 nm Au as source and drain electrodes by electron beam lithography (EBL). The electrical measurement was performed at room temperature in vacuum (~10⁻⁵ mbar), and the transfer characteristic of the obtained MoS₂ thin films exhibited intrinsic n-type conduction with an on/off current ratio of ~10⁵ (Figs. 4(a) and 4(b)). We measured 12 MoS₂ FETs with a channel length of 10 μ m and a channel width of 100 μ m and the estimated on/off current ratios and mobility were within the range of 10⁴–10⁵ and 0.01–0.05 cm²·V⁻¹·s⁻¹, respectively (Fig. S5 in the ESM). The measured electrical performance of our MoS₂ thin films was comparable to that of polycrystalline MoS₂ prepared by the room temperature magnetron sputtering and laser annealing [19].

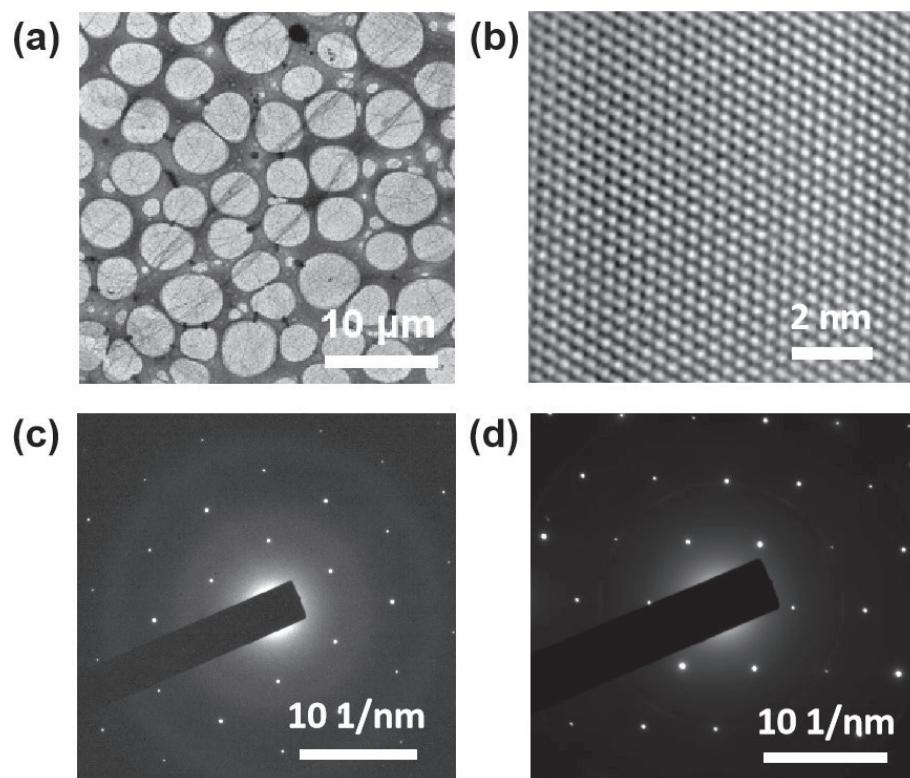


Figure 2 TEM characterizations on the obtained MoS₂ thin films. (a) Low-magnification TEM image of a MoS₂ thin film supported on holey carbon grid. (b) High-resolution TEM image of the MoS₂ thin film, showing hexagonal lattice. (c) and (d) SAED patterns collected at different positions on the same MoS₂ film.

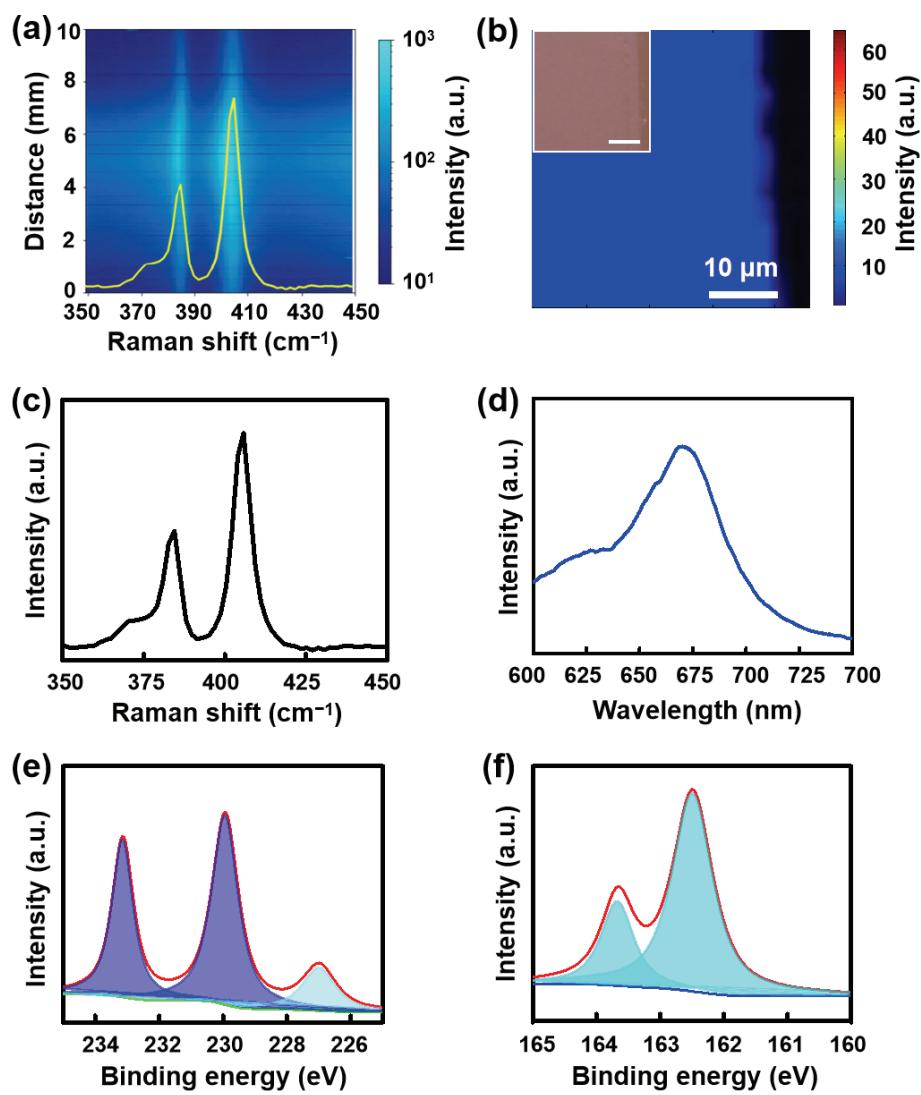


Figure 3 Spectroscopic characterizations on the obtained MoS₂ thin films. (a) The Raman line scanning image plotted with the intensities of 383.7 cm⁻¹ (E_{2g}^1) and 405.3 cm⁻¹ (A_{1g}) over a length of 10 mm with a step size of 0.8 μm . (b) Raman area mapping image. (c) Raman and (d) PL spectra of the MoS₂ thin films shown in the inset of (b). Scale bar in the inset of (b): 10 μm . XPS spectra of (e) the Mo 3d core level and (f) the S 2p core level of the MoS₂ thin films.

The relatively low mobility of our polycrystalline MoS₂ indicates that this kind of sample exhibits a lot of grain boundaries, and such MoS₂ is suitable for memristors as conductive filaments are more likely to form along the grain boundaries [20]. We fabricated vertical-structured memristors (Fig. 4(c)), where MoS₂ thin films with thickness of \sim 1.6 nm were sandwiched between bottom electrodes of Au (50 nm) and top electrodes of Ag (50 nm). Both electrodes were patterned by EBL followed by thermal evaporation of metals. The mechanism of the memory operation is based on the formation and the rupture of the conductive Ag filament through the MoS₂ thin film. When the positive bias voltage is applied to the Ag electrode, a conductive Ag filament is formed, causing the memristor to switch from high resistance state (HRS) to low resistance state (LRS), and the Ag filament is ruptured during the switch from positive bias voltage to negative bias voltage, leading to the transition from LRS to HRS [21]. The active area of the device was defined by the width of both electrodes, which was approximately 5 $\mu\text{m} \times$ 5 μm in our devices. The current–voltage (I – V) characteristics of a typical memristor displayed bipolar non-volatile memory switching property at a set voltage (V_{set}) of \sim 0.70 V and a reset voltage (V_{reset}) of \sim -0.24 V at a sweep voltage range from -0.30 to 0.70 V under ambient conditions, as shown in Fig. 4(d). Note that here the compliance current in the positive bias region was set to 10 μA to prevent MoS₂ from being broken down. The as-made memristor was in a

HRS for the V sweep from 0 to 0.70 V (sweep 1) and changed to a LRS at \sim 0.70 V. The device maintained the LRS during the sweep from 0.70 to 0 V (sweep 2) and then reset to a HRS at \sim -0.24 V during the sweep from 0 to -0.30 V (sweep 3), and remained the HRS during the sweep from -0.24 to 0 V (sweep 4). The switching voltage we measured is lower than the values reported in most of the literature (0.10–10 V) [21, 22].

This kind of MoS₂ is not only suitable for memristors, but also appropriate for efficient electrocatalysts for HER as the grain boundaries can serve as active sites for HER catalysis [23, 24]. We measured the electrocatalytic HER properties of our MoS₂ grown on carbon cloth in 0.5 M H₂SO₄ electrolyte at room temperature via three-electrode setup. The H₂ evolution overpotential at 10 mA·cm⁻² and the Tafel slope of our MoS₂ samples were estimated to be \sim 168 and \sim 78 mV·dec⁻¹ (Figs. 4(e) and 4(f), respectively, which were superior to the averaged values for 2H MoS₂ flakes (\sim 200 and \sim 90 mV·dec⁻¹, respectively) [25–28]. In addition to the enriched defects in our polycrystalline MoS₂, the formation of high-quality MoS₂–carbon cloth interfaces at a high temperature of 600 °C is advantageous for reducing contact resistance, leading to a relatively rapid charge transfer rate.

3 Conclusions

In summary, we designed a vacuum thermal evaporation system



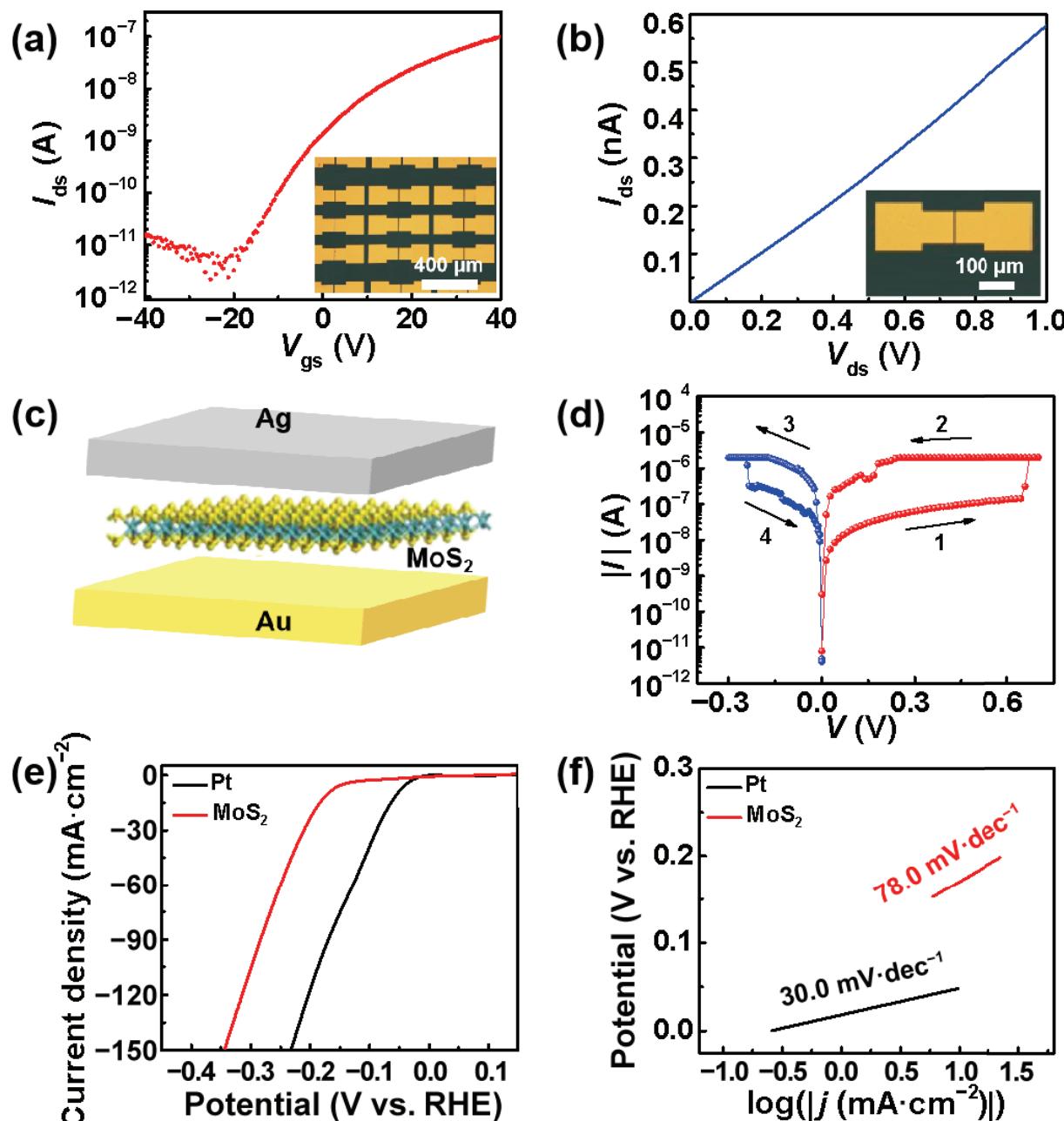


Figure 4 Potential applications of the obtained MoS₂ thin films in electronic devices and electrocatalysis. (a) The I_{ds} – V_{gs} curve measured at a bias voltage of 1 V for a typical back-gated FET fabricated with MoS₂ thin films as the channel material. (b) I_{ds} – V_{ds} curve measured at a V_{gs} of 40 V for the device in (a). Insets for (a) and (b): the optical images of an array of back-gated FETs and an individual FET, respectively. (c) Schematics for the MoS₂ memristor. (d) The I – V curve of a MoS₂ memristor measured under voltages between -0.30 and 0.70 V. The gate voltage was 0 V. The direction of the voltage sweeps 1–4 was indicated by the arrows. The sweep rate was $200 \text{ mV}\cdot\text{s}^{-1}$. (e) HER polarization curves for Pt and MoS₂ flakes grown on carbon cloth. (f) Tafel plots for Pt and MoS₂ flakes grown on carbon cloth.

for the large-scale preparation of 2D MoS₂ thin films. This homemade system allowed us to achieve highly uniform deposition of MoS₂ thin films over a large area, precise control over film thickness, patterned deposition, and sequential deposition of different materials. We obtained 1–3 layer polycrystalline MoS₂ thin films with a size of $50 \text{ mm} \times 50 \text{ mm}$ and a single crystalline domain size exceeding 130 nm . In addition to MoS₂, 2D WS₂ thin films and MoS₂–WS₂ vertical heterojunction were also obtained using this method, demonstrating its universality. We further applied the obtained MoS₂ thin films in electronic devices and electrocatalysts for HER. The as-deposited MoS₂ based vertical-structured memristor showed a lower switching voltage of ~ 0.70 V compared to other MoS₂ based memristors in most of previous reports. The HER overpotential at $10 \text{ mA}\cdot\text{cm}^{-2}$ of MoS₂ flakes deposited on carbon cloth was measured to be about 168 mV, which was superior to the averaged

values for 2H MoS₂ flakes reported previously. Our work not only opens up a new avenue for the large-scale and controllable synthesis of MoS₂ thin films by thermal evaporation technique but also demonstrates the potential applications of the obtained MoS₂ thin films in FETs, memristors and electrocatalysts.

4 Experimental section

4.1 Synthesis of K₂MoS₄ precursor

The solid precursor K₂MoS₄ was prepared following previously published method [29]. 1 g of ammonium tetrathiomolybdate (NH₄)₂MoS₄ (Sigma-Aldrich, powders, purity 99.9%) was dissolved in 10 mL of 2 M potassium hydroxide (Sigma-Aldrich, flakes, purity 81%) aqueous solution. The NH₃ produced during the synthetic reaction was evacuated by vacuum pumping for 20

h. A solution that contains K^+ and MoS_4^{2-} ions was obtained after the removal of NH_3 . Then 20 mL ethanol was added to the above solution to precipitate K_2MoS_4 . The obtained precipitation was thoroughly rinsed with ethanol to completely remove the possible KOH residue and then was freeze-dried and stored under vacuum.

4.2 Thermal deposition of 2D MoS_2 thin films

Thermal deposition of MoS_2 thin films was conducted in a homemade vacuum thermal evaporation system. To ensure the spatial uniformity of the precursor, we adopted a face-to-face precursors supply configuration (Fig. S1 in the ESM). The precursor K_2MoS_4 was uniformly dispersed on a mica substrate which was placed in the center of the heating stage. A freshly-cleaved mica substrate was fixed on the sample holder, which was placed face to face with the mica loaded with K_2MoS_4 precursor. Note that the distance between the substrate and the precursor was adjustable within a range of 7–180 mm, and the appropriate distance between them was ~14 mm for the thermal deposition of MoS_2 thin films. Before deposition, the chamber was evacuated by a turbo pump to reach a vacuum pressure of ~ 10^{-5} mbar at 25 °C. Then the furnace was heated to the temperature of 800–1000 °C with a heating rate of 20 °C·min⁻¹ and kept there for 10–30 min for the thermal deposition of MoS_2 films, followed by cooling down to room temperature naturally. During the thermal deposition process, the temperature at the growth substrate was in the range of 450–650 °C, and the pressure in the furnace was on the order of 10^{-5} – 10^{-3} mbar, which depended on the amount of the precursor and the temperature.

4.3 Transfer of 2D MoS_2 films

A poly (methylmethacrylate) (PMMA)-mediated transfer approach was used to transfer the as-grown 2D MoS_2 thin films to SiO_2/Si substrates and holey carbon grids for device fabrication and TEM characterization, respectively. First, a PMMA (AR-P679.04) thin film was spin-coated (3000 r.p.m., 1 min) onto a mica substrate covered with 2D MoS_2 films and backed at ~100 °C for 10 min. Then, the baked sample was soaked in deionized water at room temperature to allow for the water-assisted peeling of the PMMA film and underlying 2D MoS_2 films from the mica substrate. Next, the PMMA film with 2D MoS_2 thin films was attached immediately to the target substrate and dried in air. Finally, the PMMA film was removed by hot acetone.

4.4 Characterizations on the obtained 2D MoS_2 films

Optical images were captured with an Olympus BX 51M microscope. Raman and PL spectra were collected with a Horiba-Jobin-Yvon Raman system under 532 nm laser excitation at a power of 2 mW. The Raman mapping images were collected with a step of 0.8 μm . The Si peak at 520.7 cm^{-1} was used for calibration in the data analysis. AFM images were taken with a Bruker Dimension Icon in tapping mode. XPS measurements were performed with an ESCALAB 250Xi system using Al K α as the excitation source. All binding energies were referenced to the C 1s peak at 284.8 eV. SEM images were taken with the SEM system (JEOL JSM-7900F) at 5 kV. TEM images and SAED patterns were acquired with a JEOL JEM 2100 and a FEI Tecnai G20 at 200 kV.

4.5 Devices fabrication and measurement

2D MoS_2 films were transferred to SiO_2/Si substrates for device fabrications and electrical measurements. Source and drain electrodes were fabricated on 2D MoS_2 thin films by EBL followed by thermal evaporation of metals (10 nm In and 50 nm Au) and lift-off. The obtained FETs were measured using a four-probe station with Agilent B1500 A at room temperature under high

vacuum (10^{-5} mbar). The carrier mobility of FETs was estimated using the equation below

$$\mu = \frac{L}{W \times (\epsilon_0 \epsilon_r / d) V_{ds}} \times \frac{dI_{ds}}{dV_{gs}}$$

where L and W represent length and width of the channel, respectively; ϵ_0 and ϵ_r represent vacuum permittivity and relative permittivity, respectively; d is the thickness of the dielectric layer; I_{ds} , V_{ds} and V_{gs} represent source-drain current, source-drain voltage and gate voltage, respectively.

The fabrication of memristors was as follows. The bottom electrode was fabricated on SiO_2/Si substrates by EBL followed by thermal evaporation of metals (10 nm In and 50 nm Au) and lift-off. Then, the obtained bottom electrode was covered by transferred MoS_2 thin films. Finally, a top electrode was fabricated by EBL followed by thermal evaporation of Ag (50 nm) and lift-off in hot acetone. The memory operations were measured using a four-probe station with Agilent B1500 A.

4.6 Electrochemical measurements

All the electrochemical measurements were conducted with an electrochemical workstation (CHI660E). The HER was performed in 0.5 M H_2SO_4 solution using a three-electrode system with a saturated calomel electrode (SCE) as the reference electrode, a graphite rod as the counter electrode, and the carbon cloth covered by MoS_2 flakes as the working electrode. The effective surface area of MoS_2 flakes on carbon cloth was estimated to be ~50 mm^2 . The polarization curves were recorded by linear sweep voltammetry (LSV) at a scan rate of 5 $\text{mV}\cdot\text{s}^{-1}$ from -0.10 to -0.70 V versus the reference electrode. The reference electrode was calibrated against the reversible hydrogen electrode (RHE). All the potentials reported in our work were converted according to E (vs. RHE) = E (vs. SCE) + 0.2415 V. The current density was calculated by normalizing the surface area exposed to the electrolyte solution.

Acknowledgements

This work was supported by the National Natural Science Foundation of China (No. 22105114), China Postdoctoral Science Foundation (No. 2020TQ0163), Tsinghua-Toyota Joint Research Fund and Tsinghua-Jiangyi Innovation Special Fund (No. 2022JYTH01).

Electronic Supplementary Material: Supplementary material (for the set-up of a vacuum thermal evaporation system for the deposition of 2D MoS_2 thin films, the face-to-face precursor supply configuration and more data on the obtained MoS_2 thin films) is available in the online version of this article at <https://doi.org/10.1007/s12274-023-6114-z>.

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