Highly stable inverted organic photovoltaic cells with a V₂O₅ hole transport layer

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Abstract–The stability of the hole transport layer (HTL) in inverted organic photovoltaic cells is of great interest because the conventional HTL material, PEDOT:PSS, shows limited stability. In this work, solution processed vanadium pentoxide (V_2O_5) was adopted as the HTL, and the effect of annealing on the properties of the HTL was investigated. The inverted organic photovoltaic cell fabricated with V_2O_5 and annealed for 5 min at 165 °C showed the highest power conversion efficiency (PCE) of 3.92%, which is an enhancement of 16% relative to the cell with PEDOT:PSS (PCE=3.36%). The cell with V_2O_5 was also found to be more stable than the PEDOT:PSS cell, in which a 51% decrease in PCE was observed after 96 h. In contrast, over the same interval, the V_2O_5 device maintained a PCE 85% of the original value.

Keywords: Hole Transport Layer, Spin Coating, Inverted Organic Photovoltaic Cell, Vanadium Pentoxide

INTRODUCTION

Solar cells are a clean energy alternative that can convert sunlight directly to electricity, and are currently attracting attention [1-3]. Among various types of solar cells, organic photovoltaic cells (OPVs) are of great interest due to their low production cost, light weight, and potential for flexible device fabrication. Recently, OPVs with a conventional device structure have been reported to achieve power conversion efficiencies (PCE) up to 6% [4]. However, one of the major drawbacks of conventional OPVs is their poor stability under ambient air conditions, under which OPVs can only perform for few days. To resolve the performance and stability issues of OPVs, inverted structure OPVs has been proposed. The conventional OPV device structure is composed of an anode/hole transport layer/ active material/electron transport layer/cathode structure, whereas in inverted OPVs this structure is reversed. n-type semiconducting metal oxides such as ZnO or TiO2 deposited on fluorine-doped tin oxide (FTO) electrodes can act as an electron transport layer (ETL) for accepting electrons. The use of such metal oxides can dramatically improve the stability of inverted OPVs (IOPVs), and reduce the recombination rate at the interface [2,5].

A typical OPV consists of a bulk hetero-junction (BHJ) structure, in which the most commonly used hole transport layer (HTL) material is poly(4-styrene sulfonate)-doped poly(3,4 ethylenedioxythiophene (PEDOT: PSS). However, PEDOT: PSS has limited stability, and thus it has been replaced by transition metal oxides (TMO), which behave like p-type semiconducting materials. It has been proven that several TMOs show a high affinity towards hole extraction in light emitting diodes (LEDs) [6]. Recently, TMO thin films such as NiO, VO_{xx} MO_{xx} and WO_x were inserted as HTLs in OPVs

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[7-13]. These TMOs showed favorable optical and electronic properties such as high optical transmittance and low barrier height at the interface with the photoactive layer due to their high work functions. Furthermore, their broad technological compatibility with optoelectronics as well as a tendency toward low temperature deposition methods using solution based routes make them an effective alternative. Previous studies mostly used CVD, evaporation, sol-gel, and sputtering techniques under inert conditions for the coating of TMO thin films in OPVs. In contrast to earlier reports, we aimed to obtain V2O5 based IOPVs for which all fabrication steps were carried out under ambient air conditions, except for the last top electrode preparation, which was carried out under vacuum, and V2O5 at different annealing conditions. The experimental parameters for depositing V2O5 films using solution based routes were optimized for obtaining high photovoltaic performance. We report that a PCE of more than 3.9% can be achieved by using the solution based deposition of V2O5 films, while the stability of the V₂O₅ based IOPVs is superior to that of devices with PEDOT: PSS.

EXPERIMENTAL

Inverted OPVs were constructed with a cathode/ETL/photoactive/HTL/anode device structure, as shown in Fig. 1. Fig. 2 shows the band gap configurations of the materials used for the device. The holes generated from excitons in the photoactive material (P3HT : PCBM) are collected through the V_2O_5 . Spin coated V_2O_5 films were applied onto the photoactive material and were subsequently annealed. Annealing was under various conditions to investigate its effect on the device performance.

1. Materials

The substrate was FTO on glass (with a sheet resistance $\leq 15 \Omega$ / square and a transmittance >90%), purchased from the Korea Electronics Training Institute. The P3HT and PC₆₀BM were purchased from Rieke Metals and Nano-C, respectively. In addition, the hole

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Fig. 1. Schematic structure of V₂O₅-based IOPVs.



Fig. 2. Energy level distribution diagram of the materials involved in the IOPVs.

transport layer material (PEDOT: PSS) was purchased from Plexcore. All chemicals, including isopropyl alcohol (IPA), chlorobenzene, zinc acetate dihydrate (ZAD), ethanol amine (EA), ethanol, and vanadium oxytriisopropoxide, were purchased from Sigma-Aldrich and were 99% pure.

2. Synthesis of V₂O₅ Films

The V₂O₅ films were formed by the solution process method. The V₂O₅ films were spin-coated from an isopropanol solution of vanadium(V) oxitriisopropoxide with a 1:150 volume ratio. The resulting film thickness was 10 nm. The films were subsequently annealed at an optimized temperature of 165 °C at different time intervals (5, 10, 15 mins), and one of the samples was stored at room temperature for 60 min.

3. Fabrication and Characterization of IOPV Devices

FTO-coated glass substrates were cleaned by sonication, first in isopropyl alcohol and then in acetone, for 20 min. The ZnO ETL was prepared with equimolar solutions (0.23 M) of ZAD and EA in ethanol, which were mixed at 80 °C for 24 h. The solution was spin-coated on cleaned FTO substrates that were subsequently heated at 175 °C for 2 h. The resulting film thickness was 25 nm.

Then the photoactive layer (P3HT: PCBM) was prepared with a weight ratio of 1:0.8 in chlorobenzene that was spin-coated followed by room temperature solvent evaporation for 10 min. Subsequently, the HTL, PEDOT: PSS (1:1 vol%), was spin-coated on the surface of the active layer, and then annealed at 165 °C for 5-15 min. Five types of HTL samples were prepared: PEDOT: PSS, $V_2O_5 A$ (stored at room temperature for 60 min), $V_2O_5 B$ (annealed at 165 °C for 5 min), $V_2O_5 C$ (annealed at 165 °C for 10 min) and $V_2O_5 D$ (annealed at 165 °C for 15 min). The Ag electrode was thermally evaporated on top of the HTL through a shadow mask under vacuum. The effective area of the device was 3 mm².

The thickness and surface roughness of the films were measured by ellipsometry (Gaertner Scientific) and atomic force microscopy (AFM, Park system), respectively. In addition, the optical transmittance of the samples was measured by UV-visible spectrophotometry (VARIAN). The photovoltaic performance of the samples was evaluated using a solar simulator (Keithley 2400) under 100 mW/cm² AM 1.5G. Finally, the external quantum efficiency (EQE) was obtained using a 200 W Xe lamp with a grating monochromator, where light intensity was calibrated using a standard silicon solar cell as a reference (Mc-science).

RESULTS AND DISCUSSION

The V_2O_5 films were fabricated by using the solution processing method, and the thickness was optimized to 10 nm by spin coating. Thin films are preferred for solar cell applications since they reduce the interface between the photoactive materials and HTL, thereby enhancing the hole collection capability. However, in our work, the IOPV device with a HTL thickness greater than 10 nm showed poor photovoltaic performance due to increased series resistance at the interphase and longer hole extraction times [14]. In addition, we used room temperature storage conditions and annealing at different time intervals of 5, 10, 15 min at a temperature



Fig. 3. Transmittance spectra of PEDOT: PSS, V_2O_5 A film stored at room temp. for 60 mins, V_2O_5 B film annealed at 165 °C for 5 mins, V_2O_5 C film annealed at 165 °C for 10 mins and V_2O_5 D film annealed at 165 °C for 15 mins.

Devices	$V_{OC}(V)$	J_{SC} (mA/cm ²)	FF (%)	$R_{shunt} \left(\Omega \right)$	$R_{series} \left(\Omega \right)$	Best PCE (%)	Average PCE (%)
PEDOT: PSS	0.61	10.31	53.13	11296	312	3.36	3.14±0.22
$V_2O_5 A$	0.60	9.69	58.46	12858	326	3.42	3.31±0.11
$V_2O_5 B$	0.61	10.68	59.83	15868	298	3.92	3.83±0.09
$V_2O_5 C$	0.60	9.44	53.30	10476	397	3.01	2.82±0.19
$V_2O_5 D$	0.60	9.04	52.42	9258	368	2.85	2.67±0.18

Table 1. Photovoltaic properties of the IOPVs with different HTLs

 V_2O_5 A film stored at room temp. for 60 mins, V_2O_5 B film annealed at 165 °C for 5 mins, V_2O_5 C film annealed at 165 °C for 10 mins and V_2O_5 D film annealed at 165 °C for 15 mins

of 165 °C, which caused slight changes in the composition of the films, as evidenced by the XPS results discussed later. We restricted the annealing to 165 °C because higher temperatures resulted in the thermal degradation of the photo-active material, thereby lowering the device performance [15]. Generally, in UV-spectroscopic analyses, lower transmittance spectra indicate the formation of a thicker film. Fig. 3 shows the transmittance spectra of the PEDOT: PSS and the V_2O_5 films. It is evident that the PEDOT: PSS exhibits low transmittance relative to the V_2O_5 films. The transmittance of the V_2O_5 films increased with increasing annealing time and temperature, which is indicative of the conversion of the precursor to thin V_2O_5 films by removal of the solvent [16].

The photovoltaic performance parameters of the IOPVs with PEDOT: PSS and V_2O_5 films (the latter with different annealing temperatures and times) are summarized in Table 1. The corresponding J-V curves are shown in Fig. 4. The reference device, i.e., the one with the PEDOT: PSS film, shows an open circuit voltage (V_{oc}) of 0.61 V, short circuit current density (J_{sc}) of 10.31 mA/cm², fill-factor (FF) of 53.13, and PCE of 3.36%. In comparison to the reference device, the performance of the device V_2O_5 A, stored at room temperature for 60 mins, showed elevated performance parameters but was nevertheless inferior to device V_2O_5 B. The performance of device V_2O_5 B was considerably improved because of the annealing at 165 °C for 5 mins applied to the HTL. Also, the PCEs



Fig. 4. J-V characteristics of IOPVs with different HTLs. V_2O_5 A film stored at room temp. for 60 mins, V_2O_5 B film annealed at 165 °C for 5 mins, V_2O_5 C film annealed at 165 °C for 10 mins and V_2O_5 D film annealed at 165 °C for 15 mins.

of the devices decreased when the annealing time was increased to 10 and 15 mins. This was due to the prolonged annealing of the photoactive layer, which degrades when annealed for longer times, together with the HTL. The highest PCE was achieved from device V_2O_5 B, which exhibited a V_{cc} of 0.61 V, J_{sc} of 10.68 mA/cm², FF of 59.83, and PCE of 3.92%. In the case of the IOPVs annealed at 165 °C for 10 and 15 mins (V₂O₅ C and V₂O₅ D), the PCE decreased significantly. As shown in Table 1, the shunt resistance (R_{sh}) decreased while the series resistance (Rs) increased with the increase in annealing time in V_2O_5 C and V_2O_5 D, when compared with device V_2O_5 B. It is well known that the R_s value is related to the contact resistance at the interfaces and the bulk resistance of all interfacial layers in the entire IOPV. In addition, Rsh reveals the loss of charge carriers due to current leakage pathways and charge recombination in the bulk or at the interface [17,18]. Furthermore, the V_{α} values of all the devices were almost identical as there were no changes in the properties of the ETL. However, the J_{sc} and FF improved in the device with the optimized HTL, i.e., device V2O5 B.

One of the advantages of spin coating is its ability to produce



Fig. 5. AFM images of the surface of (a) V_2O_5 A film, stored at room temp. for 60 mins. (b) V_2O_5 B film, annealed at 165 °C for 5 mins. (c) V_2O_5 C film, annealed at 165 °C for 10 mins. (d) V_2O_5 D film, annealed at 165 °C for 15 mins.



Fig. 6. XPS spectra of (a) V₂O₅ A film, stored at room temp. for 60 mins. (b) V₂O₅ B film, annealed at 165 °C for 5 mins. (c) V₂O₅ C film, annealed at 165 °C for 10 mins. (d) V₂O₅ D film, annealed at 165 °C for 15 mins.

uniform films [2,5]. Fig. 5 shows AFM images of the V_2O_5 films which indicate that, as the annealing temperature and time were increased, the RMS value increased slightly from 0.26 nm to 0.30 nm. This indicates a potential risk for the formation of surface defects between the active layer and HTL as the annealing time is increased. These defects introduce current leakages at the interface and affect the R_s values, as reflected in our results shown in Table 1 [19].

Next, to confirm the successful formation of V2O5 and possibility of selective hole transport and collection, compositional analysis was performed through XPS measurement as shown in Fig. 6. The main peak of sample V₂O₅ A, V2p_{3/2}, by spin coating is at 516.4 eV, which corresponds to the binding energy of VO₂ and indicates that the oxidation state of vanadium was V⁴⁺ [14,20]. This result suggests a reason for the low photovoltaic performance of the device V₂O₅ A, in that the potential for the creation of oxygen vacancies will lead to poor hole extraction at the interface. In the case of sample V₂O₅ B, the corresponding V2p_{3/2} peak was at 516.9 eV, which matches the binding energy of V2O5 and confirms the complete oxidation of the precursor to V5+. This explains the considerably higher device performance of V2O5 B relative to the other fabricated devices. As for samples V2O5 C and V2O5 D, the corresponding V2p_{3/2} peaks were at 516.5 and 516.6 eV, respectively, which is in close agreement with the binding energy of V2O5. This suggests that prolonged annealing stabilizes the precursor at the V_2O_5 state. However, increased annealing times resulted in higher carbon contents in the samples. Notably, the carbon content in V2O5 A, V₂O₅ B, V₂O₅ C, and V₂O₅ D was 6.7, 7.7, 8.7, and 9.7%, respectively. The increased carbon content in samples V₂O₅ C and V₂O₅



Fig. 7. EQE spectra of IOPVs with PEDOT: PSS and V₂O₅ B film annealed at 165 °C for 5 mins.

D led to poor photovoltaic performance [14]. Collectively, these results indicate that the chemical binding state of the spin coated V_2O_5 HTL is close to that of sputtered V_2O_5 films [21].

The external quantum efficiency (EQE) of selected devices is shown in Fig. 7. I EQE of device V_2O_5 B is slightly better compared to the device with the PEDOT : PSS HTL. And it is increased significantly from 300 to 450 nm compared to device with PEDOT : PSS HTL. The improvement in EQE suggests a higher minority carrier lifetime, which results in an overall increase of the J_{sc} and



Fig. 8. Stability study of the power conversion efficiency of IOPVs with a PEDOT: PSS and a solution processed V_2O_5 B film annealed at 165 °C for 5 mins. The samples were kept in ambient air.

PCE [22].

The environmental stability of the devices fabricated with V2O5 B and PEDOT: PSS HTL was investigated under exposure to ambient air conditions. The temperature and humidity level of the laboratory environment were 25 °C and 50%, respectively. The PCEs of the devices were measured at different time intervals. According to the results depicted in Fig. 8, the solar cell PCE decreased rapidly in the device with the PEDOT: PSS layer compared to the device with the V2O5 layer. A decrease of 51% was observed in the PEDOT: PSS device after 96 h, whereas the PCE of the V_2O_5 B device was maintained at 85%. Therefore, the V2O5 film behaves as both an effective hole transport layer and a layer protecting the photoactive region from degradation. The accelerated degradation of the PEDOT: PSS based device was apparently due to the acidic nature of its molecules, and to its tendency to absorb moisture from the surrounding environment [20,23]. Furthermore, V₂O₅ thin films are more hydrophobic than PEDOT: PSS, which facilitates better device performance.

CONCLUSION

IOPVs were fabricated with solution processed V₂O₅ hole transport layers and ZnO electron transport layers synthesized using sol-gel methods. The inverted structure took the form FTO/ZnO/P3HT: PCBM/V₂O₅/Ag. We found the optimal thickness of the ZnO and V₂O₅ layers to be 25 nm and 10 nm, respectively. The highest PCE of 3.92% under AM 1.5 illumination at 1,000 W/m² was achieved under the optimized annealing condition of 165 °C for 5 mins. This device also showed high stability under ambient air conditions when compared with the reference PEDOT: PSS device over a time span of 96 h.

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