ORIGINAL RESEARCH ARTICLE

Gold (Au)‑Doped Lead Sulfde‑Polyvinyl Alcohol (PbS‑PVA) Nanocomposites for High‑Performance, Flexible Memristors

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Abstract

This study demonstrates fexible non-volatile resistive switching memory using gold (Au)-doped PbS-PVA nanocomposites deposited on patterned ITO-coated fexible PET substrate. X-ray photoelectron spectroscopy study showed the presence of oxidation states of lead (Pb), sulphur (S) and gold (Au) ions in the nanocomposites. The fabricated Al/Au-PbS-PVA/ITOcoated fexible PET substrate device showed a bistable non-volatile fexible memristor and revealed superior behaviour in the current ratio (I_{ON}/I_{OFF}) of ON-state and OFF-state compared to the control device Al/PbS-PVA/ITO-coated flexible PET substrate due to the presence of Au nanoparticles in PbS-PVA composites. The incorporation of Au nanoparticles in PbS-PVA nanocomposite increases charge carrier injection that further enhances the current ratio (I_{ON}/I_{OFF}) between the ON- and OFF-states of the device up to 10^6 with lower operating voltage at \pm 2V. The reversible resistance switching between OFFstate and ON-state was repeated for 100 cycles, and the two resistance states showed endurance and stability. Even after the applied bias was removed, the device maintained its OFF- and ON-states, showing bistable non-volatile memory. The fabricated flexible memristor was measured for retention, stability and flexibility by constantly bending the device from 70° bending angle to 120° bending angle and without bending (180°). Interestingly, I-V characteristics after bending the device from 180° to 70° remained stable and unvariable. Therefore, the fabricated fexible memristor holds robust potential to be used in advanced fexible electronic applications.

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Graphical Abstract

Keywords Au/PbS-PVA nanocomposites · high-performance resistive switching · flexible non-volatile memory device

Introduction

Resistive random access memory (RRAM) is an appropriate candidate for next-generation memory devices. RRAM devices possess advantages of low operation voltage, large storage density and less power consumption. Resistive switching behaviour is observed in memory devices which tends to change resistance when an appropriate voltage is applied across the electrodes. To improve the device performance, it is important to identify the exact physical transport mechanism and how it relates to resistive switching phenomena. The most commonly noticed conduction mechanisms are Poole-Frenkel emission, Schottky emission, space-charge-limited conduction (SCLC), trap-assisted tunnelling and hopping conduction. Resistive switching layers have been so far developed in inorganic, organic and hybrid materials.¹. Hybrid nanocomposite materials comprising inorganic semiconducting in an organic matrix are an emerging class of new materials that can be used in a variety of applications including sensors,^{[2](#page-12-1)} displays,³ photovoltaics,^{[4](#page-12-3)} photonic devices,^{[5](#page-12-4)} non-volatile memory devices^{[6](#page-12-5)} and neuromorphic computing systems.^{7[,8](#page-12-7)} Boybat et al.^{[9](#page-12-8)} demonstrated a work based on phase change memory devices, providing a signifcant step towards the realization of large-scale energy efficient neuromorphic computing systems. Using these memristive devices, synaptic efficacy can be realized using Ohm's law, inducing two essential synaptic attributes, namely potentiation and depression. An increase and decrease in the synaptic weight is referred to as potentiation and depression, respectively. Lead sulphide (PbS) is an essential semiconductor material with many eminent optical and electrical properties and it has been extensively applied in nonlinear optical devices, IR detectors and display devices.¹⁰ PbS is binary IV-VI semiconductor material with a narrow band gap ~ 0.41 eV at room temperature, and a large exciton Bohr radius (~18 nm). PbS is direct band gap semiconductor material, and self-assembled PbS quantum dots (QDs) are fabricated at low temperature to ensure the uniform distribution of QDs in the resistive switching memory devices. $\frac{11}{11}$ The advantages of growing hybrid semiconductor nanostructures inside an insulating polymer matrix include the prevention of oxidation and coalescence of the nanostructures, as well as acting as an outstanding capping agent for the memory device.¹² Hmar et al. performed the flexible studies of high dielectric based on Al/PbS-PVA/fexible ITO coated on PET substrate devices at different bending angles.¹⁰ Sarma et al. demonstrated the synthesis of PbS QDs embedded in polyvinyl alcohol (PVA) matrix for memory application.¹³ Chen et al. demonstrated development of advanced high-density data storage technology utilizing PbS QDs in conducting polymer poly(methyl methacrylate) (PMMA) matrix.¹⁴ Various reports suggest that metal nanoparticles on the surface of semiconductors can boost the charge separation and polarization with an increase in charge carrier in the hybrid semiconductor nanostructures.¹⁵ Metal nanoparticles may also result in enhancing the localized

electric field in the memory device.¹⁶ The incorporation of metallic nanoparticles such as Ag, Au, Mo, Co, and Cu in semiconductor nanostructures contribute to charge-trapping and can effectively improve the resistive switching memory.¹⁷ Gold nanoparticles have been noted for their optical resonance in the visible range as well as the local feld enhancement of light interaction with the resonant system. 18 The photonic properties of gold are appealing for materials applications, biochemical and biomedical usage.¹⁹ Au nanoparticles exhibit the remarkable potential of enhancing the electrical performance of a memory device by either trapping electrons or holes. 20

In this paper, we synthesize Au nanoparticles doped in PbS-PVA nanocomposites and deposited Au-doped PbS-PVA nanocomposites on ITO-coated fexible PET substrate. We also study the fexible non-volatile resistive switching memory device based on Au-doped PbS-PVA nanocomposites deposited on ITO-coated fexible PET substrate. PbS demonstrates strong quantum confinement effects attributed to the small effective masses of its electrons and holes and its high dielectric constant. PVA has excellent properties such as isolating dielectric and supporting flm for transportation of charge carriers and preventing leakage of current. The incorporation of Au nanoparticles in PbS-PVA composite enhances the ON-OFF current ratio of a device up to $10⁶$ with low voltage at \pm 2V. The charge transport mechanism of the fabricated device is possibly contributed to by both trapping and detrapping of SCLC and the formation and rupture of flaments. This non-volatile memory device exhibits bipolar resistive switching behaviour and is a promising candidate for fexible resistive non-volatile memory. Attractively, the fabricated Al/Au-PbS-PVA/ITO-coated fexible PET substrate demonstrates non-volatile bistable resistive random access memory (RRAM). Because the fabricated memory device possesses retention, stability and fexibility properties, it has the potential to be used in advanced fexible non-volatile RRAM memory application.

Experimental Details

Preparation of Gold Nanoparticles

Gold nanoparticles (Au NPs) were prepared using Frens method. 21 In this method, 50 ml aqueous solution of chloroauric acid ($HAuCl₄$) was heated at boiling temperature. After that, 0.8 ml of trisodium citrate solution was added to the previous solution with continuous stirring and the boiling temperature is constantly maintained. The boiling solution was blue in colour that changed to reddish pink, indicating the formation of Au NPs.

Preparation of Au/PbS‑PVA Nanocomposites

Au/PbS-PVA nanocomposites were prepared using chemical bath deposition (CBD) .^{[22](#page-13-5)} A transparent polyvinyl alcohol (PVA) solution was made by dissolving 4 g of solid PVA (C_2H_4O) _n (where $n = 1700$) in 150 ml DI water, then heating and stirring for 2 h at 95°C. When the temperature dropped to 40°C, an aqueous solution was added to the previous solution, containing 4.98 mmol lead acetate (99%, Nice) and 0.02 mol NaOH (97%, Merck). After a few minutes, 11.82 mmol of $CH₄N₂S$ (thiourea) was added to the solution. The Au/PbS-PVA nanocomposites were prepared when Au solution was added to the solution of PbS-PVA in 1:10 [Au: PbS-PVA] concentration ratio. Both the PbS/PVA flms and Au/ PbS-PVA flms were deposited on the fexible ITO-coated polyethylene terephthalate (PET) substrates by dipping for 30 min into the PbS/PVA composite solution and Au/PbS-PVA composite solution at 80°C bath temperature, respectively. The prepared thin flms were dried in air for a few hours and kept for further use.

Device Fabrication

For the bottom electrode (BE), ITO-coated fexible PET substrate was used. Au-PbS/PVA nanocomposites were deposited on patterned ITO-coated fexible PET substrate using chemical bath deposition (CBD). The samples were left to dry in open air. Then, aluminium metal was deposited as the top electrode (TE) using thermal evaporation through a shadow mask with an active area of 2×10^{-6} m².

Results and Discussion

Microstructural Characterization

Morphologies of PbS-PVA nanocomposites and Au-doped PbS-PVA nanocomposites were observed by high-resolution transmission electron microscopy (HRTEM). Figure [1a](#page-4-0) shows the plane view TEM micrographs of PbS nanoparticles (NPs) synthesized in PVA matrix. The TEM micrograph revealed the formation of PbS NPs embedded in PVA matrix showing PbS NPs with average size of ~7.5 nm. The PbS nanoparticles are quasi-spherical in shape and are well dispersed in the PVA matrix with minimal aggregation. The number of particles per unit area is found to be 6×10^{3} m⁻². The selected area electron difraction (SAED) pattern (inset of Fig. [1](#page-4-0)a) clearly shows the polycrystalline nature of PbS NPs from the formation of circular rings, and the prominent difraction patterns in the micrograph are indexed as (200), (220) and (311) planes of face centred cubic (fcc) rock salt PbS crystal structure. Figure [1](#page-4-0)b represents typical HRTEM micrograph of Au-doped PbS in PVA matrix with average size of \sim 17 nm. The number of Au-doped PbS inside PVA matrix per unit area is found to be 2×10^6 m⁻². The SAED pattern confrms face centered cubic (FCC) Au and rock salt PbS phases of the Au-doped PbS-PVA nanocomposites (the top right inset of Fig. [1b](#page-4-0). The SAED pattern depicts (200), (220) and (311) difraction rings with the inter-planar spacing (d_{hkl}) of 0.29 nm, 0.20 nm and 0.17 nm, respectively, of rock salt Au/PbS crystal structure (JCPDS File Card $05-0592$.²³ The SAED also shows a ring with index (111), which is due to the contribution from the cubic Au phase which gives rise to a difraction peak in the x-ray diffraction (XRD) pattern. High-resolution transmission electron microscopy (HRTEM) image (the bottom right inset of Fig. [1](#page-4-0)b) reveals that Au/PbS NPs have a high-crystalline structure with an inter-planar spacing in the lattice fringes of 0.235 nm (JCPDS File Card 04-0784), 24 indicating the formation of Au nanoclusters oriented in the (111) plane of the fcc lattice. The density of the nanoparticles increases with inclusion of Au NPs which increases polarization leading to a high current ratio of resistive switching memory. Energy-dispersive x-rays (EDX) spectroscopy analysis measurement was employed to investigate the chemical composition of PbS-PVA nanocomposites and Au/PbS-PVA nanocomposites. The EDX spectrum of Au/PbS-PVA nanocomposites is shown in Fig. [1c](#page-4-0). The EDX spectrum confrms the presence of lead (Pb), sulphur (S), and gold (Au) in Au/PbS-PVA nanocomposites. The elemental percentage of lead (Pb), sulphur (S), and gold (Au) is depicted in the inset of Fig. [1](#page-4-0)c. The additional elements C and O arise from carbon-coated copper grids and PVA polymer. Figure [1](#page-4-0)d shows the cross-sectional SEM image of Al/Au-PbS-PVA nanocomposites/ITO-coated on fexible PET substrate. The FESEM micrograph clearly indicates the ITO bottom electrode, Au-PbS-PVA nanocomposites and Al top electrode with sharp interfaces. The thicknesses of the ITO bottom electrode, Au-PbS-PVA nanocomposite active layer, and Al top electrode are estimated to be 150 nm, 380 nm, and 110 nm, respectively. Figure [1](#page-4-0)e and f shows the particle size distribution histogram of PbS-PVA nanocomposites and Au/PbS-PVA nanocomposites. The average particle size of PbS-PVA nanocomposites and Au/PbS-PVA nanocomposites is found to be \sim 7.5 nm and \sim 17 nm, respectively. The surface topography of PbS-PVA nanocomposite and Au/ PbS-PVA nanocomposite flms deposited on ITO-coated fexible PET substrates was studied by atomic force microscopy (AFM) in tapping mode. Figure [2a](#page-5-0) and b shows the plane view of surface morphology of PbS-PVA nanocomposite and Au/PbS-PVA nanocomposite flms, respectively. The surface roughness of PbS-PVA nanocomposite and Au/ PbS-PVA nanocomposite flms was estimated from AFM topography with a 200 nm \times 200 nm scan area. The calculated RMS roughness of PbS-PVA nanocomposites and Au/ PbS-PVA nanocomposites is reported to be \sim 39 nm and

 (b)

 (d)

Fig. 1 (a) Plane view TEM micrographs of PbS nanoparticles synthesized in PVA matrix. Selected area electron difraction pattern (SAED) of PbS nanoparticles (NPs) embedded in PVA matrix (inset). (b) Typical TEM micrograph of Au NPs decorated PbS in PVA matrix. Selected area electron difraction pattern (SAED) of Au/PbS-PVA nanocomposites (top right inset). HRTEM image showing the

Au/PbS-PVA nanocomposites lattice fringes (bottom right inset). (c) Energy dispersive x-ray (EDX) spectra of Au/PbS-PVA nanocomposites (Elemental percentage in the inset). (d) The cross-sectional SEM image of Al/Au-PbS-PVA/ ITO coated fexible PET substrate. (e) Particle size distribution for PbS in PVA matrix. (f) Au/PbS-PVA nanocomposites

 \sim 67 nm, respectively. The surface roughness of Au-PbS/ PVA flms shows higher value due to the presence of Au NPs in PbS-PVA nanocomposite. X-ray difraction (XRD) measurements were carried out to identify the crystal structure of lead sulfde (PbS) embedded in PVA matrix and reveal the presence of Au NPs embedded in the PbS-PVA nanocomposite sample. The XRD spectra of undoped PbS-PVA nanocomposites and Au-doped PbS/PVA nanocomposites deposited on ITO-coated glass substrates are shown in Fig. [3](#page-5-1)a. The XRD result shows that undoped PbS-PVA

Fig. 3 (a) X-ray (Cu-Kα radiation) difraction patterns of undoped PbS-PVA nanocomposites and Au-doped PbS-PVA nanocomposites deposited on ITO coated glass substrates. (b) UV-visible absorption

spectra of undoped PbS-PVA nanocomposites and Au-doped PbS-PVA nanocomposites. (c) $(ahv)^2$ vs hv plot for determining the band gap

nanocomposites and Au-doped PbS-PVA nanocomposites are highly crystalline. For PbS-PVA nanocomposites sample, the dominant peaks at 2*θ* values of 26.09°, 30.18°, 43.08°,51.04° and 53.55° which are assigned to (111), (200), (220), (311) and (222) planes of PbS with lattice parameters $a = b = c = 5.931$ Å (JCPDS File Card 78-1901) indicating the fcc rock salt structure.^{[25](#page-13-8)} The XRD spectra of Au/PbS-PVA depicts four additional peaks at 38.1°, 44.3°, 64.5° and 77.7° as assigned to (111), (200), (220), (311) associated with fcc structure of gold. The intense diffraction at angle 38.1° is due to preferred growth orientation of gold along (111) direction. XRD peaks of PbS do not show any shift and broadening which indicates that crystal structure of PbS remains unaltered with the inclusion of gold nanoparticles. The XRD results are in consistent with HRTEM results.

Optical Properties

For the purpose of investigating the infuence of Au doping on the optical absorption of PbS-PVA nanocomposites, ultraviolet (UV)-visible absorption spectroscopy was used to study the change of absorption. Figure [3](#page-5-1)b shows the absorption spectra of undoped PbS-PVA nanocomposites and Audoped PbS-PVA nanocomposites deposited on fexible PET substrates. The broad absorption peak in the wavelength range of 400–2600 nm can be attributed to the intrinsic band gap of PbS nanoparticles $(\sim 1.15 \text{ eV})$, which is generally related to electronic transition from the valance band to conduction band. 26 It is clearly seen that the presence of Au ions made the absorption band edge shift toward higher wavelength, namely a redshift, which means the Au-doped PbS-PVA nanocomposite possessed a narrower band gap that is good for a redshift of light adsorption and increases electrical conductivity.²⁵ The optical band gaps (E_{ρ}) of the prepared samples have been calculated from UV-Vis spectroscopy by using the following Tauc's formula²⁷:

$$
(\alpha h v)^{\frac{1}{n}} = A(hv - E_g)
$$
 (1)

where α is the absorption coefficient, *h* is the Planck constant, ν is the frequency and it is the reciprocal of wavelength. *A* is the absorbance of the sample, E_g is the optical band gap, and the exponent *n* depends on the type of transition. As PbS is a direct band gap semiconductor, $n = \frac{1}{2}$ for direct allowed transition. The optical band gap values are estimated by extrapolating the linear part of the plot $(ahv)^2$ versus (hv) on the energy axis (hv) , as shown in the inset of Fig. [3c](#page-5-1). The calculated optical band gap energy for pristine PbS-PVA nanocomposite flm and Au-doped PbS-PVA nanocomposite flm are found to be 1.15 eV and 0.95 eV, respectively. The narrowing of the optical band gap energy of Au-doped PbS-PVA nanocomposite flm indicated an obvious redshift of light adsorption. The reduction of the

optical energy gap is due to intense doping of Au NPs. This may be attributed to the band shrinkage efect because of increasing charge carriers. 28 Figure [4](#page-7-0)a represents the fullrange XPS spectrum of Au-doped PbS-PVA nanocomposites which demonstrates clearly the presence of gold (Au), lead (Pb), sulfur (S), carbon (C) and oxygen (O) in the sample. Figure [4b](#page-7-0) represents the detailed scan of Au 4f peaks which can be decovulated to two peaks, Au $4f_{5/2}$ and Au $4f_{7/2}$, at binding energies 87.1 eV and 83.4 eV, respectively. The binding energy of 4f electrons in Au is shifted to the lower energy side as compared to the bulk which indicates that the surface charge of Au nanoparticles is partially negative without any chemical bonding with PbS. The HOMO level of PbS is near the Fermi level of Au which may result in transfer of electrons from PbS to gold that may result in an increase in charge density on the surface of Au NPs. 29,30 29,30 29,30 29,30 A detailed scan of S2p spectrum as shown in Fig. [4](#page-7-0)c can be ftted into four peaks. The two peaks at 162 eV and 163 eV can be attributed to $2p_{3/2}$ and $2p_{1/2}$ of the lower valence state of S^{2-} . The characteristic peak observed around 162 eV may be assigned to sulphide S2p which indicates the formation of PbS fcc structure in accordance with XRD results. The binding energy at 167 eV and 168 eV is from S^{6+} 2p_{1/2} and S^{6+} $2p_{3/2}$, respectively, suggesting that S is in the highest oxidation state $(+6)$. These peak positions belong to the strongly oxidised sulphate group. Figure [4d](#page-7-0) presents the XPS spectra of two deconvulated Pb 4f peaks corresponding to the Pb $(4f_{70})$ and Pb $(4f_{50})$ core levels of the Pb²⁺cations associated with PbO formation. They are located at the binding energies of 137 eV and 142 eV, respectively. The energy spacing between two position peaks is 5 eV may refect binding between Pb and S atoms. In other words, the binding energy peak around 137 eV is typically assigned to Pb-S bonding. $31-33$ $31-33$

Electrical Characterization

The electrical characteristics of the non-volatile memory device were investigated by using current-voltage (I-V) source measurements. The samples fabricated for investigations are (i) Al/PbS-PVA/ITO/fexible PET substrate (denoted as D1), (ii) Al/Au/PbS-PVA/ITO/fexible PET substrate (denoted as D2). For electrical measurements, Al top electrodes (TE) were linked to the negative ('-') terminal of the source meter, and ITO bottom electrodes (BE) were connected to the positive (4) terminal of the source meter. The schematic diagram of the fexible D2 memory device is shown in Fig. [5](#page-8-0)a. Figure [5](#page-8-0)b shows the I-V switching characteristics of the D2 memory device. The external stimulation is provided to the device with voltage sequence of $0V \rightarrow +2V \rightarrow 0V \rightarrow -2V \rightarrow 0V$. The compliance current is set at 10 mA to prevent permanent dielectric breakdown of the D2 device (Icc). The electrical hysteresis behaviour

Fig. 4 (a) Typical XPS survey scan spectrum of Au/PbS-PVA nanocomposites. The high resolution XPS spectra of (b) Au 4f peak (c) S 2p peak and (d) Pb 4f peak

is observed in electrical measurements which is essential for resistive switching memory. Initially, the device was in a high-resistance state (HRS) and the device current was 5 nA at a read voltage of 0.2 V. When the device is subjected to forward voltage sweep, the current increases gradually along the sweeping direction and a rapid increase in current from 6.7×10^{-8} A to 2×10^{-2} A is observed at a voltage of $+1$ V, indicating the resistive switching from HRS to a low-resistance state (LRS). This switching process from OFF-state (i.e., HRS) to ON-state (i.e., LRS) is referred to as the 'SET' process, and in other words, termed as the writing process. After reaching LRS, the SET state is maintained from $+2.0$ V to -1.4 V, while reverse sweeping. The device then switches from LRS to HRS at -1.4 V. This transition from LRS to HRS is referred to as the 'RESET' process. In other words, this process is known as erasing. The SET voltage and RESET voltage occurs at $+1$ V and −1.4 V, respectively. The existence of two resistance states, namely, high resistance state (i.e., OFF state) and low resistance state (i.e., ON state) was maintained even after removal of the applied bias voltage, exhibiting bistable non-volatile memory behaviour of the D2 device. The reversible resistance switching between HRS and LRS was repeated for 100 cycles and the two resistance states showed stability. The current ratio (I_{ON}/I_{OFF}) between the ON- and OFF states is about 10^6 in magnitude at a reading voltage of 0.5 V as shown in Fig. [5b](#page-8-0). The high value of current switching ratio confrms the presence of multilevel cells for storage that contribute to high current density memory device. Thus, the device has potential for application in fexible memory devices.[34,](#page-13-16)[35](#page-13-17)

In order to understand the effect of gold nanoparticles on the D2 memory device, a control sample, device D1, has been fabricated and corresponding current-voltage

Fig. 5 (a) Schematic diagram of Al/Au-PbS-PVA/ITO coated fexible PET substrate device. I-V switching characteristics of the (b) Al/Au-PbS-PVA/ITO coated fexible PET substrate device and (c) Al/PbS-PVA/ITO coated fexible PET substrate

characteristics were measured for comparison as shown in Fig. [5c](#page-8-0). Applying the positive bias to device D1, the current started increasing gradually and at a voltage of 1V the resistance changes from HRS (i.e., OFF state) to LRS (i.e., ON state). This ON state is maintained from 2 V to -2 V during reverse sweeping. Further, the current decreases suddenly at a voltage of -1.45 V. The current ratio (I_{ON}/ I_{OFF}) for device D1 is 10³. The performance of device D1 was inferior as compared to the D2 memory device. The higher ON/OFF current ratio up to $10⁶$ achieved for device D2 is due to inclusion of gold nanoparticles in the PbS-PVA matrix. The enhancement in resistive switching behaviour may be attributed to the increased of charge injection that further improves storage properties of PbS nanoparticles inside the polymer PVA matrix.^{[36](#page-13-18)}

Conduction Mechanism

By ftting the I-V characteristics in a log-log scale, the conduction mechanism of the D2 memory device was explored in depth. Figure [6](#page-9-0)a and b shows linear ftting of I-V characteristics in the SET and RESET process, respectively. In the low-voltage region of the SET process, the I-V characteristic exhibits ohmic conduction ($I \propto V$) with a slope of ~1 attributed to the thermally generated electrons. As the voltage increases, the conduction follows Child's square law $(I \propto V^2)$ with a slope of ~2.5 that indicates domination of injected charge carriers over thermionic emission, where the current is fully controlled by shallow traps with space charge formed by sulphur ions. Thereafter, the trap becomes saturated with sufficient injection of charges. These injected carriers can move freely, and consequently, an abrupt increase in current results in the switching of device D2 from HRS to LRS. The I-V characteristics in LRS follows ohmic conduction with a slope of \sim 1.1 which confirms the formation of highly conductive flaments in the composite layer. Figure [6b](#page-9-0) represents I-V characteristics in the RESET process that follows similar conduction behaviour. The conduction follows a trap unflled space-charge limited current (SCLC) model with a slope of \sim 2.4 in which all the injected charge carriers come out of traps leading to rupture of flaments. As a result, the device switches from LRS to HRS. Figure [6c](#page-9-0) is a linearly fitted plot of ln (I/V) versus $V^{1/2}$ which corresponds to the Poole-Frenkel (PF) emission model for the

Fig. 6 (a) Linear ftting for the I-V curve of the D2 memory device on a log-log scale a SET process, (b) RESET process and (c) shows the Poole-Frenkel emission ft

D2 memory device. It is observed that the plot of ln (I/V) against $V^{1/2}$ is not linear which means the conduction in this D2 device is fully controlled by space charges formed by sulfur ions owing to the SCLC mechanism.

In the D1 device, when voltage is applied lead cations and sulfur anions are induced to form space charges. These charges are trapped by defects resulting in expedition of polarization of space charge. As a result, the charge carrier reaches the bottom electrodes constructing the current pathways known as flament formation between the top and bottom electrodes. Thus, the D1 device shows resistive switching behavior with the current ratio (I_{ON}/I_{OFF}) of 10−3. It is noted that Au NPs play an important role in resistive switching behaviour of the D2 memory device. The switching current ratio (I_{ON}/I_{OFF}) in the D2 memory device increases by 10^6 with the inclusion of Au NPs. Hence, the switching current ratio of the D2 memory device is higher than the control D1device.The rise in switching current ratio

of the D2 memory device may be attributed to charge injection properties of Au NPs. Further, the charged state of Au NPs may extend to space charge feld that may inhibit or speed up carrier transport. 37 The trapped charge carriers can remain inside the Au NPs for comparably elongated time even after the removal of applied bias.^{[38](#page-13-20)}

The SCLC transport mechanism for the D2 device is due to the charge trapping states available in the PbS NPs as well as Au NPs acting as trap centers. The origin of resistive switching behaviour may be attributed to the formation and rupture of conductive flaments between top and bottom electrodes. This is due to current pathways formed by lead (Pb) vacancies in the active region. Figure [7a](#page-10-0) shows schematic diagrams of energy levels corresponding to the D2 memory device's operating mechanism. The reported values of work function for Al, Au, PbS and ITO have been utilized in the proposed model while the electron afnity of the PVA barrier is assumed to be 1.5 eV, similar to that of a non-conducting polymer. Initially, when positive voltage is applied to the top electrode of the D2 memory device, the oxidation of PbS takes place results in formation of positively charged Pb^{2+} ions (holes) and negatively charged $S²$ ions (electrons) as depicted in Fig. [7](#page-10-0)b. With the further increase of voltage as shown in Fig. [7](#page-10-0)c, Pb^{2+} vacancies start migrating toward the bottom electrode and $S²$ ions move towards the top electrode. Since Au NPs are negatively charged, they accumulate near the Al top electrode. Au NPs can access the electrons causing them to be trapped inside acting as trap centers because of the high potential barrier between the electron affinity of PbS and the work function of Au NPs. As a result, $S²$ ions moving towards the top electrode encounter these traps and become trapped in these sites causing generation of an internal electric feld. The high electric feld and trapped electrons result in separation of charges which creates various dipoles and enhances the polarization efect in the switching layer. With the inclusion of Au NPs, the polarization efect multiplies in the device. Because of this effect, the density of lead ions increase and results in their acceleration towards the bottom electrode. When applied positive voltage reaches the SET voltage, i.e. 1 V, a highly conductive path is formed between top and bottom electrodes which results in resistive switching of the D2 device from HRS to LRS known as the SET process. Conversely, when the negative voltage is applied to the top Al electrode, negatively charged Au NPs are repelled by the Al electrode. The S^2 ions (electrons) come out of traps and move towards the bottom electrodes and Pb^{2+} vacancies (holes) move toward the top Al electrode resulting in rupture of the conductive flament, which is known as the RESET state, as shown in Fig. [7](#page-10-0)d. Figure [8](#page-10-1)a represents the

Fig. 7 (a) Schematic diagrams of energy level corresponding to the operating mechanism of D2 device. Schematic diagrams of the electronic structures corresponding to the operating mechanisms for D2 device at the (b) low voltage (c) SET voltage and the (d) RESET voltage

Fig. 8 (a) Retention time measurement of the D2 device with a read voltage of 0.5 V on logarithmic scale. (b) Cumulative probability of D2 memory device

data retention characteristics of the ON-state and OFF-state currents varying as functions of time for the D2 memory devices with a read voltage of 0.5 V at room temperature on a logarithmic scale. No discernible degradation of the D2 memory device was observed in either the ON state or OFF state for 2×10^3 -s continuous operation, which shows excellent stability of the device, maintaining a high current ratio (I_{ON}/I_{OFF}) of about ~10⁶. Both the ON and OFF states have current density values of 2.8×10^{-2} A and 3.4×10^{-8} A, respectively. These observations show that the D2 memory device possesses outstanding stability and reproducibility, making it ideal for non-volatile memory applications. Moreover, the cumulative probability of the D2 memory device was examined in both ON states and OFF states. Figure [8](#page-10-1)b depicts the cumulative probability of resistance switching of the D2 memory device. The resistance values measured at a read voltage of 0.5 V were plotted as LRS and HRS. The distribution of LRS was quite narrow compared to the distribution of HRS. The average values of LRS and HRS were found to be 3.4 Ω and 3 M Ω , respectively, with a high

resistance ratio (LRS/HRS) as $\sim 10^6$ between the two resistance states. The D2 memory device proposed here is a promising candidate for high-performance fexible non-volatile memory devices.

Flexibility Study

The D2 memory device has been shown to be a good choice for non-volatile bistable memory in fexible electronic applications. Figure [9a](#page-11-0)–d shows the I-V switching characteristics of a non-volatile bistable memory device at 180° (without bending) and with bending angle 70°, 90° and 120° of the PET substrates, respectively. The pictorial images for individual cases have been represented in the respective insets. Even after bending the device from 180° to 70° angles, I-V switching characteristics remained consistent which meets the condition for desirable fexible electronic applications.

Fig. 9 (a) I-V resistive switching characteristic of the D2 memory device at 180° (without bending), (b) I-V switching characteristic were recorded at 70° bending angle, (c) I-V switching characteristic

were recorded at 90° bending angle and (d) I-V switching characteristic were recorded at 120° bending angles of the PET substrates with corresponding pictorial image represented in the inset

Conclusion

In summary, we have demonstrated the fexible non-volatile memory device using Au/PbS-PVA nanocomposites deposited on ITO coated fexible PET substrate. TEM micrograph revealed the formation of Au/PbS nanoparticles in the PVA matrix. The SAED pattern confrms fcc Au and rock salt PbS phases of the Au-doped PbS-PVA nanocomposites. The EDX spectrum of nanocomposites clearly indicates the presence of Au, Pb and S in Au/PbS-PVA nanocomposites. AFM topography of 1 μm \times 1 μm scan area was used to investigate the surface roughness of PbS-PVA nancomposites and Au/ PbS-PVA nanocomposite flm. The PbS-PVA nanocomposites and Au/PbS-PVA nanocomposites were reported to have surface roughness of 46 nm and 78 nm, respectively. The XRD result shows that undoped PbS-PVA nanocomposites and Au-doped PbS-PVA nanocomposites are highly crystalline. The UV results revealed the narrowing of the optical band gap energy when Au nanoparticles were doped to PbS-PVA nanocomposites, indicating an obvious redshift of light adsorption. This may be attributed to the band shrinkage efect because of increasing charge carriers. The XPS survey scan of nanocomposites depicts the presence of oxidation states of Pb, S and Au ions. The charge transport mechanism may be accredited to SCLC by intently analyzing *I-V* characteristics of the prepared nanocomposites. The retention time measurement of device emphasizes its stability traits. The fabricated D2 memory device showed stable nature by passing endurance test against 100 cycles. The D2 memory device was measured for retention, stability and fexibility by constantly bending D2 device from 70° bending angle to 120° bending angle and without bending (180°). Interestingly, the resistances of both ON state and OFF state unveiled no degradation in the memory device. Moreover, I-V characteristics parameters after bending the D2 device at 70°, 90°, 120° and 180° (without bending) angles remained stable and unvariable. Therefore, the fabricated D2 memory device has high potential to be used in advanced fexible non-volatile memory application due to its stable and mechanical fexible properties.

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Data Availability The data that support the fndings of this study are available from the corresponding author upon reasonable request.

Declarations

Conflict of interest There is no confict to declare.

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