# Improvement of Electrical Contact Between TE Material and Ni Electrode Interfaces by Application of a Buffer Layer

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A single  $\pi$ -structure thermoelectric (TE) module based on p-type NaCo<sub>2</sub>O<sub>4</sub>,  $n$ -type Mg<sub>2</sub>Si, and Ni electrode was fabricated by the spark plasma sintering (SPS) method. The  $NaCo<sub>2</sub>O<sub>4</sub>$  powder was synthesized by using a metal–citric acid complex decomposition method. Bulk  $Mg_2S$ i prepared by melt quenching was ground into a powder and sieved to particle size of 75  $\mu$ m or less. To obtain a sintered body of  $NaCo<sub>2</sub>O<sub>4</sub>$  or  $Mg<sub>2</sub>Si$ , the powder was sintered using SPS. Pressed Ni powder or mixed powder consisting of Ni and  $S_rRuO_3$  powder was inserted between these materials and the Ni electrode in order to connect them, and electrical power was passed through the electrodes from the SPS equipment. The open-circuit voltage  $(V_{OC})$  values of a single module in which TE materials were connected to the Ni electrodes by using pressed Ni powder was 82.7 mV, and the maximum output current  $(I_{\text{max}})$  and maximum output power ( $P_{\text{max}}$ ) were 212.4 mA and 6.65 mW at  $\Delta T = 470$  K, respectively. On the other hand,  $V_{OC}$  of a single module in which TE materials and an Ni electrode were connected with a mixed powder (Ni:SrRuO<sub>3</sub> = 6:4 volume fraction) was 109 mV, and  $I_{\text{max}}$  and  $P_{\text{max}}$  were 4034 mA and 109 mW at  $\Delta T = 500$  K, respectively. These results indicate that the resistance at the interface between the TE materials and the Ni electrode can be decreased and the output power can be increased by application of a buffer layer consisting of Ni and  $SrRuO<sub>3</sub>$ .

Key words: NaCo<sub>2</sub>O<sub>4</sub>, Mg<sub>2</sub>Si, SrRuO<sub>3</sub>, thermoelectric module, spark plasma sintering

# INTRODUCTION

Thermoelectric (TE) power generation has recently attracted much interest as a technology for conversion of heat into electricity. High-performance TE modules based on intermetallic compounds have been studied intensively in recent years.<sup>[1](#page-5-0),[2](#page-5-0)</sup> Additionally, some modules based on Bi-Te intermetallic compounds have already been put to practical use.  $3,4$ On the other hand, TE modules based on metal oxides have also been studied widely. However, there are few reports of such modules that have high performance when compared with TE modules based on intermetallic compounds. In this paper, we describe the fabrication of  $\pi$ -structure TE modules based on  $NaCo<sub>2</sub>O<sub>4</sub>$  and  $Mg<sub>2</sub>Si.$ 

Metal oxides are thermally more stable and less harmful than intermetallic compounds such as  $Bi<sub>2</sub>Te<sub>3</sub>$ . Furthermore, metal oxide is better in terms of ease of preparation and control of chemical composition. Many researchers are looking for TE materials that can provide high thermopower, low resistivity, and low thermal conductivity. Recently, Received July 16, 2011; accepted March 1, 2012;<br>
published online April 3, 2012) layered cobalt oxides, i.e., Na-Co-O and Ca-Co-O,

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have been shown to have low resistivity and low thermal conductivity.<sup>[5–12](#page-5-0)</sup> NaCo<sub>2</sub>O<sub>4</sub> and Ca<sub>3</sub>Co<sub>4</sub>O<sub>9</sub> consist of Na ions or  $Ca<sub>2</sub>CoO<sub>3</sub>$  and  $CoO<sub>2</sub>$  blocks stacked alternately along the c-axis to form a lay-ered structure.<sup>[13](#page-5-0)</sup> Thus, the physical properties are highly two-dimensional in the  $a-b$  plane compared with the c-axis direction. These materials are promising for TE applications at high temperatures in air. In particular, Co-based oxides that have a layered structure (i.e.,  $\text{NaCo}_2\text{O}_4$  and  $\text{CaCo}_4\text{O}_9$ ) are good  $p$ -type thermoelectric oxide materials.<sup>[5,6](#page-5-0)</sup> The preparation of fine  $NaCo<sub>2</sub>O<sub>4</sub>$  powder by the metal– citric acid complex decomposition method, $^{14}$  $^{14}$  $^{14}$  and highly oriented  $NaCoO<sub>4</sub>$  ceramics prepared by the SPS method have been reported.<sup>[15](#page-5-0)</sup> On the other hand, magnesium silicide  $(Mg_2Si)$  has been identified as a promising TE material for use at operating temperatures from 500 K to 800 K. The dimensionless figure of merit,  $ZT$ , which is used to characterize the efficiency of TE materials, has already reached  $\sim$ 1.0 at 873 K for *n*-type Mg<sub>2</sub>Si.<sup>[16](#page-5-0)</sup> A large number of studies have investigated the TE characteristics of Mg2Si, and the output characteristics of Mg2Si and associated modules have been reported by Iida and others.<sup>17–[19](#page-6-0)</sup> Therefore, we fabricated a  $\pi$ -structure TE module with NaCo<sub>2</sub>O<sub>4</sub> as the p-type leg and  $Mg_2Si$  as the *n*-type leg by using the spark plasma sintering (SPS) method.[15](#page-5-0) However, the module had high contact resistance, which usually occurs at the interface between  $NaCo<sub>2</sub>O<sub>4</sub>$  and Ni electrodes. Contact resistance at the interface between the semiconductor and the metal electrode is a serious problem in many TE modules. $20,21$  In particular, oxide–metal junctions cause high contact resistance. Accordingly, some reports on the fabrication of oxide-based TE modules show that their performance is much lower than expected; $22,23$  for example, electrodes made of silver pastes containing different contents of oxide powders have been reported to improve the electrical contacts. $24,25$  In this study, we tried to improve the modules by the introduction of a buffer layer consisting of Ni and  $SrRuO<sub>3</sub>$ , and the TE performance of the modules was evaluated.  $SrRuO<sub>3</sub>$  has high thermal stability and high resistance to chemical corrosion, and its perovskite-type crystal structure favors integration with other oxides that have high potential in technological applications, such as superconductors or ferroelectrics.<sup>[26,27](#page-6-0)</sup> It is expected that interface resistance will be decreased by applying the buffer layer.

# EXPERIMENTAL PROCEDURES

 $NaCo<sub>2</sub>O<sub>4</sub>$  powder was prepared using a modified Pechini's method.<sup>28</sup> Pechini's method is a simple way of preparing metal oxide powders.<sup>29,30</sup> This process offers several advantages for the fabrication of metal oxide powders, including low cost, homogeneous composition, high purity, and low heattreatment temperatures. The precursor solutions were prepared from metal salts  $[CH<sub>3</sub>COONa$  and  $Co(NO<sub>3</sub>)<sub>2</sub>·6H<sub>2</sub>O$ , citric acid, and ethylene glycol monomethyl ether. These metal salts were dissolved in a solution of the citric acid and the ethylene glycol monomethyl ether. The solution was refluxed at 333 K for 1 h. After drying at 378 K, the precursor solution was heated at 723 K for 2 h. To obtain the plate-like precursor powder, the dried powder was heat-treated at 1173 K in air for 1 h.

SrRuO3 powder was also prepared using the modified Pechini's method. The precursor solutions were prepared from metal salts  $[Sr(CH_3COO)_2 \cdot 0.5 H<sub>2</sub>O$  and  $RuCl<sub>3</sub>:H<sub>2</sub>O$ ], citric acid, and  $H<sub>2</sub>O$ . These metal salts were dissolved with a solution of citric acid and  $H_2O$  in  $N_2$  atmosphere. The precursor solution was heated at 823 K for 5 h after being dried at 353 K for 8 h to obtain the precursor powder. To obtain  $SrRuO<sub>3</sub>$  as a single-phase powder, the dried powder was heat-treated at 1273 K in air for 5 h.

Presynthesized polycrystalline Mg<sub>2</sub>Si was provided by Union Material Inc. The presynthesized Mg2Si was prepared from a mixture of high-purity Mg (99.95 %) and Si (99.99999 %) at stoichiometric ratio of  $Mg:Si = 2:1$ , and it was heat-treated in an electrical furnace. The bulk polycrystalline  $Mg_2Si$ was ground to a powder and sieved to particle size of  $75 \mu m$  or less.

Spark plasma sintering (SPS) was carried out using a DR. SINTER LAB 515S system (SPS Syntex, Inc., Japan). For the SPS, the precursor powder was placed in a graphite die and heat-treated up to a predefined temperature (NaCo<sub>2</sub>O<sub>4</sub>: 1073 K, Mg<sub>2</sub>Si: 1123 K) at a rate of 100 K/min, after which it was maintained at that temperature for 5 min under uniaxial pressure of 60 MPa in Ar atmosphere (1013 hPa). After attaining and holding the sintering temperature in the SPS process, the electrical power was turned off at the holding temperature, and the sample was cooled to room temperature in the SPS chamber. The sintered blocks were cut and polished. The relative densities of the samples were measured using the Archimedes method.

The thermoelectric elements for the I–V measurements of the  $NaCo<sub>2</sub>O<sub>4</sub>$  ceramics with Ni electrodes or composite electrodes consisted of Ni and  $SrRuO<sub>3</sub>$  (Ni:SrRuO<sub>3</sub> = 6:4 by volume fraction). Figure [1](#page-2-0) shows an illustration and a photo of a thermoelectric element. The I–V measurements were carried out using the standard four-probe method (ULVAC-RIKO, ZEM-2) in He atmosphere.

The specific contact resistance was measured by the four-probe method. Figure [2](#page-2-0) shows an illustration of the specific contact resistance measurement by fourprobe method and a photo of the sample. The sample was fabricated using SPS equipment. The samples consisted of NaCo<sub>2</sub>O<sub>4</sub> (5.0 mm  $\times$  10.0 mm  $\times$  6.0 mm), Ni powder or mixed powder (Ni:SrRuO<sub>3</sub> = 6:4 by volume fraction), and Ni plate electrode (5.0 mm  $\times$ 10.0 mm  $\times$  0.5 mm). To connect the NaCo<sub>2</sub>O<sub>4</sub> and the electrode, either pressed Ni powder or mixed

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Fig. 2. Illustration of specific contact resistance measurement by four-probe method and a photo of the sample.



powder was put between the Ni plate electrodes and the  $NaCo<sub>2</sub>O<sub>4</sub>$  sintered body, and then electrical power was applied between the Ni plate/pressed Ni or the powder mixture/sintered body/pressed Ni or the powder mixture/Ni plate in the SPS chamber.

The thermoelectric module was fabricated using SPS equipment. The TE module consisted of  $NaCo<sub>2</sub>O<sub>4</sub>$  $(4.95 \text{ mm} \times 11.9 \text{ mm} \times 7.7 \text{ mm})$  as the *p*-type semiconductor,  $Mg_2Si(5.5 \text{ mm} \times 11.95 \text{ mm} \times 7.7 \text{ mm})$  as the *n*-type semiconductor, Ni plates (13.0 mm  $\times$ 13.0 mm  $\times$  0.5 mm) as the electrodes for the hot top, and Ni plates (5.5 mm  $\times$  13.0 mm  $\times$  0.5 mm) as the electrodes for the cold bottom (Fig. 3). To connect the TE elements and the electrodes, either pressed Ni



Fig. 4. XRD patterns of NaCo<sub>2</sub>O<sub>4</sub> powder, ceramics, and a JCPDS reference.

powder or mixed powder (Ni:SrRuO<sub>3</sub> = 6:4 by volume fraction) was put between the Ni plate electrodes and the  $NaCo<sub>2</sub>O<sub>4</sub>$  or  $Mg<sub>2</sub>Si$  sintered body, and then electrical power was applied between the Ni plate/pressed Ni or the powder mixture/sintered body/pressed Ni or the powder mixture/Ni plate in the SPS chamber.

The open-circuit voltage and the thermoelectric power outputs of  $\pi$ -structure modules with temperature differences,  $\Delta T$ , ranging from 100 K to 500 K were measured in air using a Union Material UMTE-1000 M. The top of the module was heated by an electrically heated stainless-steel block, whereas the base was cooled using an aluminum block. The heat that was applied to the heating block came from an electrical heater working at 473 K to 873 K, whereas the cooling block was maintained at 373 K by a combination of water cooling and electrical heating. The output current and output power were measured under closed-circuit conditions by varying the value of the external load.

#### RESULTS AND DISCUSSION

Figure 4 shows the XRD patterns of the  $NaCo<sub>2</sub>O<sub>4</sub>$ powder, the ceramics, and a JCPDS reference. Comparing the diffraction patterns of the powder and the ceramics with those of the reference, one can see that the peaks of the (00l) phases were more intense than those of the other phases. The XRD measurements indicated that the c-axis of the prepared  $NaCo<sub>2</sub>O<sub>4</sub>$  ceramic was perpendicular to the uniaxial pressure direction. The degree of texture was evaluated in terms of the Lotgering factor,  $\frac{11}{11}$  $\frac{11}{11}$  $\frac{11}{11}$ 

$$
f = (p - p_0)/(1 - p_0), \tag{1}
$$

where

$$
p = \Sigma I_{(00l)} / \Sigma I_{(hkl)} \tag{2}
$$

for the diffraction pattern of the sample, and  $p = p_0$ for a randomly oriented powder diffraction pattern of the reference. The Lotgering factor of the sample was above 50 %. Scanning electron microscopy (SEM) revealed that the  $NaCo<sub>2</sub>O<sub>4</sub>$  particles had a plate-like shape and a high aspect ratio. The theoretical relative densities of the  $NaCo<sub>2</sub>O<sub>4</sub>$  and the Mg2Si sintered body were above 98 %.

Figure 5 shows current–voltage curves for  $NaCo<sub>2</sub>O<sub>4</sub>$  with Ni electrodes (a), and for  $NaCo<sub>2</sub>O<sub>4</sub>$ with "mixture" electrodes  $(Ni:SrRuO<sub>3</sub> = 6:4)$  (b). The voltages of the samples were measured by changing the current over the temperature range from 373 K to 973 K in increments of 50 K. In the low-temperature range (up to 623 K), the electrical properties of  $NaCo<sub>2</sub>O<sub>4</sub>$  with Ni electrodes could not be measured because a high-resistance barrier (i.e., a Schottky barrier junction) was formed at the interface between the sample and the Ni electrodes, whereas the resistivity of the sample could be measured above 673 K (Fig. 5a). The resistivity of the sample decreased nonlinearly with increasing measurement temperature. According to the results, it seems that the current for the  $NaCo<sub>2</sub>O<sub>4</sub>$ -Mg<sub>2</sub>Si TE module decreased significantly on the cold side. On the other hand (Fig.  $5b$ ), investigations of the  $I-V$ curves could be carried out above 373 K when a buffer layer was inserted at the interface between the  $NaCo<sub>2</sub>O<sub>4</sub>$  and the Ni electrodes. It is believed that much lower electrical resistance contacts than between the  $NaCo<sub>2</sub>O<sub>4</sub>$  and the Ni can be formed between  $SrRuO<sub>3</sub>$  and Ni, and  $SrRuO<sub>3</sub>$  and  $NaCo<sub>2</sub>O<sub>4</sub>$ , respectively, and that the interface resistivity decreases as a result. The temperature dependence of the electrical resistivity  $(\rho)$  was calculated from the current–voltage curves shown in Fig. 5a, b. The theoretical electric resistivity  $(\rho)$  of the thermoelectric elements can be expressed as

$$
\rho = R_{\text{cal}}S/l,\tag{3}
$$

where  $R_{\text{cal}}$  is calculated from the value of the slope of the line of the current–voltage curve (contributing rate  $>0.97$ ). S and l are the cross-section of the thermoelectric element and the distance between the electrodes, respectively. As shown in Fig. 6, the electrical resistivity of the thermoelectric element with a buffer layer included decreased significantly relative to the element without the buffer layer. The value of the electrical resistivity decreased to about 1/30th at 873 K. It seems that the electrical conductive path provided by  $S_rRuO_3$  made it possible for the thermoelectric element to have high electrical conductivity at low temperature.



Fig. 6. Temperature dependence of electrical resistivity.



Fig. 5. Current–voltage curves of an NaCo<sub>2</sub>O<sub>4</sub>-Ni thermoelectric element: (a) without and (b) with a buffer layer.



Fig. 7. Specific contact resistance measurements between Ni and  $NaCo<sub>2</sub>O<sub>4</sub>$ .



Fig. 8. Specific contact resistance measurements between Ni and buffer layer (Ni:SrRuO<sub>3</sub> = 6:4 by volume fraction), and between buffer layer and  $NaCo<sub>2</sub>O<sub>4</sub>$ .

Figure 7 shows specific contact resistance measurements between Ni and  $NaCo<sub>2</sub>O<sub>4</sub>$ . Figure 8 shows specific contact resistance measurements between the Ni and buffer layer (Ni: $SrRuO<sub>3</sub> = 6:4$  by volume fraction), and between the buffer layer and  $NaCo<sub>2</sub>O<sub>4</sub>$ . It seems that there is no (negligibly low) contact resistance between the Ni and buffer layer. The value of the specific contact resistivity between the electrode and  $NaCo<sub>2</sub>O<sub>4</sub>$  decreased to about 1/260th at room temperature by inserting the buffer layer.



Fig. 9.  $\textsf{FV}$  characteristics and output power of the module versus temperature differences,  $\Delta T$ , ranging from 100 K to 500 K.

Figure  $9$  shows the  $I-V$  and  $I-P$  curves for the  $NaCo<sub>2</sub>O<sub>4</sub>$ -Mg<sub>2</sub>Si TE module with the buffer layer for various temperature differences,  $\Delta T$ , ranging from 100 K to 500 K. Under the same temperature conditions, the external resistance  $(R_{ex})$  of the circuit changed from  $R_{\text{ex}}$  = maximum (open circuit) to 0 [voltage  $(V) = 0$ ]. The open-circuit voltage  $(V<sub>OC</sub>)$  is measured at  $R_{\text{ex}}$  = maximum. The maximum output current ( $I_{\text{max}}$ ) is measured at  $R_{\text{ex}} = 0$ . On the other hand, the output power increased with decreasing external resistance, and exhibited a maximum value of output power  $(P_{\text{max}})$  at  $V = V_{\text{OC}}/2$  [current  $(I) = I_{\text{max}}/2$ , and then decreased and reached zero at  $V = 0$  ( $I = I_{\text{max}}$ ). The maximum value of  $V_{\text{OC}}$  reached 109 mV at  $\Delta T = 500$  K. The obtained maxima,  $I_{\rm max}$ and  $P_{\text{max}}$ , for the single module were 4034 mA and 109 mW, respectively, at  $\Delta T = 500$  K. Table [I](#page-5-0) presents the thermoelectric performance of two single TE modules. One of these is a module without a buffer layer that we fabricated previously,  $15$  and the other was fabricated as part of this work. The output power at high temperature ( $\Delta T = 500$  K, 470 K) of the latest module was more than 16 times higher than the module that we fabricated in the past. This is believed to be due to a decrease in contact resistance at the interface between the  $NaCo<sub>2</sub>O<sub>4</sub>$  thermoelectric material and the Ni electrode due to the insertion of the buffer layer. Moreover, the buffer layer produced a marked improvement in the electrical conductivity at low temperature  $(\Delta T = 100 \text{ K})$ . It seems that the electrical conductive path of  $SrRuO<sub>3</sub>$  enables high electrical conductivity at low temperature.



# <span id="page-5-0"></span>Table I. Thermoelectric performance of the single  $\pi$ -structure TE modules

Table II. Thermoelectric performance results for reported oxide modules

| Author                            | <b>Materials</b>   | <b>Element Dimensions</b><br>(mm)                    | $p-n$ Couples | $\Delta T$<br>(K) | $\mathbf{V_{OC}}$<br>(V) | $P_{\text{max}}$<br>(mW) | $\bm{P_{\max}}/\Delta \bm{T}$<br>(mW/K) |
|-----------------------------------|--|--|---------------|-------------------|--------------------------|--------------------------|---|
| This work                         | $p: \text{NaCo}_2\text{O}_4$   | $4.95 \times 11.9 \times 7.7$                        |               | 500               | 0.109                    | 109                      | 0.218                                   |
| Souma et al. <sup>31</sup>        | n: Mg <sub>2</sub> Si<br>$p: \text{NaCo}_2\text{O}_4$<br>n: ZnO                                | $5.5 \times 11.95 \times 7.7$                        | 12            | 455               | 0.79                     | 52.5                     | 0.115                                   |
| Funahashi and Urata <sup>21</sup> | $p: \text{Ca}_{2.7}\text{Bi}_{0.3}\text{Co}_{4}\text{O}_{9}$<br>$n: La0$ $_9Bi0$ $_1NiO3$      | $1.3 \times 1.3 \times 5$                            | 140           | 551               | 4.5                      | 150                      | 0.272                                   |
| Funahashi and Urata <sup>21</sup> | $p: \text{Ca}_{2.7}\text{Bi}_{0.3}\text{Co}_4\text{O}_9$<br><i>n</i> : $La_{0.9}Bi_{0.1}NiO_3$ | $3.7 \times 4 - 4.5 \times 4.7$                      | 8             | 565               |                          | 170                      | 0.301                                   |
| Choi et al. $32$                  | $p: Ca3Co4O9$<br><i>n</i> : $(\text{ZnO})_7\text{In}_2\text{O}_3$                              | $15 \times 15 \times 27$<br>$15 \times 15 \times 18$ | 44            | 673               | 1.8                      | 423                      | 0.629                                   |

Table II presents the reported thermoelectric performance results for oxide-based modules. The thermoelectric performance results of the modules with p-type  $NaCo<sub>2</sub>O<sub>4</sub>$  and n-type Mg<sub>2</sub>Si couples were reported by us. The open-circuit voltage (0.109 V) of this work is the lowest value among those reported to date. For this reason, the module reported in this work has only one  $p-n$  junction. However, the maximum value of output power (109 mW) in this work is by far the highest value for a single module (one  $p-n$  junction). Therefore, it can be expected that improvements could be realized by connecting the modules described in this work in order to generate increased electrical power.

## **CONCLUSIONS**

A thermoelectric module with a  $\pi$  structure consisting of p-type  $NaCo<sub>2</sub>O<sub>4</sub>$  and n-type  $Mg<sub>2</sub>Si$  has been successfully fabricated by SPS. With the insertion of a mixed powder (Ni:SrRuO<sub>3</sub> = 6:4 by volume fraction) buffer layer, the electrical resistivity of the  $NaCo<sub>2</sub>O<sub>4</sub>$ –Ni element was reduced to about 1/30th of previous levels at 873 K. The opencircuit voltage of a single module with an  $SrRuO<sub>3</sub>$ Ni buffer layer was 109 mV, and the maximum output current and maximum output power were 4034 mA and 109 mW at  $\Delta T = 500$  K, respectively.

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