

Design of a Modifed Wilkinson Power Divider with Size Reduction and Harmonics Suppression using Triangle‑Shaped Resonators

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Published online: 21 June 2019 © Springer Science+Business Media, LLC, part of Springer Nature 2019

Abstract

In this study, a miniaturized microstrip Wilkinson power divider (WPD) with harmonic elimination using low pass flter is proposed. In proposed design, two large conventional quarter wavelength transmission lines are replaced with compact low pass flters. Applied low pass flters are consisted of three triangle resonators, which result in harmonics suppression and size reduction of the proposed WPD. The proposed WPD has 56% size reduction compared to the conventional one. The proposed divider correctly works at 2 GHz and suppresses second to eighth harmonics with more than 20 dB attenuation level. According to the experimental results the insertion loss of the proposed design is less than 0.1 dB n pass band. The return losses at all ports and output ports isolation parameters are better than 20 dB and 26 dB, respectively, which shows good performance of the proposed device.

Keywords Wilkinson power divider (WPD) · Triangle-shaped resonator · Harmonic suppression

1 Introduction

One of the widely used device in the modern wireless circuits and systems is Wilkinson power divider (WPD). Input signals are divided by WPDs into two or more signals [\[1–](#page-6-0)[3](#page-7-0)] and used in many systems such as antenna arrays, power amplifers, feeding networks. A conventional WPD, consists of two quarter wavelength $(\lambda/4)$ lines and a 100 ohms isolation resistor between two lines.

Relatively large size and presence of unwanted harmonic in the frequency response are the main drawbacks of the conventional divider. So far, various methods are used to sup-press unwanted harmonics and decrease the circuit size. In [\[4](#page-7-1)] by using a π type structure,

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Fig. 1 The conventional Wilkinson power divider

which includes a capacitor between two lines, instead of quarter wavelength transmission line, 3rd harmonic of the power divider is suppressed; however, this design sufers from large size.

In [\[5](#page-7-2)], a divider with wide pass-band, using ring resonator is designed, which suppresses the unwanted harmonics. The return loss parameter at the operating frequency is not good. In [[6](#page-7-3)], a simple power divider is presented by using microstrip open stubs, which suppresses 2nd to 6th harmonics. In [[7](#page-7-4)] a compact WPD with anti-coupled line and capacitive load is reported. This divider has good harmonics suppression but, isolation parameter in this design is not very good.

Except using resonators [[5](#page-7-2)] and coupling lines [\[7](#page-7-4)], some other methods such as the defected ground structure (DGS) [\[8\]](#page-7-5), and photonic band gap (PCB) [[9](#page-7-6)], are used to design compact WPD with harmonics suppression. The main drawback of these works is the complex fabrication process.

In this presented paper, a miniaturized WPD using low pass flter instead of large conventional quarter wavelength line is proposed. Furthermore, the proposed WPD has low insertion loss, wide harmonics suppression from 2nd harmonic to 8th and good isolation. In design procedure at frst, a low pass flter using triangle-shaped resonators is described step by step. Then, the designed power divider by using the proposed low pass flter, has been presented.

2 Design Process

The structure of the conventional Wilkinson power divider (WPD) is depicted in Fig. [1.](#page-1-0) In the conventional divider structure an isolation resistor ($R=100\Omega$) and two quarter wavelength (λ /4) transmission lines with $\sqrt{2} Z_0$ impedance are used, which occupied relatively large size. In the proposed WPD, the relatively long $\lambda/4$ transmission lines, which used in conventional divider, are replaced by low pass flter structure.

2.1 Resonator Design

The layout of the main resonator (triangular shape) is demonstrated in Fig. [2a](#page-2-0). In the main resonator structure a high impedance line with short length and a triangle shaped resonator is connected to a high impedance transmission line. The dimensions of the designed resonator

Fig. 2 The main resonator: **a** Layout, **b** LC equivalent circuit, **c** LC and EM simulation results

are as follows: L₁=2.78 mm, W₁=2.88 mm, L₂=8.65 mm, W₂=0.28 mm. The equivalent LC circuit of the main resonator is depicted in Fig. [2](#page-2-0)b. According to Eqs. [\(1](#page-2-1)) and ([2](#page-2-2)) the low and high impedance transmission lines, are obtained as follows [\[10](#page-7-7)]:

$$
l_s = \frac{1}{\omega} \times z_s \times \sin\left(\frac{2\pi}{\lambda_g}l\right)
$$
 (1)

$$
c_s = \frac{1}{\omega} \times \frac{1}{z_s} \times \tan\left(\frac{\pi}{\lambda_g}l\right)
$$
 (2)

where z_s is the characteristic impedance of the applied transmission line, λ_g is the guided wavelength at the cut-off frequency and *l* is the physical length of line.

Table [1](#page-2-3) shows the optimized L-C parameters, which calculated by ADS software.

Fig. 3 Primary low pass flter: **a** Layout, **b** EM simulation results

Fig. 4 Proposed low-pass flter: **a** Layout, **b** EM simulation results

S-Parameters simulation of the main resonator are depicted in Fig. [2c](#page-2-0). Both of the EM and LC simulations responses are depicted. The agreement between curves are proved the validity of the equivalent LC circuit.

In order to improve the sharpness of transition-band and achieve wide harmonic suppression, two triangle shaped resonators are Inserted in both sides of the main resonator. The layout of the primary low pass flter is illustrated in Fig. [3a](#page-3-0). The EM simulation results of the S-parameters for the primary low pass flter is illustrated in Fig. [3b](#page-3-0). As seen in Fig. [3b](#page-3-0), these three sections, are created three transmission zeroes.

Finally, in order to have better results and also reduce the circuit size of the primary flter, the long lines of symmetrical resonators have been bended. So the layout of the proposed lowpass flter and the EM simulations results of the flter are illustrated in Fig. [4](#page-3-1)a, b, respectively. The dimensions of the proposed filter are as fallow: L_3 = 3.63 mm, $L_4=1.4$ mm, $L_5=0.72$ mm, $L_6=1.89$ mm and $W_3=1.5$ mm, $W_4=0.1$ mm. As shown in Fig. [4b](#page-3-1), the proposed triangle-shaped low pass flter creates three transmission zeroes in 4 GHz, 12.6 GHz and 15.5 GHz frequencies.

2.2 Power Divider Design

By inserting two proposed LPFs symmetrically instead of quarter wavelength lines in conventional power divider, a compact WPD is proposed as illustrated in Fig. [5.](#page-4-0) In this structure the 100 Ω isolation resistor is used.

3 Simulated and Measured Results

The proposed WPD is fabricated on the Rogers4003c substrate with a dielectric constant of 3.38, thickness of 20 mil and loss tangent of 0.0022. The overall dimension of the fabricated device is 8.3 mm × 8.95 mm (0.09 λ g × 0.097 λ g), where λ g is the guided wavelength of the center frequency (2 GHz). The proposed divider has about 56% size reduction compared to the conventional 2 GHz WPD, which shown in Fig. [6.](#page-4-1) The fabricated image of the designed WPD is illustrated in Fig. [7](#page-5-0).

Figure [8](#page-5-1) shows the measured and simulated S-parameters of the proposed WPD. According to the Fig. [8](#page-5-1)a, b, the experimental results show that at the operating frequency of 2 GHz the input (S_{11}) and output $(S_{22} = S_{33})$ return losses are better than 20 dB. From the

Fig. 8 EM simulation and experimental scattering parameters results: **a** the input return loss **b** the output return loss **c** the insertion loss **d** the isolation response

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References	FBW $(\%)^a$	Center frequency (GHz)	Harmonic suppressions (dB)								Size
			2nd	3rd	4th	5th	6th	7th	8th	9th	reduction (%)
[11]	48	2.65		29	$\overline{}$	34					36.5
$[12]$		2		53	25	56	20				29.3
$[13]$		0.9		22							47
[14]	34		31	50	20	31	45	21	15		45
$\lceil 15 \rceil$	21		30	30	20	20					55
This work	59	2	22	20	23	25	31	33	42	21	56

Table 2 A comparison among some reported works and the proposed WPD

 a **FBW**(%) = $\frac{f_{2} f_{1}}{0.5(f_{1}+f_{2})} \times 100$

measurement results in Fig. [8](#page-5-1)c could be observed that, the insertion loss at the 2 GHz frequency is less than 0.1 dB ($S_{12} = S_{13} = 3 \pm 0.1$). As seen in Fig. [8d](#page-5-1) the output ports isolation (S_{23}) parameter is better than 26 dB.

As seen in Fig. [8](#page-5-1)a, from Eq. ([3](#page-6-1)) 59% fractional bandwidth (1.44–2.66 GHz) was observed from measured results by considering $|S_{11}|$ greater than 15 dB.

Moreover, as seen in Fig. [8](#page-5-1)c the proposed divider features a wide range of harmonics suppression from 2nd up to 9th harmonic (4–18 GHz) with better than 20 dB attenuation level.

A performance comparison among some reported works and the proposed WPD are listed in Table [2.](#page-6-2) The results show that the proposed divider has very good specifcations compare to others works.

$$
FBW\left(\% \right) = \frac{f_2 - f_1}{0.5(f_1 + f_2)} \times 100\tag{3}
$$

4 Conclusion

In this paper, a compact Wilkinson power divider (WPD) with ultra wide stop band (4–18 GHz) and low insertion loss with 2 GHz operating frequency is designed and fabricated. By using a low pass flter based on triangle-shaped resonators instead of quarter wavelength lines of a conventional divider, not only the unwanted harmonics from 2nd to 9th are suppressed, but also the size of the divider is reduced over 56% compared with the conventional divider. Moreover the proposed WPD has 59% fractional bandwidth (1.44–2.66 GHz). Good agreement are observed among simulation and experimental results.

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