An improved high capacity data hiding scheme using pixel value adjustment and modulus operation

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Abstract

High data hiding capacity, stego image quality and security are the measures of steganography. Of these three measures, number of bits that can be hidden in a single cover pixel, bits per pixel (bpp), is very important and many researchers are working to improve the bpp. Recently, to enhance the embedding capacity, some of the schemes were proposed which are based on the fixed equation that is used as an extraction function of secret data from the stego image. We propose an improved high capacity data hiding method that maintain the acceptable image quality that is more than 30 dB and improves the embedding capacity higher than that of the methods proposed in recent years. The method proposed in this paper uses $n^2 - ary$ notational system and achieves higher embedding rate of 4 bpp and also maintain the good visual quality.

Keywords Cover image . Data hiding . Stego image . Exploiting modification metod . Pixel value adjustment

1 Introduction

Data hiding techniques concentrates mainly on security, hiding capacity and good visual quality of stego image. There are many data hiding techniques, for instance, Least Significant Bit [\[1](#page-9-0)], Pixel Value Differencing [\[13](#page-9-0)], and Extraction Function with Modulus [\[4](#page-9-0)] method, Exploiting Modification Direction [[16\]](#page-9-0) method. Each method was proposed to improve the embedding capacity at the same time maintaining good visual

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quality. Xinpeng et al. [\[16](#page-9-0)] proposed efficient embedding method by EMD that achieves good PSNR and also maintains less changes in the cover image pixel so that it ensures the security. The highest embedding capacity achieved by this method was 1.16 bpp. With the aim of improving the embedding capacity, Chin-Feng Lee et al. [\[11\]](#page-9-0) proposed an improved EMD method that achieves maximum embedding capacity of 393,216 for an image of size 256×256 with 8 - ary notation systems and the bits per pixel (bpp) was improved to 1.5. Ki-yun Jung et al. [[4\]](#page-9-0) proposed an improved EMD method that uses 5- ary notational system and achieved maximum rate of 2 bpp and PSNR of 47.95 dB. In the year 2012, Cheonchikkim [\[6](#page-9-0)] proposed data hiding method based on code book that uses $(2^{n+x}-1)$ -ary notational system and the bpp was improved to an average of 3.4943 with $n = 2$ and $x = 5$ but the PSNR was reduced to 43.93 dB when compared with that of Ki-yun Jung et al. [\[4](#page-9-0)] method. It reveals that there is a tradeoff between PSNR and embedding rate. Multi-bit encoding function based high capacity data hiding scheme was proposed by Wen-Chung Kuo et al. [[8\]](#page-9-0) and they achieved highest embedding rate of 3.6 bpp. The extraction function uses a predefined coefficients c_i 's with the modulus operator 2^{nk+1} and achieved maximum embedding capacity of 851,968 bits for a cover image of size 512×512 . The method proposed by Wen-Chung Kuo et al. [\[8](#page-9-0)] motivated us to further improve the embedding capacity with good stego image quality. There are other data hiding techniques based histogram shifting [\[3](#page-9-0)],based on contrast enhancement [[15](#page-9-0)] to attend the same problem of improving hiding rate.

The rest of the paper is organized as follows. Section II, a brief discussion on methods related to the research, Section III, the detailed description of proposed method, Section IV and V respectively experimental result and conclusion.

2 Related works

Algorithm 1 In the year 2006, Xinpeng Zhang et al. [[16\]](#page-9-0) introduced the Exploiting Modification Direction (EMD) method that uses n cover pixels to carry each secret digit which is in $(2n + 1)$ - ary notational systems, the algorithm used in [\[16](#page-9-0)] as follows.

Step 1: Compute the extraction function given in Eq. (1).

$$
f = \left[\sum_{i=1}^{n} x_i^* i\right] \text{mod}(2n+1) \tag{1}
$$

Where $*$ is the normal multiplication operation and not the convolution.

Step 2: Embed the $(2n + 1)$ -ary notational secret data into the uncompressed cover image. Let the secret digit be 'd', 'g' be the pixel value in cover image and 'G' be the pixel value of stego image of same size as that of cover image.

```
if f is equal to dG=g // that is, no need to modify the cover image pixel, indicates that no secret data is
embedded.
  else if d is not equal to fcompute, D = (d - f) \mod (2n + 1)if D is greater than nG = g_D + 1
```

```
else
                    G = g_{(Cn+1)-Dn} - 1end
end
```

```
end
```
Step 3: Use the extraction function given in Eq. [\(1](#page-1-0)), to extract the secret data from the pixels G. Embedding rate achieved by this method was 1.16 bpp.

Algorithm 2 With the aim of improving embedding capacity and maintaining the good visual quality, in the year 2009, Ki-Hyun Jung [[4](#page-9-0)] introduced a new algorithm that can embed maximum of 2 bpp. The algorithm proposed in $[11]$ $[11]$ is not using group of pixel instead it uses single pixel and modulo operation. The algorithm proposed in [\[11\]](#page-9-0) as follows.

Let 'g' be the pixel value in a cover image and 'G' be the pixel value stego - image, after embedding operation.

Step 1: For each pixel in cover image, 'g', Compute the extraction function f, given in Eq. (2) and choose the 'x' value, in the range $[-(2n+1), +(2n+1)]$, such that 'f' is equal to 'd', where 'd' is the secret data.

$$
f(g+x) \text{mod}(2n+1) \tag{2}
$$

Step 2: For the value of x computed in step 1, compute the stego-pixel G by adding x with g. Step 3: Use the extraction function, given in Eq. (2), to extract the secret data embedded in the pixels of stego image. This algorithm efficiently embeds the data in the cover image pixel and also maintains the good visual quality but the maximum embedding rate per pixel is limited to 2.

Algorithm 3 In the year 2013, Wen-Chung Kuoa et.al. [[8](#page-9-0)] proposed a method for high capacity data hiding with acceptable stego image quality. The method uses predefined weight value c_i and extraction function based on the algorithm proposed in [\[16\]](#page-9-0). The weights c_i were used to compute the extraction function, calculated weights in [\[8](#page-9-0)] are, $c_1 = 1$, $c_2 = 9$, $c_3 = 73$, c_4 = 585 for the value of $k=3$ and $n=4$, where 'k' indicates number of embedding bits, and 'n'

indicates number of pixel in a group of cover image. While computing the extraction function, a group of four pixels were considered. The algorithm proposed in [\[8\]](#page-9-0) as follows

Step 1: Compute the extraction function using the Eq. (3)

$$
f = \left[\sum_{i=1}^{n} \left(c_i^* g_i\right)\right] \text{mod} 2^{nk+1} \tag{3}
$$

Where * indicates normal multiplication operation between weight value and pixel from cover image and not the convolution operation.

- Step 2: Transform the secrete value. The transformation of each secret value $\{s_{nk}\}\in\{0,1\}$ is done by sum of product.
- Step 3: Compute the difference 'D' between 'f' and secret value 's' using the equation given below

$$
D = (s-f) \bmod 2^{nk+1}
$$

Step 4: Embedding the secret data in to cover image as follows,

if $D=0$ than $G=g$ // do not change the cover pixel vause else if $D = 2^{nk}$ $G_n = g_n + (2^k - 1)$ and $G_1 = x_1 + 1$ else if $D < 2^{nk}$ transform $D = (d_{n-1}, d_{n-2}, \dots, d_0)_{\alpha k}$ then $G_{i+1} = g_{i+1} + d_i$ and $G_i = g_{i+1} - d_i$ else if $D > 2^{nk}$ transform $D = 2^{nk+1} - D = (d_{n-1}, d_{n-2}, d_{n-3},..., d_0)_{\gamma k}$ then $G_{i+1} = g_{i+1} - d_i$ and $G_i = g_i + d_i$ end

end

end

end

to perform step 4, a group of $n = 4$ pixels were considered and each pixel in a group was adjusted using the above algorithm and the stego image I_s was constructed. Each group, consisting of four pixel, can embed 13 secret bits, therefore, embedding rate is calculated as, $13/4 = 3.25$ bpp. For an image of size 512×512 , total number of secret bits can be embedded is $512*512*3.25 = 8,51,968$ but the PSNR is reduced to an average of 37.25 db.

3 Proposed method

To improve the embedding rate, we propose a method that not only enhances the embedding capacity but also maintains the good visual quality of stego image, that is, the PSNR of the stego image is maintained more than 30 db. In our proposed method, the secret data is converted to n^2 -ary notational system. Where n- is number of binary digits considered to represent secret data in n^2 - ary notational systems. In [\[8](#page-9-0), [16\]](#page-9-0), while computing the extraction function, the variable n represents number of pixels in a group of cover image. But in our proposed method the same variable n is used to represents number binary digits considered to convert, secrete binary digits, to n^2 -ary notational systems. The proposed algorithm, given in detail, as follows

- Step 1: Convert the secrete information in binary digits in to n^2 -ary notational system before applying the algorithm, by considering $n = 4$ bit at a time and represent the secret data as 'd'.
- Step 2: For each pixel value 'g' in a cover image and for a secret value 'd' do the following

for
$$
-\lfloor n^2/2 \rfloor \le x \le \lfloor n^2/2 \rfloor
$$

$$
f = (g + x) \operatorname{mod} n^2 \tag{4}
$$

if $f = d$

$$
G = g + x \tag{5}
$$

break else continue to search another value of x to satisfy the condition that f is equal to d. end end

Where $n-$ is number of binary digits considered to represent secret value in n^2 - ary notational systems and $\lfloor \cdot \rfloor$ is the flooring operation

Step 3: All the embedded values (informations), from the stego - image with adjusted pixel value G, are extracted by applying the extraction function, given in eq. (6), to each value of 'G'. that is,

for all pixels in Stego - Image

compute,

$$
f_1 = G \bmod n^2 \tag{6}
$$

end

The extracted values f_l are the embedded information in the pixels g.

The major advantage with our proposed n^2 -ary notational system of secrete digit representation is that we achieved flexibility to scale down or scale up our modulo operator, that is, when the PSNR in dB is the major consideration n can be kept as 2 or 3, that is scaling down 'n' to 2 or 3, on the other hand when the higher embedding rate is the major requirement then n can be scaled up to 4 so that we can achieve higher hiding rate than in [[8\]](#page-9-0).

4 Experimental result and performance discussion

We tested our proposed method with standard gray scale image of size 512×512 , all the images were taken from USC-SIPI image data base [http://sipi.usc.edu/database/.](http://sipi.usc.edu/database/)

PSNR and SSIM are the two performance metrics used to analysis the performance of proposed method. The peak signal-to-noise ratio is calculated between the cover image and the stego - image. (Fig. [1\)](#page-6-0).

$$
PSNR = 10\log_{10}\left(\frac{255^2}{MSE}\right)dB
$$

$$
MSE = \frac{1}{M \times N} \sum_{i=1}^{N} \sum_{j=1}^{M} \left(g_{ij} - G_{ij}\right)^2
$$

Where,

MxN represents number of rows and columns in cover image respectively g_{ii} and G_{ii} pixel value in cover image and stego - image respectively

Structural Similarity Index (SSIM) is an another metric for measuring image quality that calculates the Structural similarity Index (SSIM) value for Cover image, with the stego –image. Comparison is made between proposed and ref. [[4\]](#page-9-0), since both method use same number of binary digits, that is $n = 2$, to create secret value but uses different notational system. In Table [1,](#page-7-0) from the columns of percentage increase in PSNR and percentage increase in SSIM, it is obvious that proposed method has considerable percentage of increase in PSNR and SSIM.

Another comparison is made in terms of embedding capacity, Table [2.](#page-7-0) The observed embedding capacity of proposed method is compared with methods in [\[4](#page-9-0), [16\]](#page-9-0) and [\[8](#page-9-0)].

The proposed method achieves significant embedding capacity that is 85.48% greater than that of $[16]$, 50% greater than that of $[4]$ $[4]$ and 18.75% greater than that of $[8]$ $[8]$. From the comparison Table [2,](#page-7-0) the observation is that there is always a tradeoff between embedding capacity and PSNR.

The secret information which is embedded in the cover image is imperceptible when the PSNR is higher than 30 dB. To ensure that PSNR and SSIM is calculated with $n = 4$ for different ranges of randomly generated data. From Table [3,](#page-7-0) it is obvious that the

Cover Image of 512×512	Ref[4](A)		Proposed (B)		% Increase in PSNR	% Increase in SSIM	
	PSNR	SSIM	PSNR	SSIM	$\frac{B-A}{B}^*100$	$\frac{B-A}{R}^{*}100$	
Barcode					44.0524 0.3482 45.7472 0.3547 3.704708	1.832535	
House -car	41.5053 0.8750				42.7513 0.8987 2.914531	2.637143	
Jet plane					42.3442 0.8664 43.5804 0.9026 2.836596	4.010636	
Clock	42.7755 0.6833				44.0197 0.7419 2.826462	7.898639	
Tiffany	43.5584	0.9159	44.8005		0.9422 2.772514	2.791339	
Average	42.847	0.7378	44.18	0.768	3.011	3.8341	

Table 1 Comparison of Proposed Method with Ref [\[4](#page-9-0)]

Table 2 Comparison of proposed method with ref. [\[4,](#page-9-0) [16\]](#page-9-0) and [[8\]](#page-9-0) in terms of Embedding Capacity

Cover Image of 512×512	Ref[16] (A)	Ref[11] (B)	Ref [8] (C)	Proposed (D)	% Improvement in Embedding Capacity		
							$\frac{D-A^*}{D}$ 100 $\frac{D-B^*}{D}$ 100 $\frac{D-C^*}{D}$ 100
Capacity	1,52,169	5.24.288	8,51,968	10,48,576	85.48	50	18.75

proposed method not only ensure the imperceptibility and also ensure the consistency for all range of secret message.

The bar chart, Fig. [2](#page-8-0), shows that proposed method achieves the highest embedding capacity of 10,48,576 bits in terms of bit rate 4 bpp, which is 50% and 18.75% higher than that of the algorithm proposed in $[4, 8]$ $[4, 8]$ $[4, 8]$ $[4, 8]$ respectively.

Visual quality of the proposed method with $n = 2$ bits and $n = 4$ bits was verified, Fig. [1,](#page-6-0) by considering five standard images, and compared with experimental result of ref. [[4\]](#page-9-0). The experimental result shows that proposed method has good visual quality. Table.[4](#page-8-0) gives the overall comparison between the proposed and ref. [\[4](#page-9-0), [16\]](#page-9-0) and [[8](#page-9-0)].

For the simulation of our proposed method as well as the ref. [\[4](#page-9-0), [8\]](#page-9-0) we used the HP Pavilion 15 Notebook PC with Windows 10 home 64-bit operating system having the

Table 3 Verification of Consistency of proposed method for different ranges of randomly generated secret message with $n = 4$

Cover Image (512×512) [0 - 3]			$[5-7, 10]$		$[2, 3, 8, 9, 12-15]$		$[0-15]$	
	PSNR	SSIM	PSNR	SSIM	PSNR	SSIM	PSNR	SSIM
Barcode	34.1814	0.3106	34.1966	0.3106	34.1820	0.3106	34.1963	0.3107
House-car	31.1936	0.7055	31.1949	0.7056	31.2055	0.7066	31.2068	0.7058
Jet plane	32.0216	0.5915	32.0201	0.5916	32.0204	0.5915	32.0307	0.5923
Clock	32.4496	0.3885	32.4498	0.3881	32.4648	0.3882	32.4437	0.3878
Tiffany	33.2433	0.6285	33.2460	0.6287	33.2521	0.6285	33.2579	0.6293
Average	32.6179	0.52492	32.62148	0.52492	32.62496	0.52508	32.62708	0.52518

Fig. 2 Comparison of Embedding capacity

following specification intel(R) Core(TM) i5-4210 CPU $@1.70\text{GHz}$ and 12 GB RAM. Simulation tool used: MATLAB, Version: 8.4.0.150421 (R2014b).

5 Conclusion

We have proposed an improved high capacity data hiding method that uses n^2 -ary notational system to represent secret data and the desired value of 'x' to adjust the pixel of cover image is chosen within the restricted limit $-\frac{n^2}{2} \leq x \leq \frac{n^2}{2}$ that enable us to achieve good quality stego-image for $n = 4$. Wen-Chung Kuo et al. [[8](#page-9-0)] achieved maximum embedding capacity of 8,51,968 bits, for gray scale image of size 512×512 , at the cost of an average PSNR of 37.24 dB and we achieved embedding capacity of 10,48,576 bits for an image of size 512×512 at cost of an average PSNR of 32.62708 dB which is 18.75% higher than Wen-Chung Kuo et al. [[8\]](#page-9-0) method and still we are maintaining good visual quality of stego-image. Based on our experimental result and ref. [[8\]](#page-9-0), there is always a tradeoff between the hiding capacity and stegoimage quality. Though we achieved highest embedding capacity, still there is a room to improve both bpp and PSNR, improvement in the proposed work will be our future work.

Items	Ref $[16]$	Ref[4]	Ref[8]	Proposed
Embedding bit rates (bpp) Stego - Image Quality (>30 dB)	1.16 Yes	Yes	3.25 Yes	Yes
Embedding algorithm Modulo Operator	Easy $2n + 1$	Easy $2n + 1$	Complex $2(nk+1)$	Easy n ²

Table 4 Comparison of proposed with [[4,](#page-9-0) [16\]](#page-9-0) and [[8\]](#page-9-0)

References

- 1. Chan C-K, Cheng LM (2004) Hiding data in images by simple LSB substitution. Pattern Recogn 37(3): 469–474
- 2. Datta B, K. Bandyopadhyay S, Kedia A (2014) High Imperceptible Data Hiding using Remainder Method. Int J Comput Appl 95:12–19
- 3. Fua D-S, Jinga Z-J, Zhaoa S-G, Fana J (2014) International Journal of Electronics and Communications 68(10):933–943
- 4. Jung K-H (2009) Improved Exploiting Modification Direction method by modulus operation. International Journal of Signal processing, Image processing and pattern 2(1):79–87
- 5. Khan A, Siddiqa A, Munib S, Malik SA (2014) A recent survey of reversible watermarking techniques. Inf Sci 279:251–272
- 6. Kim C Data hiding by an improved exploiting modification direction. Multimed Tools Appl 69(3):569–584
- 7. Kuo W-C, Kuo S-H, Huang Y-C (2013) Data hiding schemes based on the formal improved exploiting modification direction method. Applied mathematics and information sciences letters 1(3):81–88
- 8. Kuo W-C, Kuob S-H, Wangb C-C, Wuua L-C (2016) High capacity data hiding scheme based on multi-bit encoding function. International Journal for Light and Electron Optics 127(4):1762–1769
- 9. Kuo W-C, Wangb C-C (2013) Data hiding based on generalised exploiting modification direction method. The Imaging Science Journal 61(6):484–490
- 10. Kuo WC, Wuv LC, Kuo SH (2012) The High embedding steganographic Method based on General Multi EMD", IEEE International Conference on Information Security and Intelligence Control (ISIC), pp 286–289
- 11. Lee CF, Wang YR, Chang CC (2007) A Steganographic Method with High Embedding Capacity by Improving Exploiting Modification Direction. Intelligent Information Hiding and Multimedia Signal Processing, Third International Conference, 01: 497-500
- 12. Qiu Y, Qian Z, Yu L (2015) Adaptive Reversible Data Hiding by Extending the Generalized Integer Transformation. IEEE Signal Processing Letters 23(1):130–134
- 13. Shen S-Y, Huang L-H (2015) A data hiding scheme using pixel value differencing and improving exploiting modification directions. Computers & Security 48:131–141
- 14. Tseng HW, Leng HS (2014) High-payload block-based data hiding scheme using hybrid edge detector with minimal distortion. IET Image Process 8(11):647–654
- 15. Wu HT, Dugelay YQ (2014) Shi. IEEE Signal Processing Letters 22(1):81–85
- 16. Zhang X, Wang S (2006) Efficient Steganographic Embedding by Exploiting Modification direction. IEE Communications Letter 10(11):781–783

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