

# **Fabrication of High-Resolution Gamma-Ray Metallic Magnetic Calorimeters with Ag:Er Sensor and Thick Electroplated Absorbers**

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**Abstract** We are developing metallic magnetic calorimeters for high-resolution gamma-ray spectroscopy for non-destructive assay of nuclear materials. Absorbers for these higher-energy photons can require substantial thickness to achieve adequate stopping power. We developed a new absorber fabrication process using dry-film photoresists to electroform cantilevered, thick absorbers. Gamma detectors with these absorbers have an energy resolution of 38 eV FWHM at 60 keV. In this report, we summarize modifications to STARCryo's "Delta 1000" process for our devices and describe the new absorber fabrication process.

**Keywords** MMC · Metallic magnetic calorimeter · Gamma-ray spectroscopy · Non-destructive assay · Thick absorber · Dry-film photoresist

# **1 Introduction**

High-resolution gamma-ray spectroscopy using metallic magnetic calorimeters (MMCs) has already demonstrated improved accuracy over high-purity germanium (HPGe) detectors for a non-destructive assay (NDA) of nuclear materials [\[1\]](#page-4-0).

MMCs are low-temperature particle detectors with an absorber that is thermally coupled to a paramagnetic sensor in a weak magnetizing field. The paramagnetic sensor is magnetically coupled to a SQUID, and the energy of an absorbed particle

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is determined by measuring the temperature-induced magnetization change [\[2\]](#page-4-1). For the gamma rays, the absorbers need to be up to a few hundred microns thick to have enough stopping power [\[3,](#page-4-2) [4\]](#page-4-3).

Our group has been developing gamma-ray MMCs that integrate the SQUIDs and sensing coils to increase flux coupling and signal-to-noise ratio [\[5,](#page-4-4) [6\]](#page-4-5). Fabrication of the absorbers for such MMCs is challenging because SQUIDs are sensitive to chemical attack and elevated processing temperatures. We have recently reported a complete fabrication process to electroform cantilevered Au absorbers by using AZ125nXT photoresist to define the tops and a sacrificial, sputtered Cu blanket layer to define the posts [\[5\]](#page-4-4). Although this process was successfully applied on several test structures, on the real MMC chip we found that film stress of the sputtered Cu damaged underlying structures. We found that the Cu film stress problem could be eliminated if the film was evaporated rather than sputtered. However, an easier fabrication process was highly desirable to reduce the time and frequent rework required for the Cu/AZ125nXT process.

In this report, we introduce a new thick absorber fabrication process by using two layers of dry-film photoresist to define both posts and tops. The new process is completely compatible with the STARCryo Delta 1000 SQUID microfabrication process, enabling future commercial deployment of our integrated SQUID/sensor detector designs. Using the dry-film approach, we have completed fabrication of 14-pixel arrays of integrated SQUID/sensor MMCs with attached absorbers.

## **2 Fabrication**

The MMCs used in this paper were designed by UNM and fabricated in collaboration with STARCryo. Description of all layers is given in Table [1.](#page-2-0) Steps 1–8 are performed on the full 4" wafer and are a modified version of the commercial STARCryo Delta 1000 SQUID process. These steps define the trilayer [\[7\]](#page-5-0) and two Nb wiring layers from which the SQUIDs, sensing coils, input circuits and magnetizing circuits are made. The wafer is then diced into cm-scale MMC chips, and layers 9–13, the Ag:Er paramagnetic sensor, gold thermalization layer, Nb superconducting cap and cantilevered gold absorbers are added.

#### **2.1 Modifications to "Delta 1000" Process for MMC**

The Delta 1000 process was modified to increase the thickness of all wiring layers up to about 500 nm. A second modification to the Delta 1000 process was to reduce the thickness of the  $SiO<sub>2</sub>$  insulation layers, primarily to increase magnetic coupling of the MMC sensing coils to the paramagnet, but measurements with test structures indicated that this also improved the current-carrying capacity of the vias. For our current MMC wafer, the target oxide layer thickness was 100 nm. Although similar thicknesses worked well on test structures, we found random electrical shorts on the wafer. We have recently found that a subtle problem with the PECVD  $SiO<sub>2</sub>$  tool may have been responsible for these shorts. That problem has been fixed and we plan to

#	Layer	Description	Thickness (nm)
1	$Nb/Al-AlOr/Nb$	JJ definition and wiring	420/9.4/60
$\overline{c}$	Nb:Ta62	Passive persistence shunts	250
3	SiO <sub>2</sub>	Insulation	100
4	AuPd	SQUID shunt resistor	175
5	Nb	Second wiring	500
6	SiO <sub>2</sub>	Insulation	100
7	Nb	Third wiring	500
8	Au	Pads	400
9	Ag:Er	Paramagnet	1500
10	Au	Thermalization	250
11	SiO <sub>2</sub>	Insulation	200
12	Nb	Superconducting cap	350
13	Au	Absorber (posts + top)	$20k + 30k$

<span id="page-2-0"></span>**Table 1** Summary of the MMC layers

Given thicknesses are the target values

test the thin oxide again. The third change to the Delta 1000 process was to add a step to deposit Nb:Ta alloy passive persistent-current switches [\[6,](#page-4-5) [8\]](#page-5-1) onto trilayer wiring.

### **2.2 cm-Scale Chip Level Fabrication**

The first step of the cm-scale chip level process is the deposition of the paramagnetic sensor material. To address the parasitic heat capacity problem in Au:Er sensors due to quadrupole splitting of Au nuclei in the presence of Er ions [\[9\]](#page-5-2), we are exploring the use of Ag as a host material. 1000 ppm Ag:Er paramagnet was synthesized in a home-made vacuum/controlled atmosphere induction furnace and sputter deposited on to the chip in a dedicated sputtering system [\[10\]](#page-5-3).

Then, Au thermalization layer is electroplated over the paramagnetic sensor and it is followed by PECVD  $SiO<sub>2</sub>$  blanket insulation layer to isolate the paramagnet from the following superconducting Nb layer. Next the Nb superconducting cap layer is sputter deposited to increase the magnetic field concentration in the paramagnet [\[11\]](#page-5-4). There are circular openings patterned in the superconducting cap to provide access to the paramagnet underneath. As a last step, the cantilevered Au absorbers are electroplated.

#### **2.3 Novel Cantilevered Absorber Fabrication Process**

Two layers of MX5000 series dry-film photoresists from DuPont are used to define the molds for cantilevered absorbers. To define posts,  $20$ - $\mu$ m-thick MX5020 is used, and 50-µm-thick MX5050 is used for the tops. Processing both photoresist layers is very similar. The basic process includes lamination, exposure and development. Prior to



<span id="page-3-0"></span>**Fig. 1** Left, schematic cross section of MMC coil area with two-layer dry-film photoresist mold ready for plating. Nb cap and oxide layer are opened for the electroformed gold to make direct contact to the paramagnet's gold thermalization layer. Right, diagram of the same area after the absorber is electroformed and mold is removed (Color figure online)

laminating the first film, the cm-scale chip is cleaned by rinsing with acetone and IPA and then descummed for 5 min under  $O_2$  plasma. To laminate the film, the protective polyethylene layer is removed from one side. The film is rolled onto the chip on a hot plate at  $90-100$  °C, with gentle pressure applied by a homemade hand roller. Then, the photoresist is exposed with 365-nm UV light with the intensity of 18 mW/cm<sup>2</sup> for 2 s. Although 20 mW/cm<sup>2</sup> or higher intensities are recommended for high resolution,  $18 \text{ mW/cm}^2$  was the maximum intensity available to us. The photoresist is developed in 0.75 wt% K<sub>2</sub>CO<sub>3</sub> solution at 30 °C for 1 min (3 min for MX5050). Then, the chip is rinsed in DI water and dried with a  $N_2$  gun. After each of the lamination, exposure and development steps, the chip is baked on a  $90-100$  °C hot plate for 45 s. These are applied to enhance adhesion, resolution and resistance to chemicals, respectively.

After the posts are defined over the circular holes in the Nb cap layer, the  $SiO<sub>2</sub>$ layer that would otherwise prevent direct contact of the posts to the paramagnet thermalization layer is removed using a dry etch. Then, sample is ashed for 5 min with  $O<sub>2</sub>$ plasma and 250-nm Au seed layer is sputter deposited. Following the seed layer, the 50-µm-thick photoresist film is rolled on and processed to define the absorber tops. A diagram of two-layer mold is shown in Fig. [1](#page-3-0) (left).

Electroplating is done in a 1-L beaker with 250 ml fresh Technic D7990 RTU solution with Au density of 8.2 g/L. Vigorous agitation is sustained by using a water pump directed toward the chip.  $0.25$  A/dm<sup>2</sup> rectangular pulses are applied during plating. After plating both photoresist layers are removed in DMSO with ultrasound at 50–60 °C, typically for 30 min. A diagram of the cantilevered absorber after the mold removed is shown in Fig. [1](#page-3-0) (right).

SEM images of a completed MMC array are shown in Fig. [2](#page-4-6) (left), and on the right, we see a single  $475 \times 475 \times 30 \mu m^3$  absorber on eight  $50$ - $\mu$ m-diameter posts. The thickness of the absorbers is uniform across the cm-scale chip. They are robust, surviving multiple rapid immersions into liquid nitrogen and long sonications in solvents. The residual resistivity ratio (RRR) of meander shape samples which were designed to obtain measurable resistance and were electroplated in the same setup as absorbers was measured to be  $\sim$ 7. This is significantly smaller than what we have previously reported with samples made by using Techni Gold 25ES solution [\[5\]](#page-4-4). Other plating parameters, such as plating temperature and agitation, may have influence on this as well [\[12\]](#page-5-5). Further investigation is on the way to improve the RRR.



**Fig. 2** Left SEM image of complete 14-pixel MMC. Right SEM image of single  $475 \times 475 \times 30 \mu m^3$ cantilevered absorber supported by 8 posts of 50  $\mu$ m dia. (Color figure online)

<span id="page-4-6"></span>In addition to the 30-µm-thick absorbers on the MMC array chip shown in Fig. [2,](#page-4-6) we have successfully produced  $100-\mu m$ -thick absorbers on test structures by layering two 50-µm-thick dry-film photoresists and exposing them together. Resolution of the molds was slightly degraded, and this can be improved by increasing the intensity of the exposure. Additionally, the top surface finish of electroformed 100-µm absorbers is not as smooth as 30- $\mu$ m absorbers. RRR measurements on a rough-surface 100- $\mu$ mthick meander shape sample show no change compared to a smooth-surface  $5-\mu m$ thick meander shape sample, indicating that the rough-surface finish does not imply degradation of RRR, which agrees with the experience of the Heidelberg group [\[13\]](#page-5-6).

The completed devices shown in Fig. [2](#page-4-6) were characterized at 10 mK in a cryostat with dilution refrigerator at LLNL [\[6\]](#page-4-5). Signal rise times of 7  $\mu$ s indicate good thermal coupling between the Au absorber and the Ag:Er sensor. Gamma detectors with these absorbers have an energy resolution of 38 eV FWHM at 60 keV and a Gaussian response function. These properties make our gamma-ray MMCs well suited for high-accuracy non-destructive assay of radioactive materials and other applications in nuclear safeguards.

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