



A comparison electric-dielectric features of Al/*p*-Si (MS) and Al/ (Al₂O₃:PVP)/*p*-Si (MPS) structures using voltage–current (*V–I*) and frequency–impedance (*f–Z*) measurements

B. Akin^{1,*} , J. Farazin², Ş. Altındal¹, and Y. Azizian-Kalandaragh³

¹Department of Physics, Faculty of Science, Gazi University, 06500 Ankara, Turkey

²The Institute for Advanced Studies in Basic Sciences (IASBS), Zanjan 45137-6673, Iran

³Photonics Application and Research Center, Gazi University, 06500 Ankara, Turkey

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ABSTRACT

In this work, both the Al-(*p*-Si) (MS) and Al-(Al₂O₃:PVP)-(*p*-Si) (MPS) structures were grown onto the same *p*-type Si wafer in the same conditions to determine the (Al₂O₃:PVP) organic-interlayer whether the MPS build improves performance or not. For this aim, first, X-ray diffraction (XRD) and field-emission scanning electron microscopy (FE-SEM) was used to investigate the structure of the (Al₂O₃-PVP) inter-layer. Secondly, both the current–voltage (*I–V*) and capacitance/conductance–frequency (*C/G–f*) measurements of them were performed at ambient temperature to the comparison of their electric and dielectric properties. Energy-dependence profile of surface states (*N_{ss}*) was extracted from the positive bias *I–V* data by considering the voltage-dependence of BH and *n*. We found that the (Al₂O₃: PVP) inter-layer leads to a decrease in surface-states (*N_{ss}*), ideality-factor (*n*), leakage-current, series-resistance (*R_s*), and increase in barrier (BH), shunt resistance (*R_{sh}*), rectification-ratio ($RR = I_{for}/I_{rev}$ at ± 6 V). Dielectric permittivity and loss (ϵ' , ϵ''), loss-tangent ($\tan \delta$), real & imaginary components of electric modulus (*M'*, *M''*), and ac-conductivity (σ_{ac}) were extracted from the *C–f* and *G–f* measurements in the wide frequency range of 200 Hz–1 MHz at 1.5 V. The observed higher values in the ϵ' and ϵ'' at lower frequencies for MS and MPS structures were attributed to the *N_{ss}* and easy polarization of interlayer under electric field.

Address correspondence to E-mail: bktakn90@gmail.com

1 Introduction

It is essential to improve cost-effective and high-performance semiconductor devices rather than traditional metal–semiconductor (MS) type structures using an inter-layer at the M-S interface by growing various simple and cheap methods. Therefore, a recent publication from research groups concentrates on the increase in the performance of MS structures by combining doped metal and metal-oxide materials with an interfacial polymer layer [1–8]. Drop-coating, dip-coating, ink printing, spraying, electrospinning, sol–gel, and spin coating are all methods for growing a thin polymer film [6–8]. Among them, polyvinylpyrrolidone (PVP) and polyvinyl-alcohol (PVA) has essential and valuable properties like easy processability, low toxicity, high water-solubility, and good chemical, thermal, and mechanical stability [9–14]. Because nanoparticles have a large aspect ratio, the substantial inter-layer area at the matrix/nano-filler interface is also a field of research in nanocomposites. The grown organic (polymer) inter-layer at the M/S interface, MS transforms to MPS type structure. It is anticipated that the chosen high-dielectric polymer or its composites as interfacial-layer can minimize the N_{ss} , leakage current, R_s , and high BH, R_{sh} , RR, and controllable the current-transport-mechanism (CTMs) [1, 4, 5, 7–9, 15].

In this study, sapphire (Al_2O_3) is also a unique optical material with a large transmittance window from ultraviolet to mid-infrared [16]. Therefore, it is utilized in photonics, optoelectronic devices, and nano-optics because of its robust and outstanding dielectric properties [17]. Alumina also has various polymorphs, such as γ , θ , and α , and is the most widely used α - Al_2O_3 . The crystalline α phase is the most stable among other polymorphs, but metastable phases such as γ and θ also occur during phase transitions and reactions. Therefore, in this study, the (Al_2O_3 :PVP) interlayer was performed on the *p*-Si wafer by the sol–gel method, which has a low cost, low temperature process, and accessible technology preferred to synthesize the high purity Al_2O_3 composite. This method has low cost, low temperature process, and accessible technology to perform high purity Al_2O_3 composite [18, 19].

In this work, we utilized and performed the electric and dielectric characteristics of the Al-*p*-Si (MS) and Al-(Al_2O_3 :PVP)-*p*-Si (MPS) structures on the same *p*-Si wafer to determine the effect of (Al_2O_3 :PVP)

interlayer on these characteristics. First, the structural analysis of the (Al_2O_3 :PVP) nanocomposite was analyzed using XRD and FE-SEM. Then, these two structures' *I*-*V* and (*C*/*G*)-*f* measurements were performed at ambient temperature. According to experimental data, the chosen (Al_2O_3 :PVP) interlayer reduces N_{ss} , leakage current, and R_s while increasing BH, R_{sh} , RR, and more significant electrical charges or energy storage. The results contribute to improving the performance of the MPS diode. Our findings show that the use of (Al_2O_3 :PVP) organic interfacial layer can effectively replace traditional insulator layers and also it has many advantages: low cost, low energy consumption during growing processes, low molecular weight, high-dynamic stress, charge storage capability, and flexibility.

2 Experimental details

In this paper, Al-*p*-Si (MS) and Al-(Al_2O_3 :PVP)-*p*-Si (MPS) structures were grown onto a *p*-Si wafer (Boron doped) with $\langle 100 \rangle$ orientation, 2-inch diameter, $\sim 300 \mu\text{m}$ thickness, and 1–10 $\Omega \text{ cm}$ resistivity. First, the wafer was cleaned in acetone at 60 °C, methanol, and rinsed in distilled water for five min. in an ultrasonic bath. Then, it was cleaned in an 80 °C solution of H_2O , H_2O_2 , and NH_4OH (7:1:1) and finally cleaned using (HF: H_2O) (1:7) solution and rinsed in distilled water, and then the wafer was placed in the deposition chamber. Second, for the fabrication processes, high pure (99.999%) Al with 100 nm thickness was thermally evaporated onto the whole back-side of the wafer at 10^{-6} Torr. Afterward, the *p*-Si/Al wafer was annealed at 500 °C in nitrogen ambient to achieve a better ohmic contact. The *p*-type Si wafer was divided into four quarters with a crystal cutter to perform both MS and MPS structures. The high pure Al-dots (with 1.4 mm radius and 100 nm thickness) were thermally grown onto the first piece of *p*-Si wafer. Hence, the fabrication of Al/*p*-Si (MS) structures was completed. After that, the MPS with MS structures and Al_2O_3 nanostructures were synthesized to compare. For this purpose, the Aluminum-nitrate ($\text{Al}(\text{NO}_3)_3 \cdot 9\text{H}_2\text{O}$) and Ammonia solution (NH_3) purchased from the Loba Chemie were used.

A thin layer of (Al_2O_3 :PVP) was produced and then placed on the *p*-Si wafer using a spin coating process to prepare Al/(Al_2O_3 :PVP)/*p*-Si structures [20]. In an

ultrasonic bath, 10 mg of Al_2O_3 nano-powder was dissolved in 5 ccs of PVP solution, yielding a thin (Al_2O_3 :PVP) 5 percent solution of PVP polymer. Then, the produced (Al_2O_3 :PVP) composite was coated on the top-surface *p*-Si wafer using a spin coating process. The fabrication of $\text{Al}/(\text{Al}_2\text{O}_3$:PVP)/*p*-Si (MPS) structures was then completed by thermally evaporating high-purity Al dots with the same area and thickness onto (Al_2O_3 :PVP)/*p*-Si wafers. Figure 1a, b depicts the synthesis stages for Al_2O_3 nanostructures and the processing methods for $\text{Al}/(\text{Al}_2\text{O}_3$:PVP)/*p*-Si structures.

The produced MS and MPS structures were mounted on the Cu holder using a silver (Ag) paste, and the electrical probes were made with silver-coated Cu wires to perform *I*–*V* and (*C*/*G*)–*f* measurements. XRD (Philips X pert, Netherlands) was used to study the structural properties of produced nanostructures using Cu K radiation ($= 1.5406$) as an X-ray source, with 2θ ranging from 10° to 80° . Surface topography examined FE-SEM (Tescan-Mira III, Czech Republic). More information on the XRD and FE-SEM can be found in [21]. Optical Analysis of Al_2O_3 nanostructures was done using an Ultraviolet-visible spectroscope (UV-1800, Shimadzu, Japan). The distinctive spectrum absorption peak at 265 nm/4.7 eV in the range can be attributed to Al_2O_3 intrinsic band-gap absorption. The energy gap was also determined from the $(\alpha h\nu)^2$ –*h* ν plot, and the band-gap value of Al_2O_3 nanostructures for direct transitions was found as 4.9 eV. The *I*–

V measurements were done by a Keithley 2400 *I*–*V* source meter, and *Z*–*f* including *C*–*f* and *G*/ ω –*f* measurements were done by a KEYSIGHT (E4980A1 20 Hz–1 MHz).

3 Results–discussion

3.1 Electrical properties

The semi-logarithmic *I*–*V* curves of the MS and MPS structures are given in Fig. 2. This figure shows that these two structures have linear regions at moderate bias voltage and then deviate from the linearity owing to R_s and interfacial layer at the semiconductor/metal (S/M) interface. Due to the depletion layer, R_s , and the interlayer will share the voltage provided to the diode. However, in the reverse bias region, the current increases gradually with the voltage bias, and this is an undesirable situation. This non/soft-saturation behavior was attributed to the tunneling via surface states, image-force lowering of BH, barrier inhomogeneities between semiconductor and metal, and lower shunt resistance values (R_{sh}) [5, 7, 13].

Since the applied voltage is higher than 3 (kT/q) and the diode has both a series resistance and ideality factor higher than unity, the charge carries flow through the BH of the diode obeys the TE theory at room or above temperature as given below [7, 22].

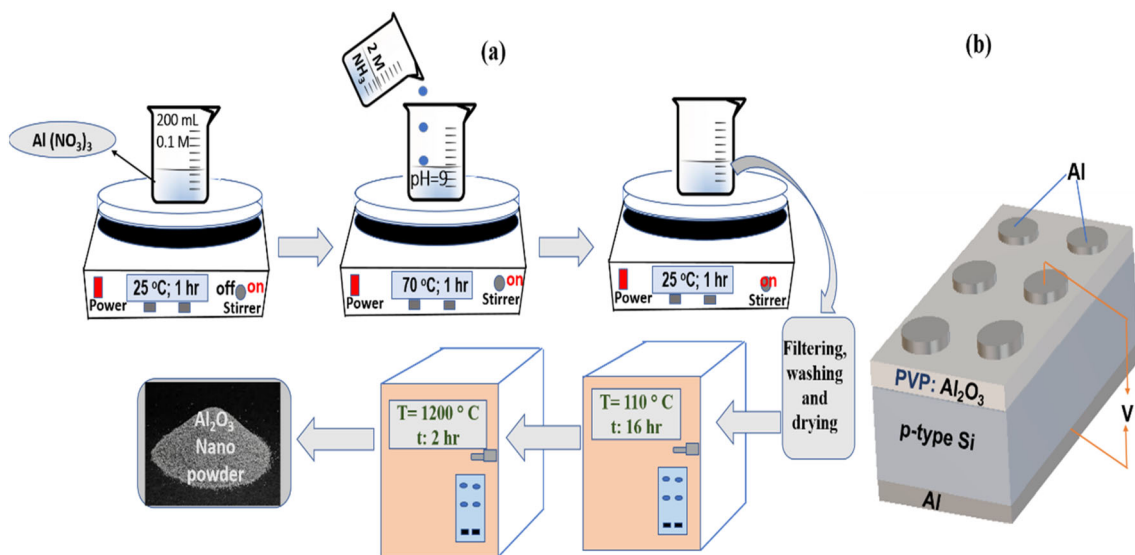


Fig. 1 The schematic diagram of **a** synthesis steps of Al_2O_3 nanostructures, **b** the fabricated of $\text{Al}/(\text{Al}_2\text{O}_3$:PVP)/*p*-Si (MPS) structures

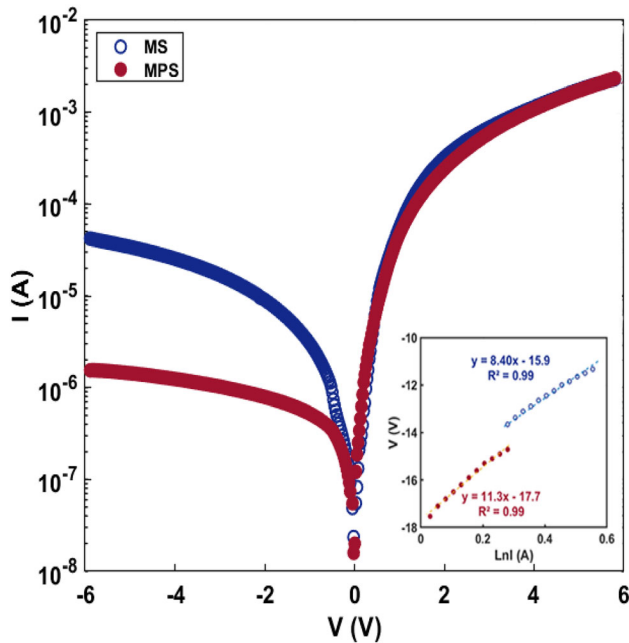


Fig. 2 The semi-logarithmic I - V curves of the Al- p -Si and Al(Al_2O_3 :PVP)- p -Si type structures

$$I = AA^* T^2 \exp\left(-\frac{q\Phi_{\text{Bo}}}{kT}\right) \left[\exp\left(\frac{q(V - IR_s)}{nkT}\right) - 1 \right] \quad (1)$$

In Eq. 2, A^* , Φ_{Bo} , k , T , and q , are the Richardson constant ($= 32 \text{ A}(\text{cm K})^2$) for p -Si [23, 24], respectively, the effective barrier height (BH), Boltzmann's-constant, the temperature (in Kelvin), and the electron charge, respectively. Here, the term in front of the square brackets is the saturation current (I_s) and can be determined from the intercept of the $\ln(I)$ - V plot at $V = 0$. On the other hand, the value of the ideality factor (n) can also be calculated from the slope $\ln(I)$ - V curve by applying relation [7, 22, 25].

$$n = \frac{q}{kT} \cdot \left[\frac{d(V - IR_s)}{d \ln(I)} \right] = 1 + \frac{\delta}{\epsilon_i} \left[\frac{\epsilon_s}{W_D} + qN_{\text{ss}} \right] \quad (2)$$

After the obtained value of I_s , the value of Φ_{Bo} can be calculated using the Schottky contact area (A) following relation [22, 23].

$$\Phi_{\text{Bo}} = \left(\frac{kT}{q} \right) \cdot \ln \left[\frac{AA^* T^2}{I_s} \right] \quad (3)$$

Thus, the obtained I_s , n , Φ_{Bo} values from the linear sections $\ln(I)$ - V plot and RR at $\pm 6 \text{ V}$ of the fabricated MS and MPS structures were calculated using forward bias I - V data based on TE theory and have

also been reported in Table 1. The electrical properties or current-transport mechanisms (CTMs) of the MS, MIS, or MPS type Schottky-diodes/structures depend on many factors such as the barrier-inhomogeneity, native or deposited an interlayer at the M/S interface, surface-states (N_{ss}), the concentration level of doping atoms into semiconductor or depletion layer width (W_d). Surface-states with energies located in the band gap of semiconductors act as recombination centers that can capture or release some electrons. According to Card and Rhoderick, these traps may be occurred by unsaturated dangling bonds of the surface atoms and some organic contaminations in a laboratory environment [23, 26]. According to Eq. 2, the value of n may be approximated to the ideal case ($n = 1$) using a high-dielectric interlayer and so passivated the level of N_{ss} . The existence of barrier inhomogeneity at the M/S interface with many lower barriers, patches, or pinch-off at around mean BH is also more effective on the CTMs through the barrier. Thus, the value of current and so n become increased. Therefore, as seen in Table 1, the obtained values of n are much higher than the ideal case. The higher value of n was attributed to the inter-layer thickness (δ) and its permittivity (ϵ_i), surface-states (N_{ss}) located at the junction, and depletion-layer thickness (W_D), as shown in Eq. 3. BH formation in-homogeneity is another reason for a higher value of n [26–30].

Table 1 To compare them, they calculated some basic electrical parameters of the MS and MPS type structures from the TE theory, Cheung, and Norde models

Sample	MS	MPS
<i>TE theory</i>		
I_0 (nA)	127.0	20.6
n	4.60	3.42
Φ_{Bo} (eV)	0.688	0.735
R_s (k Ω), (at 6 V)	2.45	12.13
R_{sh} (M Ω), (at -6 V)	0.14	3.90
RR (at $\pm 6 \text{ V}$)	57	322
<i>Cheung functions</i>		
n , from $dV/d \ln(I)$ vs I plot	6.15	5.18
Φ_B (eV), from H vs I plot	0.683	0.780
R_s (k Ω)	5.81	16.30
Φ_B (eV), from Norde method	0.700	0.801
R_s (k Ω) from Norde method	3.20	15.80

The non-linear region in the forward bias of these diodes I - V characteristics (Fig. 3) results from R_s and interlayer. However, in both linear and non-linear forward bias regions, the BH and n are critical. Because they are dependent on voltage as well as temperature/illumination. Moreover, if the I - V curve is not sufficiently linear, the accuracy and reliability of these calculated parameters become questionable. Therefore, Cheung & Cheung proposed a new method utilizing the following relations to obtain the n , BH, and R_s values in a second way [24, 26].

$$dV/d\ln(I) = (kT/q).n + R_s I \tag{4a}$$

$$H(I) = V - (n.kT/q). \ln(I/(A^*AT^2)) = (F_B.n) + R_s I \tag{4b}$$

The $dV/d\ln(I)$ vs. I and $H(I)$ vs. I curves of the fabricated MS and MPS structures, respectively, are shown in Fig. 3a and b. As seen in Fig. 3, these figures have good linear parts in a broad range of forwarding currents. According to Eq. 4a and b, the intercept and slope of the $dV/d\ln(I)$ - I plot gave us the $n.(kT/q)$ and R_s , respectively. The $H(I)$ vs. I plot was generated for these two diodes that use this value of n in Eq. 4b. After that, the intercept and slope of this plot gave us the value of $(n.\Phi_B)$ and the second value of R_s , respectively. In this way, the obtained values of n , BH, and R_s are also tabulated in Table 1. These essential electrical characteristics acquired from the TE and Cheung & Cheung functions are in agreement and suitable for MIS or MPS type structures, as illustrated in Table 1. The voltage and the

computation method account for the little discrepancies between these parameters [5, 22, 23, 31] (Fig. 4).

In MS, MIS, and MPS type diodes/structures, the Norde method can also calculate the R_s and BH value [32]. For cases where the ideality factor is considerably more significant than the ideal case ($n > > 1$), Bohlin has modified the Norde function as follows [33]. The $F(V)$ - V plots for MS and MPS type diodes were obtained from Eq. 5a. According to the Norde method, the $F(V)$ - V curve must pass through a minimum at moderate forward bias voltages if the diode has a series resistance.

$$F(V) = (V/\gamma) - (kT/q) [\ln(I(V)/(A^*AT^2))] \tag{5a}$$

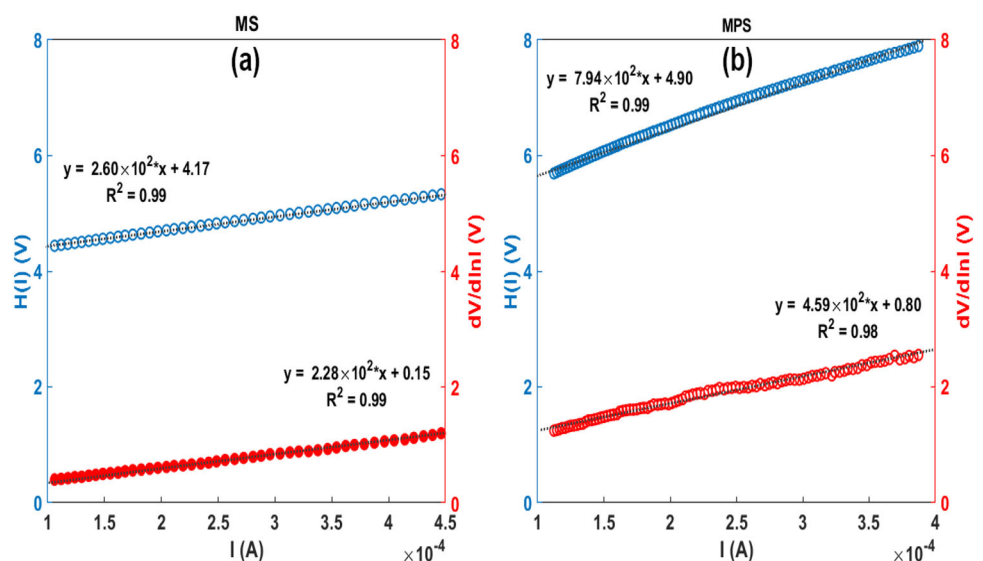
Here, γ is a dimensionless constant and must be chosen bigger than n . As shown in Fig. 6, $F(V)$ vs. V plots for two different structures have a minimum point corresponding to I_{min} or I_o current and V_{min} or V_o voltage. The values of R_s and BH (Φ_B) were calculated via relations by these equations:

$$R_s = \frac{kT(\gamma - n)}{qI_o} \tag{5b}$$

$$\Phi_B = F(V_o) + \frac{V_o}{\gamma} - \frac{kT}{q} \tag{5c}$$

Using Eq. (5b) and (c); the values of V_o , F_o , Φ_B , and R_s were found as 0.680 V, 0.630 eV, 0.702 eV, and 3.2 k Ω for MS structure and 0.635 V, 0.700 eV, 0.801 eV, and 15.8 k Ω for MPS structure, respectively.

Fig. 3 The $dV/d\ln(I)$ - I and $H(I)$ - I curves of the MS and MPS type structures



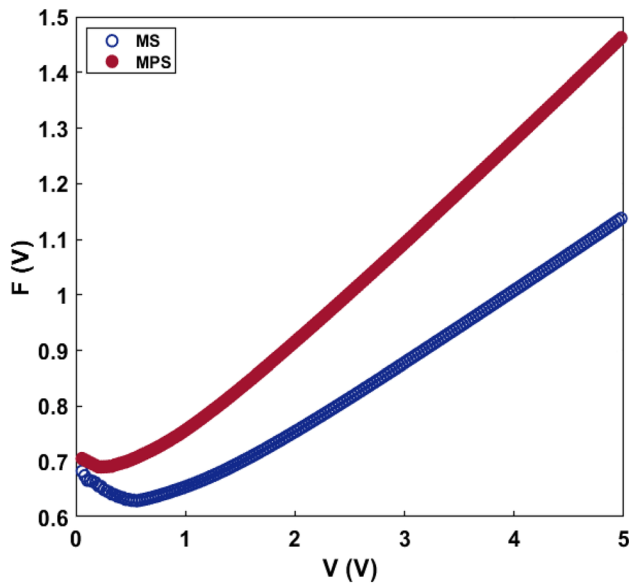


Fig. 4 The $F(V)$ – V curves of the MS and MPS type structures

As shown in Table 1, the electrical parameters of the MS and MPS type structures depend on the calculation method equaled to the different forward applied bias voltage. In other words, all these electrical parameters are dependent on voltage and temperature. Because while standard TE theory is valid for intermediate bias voltage, Norde and Cheung functions are valid around the concave curvature of the semi-logarithmic I – V plots, as shown in Fig. 2. In addition, the values of I_0 , leakage current, and n for the MPS structure are less than that for the MS structure, R_{sh} , RR, and BH values are higher. On the other hand, R_s for the MPS structure is higher than MS structure and its effect in the linear part of the $\ln I$ – V plot can be ignored. R_s is active in enough high forward voltage regions and causes the $\ln I$ – V curve to deviate from highly non-linearity. The applied voltage in this region will be shared by the depletion layer, polymer interface layer, and R_s . In other words, the high series resistance value can be rooted in the interfacial layer, its homogeneity, space-charge injection, and interface states or dislocations.

These results show that the high-dielectric (Al_2O_3 :PVP) inter-layer between semiconductor and Schottky contacts increases MPS structure performance compared to MS structure.

Surface states are usually rooted in the dangling bonds at the interlayer-semiconductor and during the cleaning and fabrication processes and their energy levels in the band-gap (E_g) of semiconductors. They

affect both the I – V and Z – f characteristics. These states/traps act as recombination centers. So that they can capture some electrons if they are empty and release them if they are trapped under appropriate forward applied bias. Therefore, these states regulate the charge transfer between metal and semiconductors depending on interfacial layer thickness. They are in equilibrium metal if the interfacial layer thickness is less than 3 nm, and they are in equilibrium semiconductors if it is greater than 3 nm [5–7, 23]. The N_{ss} – $(E_v - E_{ss})$ profile was also obtained from the forward current and voltage data in this investigation by taking voltage-dependent $n(V)$ and $\Phi_B(V)$ into account, as shown below [23]:

$$n(V) = \frac{qV}{kT \cdot \ln(I/I_0)} = 1 + \frac{\delta}{\epsilon_i} \left[\frac{\epsilon_s}{W_D} + qN_{ss}(V) \right] \quad (6a)$$

$$\Phi_e = \Phi_{B0} + a(V) = \Phi_{B0} + \left(1 - \frac{1}{n(V)} \right) V \quad (6b)$$

$$E_{ss} - E_v = q(\Phi_e - V) \quad (6c)$$

Here, α is equal to $d\Phi_e/dV$ or $(1-n(V))^{-1}$, Φ_e is the efficient BH and ϵ_i is the permittivity of the interlayer. Thus, the $N_{ss} - (E_{ss} - E_v)$ profile of the MS and MPS type structures was plotted using Eq. (6a)–(c) and given in Fig. 5. As shown in Fig. 5, the N_{ss} values substantially increased from the mid-gap of p -Si to the top of the valance band (E_v). The value of N_{ss} for MPS is considerably lower than the conventional MS structure because of the passivation of some surface

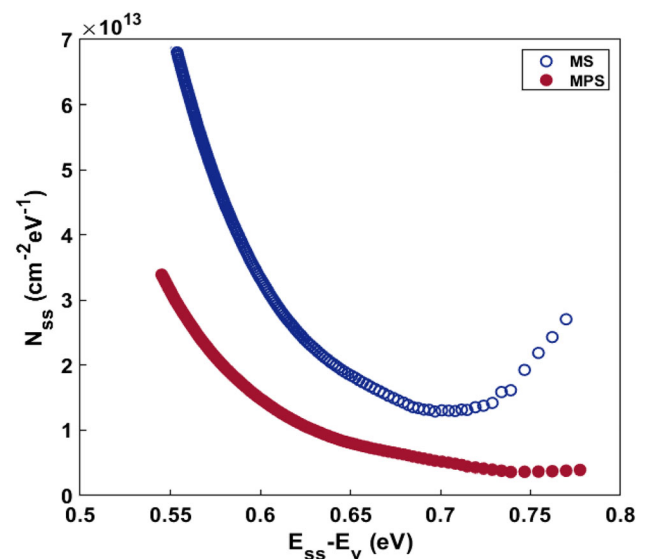


Fig. 5 The N_{ss} – $(E_{ss} - E_v)$ profile of the MS and MPS type structures

states using (Al₂O₃:PVP) organic interlayer between Al and p-Si semiconductor.

The forward double-logarithmic I_F-V_F and reverse $\ln(I_R)$ vs. $\sqrt{V_R}$ plots were generated and given in Fig. 6a and b, respectively, to investigate the current-transport mechanisms (CTMs) in the MS and MPS type structures. As illustrated in Fig. 6a, the double logarithmic I_F-V_F for the Al/(Al₂O₃:PVP)/p-Si structure has three different segments with different slope (m) and intercept points which is proportional to the $I \sim V^m$ power law. The value of m was obtained as 1.10, 3.18, and 1.87, corresponding to low, intermediate, and higher bias voltages, respectively. At lower forward bias voltages, m ($= 1.10$) is considerably lower than 2, so the dominant CTM for low forward bias voltages is ohmic voltages.

Surface states fill up as the number of injected electrons increases. At higher bias voltage, m ($= 1.87$) is closer to 2, so the dominant CTM is space charge limited-current (SCLC) due to the strong electron injection. On the other hand, the $\ln(I_F)-\ln(V_F)$ plot has only two linear segments at low and higher voltages. The value of m was found as 1.12 and 2.32 in these regions, which indicated that ohmic and SCLC mechanisms are dominated in these regions, respectively. These results show that the CTM may depend considerably on applied bias voltage, interfacial layer, and R_s . As a result, to investigate the effective CCMs of MS, MIS, or MPS type Schottky diodes (SDs) in the whole forward bias voltage, the $\ln(I_F)-\ln(V)$ plot should be drawn as shown in Fig. 6a. As shown in Fig. 6a, both the MS and MPS type SDs have different linear parts with different slopes (m) and intercepts. This linearity indicates the power-

law behavior, which is the $I \sim V^m$ relationship between V and I . The m value is corresponding to the double-logarithmic I_F-V_F . This difference between MS and MPS SDs is the result of interlayer and a special distribution N_{ss} at the native/deposited interlayer/semiconductor interface. These obtained m values for each region can be explained as follows [1–3, 34, 35]: (a) When the value of m is close to unity (~ 1), then Ohmic behavior is effective CCM due to the thermally generated carriers. (b) When the value of m for two SDs is closer to two, then the strong electron injection may occur and hence they contribute to the SCLC. (c) When the value of m is considerably greater than two, then dominant CCM has been interpreted as TLC because many injected electrons lead to the filling of surface states or traps.

Additionally, to investigate the CTMs in the reverse bias region, semi-logarithmic $\ln(I_R)-(V_R)^{0.5}$ plots for the MS and MPS type structures are given in Fig. 7b. Generally, Poole Frenkel (PF) or Schottky emission (SE) mechanism CTMs may dominate. The reverse current for the PF and SE is given as follows, respectively [34, 36].

$$I_R = I_o \exp\left(\frac{\beta_{PF} V^{0.5}}{kTd^{0.5}}\right) \tag{7a}$$

$$I_R = I_o \exp\left(\frac{\beta_{SC} V^{0.5}}{kTd^{0.5}}\right) \tag{7b}$$

$$\beta_{PF} = \left(\frac{q^3}{\pi\epsilon_0\epsilon_r}\right)^{0.5} = 2\beta_{SC} \tag{7c}$$

In Eq. (7a) and (7b), β_{PF} is the PF field decreasing coefficient; β_{SC} is the SE field decreasing coefficient; d

Fig. 6 a The $\ln(I_F)-\ln(V_F)$, and **b** $\ln(I_R)-\sqrt{V_R}$ curves of the MS and MPS type structures

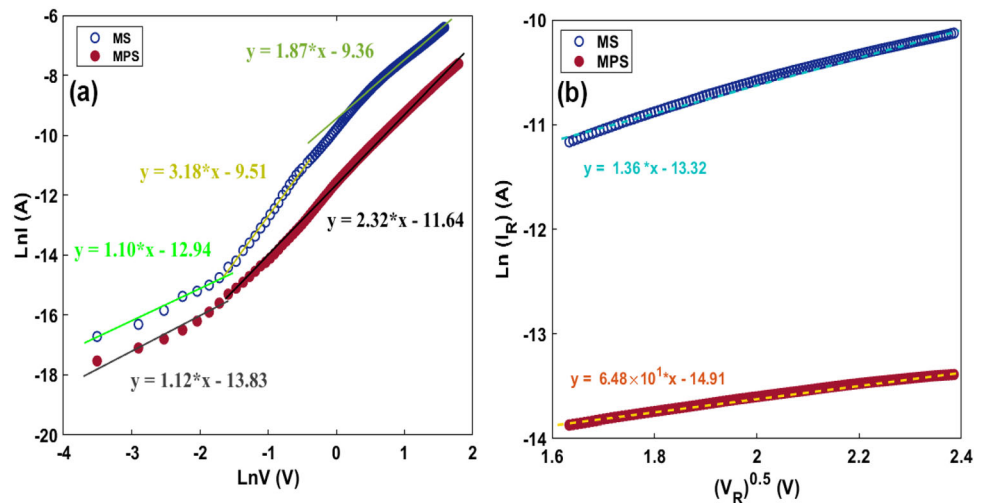
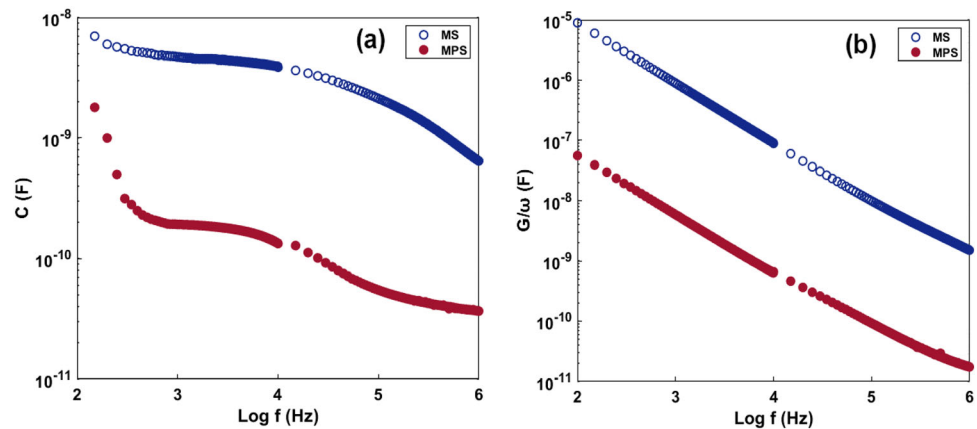


Fig. 7 The curve of **a** $C-\log(f)$ and **b** $G/\omega-\log(f)$ of the MS and MPS type structures



is the interlayer's thickness, and ϵ_r is its permittivity. According to Eq. (7c), the β_{PF} is twice β_{SC} , and the value of β can be derived from the $\ln(I_R)-\sqrt{V_R}$ curve for two types of diodes. The theoretical values of β_{PF} and β_{SC} were determined to be 3.89×10^{-5} and 1.95×10^{-5} (m.V) $^{0.5}/\text{eV}$ for MS, 2.53×10^{-5} and 1.26×10^{-5} (m.V) $^{0.5}/\text{eV}$ for MPS type diodes, respectively. Figure 7b shows that the semi-logarithmic $I_R-(V_R)^{0.5}$ plot has a strong linear regime for the MS and MPS type SD. The field-lowering coefficient of the MS and MPS type diodes were calculated to be 1.32×10^{-5} (m.V) $^{0.5}/\text{eV}$, 2.32×10^{-5} (m.V) $^{0.5}/\text{eV}$, respectively. These values are indicated that SE governs the dominant CCM for MS diode because the experimental value of the field-lowering coefficient is quite closer to the theoretical value. β_{SC} theoretical value. On the other hand, CCM for the MPS diode is governed by PF since the experimental value is extremely near to the theoretical value.

CCM is usually dependent on various parameters such as the fabrication or surface processes, the nature of height, the interfacial layer at the M/S interface, surface states localized in the forbidden band-gap of semiconductor, sample temperature, applied voltage, and the doping concentration of donor or acceptor atoms [24, 26]. But a number of them such as TE, TFE, FE, GR, tunneling via surface states or dislocations, and Gaussian distribution (GD) of BHs compete unusually, one of them may dominate over the others in a certain temperature, and applied bias voltage region [37, 38]. Among them, the tunneling mechanism (FE and TFE) is important at low temperature and high doping concentration levels, and hence these two mechanisms are ruled out in this study. Therefore, obtained high value of n and the deviation linearity of $\ln I$ vs V plot at enough high

forward bias voltage can be attributed to the existence of barrier inhomogeneity, N_{ss} , native or deposited interfacial layer, and R_s , and among them, N_{ss} and barrier inhomogeneity are effective at moderate forward bias voltage, but interfacial layer and R_s are effective at higher forward bias voltage, respectively.

3.2 Dielectric properties

In recent years, the investigation of real and imaginary parts of complex-dielectric ($\epsilon^* = \epsilon' - j\epsilon''$), complex-dielectric modulus ($M^* = M' + jM''$), and AC conductivity in a wide frequency range for technological applications have had tremendous interest in. On the other hand, to get an ultra-capacitive with more charges/energy storage capacity, it is necessary to perform flexible, higher values of ϵ' , lower values of dielectric loss (ϵ''), lower values of tangent-loss ($\tan\delta = \epsilon''/\epsilon'$), thin, cheap, environmentally friendly, and light-weight an inter-layer between semiconductor and metal. As a result, the impedance spectroscopy (IS) technique is a critical and reliable approach for revealing the electrical and dielectric properties of metal–semiconductor (MS) devices with an interfacial layer such as oxide/insulator, polymer, or ferroelectric materials at the M/S interface [36, 39–42]. The values of ϵ' , ϵ'' , $\tan\delta$, M' , M'' , and ac electrical-conductivity (σ_{ac}) can be used to derive the measured real and imaginary components of admittance ($Y^* = 1/Z^* = G + j\omega C$) as given following relations, respectively [36, 39, 40]:

$$\epsilon^*(\omega) = \frac{Y^*(\omega)}{j\omega C_o} = \frac{C_m d_i}{\epsilon_o A} - j \frac{G_m d_i}{\omega \epsilon_o A} = \frac{C_m}{C_o} - j \frac{G_m}{\omega C_o} \quad (8a)$$

$$M^*(\omega) = \frac{1}{\epsilon^*(\omega)} = M' + jM'' = \omega C_o Z'' + i\omega C_o Z' \\ = \frac{\epsilon'}{\epsilon'^2 + \epsilon''^2} + j \frac{\epsilon''}{\epsilon'^2 + \epsilon''^2} \tag{8b}$$

$$\tan \delta = \frac{\epsilon''}{\epsilon'} = \frac{G_m}{\omega C_m} \tag{8c}$$

$$\sigma_{ac}(\omega) = \left(\frac{d_i}{A}\right) \omega C \tan \delta = \epsilon'' \omega \epsilon_o = \epsilon'' (2\pi f) \epsilon_o \tag{8d}$$

Here, the $C_o(= \epsilon_o A/d_i)$, A , j , $\omega(= 2\pi f)$, and d_i quantities are the geometrical/empty capacitance, rectifier contact area, angular frequency, j is the square root of -1 , and inter-layer is the thickness. As illustrated in Fig. 7a, b, the value of C and G at 1.5 V decreases with increasing frequency for MS and MPS type structures. The capacity of the charge carriers to follow the ac signal causes this phenomenon. It is associated with the N_{ss} and their lifetime (τ) between native/deposited interfacial layer and Schottky metal contacts. Because N_{ss} and polarization (surface/dipole) can easily follow the ac signal at lower frequencies and provide an excess C and G compared to their real values [41], the charges at states/traps cannot follow the ac signal at higher levels of frequencies. So they cannot contribute to the measured real value of them. Similar to C vs. $\ln(f)$ and G/ω vs. $\ln(f)$, as indicated in Fig. 8a and b, the N_{ss} and interfacial layer are the cause of altering the frequency dispersion property in the value of ϵ' and ϵ'' especially at lower frequencies.

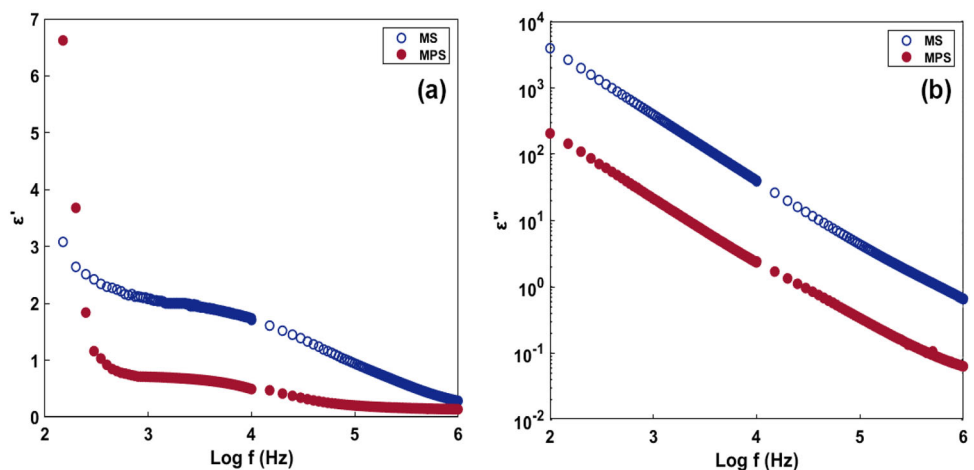
Therefore, the observed decrease in these values with increasing frequencies in the fabricated MS and MPS type structures can be explained by the existence of N_{ss} and easy polarization of the interlayer

layer under an electric field. These results indicate that the (Al_2O_3-PVP) interlayer can be successfully used in place of the conventional native or deposited SiO_2 insulator layer by growing traditional techniques such as thermal oxidation and sputtering methods, and such high dielectric it has some advantages in respect of low cost, low energy consumption all through growing processes, low weight per molecule, high-dynamic stress, high-charge storages capability, and flexibility [43].

Figure 9 shows the tangent-loss vs. $\log(f)$ curves of the MS and MPS type structures obtained in Eq. 8c. The value of $\tan \delta$ declines with increasing frequency, and it approaches the optimal outcome at sufficiently high frequencies because both N_{ss} and polarization effects can be ignored at high frequencies [40–42, 46]. As shown in Fig. 9, the value of $\tan \delta$ increases with increasing frequency until at about 600 Hz and then decreases with an increasing frequency almost exponentially for MPS type structure depends on frequency-dependent values of ϵ' and ϵ'' . In general, at low frequencies, Maxwell–Wagner type interfacial polarization and hopping mechanisms of charges can occur between traps or conduction bands under an external electric field can be used to explain the peak in the loss tangent. Because, at low frequencies, surface states can easily follow the ac signal and dipoles have enough time to rotate around themselves [44].

In Fig. 10a, the computed resistivity (R_c) for two types of structures using the Nicollian and Brews method [47] declines with increasing frequency, and it approaches the actual value R_s at a sufficiently high frequency. The increasing value of σ_{ac} with frequency rise is attributed to the space charge polarization. It is also almost frequency independent at

Fig. 8 The curve of **a** ϵ' - $\log(f)$ and **b** ϵ'' - $\log(f)$ of the MS and MPS type structures



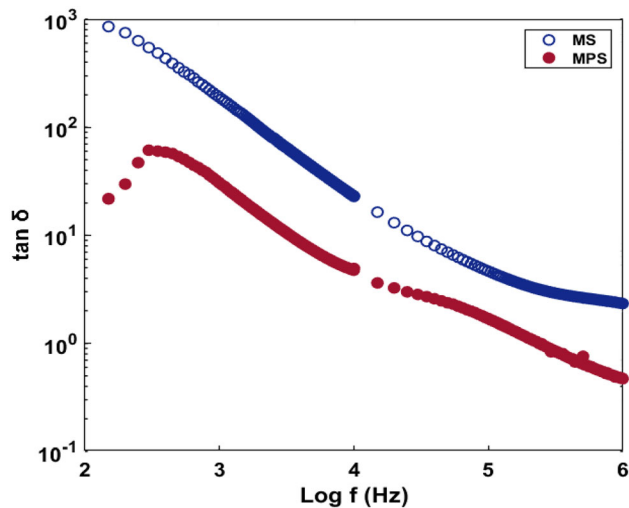
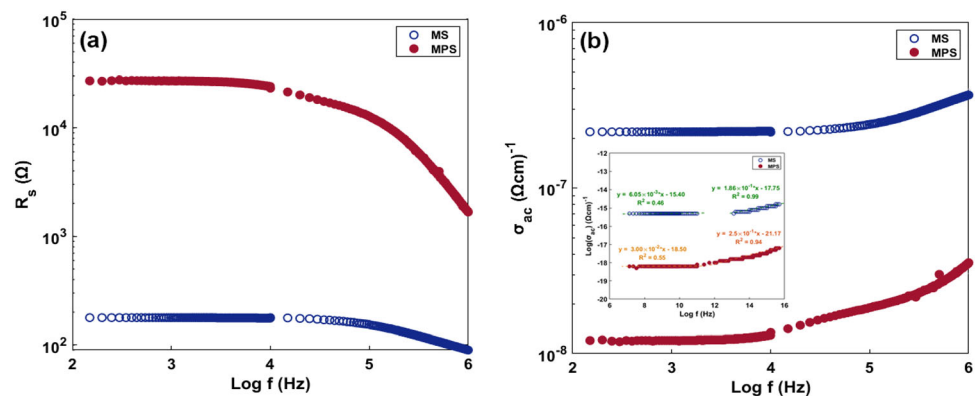


Fig. 9 The $\tan(\delta)$ – $\log(f)$ curve of the MS and MPS type structures

Fig. 10 The curve of **a** R_s – $\log(f)$, **b** the $\log(\sigma)\log(f)$ plot of the MS and MPS type structures



low frequencies. As the frequency decreases, charge accumulation occurs at the polymer-semiconductor interface. Due to the utilized polymer interlayer, the MS has a lower ac electrical conductivity than MPS. The low value of R_s in the MPS relative to the MS diode is what causes this behavior. Therefore, the conductivity value is low at low frequencies. This result is also compatible with the literature, where it is suggested in [48].

In addition, as shown in Fig. 10b, electrical conductivity often rises with increasing frequency due to a decrement in R_s with rising frequency [42, 46].

Double-logarithmic σ_{ac} – f plots of the $\tan(\delta)$ Al/*p*-Si and Al/(Al₂O₃:PVP)/*p*-Si structures are given in Fig. 10b, and the $\log(\sigma)\log(f)$ plot for forwarding bias demonstrates plateau for low and high-frequency regions related to σ_{dc} and σ_{acr} , respectively. The dual logarithmic σ_{ac} – f plot is acceptable with a power-law frequency dependency which is shown in Fig. 10b [39, 40]:

$$\sigma(\omega) = \sigma_{dc} + A\omega^s \quad (9)$$

In Eq. (9), σ_{dc} represents dc electrical conductivity with a total electrical conductivity that is nearly constant at low and intermediate frequencies, A represents a constant, and s represents the slope of the $\ln(\sigma_{ac})$ – $\ln(f)$ plots for a given bias voltage. When s is less than 1, it is assumed that the jumping motion consists of a translational motion followed by an abrupt jumping. In the event of a value greater than 1, however, the mobility includes confined jumping without the species leaving its neighbors [41, 42, 46, 47].

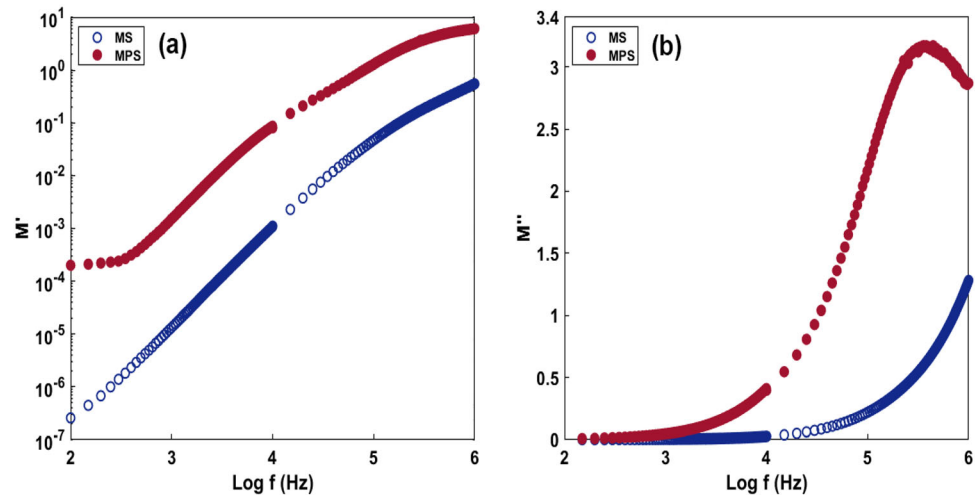
The change of M' and M'' with frequency at 1.5 V at room temperature is shown in Fig. 11a and b. Due to the short-range mobility of carriers, the value of M'

clearly increases as frequency increases. M'' – $\log(f)$ plots for the dielectric relaxation process should give a peak for MPS structure corresponding to the $\omega\tau = 1$ point due to the transition between short and long-range charge carrier mobility. Moreover, it is shown that the relaxation frequencies shift towards higher frequencies. In recent years, the literature has reported similar results [49, 51].

4 Conclusion

In this study, either the MS or MPS type structures were grown onto the same pSi wafer in identical conditions to see the effects of the high-dielectric (Al₂O₃–PVP) organic inter-layer on the electrical and dielectric characteristics at room temperature. Some critical basic electrical parameters were first extracted from the forward and reverse bias I – V data using TE, Norde, and Cheung functions. They were discovered

Fig. 11 The curves of **a** M' and **b** M'' vs. frequency of the MS and MPS type structures



to be a function of voltage and the computed method utilized. While the MPS type structure has smaller I_{or} , leakage current, and n than the MS structure, the MPS structure has higher I_{or} , leakage current, and n , and the R_{sh} , RR, and BH values have higher. These results show that using a high-dielectric (Al_2O_3 :PVP) interlayer between semiconductor and Schottky contacts leads to increased MPS structure performance compared to MS structure. Considering BH's dependence, the energy-dependent profile of N_{ss} was derived from the forward bias I - V data. The magnitude of N_{ss} for the MPS structure is also lower than the MS structure because of the passivation effect of the (Al_2O_3 :PVP) interfacial layer. To investigate the effects of polarization, D_{itr} and the (Al_2O_3 -PVP) interlayer on these parameters, the values of ϵ' , ϵ'' , $\tan\delta$, σ_{acr} , M' , and M'' were acquired from C - f and G/ω - f data over a significant frequency range of 100 Hz–1 MHz. These parameters were found to have a vital function of frequency. Using this, sapphire interfacial layer increases the performance of the MS-type SBDs concerning drops in n and leakage current and a rise in rectifying rate (RR), BH, ϵ' . As a result, the used (Al_2O_3 -PVP) interlayer can be successfully fabricated in place of the traditional insulator layer by growing traditional techniques such as thermal oxidation and sputtering techniques, and it has many advantages such as low cost, low energy consumption during growing processes, low weight per molecule, high-dynamic stress, high-charge storages capability, and flexibility.

Author contributions

All authors contributed to the study conception and design. Material preparation, data collection and analysis were performed by the corresponding author Dr. Buket Akin. The first revised draft of the manuscript was written by Dr. Buket Akin and all authors commented on previous versions of the manuscript. All authors read and approved the final manuscript.

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Data availability

The datasets analyzed during the current study are available from the corresponding author on reasonable request.

Declarations

Conflict of interest The authors declare that they have no conflict of interest.

Informed consent The research is not involving Human Participants and/or Animals and informed consent.

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