Review



Poly(α -methyl styrene) polymer additive for organic thin film transistors

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ABSTRACT

While great progress has been achieved in the research of various solutionprocessed organic semiconductors, the randomized crystal orientations and charge carrier mobility variations have posed tremendous challenges to implement the organic semiconductors for organic electronic device applications. Among the miscellaneous polymer additives reported to tune the crystal growth and modulate charge transport, $poly(\alpha$ -methyl styrene) (P α MS) has been extensively studied for its capability to improve semiconductor crystallization, reduce bulk crystal misorientation, induce phase segregation, enhance morphological uniformity and boost electrical performance of organic thin film transistors and organic electronic devices. In the first section of this article, we review the recent progress of organic electronics and highlight the crystal misorientation and mobility variation as the challenges that need to be overcome. Then, the various merits from mixing polymeric additives with organic semiconductors are discussed. In the second section, we provide an overview of the previous works that employ $P\alpha MS$ for regulating the crystal orientation alignment and modulating charge transport of miscellaneous solution-processed small-molecular organic semiconductors including 6,13-bis(triisopropy-(TIPS and lsilylethynyl) pentacene pentacene) 2,8-difluoro-5,11bis(triethylsilylethynyl)anthradithiophene (diF-TESADT). By discussing these important examples, we intend to demonstrate that $P\alpha MS$ can be versatilely implemented to improve other new organic semiconductor crystallization and mobility for high-performance organic electronic applications.

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1 Introduction and background

In spite of the fast development of solution-processable organic semiconductors, the randomized crystal orientation and charge carrier mobility variations have posed technical challenges to implement the organic semiconductors in organic electronic device fabrication and application. In this section, we first provide an overview of the recent advances of the organic electronics field and highlight the cause and effect of anisotropic charge transport in organic semiconductors. Then, we discuss the various merits of mixing organic semiconductors with the general polymeric additives, which has a significant influence on controlling semiconductor crystallization and enhancing electrical charge carrier transport.

1.1 Recent progress in organic electronics

In recent years, major advances have been demonstrated in the development of organic electronics [1–9]. Various solution-processable organic semiconductors have been reported that exhibit improved solubility in organic solvents [10, 11], large charge carrier mobilities [12–14] and good air stability [15, 16]. Here we discuss the development and application of 6,13-bis(triisopropylsilylethynyl) pentacene (TIPS pentacene) as benchmark examples to shed lights on the recent progress of organic semiconductors and organic electronics devices [17-24]. TIPS pentacene is a p-type organic semiconductor which was originally reported in a series of studies by Anthony et al. as a derivative of pentacene [25-27]. The attachment of the bulky side groups to the backbones disrupts the herringbone packing motif and allows TIPS pentacene to dissolve in a variety of solvents [28–32]. Chen et al. reported that TIPS pentacene can find decent solubility in various organic solvents by comparing the solubility parameters of TIPS pentacene and the solvents [27]. TIPS pentacene has a solubility parameter of $\delta = 18-19$ MPa^{1/2}. In order for TIPS pentacene to well dissolve in an organic solvent, the solubility parameter of the solvents must be close to that of TIPS pentacene. Thereby, chloroform ($\delta = 19$ MPa^{1/2}), tetrahydrofuran (δ = 18.6 MPa^{1/2}) and toluene (δ = 18.2 MPa^{1/2}) were considered to be "good solvent" for TIPS pentacene based on this criterion. Kim et al. reported a binary solvent method to grow TIPS pentacene microribbon crystals [33]. The binary solvent

comprised toluene as the main solvent and acetonitrile as the additive solvent. Microribbon crystals were formed as a result of the solvent exchange process by injecting the TIPS pentacene/toluene solution into acetonitrile, yielding an enhanced hole mobility of $1.42 \text{ cm}^2/\text{V}$ s.

In addition, the bulky side group of TIPS pentacene is pushed away from the backbones as a result of the carbon-carbon triple bonds, giving rise to face-to-face interaction (π – π stacking) in TIPS pentacene [34–36]. The π - π stacking in TIPS pentacene molecules greatly enhances charge transport as compared to pentacene [37]. For example, Lee et al. studied the crystallization modes of TIPS pentacene by optimizing the spin coating time and reported a hole mobility of $1.3 \text{ cm}^2/$ V s from TIPS pentacene spherulites due to the large grain size and crystal perfection [38]. Kim et al. reported a blade coating method to grow highly oriented TIPS pentacene crystals, yielding a mobility of $1.74 \text{ cm}^2/\text{V} \text{ s}$ [39]. Wang et al. optimized the dielectric surface and reported a mobility of $2.26 \text{ cm}^2/\text{V} \text{ s}$ from TIPS pentacene microwires when the polar and dispersive components of surface energy matched those of the semiconductor [40]. Bharti et al. reported a mobility of 2.6 cm^2/V s from phase segregated TIPS pentacene and polystyrene (PS) blends due to improved quality of charge transport interface [41]. Rocha et al. developed a solution shearing method to grow ordered TIPS pentacene crystals and reported a high mobility of 6.7 cm^2/V s [42]. Diao et al. reported a fluid-enhanced crystal engineering (FLUENCE) technique to control the morphological features of solution-printed TIPS pentacene thin films [43]. The recirculation in the ink induced by the micropillarpatterned printing blade effectively enhanced the crystal nucleation and growth. The FLUENCE technique gave rise to highly aligned TIPS pentacene ribbons with width and length in millimeter and centimeter scale, respectively, producing a hole mobility of 11 cm²/V s. Su et al. reported a zone casting method to uniaxially achieve crystal alignment of TIPS pentacene crystalline arrays [44]. Zone casting TIPS pentacene in a solvent with a low boiling point and a large casting speed was found to favor the formation of aligned crystal ribbons, yielding a mobility of 0.67 cm²/V s. Kim et al. reported a "confined evaporative capillary flow (CEC)" method to develop TIPS pentacene ribbon-shaped crystals [45]. The CEC method modulated the solution evaporation, reduced the contact line pinning and

controlled the outward hydrodynamic flow, giving rise to crystal ribbons with enlarged grain width and length. The crystal ribbons exhibited preferential crystal orientation and enhanced charge transport in a lateral direction, leading to an improved mobility of $0.72 \text{ cm}^2/\text{V} \text{ s}.$

In addition to TIPS pentacene, other organic semiconductors have been reported with even higher mobilities by combining polymer additives and external forces. For instance, Shen et al. reported the optimization of 2,7-dioctyl[1]benzothieno[3,2b][1]benzothiophene (C8-BTBT)/PS phase segregation and crystallization by tuning the spin coating speed and PS weight ratio [46]. The vertically segregated C8-BTBT was observed to crystallize at the interface between the active layer and air, giving rise to a mobility of 6.8 cm^2/V s. Haase et al. reported a solution shearing based coating method to grow C₈-BTBT [47]. By mixing C_8 -BTBT with the polymer additive PS, the shearing-deposited active layer yielded a high mobility of 12 cm^2/V s. Panidi et al. reported an admixing approach of the molecular Lewis acid $B(C_6F_5)(3)$ in 2,8-difluoro-5,11-bis(triethylsilylethynyl)anthradithiophene (diF-TESADT) formulation before solution deposition to enhance its charge transport [48]. diF-TESADT/PTAA blends based on $B(C_6F_5)(3)$ showed an enhanced mobility of 8 cm²/V s. Zhang et al. reported a mixed solvent system with blade coating to deposit pinhole free diF-TESADT semiconductor film with enhanced uniformity [49]. The resultant ultra-thin diF-TESADT film showed improve crystallinity and a mobility of 5.54 $cm^2/V s$.

The improved solubility in organic solvents and high charge carrier mobilities has attracted considerable attention to implement the organic semiconductors in various high performance organic electronic device applications, including organic thin film transistors [50, 51], gas sensors [52, 53], phototransistors [54–56], photodetectors [57, 58] and complementary inverters [59, 60]. For instance, gas sensors incorporating the organic semiconductors have the capability to detect the traces of gases by characterizing the fluctuation of saturation currents as a result of the gas diffusion into the transistor channel and absorption into the interfacial traps [61]. Zhuang et al. reported an off-center spin coating method to grow TIPS pentacene nanofibrillar structures as the active layer of nitrogen dioxide (NO₂) gas sensor [53]. Gas responsivity 30 ppm NO₂ was enhanced by 221.5% by using this coating method. The organic thin film transistor based photodetectors are able to characterize the photo-to-dark current ratio from exposure with incident light of different wavelengths [62]. Benavides et al. reported the utilization of TIPS pentacene as an interlayer to decrease the dark current of poly-3-hexyl-thiophene (P3HT) and [6, 6]-phenyl c(61) butyric acid methyl ester (PCBM) based photodetector [57]. Inserting TIPS pentacene interlayer also improved the detectivity and dynamic range of the photodetector. Furthermore, the fabrication of both a p-type and n-type thin film transistor on the same substrate gives rise to complementary inverters [63, 64]. Kim et al. demonstrated a vertical stacking structure for complementary inverters comprising an n-type PCBM and p-type TIPS pentacene [65]. The n-channel and p-channel transistor showed a mobility of $0.004 \text{ cm}^2/$ V s and 0.25 cm^2/V s, respectively, giving rise to a gain value of - 24 V/V for the inverter. These organic electronic devices can further find applications in a variety of fields such as display electronics [66], biomedical analysis [67] and pharmaceutical industry [68].

1.2 Random charge transport in organic semiconductors

Despite these advancements, a few significant challenges holding back the future development and application of the organic semiconductors include the randomized patterns of semiconductor crystallization in solution, mobility variation, thermal cracks caused by high deposition temperature, and effects from solvents [69-71]. For example, when the organic semiconductor TIPS pentacene is deposited by using the drop casting method, the semiconductor shows a dendritic motif of crystal growth [72, 73]. Chen et al. designed a deposition system to systematically control the crystal orientations of TIPS pentacene and correlated the crystal orientation to the charge carrier mobilities of TIPS pentacene based organic thin film transistors [74]. This research indicated that as the crystals were formed in a parallel direction with the source and drain contact electrodes, the transistors exhibited a mobility 10 times higher than that obtained from the counterparts with crystals deposited in a perpendicular direction. A similar study conducted by Akkerman et al. showed that the misoriented TIPS pentacene crystals can cause a much larger variation in charge carrier mobility of up to a 50-fold difference [75]. In addition to TIPS pentacene, the correlation between crystal misorientations and mobility variations has also been observed in solution-based drop casting of 5,6,11,12-tetrachlorotetracene [76] and 2,5-di-(2-ethylhexyl)-3,6bis(5"-n-hexyl-2,2',5',2"]terthiophen-5-yl)-pyrrolo[3,4c] pyrrole-1,4-dione (SMDPPEH) [77, 78], 6,13bis(triethylsilylethynyl) pentacene (TES pentacene) [79] and C_8 -BTBT [80] organic semiconductors. James et al. reported the random charge transport in TES pentacene organic crystals with different morphologies [79]. By using a zone casting method, the TES pentacene crystals were intentionally aligned with various orientations with the channel direction. The ratio of "parallel mobility" to "perpendicular mobility", which refers to the mobility measured when crystal is parallel with and perpendicular to the channel, respectively, was employed as a mobility anisotropy parameter. A mobility anisotropy parameter of 5 was measured from the TES pentacene broad domain film, whereas a much higher parameter of 21-47 was obtained from the elongated TES pentacene ribbons. Li et al. reported the random charge transport in C₈-BTBT plate-like crystals with different crystal size [80]. Tuning the concentrations of both C₈-BTBT semiconductor and the polymethyl methacrylate (PMMA) additive gave rise to different crystal size. Enlarged crystal size was observed to yield improved mobility uniformity, which was attributed to the reduced crystal orientation and alignment variations. These important results necessitated the homogeneous alignment of the organic semiconductor crystal orientation.

Besides randomized crystallization, deposition temperature and solvents can also pose challenges on the processing of organic semiconductors. When the substrate is heated to an elevated deposition temperature in order to promote the crystallization and alignment of the organic semiconductor [81], heating has been reported to cause thermal cracks and induce a blocking effect on charge transport [31]. Different solvent choices can impact the organic semiconductor dissolving, nucleation, crystallization, morphology, crystallinity, and charge transport. Hansen's solubility theory can usefully help determine the solvent affinity for the organic semiconductor. Hansen's solubility parameters determine the extent of mismatch between the solvent and semiconductor, which is shown by the following equation:

$$\mathbf{R} = \sqrt{\Delta \widehat{\mathbf{\partial}}_{\mathbf{P}}^2 + \Delta \widehat{\mathbf{\partial}}_{\mathbf{d}}^2 + \Delta \widehat{\mathbf{\partial}}_{\mathbf{h}}^2}.$$
 (1)

In Eq. (1), ∂_p , ∂_d , and ∂_h is the polar, dispersive and hydrogen bonding part of the intermolecularly interacting force [82, 83]. Good solubility in the solvent can be expected based on similar Hansen's solubility parameters.

1.3 Merits of polymer additives in controlling organic crystal growth

Polymeric additives have been demonstrated with multi-fold benefits on the crystal growth and charge transport of organic semiconductors. In this section, we will briefly discuss these benefits including improving morphology uniformity, reducing crystal misorientation, enlarging grain width, enhancing electrical stability, inducing phase segregation, and improving charge transport.

1.3.1 Improving morphology uniformity

The blending of polymeric additives with organic semiconductors can not only improve the thin film morphology uniformity but also reduce the crystal misorientation [84-87]. Still taking TIPS pentacene as an example, the concept of crystal misorientation angle is introduced to better characterize the different crystal orientations on a large scale. The crystal misorientation angle was defined as the angle between the crystal long axis [210] of TIPS pentacene and a baseline direction [88]. Besides TIPS pentacene, crystal misorientation angle has also been reported in two other organic semiconductors, i.e., 5,6,11,12-tetrachlorotetracene and SMDPPEH. The organic semiconductor 5,6,11,12-tetrachlorotetracene was deposited on the substrate using simple drop casting, and formed randomly-oriented wires with vertical stacking, leading to a large misorientation angle [76]. By using the same method of simple drop casting, organic semiconductor SMDPPEH formed even more severely misoriented wires. The extent of random orientation and aggregations of SMDPPEH made it impossible to accurately measure the misorientation angle [77]. Previous studies indicated that the mixing of polymeric additives with TIPS pentacene can effectively reduce the misorientation angle of organic semiconductor, thereby reducing the charge carrier

mobility variations and improving the electrical performance consistency from device to device [89].

1.3.2 Enlarging grain width

Polymeric additives have been reported to enlarge the grain width of the organic semiconductor crystals [90]. It is well known that the grain boundaries can incorporate crystalline defects and trap centers, which downgrade the charge carrier transport in organic thin film transistors. The correlation between grain width and effective mobility was reported by Chen et al. [29], as indicated in Eq. (2):

$$\frac{1}{\mu_{\rm E}} = A + \frac{B}{W_{\rm G}}.\tag{2}$$

In Eq. (2), $\mu_{\rm E}$ is the effective hole mobility, $W_{\rm G}$ is the grain width of organic semiconductor crystals, and *A* and *B* are two constants. Thereby, enlarged grain width is correlated with enhanced charge transport because it essentially signifies fewer grain boundaries and reduced defects, and reduces the impact from crystalline defects on charge transport in organic semiconductors.

1.3.3 Enhancing electrical stability

Additionally, the incorporation of polymer additives shields the charge transport in the organic semiconductor active layer from the impact from the substrate as well as from the environment, which is due to the hydrophobic nature of the polymeric additive and/or the formation of a polymeric encapsulation layer. For example, in order to protect charge transport in the N,N'-1H,1H-perfluorobutyl dicyanoperylenecarboxydiimide (PDIF-CN₂) organic semiconductor from the impact of the substrate, both PaMS and PMMA were pre-deposited to passivate the silanol groups on the hydrophilic silicon dioxide before spin coating the organic semiconductor [91]. The hydrophobic nature of PaMS and PMMA repels the absorption of water moistures from the environment and thereby improves the thin film transistor stability over time. On the other hand, the incorporation of the polymer additives into the organic semiconductor induces vertical phase segregation and further forms an encapsulation layer [92–96]. Kim et al. reported that when PaMS was blended with an organic semiconductor diF-TESADT, the spin coating of the blends caused a vertical phase segregation and formed a top polymeric layer which blocks the oxygen and moistures from the ambient environment [97]. Similarly, the encapsulation effect from vertical phase segregation as a result of blending PaMS was also reported by Kang et al. using TIPS pentacene organic semiconductor [98].

1.3.4 Promoting phase segregation

Furthermore, the phase segregation from mixing polymeric additives with organic semiconductors may yield a bilayer or trilayer structure in the active layer, which modulates both crystallization and charge transport [99]. A vertically phase segregated active layer with higher concentration of organic semiconductor at the charge transport interface typically exhibits improved charge transport and higher mobilities [100]. For instance, amorphous polymers including PaMS [101, 102], PMMA [103-106] and PS [107, 108] have been reported to exert a strong effect on facilitating a vertical phase segregation according to previous reports. More interestingly, a series of polyacrylate polymers with different lengths of alkyl side groups can switch the phase segregation between lateral and vertical modes: poly(ethyl acrylate) (PEA), poly(butylacrylate) (PBA), and poly(2ethylhexyl acrylate) (P2EHA) are three polyacrylate polymers sharing a similar molecular core but comprising two, four and eight carbon atoms in the alkyl side chain [109]. As the side chain grows in its length, a dominant lateral phase segregation for the case of PEA was replaced by a coexistence of both lateral and vertical phase segregation for PBA, and further by a dominant vertical phase segregation for P2EHA. Distinctive thin film morphology and charge transport was observed accordingly dependent on the different phase segregation behaviors.

2 PαMS and organic semiconductor mixtures

As described in the previous section, the solutionbased growth of small-molecular organic semiconductors have a tendency to yield randomly-aligned crystals, which makes it technically challenging to apply these semiconductor materials for large-scale organic electronic devices that require high performance consistency. Thereby, given these challenges, it is highly desirable to control the bulk crystal alignment of the organic semiconductors, improve crystalline morphology and enhance charge transport. Here we mainly employ TIPS pentacene as a representative semiconductor material to discuss the various merits of P α MS. In Sects. 2.1 and 2.2, we will review the important reports of utilizing PaMS as additives to align bulk crystal orientations and control the organic semiconductor charge transport. Studies involving TIPS pentacene organic semiconductor are discussed first, followed by reports entailing other organic semiconductors in order to underscore a more general approach. It is important to note that although other polymers such as PMMA, PS, polyethylene glycol (PEG) and tetraethylene glycol (TEG) have also been reported as additives to modulate the organic semiconductor crystallization, PaMS has uniquely demonstrated to reduce the bulk crystal misorientation from solution-based drop casting. Therefore, PaMS is studied as a model polymer additive in this work for its effect on reducing bulk crystal misorientation and modulating charge transport of organic semiconductors. When choosing an additive for a specific organic semiconductor, various factors need to be considered such as the additive solubility and dielectric constant. The additive shall possess decent solubility in the solvent of the organic semiconductor, given that many additives need to be mixed with the organic semiconductor at a 1:1 weight ratio for optimized results. Besides, the dielectric constant of the additive need to be taken into account when determining the total capacitance of the thin film transistor device.

Figure 1a shows the molecular structure of $P\alpha MS$, while Fig. 1b, c shows the structures of the various solution processed, small-molecular organic semiconductors reviewed in this section, including TIPS pentacene and diF-TESADT. Throughout the discussion of these representative examples, we aim to demonstrate the significant importance of PaMS in application in organic electronic devices.

2.1 PαMS as additives for aligning crystal orientations

Among the various polymeric additives mixed with organic semiconductors, PaMS has been demonstrated to effectively control the semiconductor growth and regulate crystal orientation. He et al. reported the incorporation of the PaMS polymer additive at a 1:1 weight ratio, in order to eliminate the randomness of TIPS pentacene organic crystals [110]. The TIPS pentacene crystals can be depicted by the optical image shown in Fig. 2a: the organic semiconductor adopted a dendritic growth pattern and yielded crystals varying in their long axis or charge transport directions. Besides, the organic TIPS pentacene crystals only covered a section of the substrate. In this case, it became almost impossible to implement these randomly oriented crystals to achieve high-performance large-scale organic electronic applications. As PaMS was incorporated in TIPS pentacene, the resultant thin film was shown in Fig. 2c, d. The crystal randomization has been completely eliminated with the organic crystals aligning towards a uniform direction. Additionally, all substrate areas were covered with the organic semiconductor crystals. By defining the crystal misorientation angle as the angle between the long axis of a TIPS pentacene crystal and that of a baseline crystal, a large misorientation angle of $43.9^{\circ} \pm 27.8^{\circ}$ was reported from the TIPS pentacene organic crystals, whereas a significantly smaller angle of $2.2^{\circ} \pm 1^{\circ}$ was observed with the addition of the PaMS polymer. The organic thin film transistors based on the PaMS/TIPS pentacene active layer yielded an enhanced mobility of up to $0.26 \text{ cm}^2/\text{V}$ s. The performance consistency of the thin film transistors (device schematic is shown



Fig. 2 Optical photos of a TIPS pentacene and c, d TIPS pentacene/P α MS crystals. b Schematic of a bottom-gate, top-contact transistor with gold (Au) as the source (S) and drain (D) electrodes, TIPS pentacene/P α MS as the active layer, and SiO₂ as the gate insulator. Reproduced from [110] with permission from Springer



in Fig. 2b) was denoted by the ratio of the average mobility to the mobility standard deviation. The pristine TIPS pentacene organic crystal thin film transistor was reported to have a performance consistency of 1 [111], indicating the standard deviation of mobility is equivalent to the average mobility. The blend organic crystal based counterpart incorporating P α MS yielded a ratio of up to 3.35, which was a 300% improvement. It can be seen that the addition of the P α MS additive has not only enhanced the mobility but also produced mobilities in a narrower region, indicating improved performance consistency of the TIPS pentacene thin film transistors.

Asaree-Yeboah et al. combined the incorporation of P α MS polymer additive (M_w 108 K) with a temperature gradient technique to eliminate the problem of thermal cracks and to improve film uniformity of TIPS pentacene organic crystals [112]. The temperature gradient technique applied heating to the substrate and allowed the substrate temperature to gradually increase from one side to the other, resulting in different concentrations of solutes across the substrate. In particular, the solution exposed to a lower temperature hosts more solutes and reaches supersaturation sooner than the counterpart exposed to a higher temperature. Then, nucleation sites were formed at the lower temperature side, from which

TIPS pentacene started to crystallize and grow towards the higher temperature side, as illustrated by the schematic of Fig. 3g. The resultant morphology of organic TIPS pentacene crystals was shown in Fig. 3a-c, which implied that the crystals were formed along a fairly uniform direction in spite of the thermal cracks. Thereby, PaMS was mixed with the TIPS pentacene organic semiconductor in solution prior to crystal growth. The TIPS pentacene/PaMS mixture morphology was presented in Fig. 3d-f, showing that the PaMS polymer additive, coupled with the temperature gradient technique, not only aligned the crystals but also eliminated the production of thermal cracks. The surface profile of the TIPS pentacene/PaMS mixture crystals grown with the gradient temperature was shown in Fig. 3h, which showed greatly enhanced uniformity in its film topography as compared with that of the pristine TIPS pentacene deposited via a simple drop casting method. Furthermore, the PaMS additive was also implemented along with the temperature gradient technique for fabricating thin film transistors on a flexible ITO/PET substrate. Figure 3i shows a digital picture of the as-fabricated flexible transistor. Hole mobility of up to 0.25 cm^2/V s and 0.5 cm^2/V s were obtained based on the silicon substrate/silicon



Fig. 3 Polarized optical images of \mathbf{a} - \mathbf{c} TIPS pentacene films and \mathbf{d} - \mathbf{g} TIPS pentacene/P α MS mixture film, both of which were formed by applying the temperature gradient technique. \mathbf{g} A schematic showing the mechanism of how the temperature gradient technique aligned the crystal growth of the TIPS

dioxide substrate and flexible ITO/PET substrate, respectively.

Lee et al. reported that the control of 1-dimensional and 2-dimensional crystal growth of TIPS pentacene by mixing with the P α MS (M_w 3160 K) and PMMA (M_w 996 K) as polymer additive [38]. In this work, the function of the P α MS and PMMA polymer additives was mainly to prevent solution dewetting and improve film uniformity, which made it possible to tune the crystal modes between 1 and 2D. By increasing the spin coating time, reduced amount of solvent remained in the mixture which altered the growth mode of TIPS pentacene crystals from 1D mode to 2D mode. In particular, in 1D growth mode, the larger amount of residue solvent resulted in a

pentacene/P α MS mixture. **h** Film topography of both pristine TIPS pentacene from drop casting and aligned TIPS pentacene/P α MS crystals. **i** A digital image showing the as-fabricated flexible thin film transistor device based on the ITO/PET substrate. Reproduced from [112] with permission from Elsevier

shrinking droplet and nucleation at the droplet edge. The convective flow facilitated the crystal to grow from the edge towards the center of the substrate. The 1D growth led to aligned TIPS pentacene crystal ribbons in these localized regions, although crystal misorientation still remained based on the entire substrate. In comparison, when less amount of residue solvent is available in the 2D growth mode, spherulite crystals with large grain size were formed due to random nucleation sites. Besides, vertical phase segregation yielded a bilayer structure with a top TIPS pentacene and a bottom polymer layer. As a result, a higher mobility of 1.3 cm²/V s was demonstrated from the 2D spherulitic TIPS pentacene crystal based thin film transistors with the P α MS polymer

additive. This work showcases that desired electronic properties of the organic semiconductors can be controlled by tuning the amount of residue solvent.

Sim et al. reported the addition of $P\alpha MS$ into 3,6bis[5-(4-cyanophenyl)thiophen-2-yl]-2,5-dihexadecylpyrrolo-[3,4-c]pyrrole-1,4(2H,5H)-dione (DPP-PhCN), which is an n-type cyanophenyl functionalized diketopyrrolopyrrole small-molecule semiconductor, in order to control its crystal growth and orientation [113]. The pristine DPP-PhCN grew into random curved wires with the assistance of cube-induced capillary force. In contrast, the DPP-PhCN/ PaMS blends exhibited enhanced alignment in a perpendicular direction towards the tube. In addition to improved alignment, the DPP-PhCN/PaMS microwires showed enlarged length and width. As a result, with the incorporation of the PaMS additive, the DPP-PhCN organic transistors based on crosslinked poly(4-vinylphenol) (CL-PVP) gate dielectric layer demonstrated an enhanced electron mobility of $0.23 \text{ cm}^2/\text{V} \text{ s}.$

The various works from this section that discussed aligning crystal orientations are summarized in Table 1.

2.2 PaMS as additives for modulating semiconductor charge transport

In addition to aligning the orientations of bulk crystals and reducing the crystal misorientations, the incorporation of P α MS polymers has also been demonstrated as a useful pathway to control the morphology and charge transport of the organic semiconductors. In this section, we will review important works that utilize $P\alpha MS$ polymer as an additive to modify the semiconductor growth and crystallization, enhance the charge carrier mobilities and optimize the performance organic electronic devices.

Hwang et al. reported how P α MS (M_w 100 K) as a polymer additive, in conjunction with solvents with different boiling points, impacted the phase segregation behavior of TIPS pentacene and the compositional structure of the active layer [114]. TIPS pentacene/PaMS mixture was deposited in two different solvents including chlorobenzene and tetralin, which has different boiling point of 132 °C and 207 °C, respectively. The TIPS pentacene/PaMS mixture deposited in chlorobenzene exhibited elongated crystals whereas the same mixture in tetralin showed feather like crystals with much longer crystal length extending in hundreds of micrometers. Accordingly, in the vertical profile, a trilayer structure composed of mainly TIPS pentacene, PaMS and TIPS pentacene was observed from the top to bottom section of the active layer based on the chlorobenzene solvent. In comparison, a bilayer structure comprised of a top TIPS pentacene and bottom PaMS layer was obtained based on the tetralin solvent. The different phase segregation and compositional structures were attributed to the different vapor pressure of the two solvents: a lower vapor pressure allowed more time for TIPS pentacene to phase segregate and crystallize. Thin film transistors with different configurations were fabricated to examine the effect of vertical phase segregation on the semiconductor charge transport. A

Author	Semiconductor	$P\alpha MS$ M_w	Result	Mobility
He et al	TIPS pentacene	Not reported	PaMS additive reduced the crystal misorientation and significantly enhanced film morphology uniformity	$0.26 \text{ cm}^2/\text{V}$ s with the PaMS additive at 1:1 ratio
Asaree- Yeboah et al	TIPS pentacene	108 K	PaMS additive eliminated the thermal cracks caused by temperature gradient technique	0.5 cm ² /V based on flexible ITO/PET substrate with PαMS additive
Lee et al	TIPS pentacene	3160 K	PαMS additive prevented solution dewetting and improved film uniformity	1.3 cm ² /V s from 2D spherulitic crystals with PαMS additive
Sim et al	DPP-PhCN	Not reported	PaMS additive attributed to DPP-PhCN alignment in a perpendicular direction towards the tube	0.23 cm ² /V s with PaMS additive

Table 1 A summary of the works reviewed in this section including the authors, various organic semiconductors, molecular weight (M_w) of P α MS, the result findings as well as the mobility

hole mobility of up to $0.2 \pm 0.14 \text{ cm}^2/\text{V}$ s was obtained from the top-channel of TIPS pentacene/ PaMS thin film transistors based on the tetralin solvent.

Ohe et al. studied how the mixing of $P\alpha MS$ with TIPS pentacene impacted the phase segregation and vertical compositional structure of the semiconductor [102]. With the secondary ion Si⁻ used as a marker of TIPS pentacene, its three-dimensional distribution observed from time-of-flight secondary ion mass spectrometry (TOF-SIMS) results indicated the semiconductor/polymer mixture is composed of three sublayers. In particular, the top, middle and bottom sublayer is rich of TIPS pentacene, PaMS and TIPS pentacene. In order to understand how the morphology and composition changes impact the electrical performance, the ratio of the average standard deviation of on-current to the average on-current was used as a metric for an evaluation. A ratio of 52% and 11% was calculated based on without and with the PaMS polymer additive, which implied the polymer additive enhanced the electrical performance consistency of different thin film transistors on the same substrate. Besides, while charge transport in pristine TIPS pentacene thin film transistors was vulnerable to degradation from heat treatment due to the semiconductor structural-phase transition at 124 °C, the PaMS polymer additive suppressed such structural degradation and improved thermal stability as the devices were exposed to heating of up to 180 °C. A mobility approximately 0.12 cm²/V s was reported with the addition of PaMS.

Kang et al. performed a study of the effect of PaMS polymer additive on the phase segregation and film composition of TIPS pentacene [98], and utilized PaMS additive with two different molecular weight including 1.3 K and 575 K. While a trilayered active layer with three different sublayers including a top TIPS pentacene, a middle PaMS and then a bottom TIPS pentacene, were observed for PaMS with both molecular weight, the PaMS with a higher 575 K molecular weight was noticed to yield a bottom layer with a larger thickness of 117 A. Based on the grazing-incidence X-ray diffraction patterns, the TIPS pentacene film incorporating the high molecular weight PaMS showed highly crystalline morphology with an interlayer spacing of 16.6 Å. Besides, the TIPS pentacene molecular layers were desirably oriented parallel to the substrate surface, attributed to improved mobility. As compared with neat TIPS

pentacene thin film transistors with a low average mobility of 0.05 cm²/V s, the counterpart based on the P α MS additive with a higher molecular weight showed an enhanced average mobility of 0.3 cm²/V s.

Kjellander et al. reported how the different molecular weight of the PaMS additive impacted the inkjet printing characteristics and thin film morphology of TIPS pentacene organic semiconductors [115]. In this work, TIPS pentacene was deposited via inkjet printing onto a layer of PaMS polymer with different molecular weight of 59 K and 868 K. The inkjet-printed solution droplet was observed to dry out in three different stages. First, a perimeter is formed due to the well-known "coffee ring effect" and contact line pinning. Then, the contact line recedes as the solution droplet shrinks. Finally, a circular center region comprising most crystallized TIPS pentacene is formed as a result of high viscosity as the contact line becomes pinned again. The utilization of the PaMS polymer modulated the drying behavior the TIPS pentacene organic semiconductor and led to larger crystalline grains, which was responsible for the improved mobility. Besides, PaMS with a higher molecular weight was found to cause the formation of a greater content of TIPS pentacene semiconductor in the perimeter of the inkjet-printed crystalline film. A mobility of up to $0.7 \text{ cm}^2/\text{V}$ s and $0.8 \text{ cm}^2/\text{V}$ s were obtained from the TIPS pentacene thin film transistors based on the PaMS polymer with the 59 K and 868 K molecular weight, respectively.

Chou et al. utilized the P α MS (M_w 100.3 K) for mixing with TIPS pentacene for spray coating applications [116]. While spray coating of pristine TIPS pentacene caused severe dewetting of the organic semiconductor on the substrate, the incorporation of PaMS greatly improved the wettability of the film. Different solute concentrations of TIPS pentacene including 10 mg/mL, 15 mg/mL and 20 mg/mL were examined. Highest average mobility was obtained with a concentration of 15 mg/mL, which was attributed to a balance between crystal size across the channel and the crystal grain boundaries. Based on the concentration of 15 mg/mL in the solvent of THN, a loading ratio at 7:3 between TIPS pentacene and PaMS yielded continuous crystal needles of TIPS pentacene that cover a large portion of the substrate, giving rise to a hole mobility of up to $0.323 \text{ cm}^2/\text{V} \text{ s.}$

Bae et al. reported the addition of P α MS (M_w 100 K) as a polymer binder for TIPS pentacene based microprinted nonvolatile memory device [117]. In this work, the TIPS pentacene/PaMS mixture was deposited on the substrate and patterned by using a "selective contact evaporation printing" (SCEP) technique. In particular, an elastomeric poly(dimethylsiloxane) (PDMS) mold was brought in contact with the organic semiconductor/polymer solution mixture. As PaMS vertically phase segregated with TIPS pentacene, the mold allowed TIPS pentacene to be absorbed into the PDMS. Subsequently, a thin ferroelectric polymer film, i.e., poly(vinylidene fluoride) (PVDF) and trifluoroethylene (TrFE) copolymers (PVDF-TrFE), was transferred onto the substrate as an insulation layer for the top gate. AFM results indicated that the TIPS pentacene/PaMS blend film exhibited a very uniform surface with a 1.5 nm rms roughness. The TIPS pentacene/PaMS mixture based nonvolatile transistor patterned with the SCEP technique exhibited a mobility of 0.11 cm^2/V s. The reliability of the memory device was examined by testing the saturation drain current which indicated excellent protection from the ambient environment for up to 10,000 s.

Vaklev et al. reported the employment of a nanolayer of PaMS in order to realize the fabrication of high performance thin film transistors based on two different organic semiconductors including pentacene and TIPS pentacene [118]. In this work, a photolithography patterned PMMA layer was used as the gate dielectric, followed by the deposition of PaMS polymer layer with a thickness of 6 nm. The PaMS layer serves as a non-covalent layer that modifies the surface between the PMMA dielectric layer and the pentacene semiconductor layer, improves the semiconductor packing and enhances its morphology. In addition, the domain size of pentacene crystals deposited based on the PaMS layer was found to be five times larger than the counterpart without $P\alpha MS$, giving rise to an improved mobility of $0.6 \pm 0.1 \text{ cm}^2/\text{V}$ s from the pentacene based thin film transistors. On the other hand, the thin film transistors with TIPS pentacene deposited by spin coating and zone casting demonstrated an average mobility of $0.08 \pm 0.04 \text{ cm}^2/\text{V s}$ and $0.3 \pm 0.1 \text{ cm}^2/\text{V}$ s based on the PaMS layer, respectively.

Chung et al. reported the addition of $P\alpha MS$ (M_w 420 K) in order to recover the electrical performance

of small molecular semiconductor after exposure to UV radiation, which was described as a "zone-refinement effect" [119]. As the p-type semiconductor triethylsilylethynyl anthradithiophene (TESADT) based thin film transistors were exposed to UV radiation, impurities were introduced in the semiconductor active layer and significantly undermined the charge carrier mobility of TESADT. On the other hand, when PaMS was mixed with TESADT at a mass ratio of 3:7, the polymer additive caused a vertical phase segregation with the organic semiconductor and vielded a TESADT layer free of impurities at the charge transport interface between the gate dielectric layer and semiconductor active layer. While the pristine TESADT thin film transistors exhibited an extremely low mobility around 10^{-5} cm²/V s after UV radiation, the counterpart incorporating the PaMS polymer additive showed an enhanced average mobility of up to 0.040 ± 0.011 cm^2/V s. Similarly, such a zone-refinement effect was also observed in TIPS pentacene organic semiconductor mixed with the PaMS polymer additive. While a low mobility of 0.0083 cm²/V s was measured from TIPS pentacene thin film transistors that had been stored in the ambient environment for 3 years, blending PaMS yielded an improved mobility of 0.29 cm^2/V s, which is comparable to the mobility of freshly-fabricated counterpart.

Niazi et al. reported nucleation and crystallization effect, which is induced by the pentafluorobenzene thiol (PFBT) treatment of gold contact electrodes, on organic semiconductors including diF-TESADT and TIPS pentacene with the P α MS (M_w 100 K) polymer additive [120]. As shown in the optical images of Fig. 4a, the blade coating of TIPS pentacene/PaMS mixture consistently produced ribbon-shaped crystals aligned in the direction of coating, whereas the treatment using PFBT may result in spherulitic crystals on the gold contact electrodes as well as in the charge transport channel when a higher coating speed was applied. Accordingly, the diF-TESADT/ PaMS and TIPS pentacene/PaMS mixture based thin film transistors showed a hole mobility of up to 3.6 cm²/V s and 4.6 cm²/V s without PFBT treatment, respectively, which decreased dramatically for the counterparts with the treatment, as shown in Fig. 4b. In addition, the PFBT treatment on the gold contact electrodes also impacted the vertical phase segregation and compositional structures of the TIPS pentacene/PaMS mixture. As shown in the cross-



Fig. 4 a Polarized optical microscopic photos showing the morphology of TIPS pentacene/P α MS mixture film on a Si/SiO₂ substrate with and without PFBT treatment on the gold (Au) contact electrodes. Different blade casting speed including 0.75 and 1.25 mm/s were tested. **b** Hole mobility from TIPS pentacene/P α MS blend and diF-TESADT/P α MS blend based thin film

sectional energy-filtered transmission electron microscope (EFTEM) of Fig. 4c, e, the organic semiconductor and polymer additive mixture without the PFBT treatment exhibited a clearly defined vertical phase segregation with a top TIPS pentacene and a bottom P α MS structure within the active layer. In contrast, the PFBT treatment resulted in a different trilayer structure with a top TIPS pentacene, middle P α MS and bottom TIPS pentacene layer, as shown in Fig. 4d, f.

Hamilton et al. reported the addition of PaMS and PTAA polymer additives to improve performance uniformity of two soluble small molecular semiconductors including TIPS pentacene and diF-TESADT [87]. The thin film transistors based on the TIPS pentacene/PaMS blends were fabricated based on two different device configurations including a bottom-contact, top-gate structure and a bottom-contact,

transistors with and without PFBT treatment on Au. c-f Crosssectional EFTEM images showing the vertical phase separation between TIPS pentacene and P α MS on different substrates with and without PFBT treatment on Au. Reproduced from [120] with permission from Wiley-VCH

dual-gate structure. Higher mobility of 0.69 cm^2/V s was demonstrated from the transistors with a top gate which was attributed to the higher concentration of vertically phase-segregated TIPS pentacene organic semiconductor in the top channel. In contrast, the organic semiconductors mixed with PTAA yielded a much higher hole mobility of 1.1 cm^2/V s for TIPS pentacene and 2.41 cm²/V s for diF-TESADT, respectively. The improved device performance of PTAA based thin film transistors as compared to the counterparts with PaMS was likely due to the reduced channel width as a result of the lateral phase segregation between the PaMS polymer additive and organic semiconductor. Besides, the use of CYTOP as a dielectric layer for the top gate device was found to improve the operational stability of the thin film transistors. This was because the low permeability of CYTOP to water and oxygen prevents their diffusion from the ambient environment to the active channel of the transistor device.

He et al. reported the addition of PaMS as a charge trapping layer in order to promote the injection of electrons into the trap states for modulating the charge transport in nonvolatile memory device [121]. In this work, PaMS was deposited via spin coating prior to the thermal evaporation of pentacene as the organic semiconductor active layer. The emission peaks indicated the existence of distribution of defects as recombination centers in the PaMS bandgap. The incorporation of PaMS was estimated to yield energy levels of trap states from 2.9 to 2.51 eV higher than the HOMO of PaMS. By applying a continuous positive gate voltage, the electron injection is promoted along the lateral direction of the channel and results in bulk trap state occupancy. In addition, electrons can be injected along the vertical direction into the trap states of PaMS from the charge transport channel. Consequently, bistable memory behaviors with enlarged memory window is obtained due to the electron transport in the lateral direction from the addition of the PaMS trapping layer.

Xu et al. reported a hybrid charge trapping layer comprising HfO_2 and $P\alpha MS$ in order to tune the charge trapping efficiency and device operation speed of the organic nonvolatile memory transistors [122]. In this work, $P\alpha MS$ was spin coated onto the HfO₂ layer, followed by the thermal evaporation of pentacene as the semiconductor layer. Without incorporating the PaMS polymer, the memory device based on the single HfO₂ layer exhibited weak memory effect as a small sweeping voltage was applied. In contrast, the counterpart with the incorporation of PaMS polymer demonstrated a clear memory effect as the sweep voltage increased to 12 V. Such improved performance was attributed to the large grain size of pentacene grown on the hybrid layer of both HfO2 and PaMS, which gave rise to increased saturation current, more efficient trapping of charges as well as modification effect on the charge transport interface. Besides, the memory device based on the hybrid layer demonstrated a small degradation as going through P/E circles, indicating highly reliable operation property.

Kim et al. demonstrated the addition of P α MS into diF-TESADT in order to reduce the mobility variation from device to device and to improve the operational stability [97]. In this work, P α MS (M_w 110 K) was mixed with the organic semiconductor at a 1:1 ratio,

yielding a film morphology with improved uniformity over the whole substrate and in particular in narrow regions between the contact electrodes, as indicated by the AFM images shown in Fig. 5a, b. By testing a total of 30–50 transistor devices, the authors observed a narrower distribution of charge carrier mobility with the incorporation of PaMS as compared to the mobility variation of the pristine diF-TESADT semiconductor, as indicated in the mobility distribution graph in Fig. 5c, d. As a result, an average mobility of 0.234 \pm 0.13 cm²/V s and 0.16 \pm 0.05 cm²/V s were demonstrated from the diF-TESADT based thin film transistors without and with PaMS polymer additive, respectively. The much smaller standard deviation of the mobility indicates the enhanced mobility consistency due to the PaMS additive. On the other hand, while oxygen and moistures may diffuse from the ambient environment into the grain boundaries and crystalline structures of the pristine diF-TESADT semiconductor and degrade the device stability of the transistor device, the mixing of the P α MS additive resulted in the formation of an encapsulation layer that effectively prevents the penetration of oxygen and moistures into the active layer, and thereby, enhances the operational stability.

Han et al. reported the incorporation of P α MS (M_w 300 K) as a pre-deposited layer in order to improve the thin film morphology and crystalline structure of inkjet-printed diF-TESADT [123]. Without PaMS, the inkjet printing produced nonuniform semiconductor film with considerable variations in the channel shape. In contrast, the pre-deposition of PaMS polymer layer can be partially dissolved by the ink solution, which gave rise to the formation of a blending zone. As the solution dries, the ink is confined within a fixed dimension and yielded circular-shaped channel with an excellent coverage. In addition, the diF-TESADT/PaMS mixture due to the dissolution of PaMS exhibited a vertical phase segregation with a top diF-TESADT rich layer and a bottom PaMS rich layer. The effect from different solvents including DCB, tetralin and anisole were studied, indicating that the presence of the PaMS layer underneath enlarged the diameter of the organic semiconductor channel. The mobility of neat diF-TESADT was reported to be 10^{-3} cm²/V s and 0.3 cm²/V s based on the solvent of DCB and anisole, respectively. The thin film transistors with the diF-TESADT/PaMS mixture indicated good uniformity in the charge carrier mobilities of 0.34 ± 0.13 cm²/V s and Fig. 5 AFM images of diF-TESADT films a without and b with P α MS polymer additive. c and d Mobility distribution of diF-TESADT thin film transistor without and with P α MS polymer additive. Reproduced from [97] with permission from Elsevier



subthreshold slope of 0.456 \pm 0.09 V/decade from the devices.

Shin et al. reported the phase segregation behaviors between diF-TESADT and polymer binders including PaMS (M_w 400 K), PMMA (M_w 190 K) and syndiotactic PS (s-PS) (M_w 250 K) [124]. In particular, PaMS does not contain polar groups, PMMA contains some polar ester groups which would interact with hydrophilic surfaces, and s-PS has its own crystallization events. The resultant diF-TESADT/ polymer binder mixture was investigated by using neutron reflectivity and other characterization tools. The diF-TESADT/PaMS mixture exhibited a trilayer structure, comprising a top diF-TESADT layer, a middle PaMS layer and a bottom diF-TESADT layer, which was likely due to the PaMS chains remaining in the middle section of the blend film. The diF-TESADT/PMMA mixture can be divided into a top diF-TESADT layer and a bottom PMMA layer, which was formed as a result of the favorable interaction between the polar PMMA and the hydrophilic polar silicon dioxide layer. In contrast to the vertical phase segregation for P α MS and PMMA, the diF-TESADT/ s-PS mixture was found to exhibit a homogenous structure without stratification. Thin film transistors were fabricated based on the diF-TESADT/polymer binder mixture to test charge transport, yielding a mobility of 0.094 \pm 0.042 cm²/V s with P α MS and 10⁻³ to 10⁻⁴ cm²/V s with both PMMA and s-PS.

Zhong et al. reported the mixing of $P\alpha MS$ (M_w 100 K) with an n-type diketopyrrolopyrrole (DPP) based organic semiconductor, i.e., DPP-CN, to enable the successful device fabrication of thin film transistors [125]. Without the polymer additive, the DPP-CN solution failed to produce a continuous film on the hexamethyldisilazane (HMDS) treated substrate. In contrast, the DPP-CN/P α MS mixture formed continuous spherulitic crystals with extended long-range uniformity. Interestingly, the DPP-CN/P α MS mixture film exhibited two different regions comprised of large and small crystal domains with a surface roughness of 26.2 nm and 47.3 nm, respectively. Due

to a vertically-segregated organic semiconductor top layer, a bottom-contact, top-gate transistor device configuration was used to test charge transport. An electron mobility of up to $0.5 \text{ cm}^2/\text{V}$ s was measured from the DPP-CN/P α MS mixture based thin film

transistors. Besides, as the thin film transistors were exposed to the ambient environment for 1300 min, the electron mobilities still maintained approximately the same values, implying enhanced air stability of the organic semiconductor DPP-CN.

Table 2 A summary of the works reviewed in this section including the authors, various organic semiconductors, molecular weight (M_w) of P α MS, the result findings as well as the mobility

Author	Semiconductor	PaMS M _w	Result	Mobility
Hwang et al	TIPS pentacene	100 K	Different phase segregation and compositional structures were observed due to different solvent vapor pressure	$0.2 \pm 0.14 \text{ cm}^2/\text{V}$ s from the top- channel device with tetralin solvent
Ohe et al	TIPS pentacene	Not reported	Three sublayers with vertical phase segregation were formed due to $P\alpha MS$ additive	0.12 cm^2/V s with PaMS additive
Kang et al	TIPS pentacene	1.3 K and 575 K	A trilayer structure with three sublayers were formed, with sublayer thickness dependent on the molecular weight	0.3 cm ² /V s based on P α MS additive with 575 K $M_{\rm w}$
Kjellander et al	TIPS pentacene	59 K, 868 K	PaMS with different $M_{\rm w}$ impacted TIPS pentacene morphology and grain size	$0.8 \text{ cm}^2/\text{V}$ s based on P α MS with a higher M_{w}
Chou et al	TIPS pentacene	100.3 K	PαMS improved the wettability of TIPS pentacene film from spray coating	$0.323 \text{ cm}^2/\text{V} \text{ s with } P\alpha MS \text{ additive}$
Bae et al	TIPS pentacene	100 K	PaMS additive along with a "selective contact evaporation printing" technique was used for fabricating nonvolatile memory device	0.11 cm ² /V s based on TIPS pentacene/PαMS mixture
Vaklev et al	TIPS pentacene, pentacene	Not reported	PαMS as a non-covalent layer modified the charge transport interface, improved semiconductor packing and enhanced its morphology	$0.6 \pm 0.1 \text{ cm}^2/\text{V} \text{ s and } 0.3 \pm 0.1 \text{ cm}^2/\text{V} \text{ s from pentacene and}$ TIPS pentacene
Chung et al	TIPS pentacene TESADT.	420 K	PaMS caused a vertical phase segregation and a semiconductor layer free of impurities adjacent to charge transport interface	$0.040 \pm 0.011 \text{ cm}^2/\text{V} \text{ s and } 0.29 \text{ cm}^2/\text{V} \text{ s based on TESADT and TIPS pentacene}$
Niazi et al	TIPS pentacene, diF- TESADT	100 K	PαMS with PBFT treatment modified phase segregation, morphology and mobility	3.6 cm ² /V s and 4.6 cm ² /V s from diF-TESADT and TIPS pentacene with PαMS
Hamilton et al	TIPS pentacene, diF- TESADT	Not reported	PαMS and PTAA were mixed with the semiconductors to improve performance uniformity	$0.69 \text{ cm}^2/\text{V} \text{ s for TIPS pentacene}$ with P α MS additive
He et al	Pentacene	Not reported	PαMS was used as a charge trapping layer to modify the memory behaviors of memory device	Not reported
Xu et al	Pentacene	Not reported	PαMS was incorporated with HfO ₂ as a hybrid charge trapping layer to improve memory device performance	Not reported
Kim et al	diF-TESADT	110 K	PαMS improved the mobility consistency from device to device and formed an encapsulation layer, enhancing operational stability	$0.234 \pm 0.13 \text{ cm}^2/\text{V} \text{ s and}$ $0.16 \pm 0.05 \text{ cm}^2/\text{V} \text{ s without}$ and with PaMS
Han et al	diF-TESADT	300 K	PαMS as a pre-deposited layer improved morphology and crystalline structure of inkjet printed diF-TESADT	$0.34 \pm 0.13 \text{ cm}^2/\text{V}$ s with the pre- deposited PaMS layer
Shin et al	diF-TESADT	400 K	PαMS was studied for its phase segregation behavior with diF-TESADT	$0.094 \pm 0.042 \text{ cm}^2/\text{V} \text{ s with}$ PaMS additive
Zhong et al	DPP-CN	100 K	Continuous spherulitic crystals with extended long-range uniformity were formed due to adding PaMS	0.5 cm ² /V s with P α MS additive

The various works from this section that discussed generally modulating morphology and charge transport are summarized in Table 2.

3 Conclusion and future outlook

In this work, we have reviewed PaMS as an important polymeric additive to modulate the crystal orientation and improve charge transport of miscellaneous high-performance solution processable organic semiconductors including TIPS pentacene and diF-TESADT. The significant effect of the PaMS polymer additive on modulating the interfacial surface, improving thin film morphology, mediating phase segregation, enhancing charge transport and reducing mobility variation was discussed based on various organic electronic devices such as organic thin film transistors and memory devices. The influence from different molecular weight of the PaMS polymeric additive on the organic semiconductor was examined, giving rise to different phase segregation behaviors, compositional structures and charge transport. Throughout the discussion of these representative examples, we showcase that PaMS can be implemented to modulate the crystal growth and charge transport of other organic semiconductors beyond the scope of this work.

In addition to PaMS, future works can be dedicated to other types of polymeric additives by studying their effect on controlling the organic semiconductor crystallization and improving the semiconductor crystal orientations. For instance, other insulation polymeric additives such as PMMA and PS can be studied for their capability to improve bulk crystal alignment and reduce crystal misorientation, which shall improve the overall performance consistency of small molecular semiconductor based thin film transistors. Besides, conjugated polymers can effectively modulate the intermolecular interactions between the polymer and organic semiconductors, which offer a new path to tune the charge transport and electrical performance of organic semiconductors [86]. Moreover, semicrystalline polymers combine the properties of both crystalline polymers and amorphous polymers, and thereby, they can improve crystalline film uniformity while simultaneously attributing to charge transport of the organic semiconductors [126]. We believe future studies on these important issues will provide more exciting opportunities to enhance the charge transport, align crystal growth and expand potential application in organic electronic devices.

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Author contributions

All authors wrote and reviewed this article altogether.

Data availability

Research data is available upon request.

Declarations

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