

Enhanced dielectric performance in PVDF/Al-Al₂O₃ core–shell nanocomposites

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Abstract

We report on scalable, time saving, and cost effective method to synthesize core–shell based nanodielectrics for energy storage applications. Nanodielectric films were fabricated by embedding highly ferroelectric β-phase polyvinilydine fluoride (PVDF) polymer with high electrical conductivity alumina coated aluminum nanoparticles. Al-Al₂O₃ core–shell nanoparticles were successfully synthesized through a simple, cost effective, and scalable blending procedure in PVDF to yield high performance energy storage capacitors. The results on electrical permittivity values, structural morphology and phases change with thermal annealing, thermal and frequency response of capacitance, dielectric strength, and dielectric loss of the produced films with different loadings of core–shell nanoparticles are presented. The dielectric permittivity K of the nanocomposite is found to increase from 12 for pure PVDF polymer to a value of 23.6 when PVDF was embedded with a 20% volume loading of Al-Al₂O₃ core–shell nanoparticles. Electrical characterization showed reproducible and stable capacitance values of $10-25$ nF/in² over frequencies of up to 10 MHz as well as an increase in capacitance with temperature below 70 °C, and a decrease thereafter. Breakdown voltage for the Al-Al₂O₃/PVDF composite films reached 70 V/ μ m at a loading of 10% and decreased to 63 V/µm for 20% loading films. Results show that proper loadings (10–20%) of engineered oxidized aluminum (Al-Al₂O₃) in PVDF provide high permittivity low loss nanodielectrics on par with commercially leading dielectrics manufacturer (3M C-Ply) along with the added structural flexibility and cost-saving to the end user.

1 Introduction

Every generation of capacitors are primarily credited to the innovation and engineering of new dielectric materials. Dielectric materials are categorized into organic polymers, inorganic ceramics, filled and unfilled polymeric thin films, ceramic films and nanodielectric composites. Nanodielectric composites belong to a new type of materials engineered for improved functions, performance for dielectrics, and electrical insulation.

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High electrical permittivity (k) materials have received a tremendous interest recently due to their potential applications in high capacitive gate dielectrics, field effect transistors, actuators, and photonic devices $[1-5]$ $[1-5]$ $[1-5]$. Extensive interest is being invested into the research of polymer based nanocomposites films [[6](#page-6-2)[–15\]](#page-6-3). Such a material provides a more practical energy storage solution primarily for flexible electronics [[16\]](#page-6-4). The amount of stored energy depends on the nature of materials and polarizability of elaborated nanocomposites. It can be increased substantially by addition of nanoparticles to the host polymer which can result in better electrical, mechanical, and thermal properties of the nanocomposites.

Achieving permittivity values $K > 100$ is difficult even with the mature ceramic-filled polymer technology. Experimentally, relative K values of 70 are considered excellent for polymer/ferroelectric composites. The dielectric performance of the nanocomposites can only be improved through highly dispersion of the nanoparticles. Conventional mixing techniques cannot break-up nanoparticles due their easy agglomeration as a result of high surface energy. Therefore, a key issue to achieving higher performance nanodielectrics is by making well dispersed nanoparticles in the host polymer.

The commonly used metal particles are silver and aluminum with usual size in the order of 1 µm. Epon 58034 (resin) and bispheno-A epoxy (DER 661) (polymer) are commercially available from the Shell company. However, the use of micro-size fillers impacts negatively the dielectric strength of polymeric materials by creating local charging, known as Maxwell–Wagner interfacial polarization effects at the micro-clusters sites [\[17](#page-6-5)]. Nanosize fillers are predicted to enhance the dielectric strength as opposed to micro-fillers. Experimental results suggest that when the particle size becomes comparable to the size of polymer chain, the particles act cooperatively with the host matrix and mitigate the internal charge creation [[18\]](#page-6-6). The interface changes both the magnitude and distribution of the internal charge and gives rise to an interaction zone called diffuse layer, which affects the interfacial polarization through the formation of local conductivity.

This research paper reports on synthesis and characterization of high permittivity core–shell metal/ferroelectric polymers for use in energy storage capacitors. Polyvinilydine fluoride or Polyvinilydine difluoride (PVDF) is being considered as dielectric in energy storage capacitors. PVDF is highly non-reactive and resistant to solvents, acids, bases, thermal heat, and has low smoke generation during a fire event. It also has a low density (1.78) and costs less compared to other fluoropolymers. Strong piezoelectricity was observed in PVDF, with the piezoelectric coefficient of poled (placed under a strong electric field to induce a net dipole moment) thin films as large as 6–7 pC/N: 10 times larger than that observed in any other polymer. In this paper, we used PVDF as a polymer matrix and Al/Al_2O_3 core–shell nanoparticles for producing high dielectric composite materials.

PVDF exists in four different crystalline phases: α , β , γ and δ. It displays such diverse crystalline phases depending on crystallization conditions. The non-polar α phase is the common phase, which is obtained by melt crystallization at temperatures above 120 °C. The β-phase is the polar desirable phase due to its ferroelectric characteristics, and hence it is important to maximize β-phase in PVDF films used as dielectrics. The γ and δ are polar phases, but the magnitude of their dipole moment is much smaller than that of β-phase. Thus, the PVDF polymer containing β-phase has garnered the most interest for electronic applications as it can provide the best ferroelectric properties. It is also possible to extract β-phase from other phases by varying crystallization temperature and structural deformation [[19\]](#page-6-7).

The concept of putting metal nanoparticles instead of ceramics comes up because metals can, in principle, be thought of as the limiting case of high permittivity particles [\[17,](#page-6-5) [20](#page-6-8)[–23](#page-6-9)]. We have shown during a modeling and simulation study that k of the nanocomposite increase considerably with increasing metal nanoparticle loading below the percolation threshold of 16% [[20\]](#page-6-8). In order to increase the energy storage capability without compromising the inter-particle spacing, we make core–shell capacitors uniformly distributed in the host polymeric films. These improved core–shell embedded devices should allow for storage of a large amount of charge per unit volume that can be released rapidly on demand. The present work shows that nanocomposites made of low loading of metal nanoparticles $({\sim}60 \text{ nm})$ fillers into polymers such as PVDF is a forceful and influential concept for producing high electric permittivity films with a minimum dielectric loss possible. We tested the elaborated nanocomposite into standard macroscale capacitors for energy storage devices.

2 Experimental section

2.1 Materials and sample preparation

It has been reported that PVDF films derived using *N*,*N*-Dimethylformamide (DMF) polar solvent have a polar β-phase [[19](#page-6-7)]. Such β-phase PVDF films were used here in the experimental procedure to prepare polymer and nanoparticle solutions. Post-processing is one of the key stages in fabrication as it plays an important role in deciding the material characteristics of the dielectric. Post-processing a PVDF dielectric increases its breakdown strength and decreases the dielectric loss. This post-processing involves heating the spin-coated dielectric to a temperature that allows PVDF to crosslink. As part of this work PVDF samples were cured at curing temperatures over a range of 30–120 °C for 10 min.

The blending procedure for making $AI-Al₂O₃$ core–shell-PVDF nanodielectric is shown in Fig. [1.](#page-2-0) Aluminum nanoparticles used in the experiments are manufactured by American Elements Inc [[24](#page-6-10)]. These nanoparticles were thermally oxidized to form an Al_2O_3 capping shell on the metallic nanoparticle $(Al-Al₂O₃)$ for electrical insulation as shown on Fig. [2](#page-2-1). This eliminates the need of a functionalization procedure to form an insulating shell around the metal aluminum core. The solid oxide shell around the Al core prevents agglomeration of Al nanoparticles. In the event where two core-shells interact with each other, our simulation results has shown a drop in the net polarization at the interface region [[20\]](#page-6-8). Furthermore, the shell-isolated structure enhances the thermal and chemical stability of the nanoparticles, improves solubility and reactivity as well as allows conjugation of other molecules to these particles. The important step in determining the characteristics of the nanodielectric is the mixing of the PVDF and $AI-Al₂O₃$ nanoparticles in DMF (Dimethylformamide) solutions in proper

Fig. 2 Representative scheme for oxide shell-isolated metal nanoparticle in PVDF polymer

proportions as the capacitance of the film coated using Al- Al_2O_3 nanoparticles and PVDF depends on the concentration of nanoparticles (also termed as loading) in the dielectric. A solution of $AI-AI_2O_3$ nanoparticles in DMF solvent was stirred and sonicated to completely disperse the nanoparticles. A second solution of PVDF polymer in DMF solvent was made by stirring and heating for few hours. Next, these two solutions were slowly mixed together and stirred for a

Fig. 3 Fabrication steps in making planar capacitor devices **Al evaporation**

few hours on a magnetic stirrer. Finally, the mixed solution was sonicated for 2–5 min by a high power Misonix sonicator to break-up any agglomerates. A small amount of mixture, approximately 0.2 ml, was used to make thin $AI-AI_2O_3/$ PVDF films on a quarter section of a 2-inch silicon wafer (Si) using a Laurell Technologies WS-400 spinner followed by thermal curing on a hotplate. Prior to casting and curing the nanocomposite on silicon substrates, we first applied a thin layer of aps (3-aminopropyl triethoxysilane) solution to enhance the interfacial adhesion of PVDF [[25\]](#page-6-11).

2.2 Fabrication and characterization

The steps in making a capacitor device using synthesized nanodielectrics are shown in Fig. [3](#page-2-2). Prior to spin coating the nanocomposite, the Si wafer substrate is cleaned to remove grease and surface dust. Trichloroethylene, acetone and methanol are used in similar volumes to clean the wafer. 10 ml of each of these solvents are put into a beaker with the Si wafer. This is sonicated in an ultrasound sonicator bath for 1 min with the substrate then dried on a hotplate at 120 °C for 1 min. Finally, the wafer is rinsed with deionized water and blown with dry nitrogen gas. The bottom aluminum (Al) electrode of the planar capacitor was deposited by using a thermal evaporator. Al top electrodes were deposited through a mechanical mask with $3 \text{ mm} \times 3 \text{ mm}$ openings which correspond to the actual size of the tested capacitors. We fabricated one capacitor array using unloaded PVDF as a reference sample and two capacitors arrays with

10 and 20% volume loadings of $AI-Al₂O₃$ fillers in PVDF host polymer. The three types of devices were then characterized for their capacitance characteristics using an HP-4275A high frequency LCR meter.

The LCR meter was attached to the probe station which has two positioners connected to the top and bottom electrodes, respectively. Current–voltage measurements were obtained using a Keithley 6487 Pico ammeter/voltage source controlled by LabVIEW software to supply the input voltage and to read the output current. The leakage current and corresponding applied voltage till film breakdown were recorded and plotted. The thickness of the polymer was determined from the nanocomposite cross section using an FEI XL-30FEG scanning electron microscope (SEM).

3 Results and discussion

The high resolution SEM micrograph in Fig. [4](#page-3-0) shows a high dispersion of 10% volume loading of Al core–shell nanoparticles in PVDF matrix. The size of Al nanoparticles varies between 60 and 100 nm and the Al_2O_3 shell is about 15 nm thick as depicted by the inset picture. X-ray diffraction analysis was carried out on nanocomposite samples in order to identify the phase of PVDF polymer and to study the aluminum and alumina materials structure. The XRD peaks in Fig. [5](#page-3-1) and which are positioned at diffraction angles (2θ) of 20.1° and 20.3° confirm the presence of γ (110), and $β$ (200,110) phases of PVDF [[17\]](#page-6-5). The other XRD peaks at 38.52°, 44.67°, 65.13°, 78.27°, 28.56°, 26.64°, 34.67° identify the presence of Al (111), Al (200), Al (220), Al (211) [\[26\]](#page-6-12), Si (111) [[27\]](#page-6-13), α -Al₂O₃ (012), and α -Al₂O₃ (104) [\[28](#page-6-14)], respectively. Based on these results, it was determined that cured samples at 120 °C for 10 min showed β and γ phases in the composite. The β-phase has the highest share and it

Fig. 4 SEM micrographs of 10% volume loading of $AI-AI_2O_3$ NPs in PVDF matrix

Fig. 5 XRD image of $AI-AI_2O_3$ NPs in PVDF matrix

is of great interest due to its ferroelectric nature as stated above. Therefore, identical curing process was performed on all samples associated with this work. Regarding the crystallinity of the shell, it was found to be α -phase Al₂O₃ (Fig. [5](#page-3-1)), a well-known material with high dimensional sta-bility. Figure [6](#page-4-0) shows the surface morphology of $A1-A1_2O_3/$ PVDF composite films which were spin coated at 2000 RPM and thermally treated in the range of 30–120 °C.

Table [1](#page-4-1) reports the specifications of fabricated capacitors as well as the average value parameters of tested capacitors at a frequency of 10 kHz. The dielectric loss is reduced as Al nanoparticles loading is increased in the nanocomposite. This is most likely due to enhancement of electrical conductivity right at the interface between both top and bottom aluminum electrodes and the nanocomposite. The permittivity of the bare PVDF is about 12 and it doubles in value for a volume loading of 20%. Figure [7](#page-4-2) shows the frequency behavior of typical capacitors having $3 \text{ mm} \times 3 \text{ mm}$ Al electrodes. Each reading of the capacitance represents the average value of measurements taken on an array of five capacitor devices. For comparison purpose, the capacitance values are normalized to the same film thickness of 6300 nm. Figure [7](#page-4-2) indicates a very small variation (0.3–1.1%) of capacitance over 10 kHz–10 MHz frequency range.

Table [2](#page-5-0) shows the embedded dielectric characteristics of fabricated capacitors in comparison with existing commercial ones. The leading commercial dielectric under 3M trade mark "C-Ply" is made of an epoxy/BaTiO₃ composite. Such polymer-ceramic composites combine the processability of the polymer with the high electrical permittivity of the ceramics. However, if the dielectric constant of the polymer-ceramic composite has to be maintained high then it has to be heavily doped with ceramic fillers (about 80%) which unfortunately is detrimental to the

Fig. 6 Surface morphology of spin coated Al-Al2O3/PVDF composite films at different curing temperatures: 30 °C (**a**), 80 °C (**b**), and 120 °C (**c**)

Table 1 Specifications of fabricated capacitors and the average value characteristics of tested capacitors at 10 KHz

	Tested capacitors		
		2	3
Capacitance (pF)	262	250	300
Thickness (nm)	3600	5500	6300
Dielectric loss	0.01	0.006	0.0013
Loading (vol. $\%$)	0	10	20
Permittivity (K)	12.	17.3	23.7

composite's mechanical performance and sometimes to its electromechanical reliability $[29, 30]$ $[29, 30]$ $[29, 30]$ $[29, 30]$. For Al-Al₂O₃/PVDF composites, results show that proper loading (10–20%) of engineered oxidized aluminum nanoparticles in PVDF provide high permittivity, low loss nanodielectrics on par with commercially available capacitor devices along with

Fig. 7 Dependence of capacitance on frequency for tested capacitors

Table 2 Embedded dielectric characteristics of fabricated capacitors in comparison with existing commercial ones

Fig. 8 Dependence of capacitance on temperature for the capacitors **Fig. 8** Dependence of capacitance on temperature for the capacitors **Fig. 9** Breakdown field characteristics of Al-Al₂O₃/PVDF capacitor of Fig. [7](#page-4-2)

structures at different loadings

the added structural flexibility and cost-saving to the end user.

Figure [8](#page-5-1) shows the variation in capacitance with loading over a temperature range up to 170 °C for the same characterized capacitors in Fig. [7.](#page-4-2) The capacitance value showed steady response with temperature below 70 °C, and diminished thereafter, for different loadings of $\text{Al-Al}_2\text{O}_3$ core–shell nanoparticles. However, the capacitor with the high loading (20%) showed a drastic fall in capacitance when temperature goes beyond 70 °C. This phenomenon was found to be reversible i.e., capacitors retrieved their original capacitance values when temperature was brought down to room temperature. This could be related to an increase in the electrical contact resistance at the interface between both top and bottom aluminum electrodes and the nanocomposite. The change of resistance at 20% loading may be attributed to high number of core–shell nanoparticles in the diffuse layer, which affects the interfacial polarization through the formation of local conductivity. In this case, we believe as the temperature increases above 70 °C, there might be an air gap that separated the capacitor electrodes providing areas of isolation and which results in poor electrical contacts. Figure [9](#page-5-2) shows the breakdown voltage (BDV) measurements of Al-Al₂O₃/PVDF capacitor structures at different loadings. The breakdown voltage of the reference host polymer was about 50 V/µm at a standard leakage current of 1 µA. The dielectric strength reached 70 V/µm at a loading of 10% and decreases to 63 V/µm as the loading increases to 20%.

4 Conclusions

Embedding metallic nanoparticles into polymers is emerging as an effective approach for producing high permittivity films. Al-Al₂O₃ core–shell nanoparticles were successfully synthesized through a simple, cost effective, and scalable blending procedure in a PVDF polymer matrix to yield high performance energy storage capacitors. Analysis revealed a uniform nanodielectric film thickness which can be controlled by varying the concentration of the PVDF polymer solution and also showed a principally ferroelectric β-phase for the PVDF matrix. Different loadings of $AI-AI_2O_3$ nanoparticles were embedded into PVDF polymer, the dielectric permittivity K of the nanocomposite increased from 12 for pure PVDF polymer to a value of 23.6 when PVDF was embedded with a 20% volume loading of Al-Al₂O₃ nanoparticles. Electrical characterization showed reproducible and stable capacitance values of $10-25$ nF/in² over frequencies of up to 10 MHz as well as an increase in capacitance with temperature below 70 °C, and a decrease thereafter. Breakdown voltage for the $AI-AI_2O_3/PVDF$ composite films reached 70 V/µm at a loading of 10% and decreased to 63 V/ µm for the 20% loading films. We conclude that entraining metallic nanoparticles into polymers to make nanodielectric films is a viable concept for producing high permittivity material at low metal nanoparticles loading. Results show that proper loadings (10–20%) of engineered oxidized aluminum $(Al-Al₂O₃)$ in PVDF provide high permittivity low loss nanodielectrics on par with commercially leading dielectrics films along with the added structural flexibility and cost-saving to the end user. Due to the low loading of nanoparticles, capacitors fabricated with this technique retain the advantages of polymers while displaying an increased capacitance density.

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Author contributions The manuscript was written through contributions of all authors. All authors have given approval to the final version of the manuscript. NB planned this study and carried out all electrical characterizations, data analysis, and plotting. RM prepared all figures. SK wrote the manuscript. ASR prepared the SEM samples and performed analysis. AI read and commented on the manuscript.

Compliance with ethical standards

Conflict of interest The authors declare that they have no conflict of interest.

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