

# **The relationship between deep-level defects and high resistivity characteristic in CdZnTe crystals**

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**Abstract** The internal defects of CdZnTe crystals grown by the modified vertical Bridgman (MVB) method act as trapping centers or recombination centers in the band gap, which have effects on its resistivity. The relationship between deep-level defects and high resistivity characteristic in  $Cd_{0.9}Zn_{0.1}Te$ : In single crystal was studied. The deep-level defects were identified by thermally stimulated current (TSC) spectroscopy and thermoelectric effect spectroscopy (TEES) in the temperature range of 18–315 K. The trap-related parameters, e.g., activation energy, capture cross section, trap density were characterized by the simultaneous multiple peak analysis (SIMPA) method and Arrhenius fitting. The deep donor level  $(E_{DD})$  dominating dark current was about 0.664 eV near the middle gap by fitting the plots of the natural logarithm of dark current intensity  $ln(I_{DC})$  versus  $1/(kT)$ . The doubly ionized Te antisite  $(Te_{\text{Cd}}^{2+})$  below the conduction band acting as a deep donor level was mainly the origin of  $E_{\text{DD}}$  level. The energy value of Fermi-level was about 0.671 eV characterized by current–voltage (*I–V*) measurements of temperature dependence in the temperature range from 275 K to 315 K. The resistivity was about  $8.17 \times 10^9$   $\Omega$ ·cm measured by *I–V* measurement at room temperature. The high resistivity performance of  $Cd_{0.9}Zn_{0.1}Te$ : In crystal is mainly due to the Fermi-level pinned near the middle gap by the  $E_{\text{DD}}$  level.

## **1 Introduction**

Cadmium zinc telluride ( $Cd_{1-x}Zn_xTe$  or CZT) single crystal is a promising nuclear radiation detector at room temperature. It can be used to detect X-ray and gamma-ray in astronomy, environmental monitoring and nuclear medicine imaging fields because it has high charge collection efficiency for carriers and can work normally in a large electric field. This is mainly related to the performance of high resistivity and good mobility-lifetime products [\[1](#page-5-0)[–4](#page-5-1)]. The high resistivity characteristic of CZT crystals is usually obtained by using group III (Al, In) or group VII (Cl) elements as dopant to compensate the native defects of Cd vacancies ( $V_{Cd}$ ) effectively [\[5](#page-5-2), [6](#page-5-3)]. Although many researchers have successfully grown CdZnTe crystals of high resistivity, there is no a unified and impeccable mechanism for the formation of high resistivity.

In this work, The deep-level defects were identified by thermally stimulated current (TSC) spectroscopy and thermoelectric effect spectroscopy (TEES). Then, the traprelated parameters were characterized by the simultaneous multiple peak analysis (SIMPA) method and Arrhenius fitting. The deep donor level  $(E_{DD})$  was evaluated by fitting the plots of the natural logarithm of dark current intensity  $ln(I_{DC})$  versus  $1/(kT)$ . The Fermi level was evaluated by current–voltage (*I–V*) measurements of temperature dependence. The resistivity was measured by *I–V* measurement at room temperature. Finally, the high resistivity performance of  $Cd_{0.9}Zn_{0.1}Te$ : In crystal was explained by the relationship between the Fermi level and a near mid-gap deep donor level defect.

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### **2 Experimental procedure**

 $Cd_{0.9}Zn_{0.1}Te$ : In crystal used in our experiments was grown by the modified vertical Bridgman (MVB) method. From the ingot the  $5 \times 5$  mm<sup>2</sup> wafer with thickness 2 mm was cut. Next, the wafer was mechanically polished and then chemically polished by MgO turbid liquid and the solution mixed by Si sol and solution of  $H_2O_2$  to remove surface scratches. The deionized water and acetone solution were used to clean the wafer subsequently. Finally the wafer was chemically etched in 2% bromine methanol solution to remove the damaged surface layer. The wafer was deposited with electroless Au contacts on opposite surfaces in a planar structure or on both edges in a strip structure using an  $AuCl<sub>3</sub>$  solution for further measurements.

The deep level defects were characterized on the strip structure via TSC spectroscopy and TEES. In TSC measurement, the sample was placed in a closed-cycle cryostat with liquid helium (Sumitomo DE-202) and cooled to 18 K in darkness, where the deep traps were filled using a halogen lamp. Then, the trapped carriers were released by thermal emission in the dark until 315 K at a certain bias voltage and a constant heating rate. The experiments choose 10 V bias voltage and 0.10, 0.15, 0.18, 0.21, 0.24, 0.27 K/s heating rate respectively, the resulting TSC spectrums were obtained. Besides, the temperature dependence of dark current  $I_{\text{DC}}$  was determined by implementing the above described steps without illumination at low temperatures. The TEES measurement, which is similar to TSC measurement, was performed to identify the type of traps (electrons or holes). A thermal gradient across the sample is imposed for TEES, while an electric field is applied for TSC. The experiment chooses 10 V bias voltage, 0.21 K/s and

<span id="page-1-0"></span>

a small temperature gradient of several degrees to avoid perturbing the dynamics of cooling/heating cycle of the cryostat.

The planar electrode structure was selected for current–voltage (*I–V*) measurements of temperature dependence. The sample was placed in a cryostat to cool by liquid nitrogen and then was researched by *I–V* curves of temperature dependence with the bias voltages from −10 to 10 V. The temperature was 275, 285, 295, 305, 315 K respectively. The resistivity of wafer was measured by an Agilent 4155°C semiconductor parameter analyzer with the bias voltage from −1 to 1 V at room temperature.

# **3 Results and discussions**

#### **3.1 TSC and TEES analysis**

Figure [1a](#page-1-0) presents the heating rate dependence of TSC spectra as a function of temperature ranging from 18 to 315 K. It shows that the peak positions shift toward high temperatures as the heating rate  $\beta$  increases from 0.10 to 0.27 K/s. Figure [1](#page-1-0)b shows the effect of heating rate on the corresponding dark current spectra, and the inset illustration is the part of dark current spectra in the temperature range of 265 to 315 K.

Generally, the relationship between TSC peak temperature  $T<sub>m</sub>$  and heating rate  $\beta$  is described by the expression [[7\]](#page-5-4):

$$
E_{\rm a} = kT_{\rm m} \ln \left( \frac{N_{c,v} \ \sigma v k T_{\rm m}^2}{\beta \left( E_{\rm a} + 2kT_{\rm m} \right)} \right) \tag{1}
$$

<span id="page-1-2"></span><span id="page-1-1"></span>In the case of  $E_a \gg 2kT_m$ , Eq. [\(1](#page-1-1)) can be simplified as

$$
E_{\rm a} = kT_{\rm m} \ln \left( \frac{N_{c,\nu} \ \sigma v k T_m^2}{\beta E_a} \right) \tag{2}
$$



The *inset* illustration is the part of dark current spectra in the temperature range of 265–315 K

Equation ([2\)](#page-1-2) can be transformed into the form of  $\text{In}(T_{\text{m}}^2/\beta)$ as a function of  $1/T<sub>m</sub>$  in Eq. ([3\)](#page-2-0), which is called Arrhenius formula,

$$
\ln\left(\frac{T_{\rm m}^2}{\beta}\right) = \frac{E_{\rm a}}{kT_{\rm m}} - \ln\left(\frac{N_{c,\nu}\,\sigma v k}{E_a}\right) \tag{3}
$$

where  $E_a$  is the thermal activation energy of a specific trap level, *k* Boltzmann's constant,  $N_{c,v}$  is the effective density of states in the valence (conduction) band,  $\sigma$  is the trap capture cross section, and  $\nu$  is the thermal velocity of the holes (electrons). The value of  $E_a$  can be determined by the slope of Eq. [\(3](#page-2-0)).

In order to reduce the mutual interference of overlapping peaks and determine all trap levels simultaneously from the TSC spectrum in the CZT crystal, the simultaneous multiple peak analysis (SIMPA) method reported by Pavlovic and Desnica  $[8]$  $[8]$  is introduced. The fitting function  $I_{SIMPA}$ , including the sum of TSC peaks belonging to the specific deep levels  $I_{\text{TSC}}$  and dark current  $I_{\text{DC}}$  is defined by

$$
I_{\text{SIMPA}}(T) = \sum_{i=1}^{m} I_{\text{TSC}}^{i}(T) + I_{\text{DC}}(T),
$$
\n(4)

where  $I_{\text{TSC}}^i(T)$  represents the *i*th individual TSC peak current, *m* is the number of deep traps in the calculation and  $I_{\text{DC}}(T) = C \exp(-E_{DD}/kT)$ . *C* is a constant, k is the Boltzmann constant and  $E_{\text{DD}}$  is the deep donor activation energy. The  $E_{\text{DD}}$  level plays a significant role in the dark current and electrical compensation.

In the 'first order kinetics' approximation, the *i*th individual TSC peak current can be described as

And then  $N_{T_i}$  can be calculated from

$$
N_{T_i} = \frac{Q_{T_i}}{2\mu_n(T_i)\tau_n(T_i)eAE}.
$$
\n<sup>(7)</sup>

<span id="page-2-0"></span>In addition,  $D_{t,i}$  can be defined as  $D_{t,i} = 3.0 \times 10^{21} (m^*/m_0) \sigma_i$ , where  $m^*$  is the electron (or hole) effective mass,  $m_0$  is the electron (or hole) rest mass and  $\sigma_i$  is the capture cross section. The Zn concentration in  $Cd_{0.9}Zn_{0.1}Te$  crystal is relatively low, so we can use  $m^* = 0.14m_0$  for electrons and  $m^* = 0.37m_0$  for holes in CdTe [[9,](#page-5-6) [10\]](#page-5-7). Therefore, the trap-related parameters  $(E_{a,i}, N_{T_i}$  and  $\sigma_i$ ) in Cd<sub>0.9</sub>Zn<sub>0.1</sub>Te:In wafer were characterized by the above calculations.

Take the TSC measurement of 10 V bias voltage and 0.21 K/s heating rate for example, Fig. [2a](#page-3-0) shows that the SIMPA results of three deep trap levels, named from  $T_1$  to  $T<sub>3</sub>$  along the high temperature direction. In order to clarify the type of traps (electron or hole), TEES was obtained in Fig. [2](#page-3-0)b. To get the thermal activation energy of this three trap levels, Fig. [2c](#page-3-0) indicates the relationship between  $\ln(T_{\rm m}^2/\beta)$  and  $1/T_{\rm m}$  adopted Arrhenius fitting according to the Eq. [\(3](#page-2-0)). In addition, Fig. [2d](#page-3-0) shows that a deep donor level dominated by the dark current. The trap parameters of observed traps are presented in Table [1](#page-3-1) by calculation with the SIMPA method.

Among these traps, trap  $T_1$  (0.063 eV,  $7.5 \times 10^{14}$  cm<sup>-3</sup>,  $1.68 \times 10^{-17}$  cm<sup>2</sup>) may correspond to the shallow acceptor of the so-called A center originated from  $V_{Cd}$ -In<sub>Cd</sub> complex [\[11](#page-5-8)]. Trap  $T_2$  (0.102 eV,  $6.1 \times 10^{14}$  cm<sup>-3</sup>,  $4.72 \times 10^{-17}$  cm<sup>2</sup>) may be related with the point defect  $In<sub>Cd</sub><sup>+</sup>$  derived from the indium element dopant [[12\]](#page-5-9). Trap  $T_3$  (0.571 eV,  $1.4 \times 10^{14}$ 

$$
I_{TSC}^i(T) = N_{T_i} e \mu_n A E \tau_n D_{t,i} T^2 \exp\left\{-\frac{E_{a,i}}{kT} - \frac{kD_{t,i}}{\beta E_{a,i}} T^4 e^{-E_{a,i}/kT} \times \exp\left[1 - 4\frac{kT}{E_{a,i}} + 20\left(\frac{kT}{E_{a,i}}\right)^2\right]\right\},\tag{5}
$$

where  $N_{T_i}$  is the carrier concentration of the filled *i*th trap level, *e* is the electron charge,  $\mu_n$  is the carrier mobility, *A* is the area of the electrode, *E* is the applied electric field,  $\tau_n$  is the carrier lifetime,  $D_{t,i}$  is the temperature-dependent coefficient of the filled *i*th trap level, *T* is the absolute temperature,  $k$  is the Boltzmann constant,  $\beta$  is the heating rate and *Ea*,*<sup>i</sup>* is the thermal activation energy of the filled *i*th trap level.

Besides, the effective collected charge  $Q_{T_i}$  released by the filled *i*th trap level can be accurately calculated by SIMPA method, which can be described as

$$
Q_{T_i} = \int I_{TSC}^i dt = \frac{1}{\beta} \int_{T_0}^T I_{TSC}^i dT.
$$
 (6)

cm<sup>-3</sup>,  $2.54 \times 10^{-17}$  cm<sup>2</sup>) may be attributed to Cd vacancy [\[13](#page-5-10)]. Besides, the deep donor level  $E_{\text{DD}}$  related to doubly ionized Te antisite  $(Te_{Cd}^{2+})$  dominating the dark current in  $Cd_{0.9}Zn_{0.1}Te$ : In crystal is 0.664 eV, which is similar to the 'EL 2' middle gap level in SI-GaAs [[14\]](#page-5-11).

#### **3.2 Current–voltage analysis**

The relationship between resistivity (*ρ*) and Fermi level  $(E_F)$  can be described by the expression [\[15](#page-5-12)]:

$$
\ln \rho \propto \frac{E_{\rm F}}{kT} \tag{8}
$$

where  $k$  is Boltzmann's constant and  $T$  is thermodynamic temperature.



<span id="page-3-0"></span>**Fig. 2 a** TSC spectrum of 10 V bias voltage and 0.21 K/s heating rate from  $Cd_{0.9}Zn_{0.1}Te$ : In and its SIMPA fitting results. The *dashed curve* represents the experimental TSC spectrum and the *solid curve* above it represents the fitting TSC spectrum, the other three *solid* 

*curves* show the individual fitting TSC peaks belonging to specific trap levels. **b** The corresponding TEES spectrum. **c** Arrhenius fitting for trap levels in TSC spectra. **d** Determination of the corresponding  $E_{\text{DD}}$  level by the linear fit of ln(I<sub>DC</sub>)−1/(kT)

<span id="page-3-1"></span>

<b>Table 1</b> Trap parameters of observed trap levels in $Cd_{0.9}Zn_{0.1}Te$ : In crystal determined with the SIMPA method	Trap level	Peak maximum/K	Activation energy/eV	Concentration/ $cm^{-3}$	Capture cross section/ $cm2$	Trap type
	$T_2$	128 139	0.063 0.102	$7.5 \times 10^{14}$ $6.1 \times 10^{14}$	$1.68 \times 10^{-17}$ $4.72 \times 10^{-17}$	Electron Hole
	$T_3$	201	0.571	$1.4 \times 10^{14}$	$2.54 \times 10^{-17}$	Electron
	$E_{DD}$		0.664	$\qquad \qquad \blacksquare$		

$$
\rho = \frac{U}{I} \frac{S}{d} \tag{9}
$$

where *U* is bias voltage, *I* is current, *S* is the electrode area and *d* is the thickness of the wafer.

In order to get the value of Fermi level, the bias voltages between −10 and 10 V were chosen and the temperature selected 275, 285, 295, 305, 315 K respectively. The current–voltage (*I–V*) measurement under a low bias voltage can reflect the bulk resistivity of sample at room temperature  $[16]$  $[16]$ , so the bias voltages between  $-1$  and 1 V were chosen at room temperature.

Figure [3a](#page-4-0) shows the *I–V* curves of temperature dependence in the temperature range from 275 to 315 K. Fig. [3](#page-4-0)b shows the relationship between resistivity (*ρ*) and Fermi level  $(E_F)$ ,  $E_F$  was evaluated to be  $0.671 \pm 0.005$  eV near the mid-gap of CdZnTe according to the expression (8). Figure [4](#page-4-1) shows the *I–V* curve between −1 and 1 V at room temperature, the bulk resistivity of  $Cd_{0.9}Zn_{0.1}Te:In$ wafer is about  $8.17 \times 10^9$   $\Omega$  cm, which meets the performance requirement for nuclear radiation detector at room temperature.

## **4 Conclusions**

In this study, The deep-level defects of  $Cd_{0.9}Zn_{0.1}Te:In$ crystal grown by the MVB method were identified by TSC and TEES measurement, and the trap-related parameters were characterized by the SIMPA method



<span id="page-4-1"></span>**Fig. 4** *I–V* fitting result of  $Cd_{0.9}Zn_{0.1}Te$ : In wafer applied on the bias voltages from −1 to 1 V

and Arrhenius fitting. The energy value of Fermi-level was characterized by *I–V* measurements of temperature dependence. The resistivity was measured by *I–V* measurement at room temperature.

- Based on the SIMPA method, three trap levels and a deep donor level  $E_{DD}$  dominating the dark current and their origin are identified.
- The  $E_{\text{DD}}$  derived from Te<sup>2+</sup> is about 0.664 eV and the Fermi-level is about 0.671 eV.
- The resistivity of  $Cd_{0.9}Zn_{0.1}Te:In$  crystal is about  $8.17 \times 10^9$  Ω·cm. The high resistivity performance of



<span id="page-4-0"></span>**Fig. 3** a *I–V* measurements of temperature dependence for Cd<sub>0.9</sub>Zn<sub>0.1</sub>Te:In wafer and **b** linear fit of Fermi level by the plots of ln  $\rho$  versus 1/(*kT*)

 $Cd_{0.9}Zn_{0.1}Te$ : In crystal is mainly due to the Fermi-level pinned near the middle gap by the  $E_{\text{DD}}$  level.

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