Influence of annealing temperature on performance of dye-sensitized TiO₂ nanorod solar cells

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Abstract Single-crystal rutile TiO_2 nanorod (NR) arrays have been synthesized on the fluorine-doped tin oxide substrates, followed by an annealing at 200–600 °C. It is found that DSSCs fabricated using TiO₂ NR arrays which undergo annealing display an increased efficiency than those that do not undergo annealing. The optimal efficiency of 4.42% power conversion is achieved in the DSSC made with 500 °C annealed arrays, which show a 450% increase in the overall conversion efficiency. The improvement is ascribed to the increased light harvesting, the enhanced electric contact and the suppressed recombination of the injected electrons with redox species in the electrolyte.

1 Introduction

Dye sensitized solar cells (DSSC) have received a worldwide attention as they offer the possibility of extremely inexpensive and efficient solar energy conversion [1–3]. Currently, limited by a low photo-to-electricity conversion efficiency and long-term stability, there is still a long way for the commercialization of this type of solar cells. Although their efficiencies have been reported to reach 12.3% in a very recent report [4], the value is still much lower than the p-n junction based counterparts. In general,

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X. Q. Gu State Key Laboratory of Silicon, Zhejiang University, Hangzhou 310027, China a typical DSSC device consists of a TiO₂ photoanode sensitized by a monolayer of dye molecules. Much effort has been devoted to optimize the morphology and structure of the photoanode for achieving a better cell performance (both in efficiency and stability). Traditionally, the anode is composed of randomly packed TiO₂ nanoparticles (NPs) with particle sizes of 10–20 nm [5–7], which have an advantage of high surface to volume ratios. Nevertheless, the slow electron transport as well as the poor adhesion to substrate greatly limits the cell performance [8–10]. For instance, the NP film thickness is always no more than 20 μ m. Increasing the thickness would lead to a decrease of the overall photoelectric conversion efficiency due to a larger loss from electron recombination, since the electron diffusion length is limited.

Highly oriented 1D TiO₂ nanostructures grown on transparent conductive substrates may present a solution to these challenges [11, 12]. The structure represents an ideal channel for electrical carrier transport and is suitable for device integration. The electrons are transferred and collected through a direct channel rather than a disordered network structure constructed by NPs. In fact, Jenning et al. [13] have estimated the electron diffusion length was over 100 μ m in TiO₂ nanotube (NT) cells, which are at least five times higher than that in the NPs based cells [14]. The diffusion length of electrons is expected to be much longer in the ordered TiO₂ single-crystalline NRs owing to a defect-free structure. There have been a few reports on DSSCs made with TiO_2 NR or nanowire (NW) arrays [11, 15-20]. Feng and his co-workers synthesized the densely packed single crystal TiO₂ NW arrays on FTO glass by hydrolysis of Ti precursors [15]. In their report, a high efficiency of $\sim 5.02\%$ was achieved in the DSSCs fabricated using only 2–3 µm long NWs. Besides, Liu et al. [11] also reported on the preparation of single crystal TiO₂ NR

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array on FTO coated glass and achieving a solar cell efficiency of 3.0% when using a 4 µm thick NR array as the photoanode. In another paper from their group, the oriented single crystalline TiO₂ NWs were prepared on a Ti foil and a power conversion efficiency of $\sim 1.4\%$ was achieved [16]. In these cases, TiO_2 NR arrays were grown directly on the conductive substrates in a one-step, low temperature solution process, which not only reduce the manufacturing cost, but also enhance the adhesion between TiO₂ and FTO layers. In spite of these advantages, the efficiency of the NR cells remains much lower than that of the NP cells. One important reason is that TiO₂ has an isotropous crystal structure and symmetry, which makes it very difficult to achieve a longitudinal structure. Several attempts has been made to improve their surface area, such as immersing the samples in NbCl₅ or TiCl₄ solution followed by a postheating treatment at 500 °C, leading to the corresponding improvement of 25% [15] and 50% [11] in the cell efficiencies, respectively.

In this work, highly ordered and aligned TiO₂ singlecrystalline NR arrays were prepared on the FTO substrates, followed by annealing at 200–600 °C. Afterwards, both the as-prepared and annealed samples were used as the photoanodes for the assembly of the DSSCs. It was found that the cell performance was strongly dependent on the annealing temperature (T_a). The highest conversion efficiency of 4.41% was achieved when the NR arrays were annealed in air at 500 °C. By contrast, the un-sintered cells exhibited a low efficiency of 0.98% power conversion.

2 Experiment

2.1 Fabrication of TiO₂ NR arrays

Single crystal TiO₂ NR arrays are synthesized on FTO glass by a simple one-step hydrothermal method [11]. Briefly, the clean FTO substrates are placed in a sealed autoclave (60 mL volume), containing 0.6 mL of tetrabutyl titanate, 15 mL of hydrochloric acid (37 wt%) and 15 mL of DI water. The substrates were placed horizontal relative to the base of the autoclave, with the conducting side facing up. The synthesis is carried out at 150 °C and for 11 h, followed by the calcination in air at 200–600 °C for 30 min.

2.2 Device assembly

The resulting TiO₂ samples are immersed in a 0.3 mmol ethanolic solution of N719 at 80 °C for 2 h to complete the dye adsorption. Next, a DSSC device is assembled by sandwiching together a TiO₂ electrode and a 20 nm thick Pt-coated FTO. The electrodes are separated by a 60 μ m

thick thermal-plastic Suryln spacer. A commercially available ionic electrolyte solution (Dalian Hepta Chroma) is infiltrated into the space between the electrodes by capillary action using a fine 1 mL syringe. The electrolyte contains 0.6 M 1-butyl-3-methylimidazolium iodide, 0.03 M I₂, 0.5 M tert-butylpyridine, and 0.1 M guanidinium thiocyanate in acetonitrile/valeronitrile 85/15 (volume).

2.3 Characterization

The crystal structure and morphologies of the TiO₂ nanorods are examined by X-ray diffraction (XRD, D8), field emission scanning electron microscopy (FESEM, Sirion 200), high resolution transmission electron microscopy (HRTEM, JEOL–2010). The current density–voltage (J–V) measurements are carried out in dark and under illumination of 60 mW cm⁻², respectively using a Keithley model 2420 digital source meter. The simulated sunlight is provided by a 500 W Xe lamp fitted with an AM-1.5 fitter (Beijing Trusttech). The active area of the DSSCs is fixed to 0.20 cm² by a light shading mask.

3 Results and discussion

3.1 Crystal structure and morphology

Figure 1a, b are the top-surface and cross-sectional FE-SEM images of the as-synthesized TiO₂ NR arrays, respectively. Both the images show a highly uniform, vertically aligned and densely packed array of NRs with flat tetragonal crystallographic planes. The NR array is grown on the FTO layer directly with an average length of $\sim 1.8 \,\mu\text{m}$. Figure 1c is a HRTEM image of a TiO₂ NR selected randomly. The measured inter-planar distance of 0.33 nm correspond to the TiO₂ (110) planes, suggesting that the single-crystal NR grows preferentially along the [002] direction. It is also observed that the NR surface is very clean and abrupt. Figure 1d shows the XRD pattern of TiO₂ NR array. As seen, the NRs have a crystal structure of single rutile phase with the enhanced (101) and (002)peaks, which is consistent with the results obtained from the SEM and TEM characterizations.

3.2 Photovoltaic performance

Figure 2 shows the J–V curves under illumination of DSSCs containing the as-prepared and sintered TiO₂ NR arrays. The open circuit voltage (V_{OC}), short circuit current density (J_{SC}), fill factor (*FF*) and conversion efficiency (η) are summarized in Table 1. These data were collected from one kind of samples prepared in the same route (including the growth condition and assembly process) so that the







Fig. 2 J–V curves under illumination of DSSCs made with TiO₂ NRs annealed at RT–600 $^{\circ}\mathrm{C}$

experimental error caused by the operation or other accidental factors was as small as possible. As seen, the DSSC made with as-prepared TiO₂ arrays shows a η of ~0.98%. By annealing the sample at 200 °C, η is increased to 3.22%, which is mainly ascribed to a large increase in J_{SC} . Increasing the annealing temperature (T_a) from 200 to 500 °C, both the increase of J_{SC} and V_{OC} is observed from

Table 1 J_{SC} , V_{OC} , *FF* and η of TiO₂ NR DSSCs as a function of T_a under an AM 1.5 illumination (60 mW cm⁻²)

<i>T</i> (°C)	$J_{SC} (\mathrm{mA} \mathrm{cm}^{-2})$	V_{OC} (V)	FF	η (%)
RT	1.75	0.65	0.52	0.98
200	5.30	0.65	0.56	3.22
300	6.97	0.65	0.49	3.68
400	7.82	0.70	0.44	4.00
500	8.84	0.73	0.41	4.42
600	7.52	0.77	0.44	4.17

5.30 mA cm⁻² and 0.65 V to 8.84 mA cm⁻² and 0.70 V respectively, while *FF* is found to reduce from 0.56 to 0.41. As a result, the cell efficiency of 4.42% obtained at 500 °C demonstrates a 450% enhancement over the cells fabricated with as-grown samples. With further increasing T_a to 600 °C, J_{SC} decreases to 7.52 mA cm⁻² while V_{OC} increases to 0.77 V, leading to a little smaller conversion efficiency of $\eta = 4.17\%$.

3.3 Mechanism underlying the change of J_{SC} , V_{OC} , FF and η

As well known, the performance of TiO_2 NR DSSCs is mainly determined by the light harvesting, electron transport and collection. The detailed mechanism is revealed as following:

3.3.1 Light harvesting

Light harvesting is an important factor that influences the light-to-electricity efficiency of DSSCs [21]. It is known that the light harvesting is strongly dependent on the efficient surface area for anchoring dye molecules. In this work, the annealing process has been demonstrated as an efficient way to improve the performance of DSSCs. It is because that a number of water molecules are absorbed on the surface of the as-grown TiO₂ NR arrays. The water molecules are removed by annealing, leading to the increased surface sites for anchoring dye. As a result, higher values of J_{SC} and V_{OC} are expected after annealing. Such a behavior has been demonstrated by a comparison of UV-visible absorption spectra indicated in our previous report [22]. In that report, the dye-sensitized TiO_2 NR array which underwent annealing exhibited a higher absorption coefficient over the visible band than that which didn't undergo annealing. Besides, the increase of dye absorption facilitates suppressing the charge recombination at the TiO₂/electrolyte interfaces, which contributes to the increase of J_{SC} and V_{OC} .

3.3.2 Electron injection/recombination on the dye/TiO₂ interface

Generally, the electron injection from the excited dye to TiO_2 can be finished in about 50 fs. Thus, the injection efficiency is strongly dependent on the recombination rate of the charges at the $TiO_2/dye/electrolyte$ interface. In other words, a slower recombination means a more efficient injection of the electrons. Figure 3 compares the



Fig. 3 J-V curves in dark of DSSCs made with the as-grown and 500 °C annealed TiO₂ NRs

voltage-dependent dark current curves of DSSCs made with the as-grown and the sintered samples. The dark current mainly results from the back transfer process occurring at the interface, which typically represents the recombination between the electrons in the conduction band and the I₃⁻ in the electrolyte [23–25]. As indicated in these curves, the sample which underwent annealing show a lower dark current and a higher onset voltage than the asgrown one, when these samples were assembled into the DSSCs. It suggests that the charge recombination was suppressed a lot after annealing. As a result, a more efficient electron injection is obtained, leading to the increase of J_{SC} , V_{OC} and η [26].

3.3.3 Electron transport/collection

It is clear that TiO_2 anode plays two roles in a DSSC: harvest the sunlight and carry the injected electrons. Thus, the transport rate of electrons is another important characteristic of TiO_2 photoanodes. Although the obtained NRs show a single crystal structure, a few defaults or defects may be produced in the surface/interface regions during the growth process. For instance, the injected electrons may be trapped by these defects presented on the surface and interface of NRs, resulting in a lowering of the transport rate. In addition, the transporting electrons may be blocked easily at the TiO_2/FTO interface due to a poor adhesion or electric contact. As a result, the annealing process not only facilitates reducing the number of oxygen vacancies, but also improves the interface adhesion, leading to the increase in J_{SC} and η .

3.3.4 Variation in FF with T_a

In our DSSC, the major factor that limits the efficiency is the low FF. FF is 0.52 for the un-sintered NR cell, which is much lower than the NP cells (always 0.75–0.85) [1]. It is known that FF which is defined as $J_{\text{max}} \cdot V_{\text{max}}/J_{SC} \cdot V_{OC}$ is attenuated by the total sheet resistance of a solar cell, including the series resistance (R_s) and the shunt resistance (R_{sh}) . The values of R_s and R_{sh} are related to the poor surface/interface quality which may be introduced during the assembly process of DSSCs. Specifically, a low FF suggests that there may be either a high R_s or a low R_{sh} in the DSSC. With increasing T_a to 500 °C, FF decreases to 0.41 (see Table 1). Herein, the lowering of *FF* for the sintered cells with respect to the un-sintered ones is mainly attributed to the increase of both J_{SC} and V_{OC} , which can be explained by the terms of Ohm's law and the equivalent circuit model of a DSSC [27]. In details, after annealing, the increase in J_{SC} will result in an increased voltage drop across R_s and a decreased maximum voltage (V_{max}) output the external circuit. The behavior has been observed in the

DSSCs under the various illumination intensities as well by Grätzel's group [28]. In their reports, it was found that *FF* got reduced gradually with improving the illumination intensity.

Based on the aforementioned discussion, it is reasonable to speculate that the increase of J_{SC} with elevating T_a to 500 °C results from the improved light trapping, the enhanced electron injection and transport, as well as the suppressed electron recombination. With further elevating T_a to 600 °C, a slight lowering of J_{SC} is possibly ascribed to the increased sheet resistance of FTO. As is well known, V_{OC} is related mainly to the electron concentration in the conduction band of TiO₂ [29]. Hence, the increase of V_{OC} over the range of RT-600°C can be mainly ascribed to the increased electron injection or the reduced recombination of electrons [22].

4 Conclusion

The influence of T_a (200–600 °C) has been investigated on the performance of TiO₂ NR related DSSCs. The highest efficiency of 4.42% power conversion has been obtained in the solar cell made with 500 °C annealed TiO₂ NR arrays, which is 450% higher than that made with as-grown NR arrays. Such a large improvement is attributed to a higher amount of dye absorption, a faster electron transport and a lower charge recombination after annealing. We hope our work will shed light on understanding the transport mechanism of the electrons in TiO₂ electrodes.

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