

Comparison between the ferroelectric/electric properties of the $\text{PbZr}_{0.52}\text{Ti}_{0.48}\text{O}_3$ films grown on Si (100) and on STO (100) substrates

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Abstract Ferroelectric/electric properties of $\text{PbZr}_{0.52}\text{Ti}_{0.48}\text{O}_3$ (PZT) thin films grown by pulsed laser deposition (PLD) on two different substrates, Si (001) and SrTiO_3 (STO) (001), were comparatively analyzed. The structural characterization has revealed the epitaxial relationship between the grown layers and the two types of substrates, with larger density of structural defects for the films deposited on Si (001) with buffer STO layer. The ferroelectric/electric properties are also different, with lower remnant polarization (about half of the value obtained on STO substrate), higher dielectric constant (about two times larger), and lower leakage current (about two orders of magnitude lower) for the PZT films deposited on Si (001) compared to those deposited on (001) STO substrates. Nevertheless, the results show that the use of a STO buffer layer on Si can be a solution to obtain good quality PZT capacitor structures without using expensive single-crystal oxide substrates. In this way, applications based on PZT capacitors (e.g. non-volatile memories, pyroelectric

detectors, light switches, etc.) would be more easily integrated directly on Si wafers.

Introduction

Ferroelectrics, in capacitor geometry, are intensely used for applications such as non-volatile memories (NVM), infrared (IR) pyroelectric detectors, piezoelectric transducers, or micromechanical systems (MEMS) [1]. Perovskite $\text{Pb}(\text{Zr}_x\text{Ti}_{1-x})\text{O}_3$ (PZT) is one of the most studied materials for applications due to its very good ferroelectric, piezoelectric, and pyroelectric properties. For a Zr/Ti ratio of 52/48, this material is near the morphotropic phase boundary and it is known to exhibit very good piezoelectric properties [2, 3]. Thin films of PZT with Zr/Ti ratio of 52/48 are studied for sensors and actuators applications because they have a high electro-mechanical coupling factor due to existence of both rhombohedral and tetragonal phases [4, 5].

The ferroelectric/electric properties of a thin film can be affected by the compositional and structural defects such as impurities, vacancies, grain boundaries, secondary phases, etc. Many of these defects, with extrinsic contributions to the ferroelectric/electric properties, can be eliminated by the epitaxial growth of the PZT films using pulsed laser deposition (PLD) [6, 7]. Previous reports show that epitaxial films can have superior properties compared to polycrystalline ones [8, 9]. Therefore, a lot of effort was done to obtain high quality epitaxial layers [10]. However, high quality PZT films are usually obtained on single-crystal substrates with perovskite structure, such as SrTiO_3 (STO) or DyScO_3 (DyScO). Eventually, the strain in the film can be adjusted by controlling the mismatch between the lattice constants of the PZT film and of the substrate.

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This can have a significant impact on the ferroelectric properties such as remnant polarization and coercive field [11–13]. The integration of the above mentioned applications (memories, sensors, etc.) with the semiconductor technology requires the deposition of the PZT layer on Si wafers. Metal–ferroelectric–semiconductor structures, with PZT as the ferroelectric and Si as the semiconductor material, were tested for ferroelectric field effect transistors but it was found that the PZT–Si interface is compromised by the formation of silicates and other parasitic phases [14]. Thin layers of other insulating materials (e.g. MgO, SiO₂, yttrium stabilized ZrO₂, STO) were used as barriers against inter-diffusion, allowing the growth of PZT on the Si wafers [15–18]. However, the lack of free charges in the insulating layers, necessary to compensate the depolarization field in the ferroelectric, has impact on the magnitude of the remnant polarization leading to poor retention properties in a memory cell based on metal–ferroelectric–insulator–semiconductor structures [19]. Therefore, it appears that the integration of ferroelectric capacitors directly on Si is more suitable for applications. It means that, besides the buffer layer used to adjust the lattice mismatch between Si and PZT, a conductive layer has to be deposited prior the PZT growth, preferably with the same perovskite structure as the PZT [20, 21].

In this work, we perform a comparative study of ferroelectric PbZr_{0.52}Ti_{0.48}O₃ (PZT) thin films grown by PLD method on the Si (001) and STO (001) substrates. On Si substrate a STO buffer layer was deposited by molecular beam epitaxy (MBE), acting as a template for PZT deposition and as a barrier for Pb diffusion into Si [22–24]. Additionally, a 20 nm layer of SrRuO₃ (SRO) was grown by PLD on top of the STO film in order to act as back electrode for metal–ferroelectric–metal capacitors [10]. It is found that, although the polarization of the PZT film grown on single-crystal STO substrate is still superior, the PZT films deposited on STO-buffered Si wafers possess polarization of comparable value. On the other hand, it is found that the leakage current in the PZT films deposited on Si substrate is of about two orders of magnitude lower than for those deposited on STO substrate. These findings could be important for further integration of PZT capacitors in Si technology, especially for IR detectors and MEMS systems directly connected to the electronic circuitry processing the signals.

Experimental part

PZT thin films with SRO buffer layer were prepared using PLD on STO (001) and Si (001) substrates. An STO buffer layer was deposited by MBE on Si, the growth details can be found in Refs. [25, 26]. A KrF excimer laser (248 nm, 27 ns

duration, 5 Hz) was line-focused onto a target rotating at 25 rpm. The SRO and PZT targets have been bought from Praxair. The fluence of the laser beam was set at 2 J/cm² (about 350 mJ/pulse). The substrate target distance was set at 60 mm. An accurate control of the oxygen pressure in the deposition chamber and of the substrate temperature during the growth process allow us to obtain good SRO and PZT thin films. In the case of SRO layer, the substrate temperature was increased at 700 °C and the oxygen pressure was set at 0.133 mbar. For the PZT layer deposition, the temperature was decreased at 575 °C and the oxygen pressure was set at 0.2 mbar. After the PZT deposition the films were annealed in 1 bar of O₂, for 1 h, in order to compensate the oxygen vacancies. The epitaxial relationship between deposited layers was investigated by X-ray diffraction (XRD) using a Bruker D8 Advance diffractometer with nickel-filtered copper radiation ($K\alpha_1 = 1.5406 \text{ \AA}$, $K\alpha_2 = 1.5444 \text{ \AA}$) and a parabolic X-ray mirror for beam collimation at the X-ray tube output. The measurements were performed in coplanar geometry with horizontal sample stage which means that the incident and the diffracted beams are always in-plane with the normal to the sample stage, and spinning of the sample during phi-scans is allowed only around the vertical axis. The analytical transmission electron microscopy (TEM) and high resolution transmission electron microscopy (HRTEM) investigations have been performed with a JEM ARM 200F analytical microscope with C_s corrector on the probe mode. The cross-section specimens for TEM observations have been prepared by mechanical polishing followed by ion milling at low angle in a Gatan PIPS installation. Ferroelectric properties at microscale level were tested by performing piezoelectric force microscopy (PFM) with a MFP-3D-SA model microscope from Asylum Research. In order to investigate the ferroelectric properties of the PZT layer at macroscale level, Pt electrodes with 0.01 mm² area were deposited on the top surface by magnetron sputtering, using a shadow mask. The polarization–voltage (P–V) measurements were performed using a TF2000 ferritester system equipped with a FE-Module (aixACCT), while the capacitance–voltage (C–V) characteristic were recorded using a Hioki LCR meter. The current–voltage (I–V) characteristics were raised by using a Keithley 6517 electrometer with incorporated dc voltage source. The temperature measurements were performed in a Lake Shore cryoprobe model CPX-VF with CuBe needles.

Results

Structural characterization

The XRD patterns of the PZT thin films grown on the two different substrates are shown comparatively in Fig. 1a.

The analysis showed the textured growth of the layers, and did not reveal extra-phases. The lattice parameter of the STO substrate (cubic, s.g. Pm-3m) determined experimentally was $a = 3.9050 \text{ \AA}$, and that of the Si substrate (cubic, s.g. Fd-3m) was $a = 5.4307 \text{ \AA}$. These values agree well with the reference data [27, 28]. The substrate's peak positions were used as internal reference points for the out-of-plane lattice constants for both samples as well as for the reciprocal space mapping (RSM) in the case of the film on the STO substrate.

The insert in Fig. 1a points upon the layer fringes (f) that appear around the diffraction peaks of both samples. These are related to the SRO layer in the case of the PZT/SRO/STO structure and to both STO and SRO in the case of the PZT/SRO/STO/Si structure. In the last case, the regularity of the SRO fringes is perturbed by the superposition with the STO fringes, the two layers having comparable thicknesses. The presence of layer fringes proves the good crystalline quality of SRO and STO thin films as well as the flatness of the interfaces, both being characteristic to the epitaxial growth. The crystal quality of PZT was investigated by omega-scans based on the PZT 002 lines (Fig. 1b). The degree of alignment of the PZT crystal planes was found to be significantly better in the film grown on STO single crystal.

Azimuth-scans (phi-scans) were performed on tilted crystal planes in order to prove the single-crystal-like orientations of the oxide layers and to determine their relative in-plane orientations (Fig. 1c)—the phi-scans of PZT/SRO/

STO are not shown). The occurrence of four maxima, related to the fourfold rotation axis of the pseudo-cubic structures, confirm the epitaxial growth of the layers. The phi-scans analysis shows that the relative in-plane orientations are such that $\text{PZT}[100]//\text{SRO}[100]//\text{STO}[100]//\text{Si}[110]$, which means that the epitaxial growth of STO on Si is driven by the matching $a_{\text{Si}} = 5.4307 \text{ \AA} \approx a_{\text{STO}} 2^{1/2} = 5.5225 \text{ \AA}$, with the mismatch $(a_{\text{STO}} 2^{1/2} - a_{\text{Si}})/a_{\text{Si}} = 1.7 \%$.

The 2θ - ω scans in Fig. 1a show that the out-of-plane lattice parameter of PZT is significantly larger for the layer grown on STO substrate ($c_{\text{PZT}} = 4.113 \text{ \AA}$), compared to the one grown on Si (4.055 \AA). The reference data for bulk tetragonal $\text{PbZr}_{0.52}\text{Ti}_{0.48}\text{O}_3$ are $a = 4.0460 \text{ \AA}$, $c = 4.1394 \text{ \AA}$ [5].

RSM were constructed in the vicinity of the (-103) nodes of STO, SRO, and PZT in order to determine the in-plane lattice constants (Fig. 2). These revealed that PZT grown on Si is nearly cubic ($a_{\text{PZT}} = 4.069 \text{ \AA}$, $c_{\text{PZT}} = 4.055 \text{ \AA}$), while PZT on STO has a more elongated unit cell ($a_{\text{PZT}} = 4.040 \text{ \AA}$, $c_{\text{PZT}} = 4.113 \text{ \AA}$). One can note in Fig. 2b the asymmetry of the intensity levels around the PZT node suggesting a spreading of the PZT lattice constants towards lower in-plane and higher out-of-plane lattice constants, therefore towards a more elongated PZT cell. This might suggest that elongated tetragonal PZT domains, with the longer axis normal to the surface, are existent in this layer as a minor phase. On the other hand, in Fig. 1a, the profiles of the PZT lines at higher diffraction

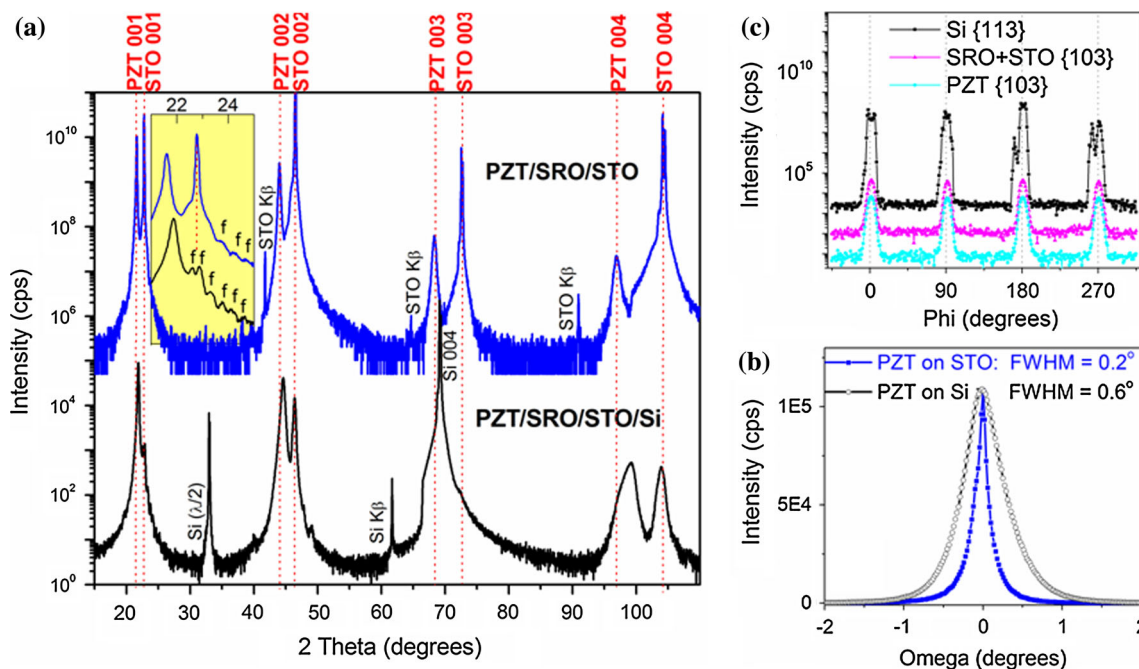


Fig. 1 a 2θ - ω scans; b PZT rocking curves around PZT 002; c phi-scans obtained on the PZT/SRO/STO/Si heterostructure

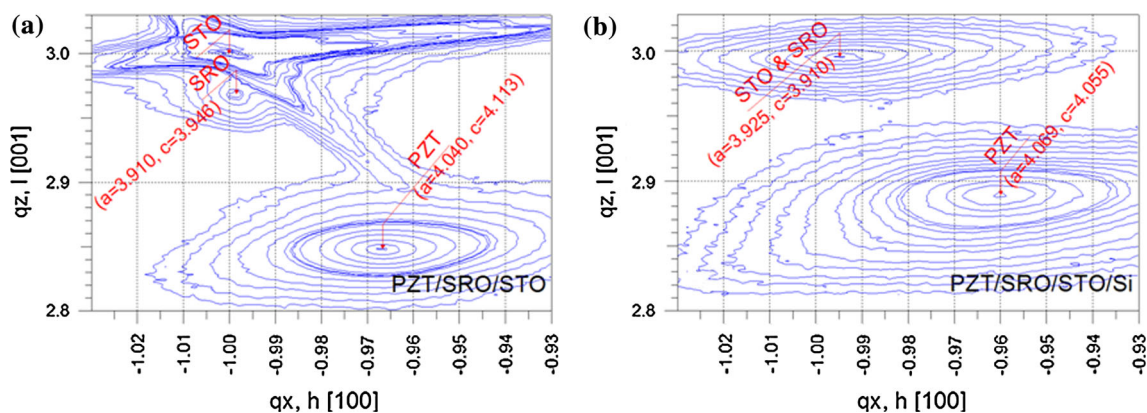


Fig. 2 Reciprocal space mappings around the (-103) nodes of STO, SRO, and PZT; **a** for PZT/SRO/STO; **b** for PZT/SRO/STO/Si. The maps are recorded between the same limits to facilitate visual comparison

angles of PZT/SRO/STO/Si structure, where the resolution is better, also suggest that there are domains with both orientations, a -domains being in majority. This kind of growth of PZT on Si is related to the structure of SRO–STO underlayers, which show abnormal large in-plane lattice constants, although the epitaxy on Si should favor lower in-plane parameter than in bulk STO. This anomaly is known and was the subject of many studies, being attributed to the presence of the amorphous SiO_2 layer between Si and STO or to the coexistence of cubic and tetragonal STO with quite different lattice parameters [25, 26]. Unfortunately, the peaks of SRO and STO for PZT/SRO/STO/Si are superimposed both in the 2θ - ω scan (Fig. 1a) and in RSM (Fig. 2b), and thus their lattice parameters cannot be distinguished.

Summarizing the results of the XRD analysis one can say that the PZT film deposited on single crystal STO substrate is relaxed, as the in-plane lattice constant is larger for PZT than for STO ($a_{\text{PZT}} = 4.040 \text{ \AA}$ compared to $a = 3.9050 \text{ \AA}$ for STO). Compared with the bulk PZT of the same composition, the film on STO substrate has a slightly lower in-plane lattice constant, corresponding to a compressive strain of about 0.15 %. Also the out-of-plane lattice constant is lower compared to the bulk, with about 0.65 %. This can be an effect of relaxation, leading to the formation of dislocations and to a lower out-of-plane lattice constant. The PZT deposited on Si substrate with STO buffer layer is also relaxed. Compared to the bulk, the film has a significantly larger in-plane lattice constant, corresponding to a tensile strain of about 0.6 %. The out-of-plane lattice constant is much smaller than in the bulk, with about 2 %. This suggests that polarization may be mostly in-plane, while for the PZT on single crystal STO the polarization seems to be oriented out-of-plane.

The low-magnification TEM image in Fig. 3 displays the deposited multilayers on both substrates. The PZT,

SRO, and STO layers on Si substrate have a thickness of 100, 20, and 33 nm, respectively (see Fig. 3b). The image reveals a bright thin layer (4 nm) between the Si substrate and the STO layer. The amorphous nature of this layer corresponds to the native SiO_2 . The thicknesses are similar for the PZT and SRO films deposited on STO single crystal substrate (see Fig. 3a). The selected area electron diffraction (SAED) patterns reveal on both structures the crystallization status of the as-deposited layers and the orientation relationship with respect to the substrate. The diffraction spots have been marked with corresponding miller indices and subscripts indicating the substrate (the strongest spots) or the as-deposited layers. The SAED images reveal the ordered single-crystal-like nature of the grown layers on both types of substrates.

The TEM images at low and high magnification (Fig. 3a) show that defects are forming at the SRO/PZT interface (misfit dislocations), suggesting that the film is at least partly relaxed. The lattice constants for the PZT film, obtained from SAED pattern (Fig. 3a), are $a_{\text{PZT}} = 4.05 \text{ \AA}$ and $c_{\text{PZT}} = 4.10 \text{ \AA}$, while for STO are $a_{\text{STO}} = c_{\text{STO}} = 3.91 \text{ \AA}$. These values are in good agreement with those obtained by XRD.

The TEM image corresponding to PZT/SRO/STO/Si structure (Fig. 3b) reveals a strain contrast, which may be observed also in the HRTEM image, as a consequence of the mismatch dislocations [29, 30]. This distorted region inside PZT close to the SRO interface exhibits clusters of dislocations for strain relaxation, that are expected to affect electrical measurements at micro and macroscale [31–33].

The lattice constants for the PZT film, obtained from SAED pattern (Fig. 3b), are $a_{\text{PZT}} = 4.06 \text{ \AA}$ and $c_{\text{PZT}} = 4.06 \text{ \AA}$, while for STO are $a_{\text{STO}} = 3.85 \text{ \AA}$ and $c_{\text{STO}} = 3.90 \text{ \AA}$. Again, for the PZT layer the results are comparable with those obtained by XRD. In any case, the structural differences between the PZT layers grown on

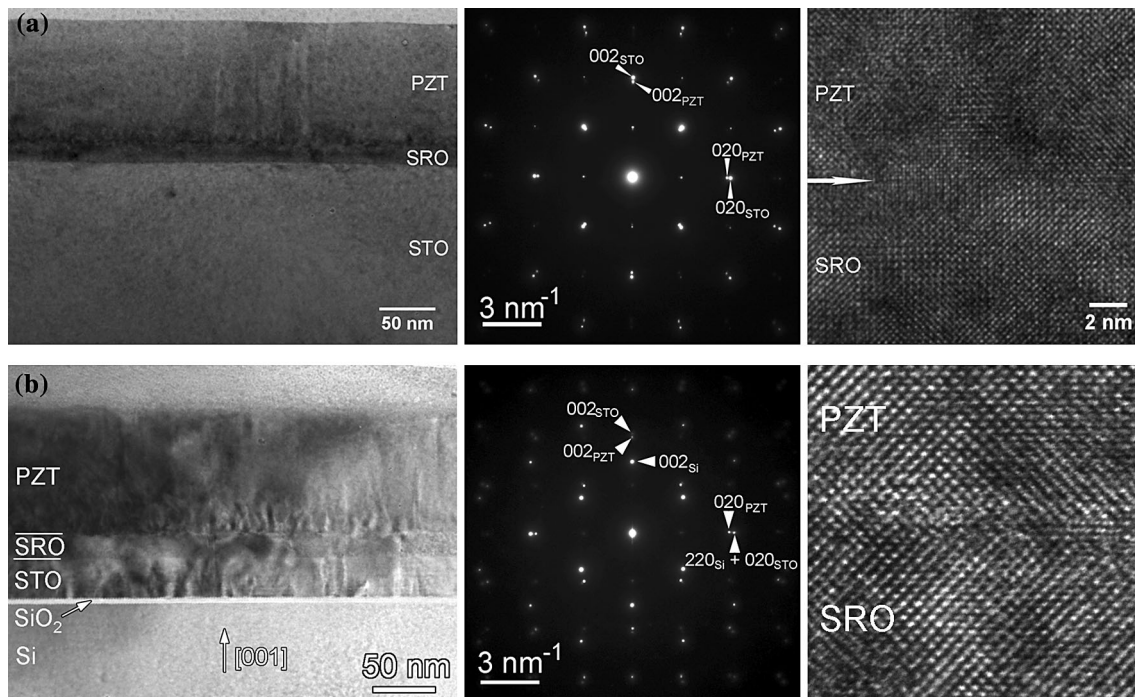


Fig. 3 **a** TEM image at low-magnification for PZT/SRO/STO structure; SAED pattern recorded on an area which includes the STO substrate and the deposited layers of SRO and PZT; HRTEM image showing the PZT-SRO interface; **b** TEM image for PZT/SRO/

STO/Si structure; SAED pattern from an area which includes the Si substrate and the deposited layers of STO, SRO, and PZT; HRTEM image showing the PZT-SRO interface

single crystal STO substrate and on single crystal Si with STO buffer layer may have some impact on the electrical properties, as it will be shown later on.

The AFM analysis performed on an area of $5 \times 5 \mu\text{m}^2$ has revealed a smooth surface in both cases, with an average roughness of $1 \text{ nm} \pm 0.1 \text{ nm}$. The PFM investigation on the bare PZT surface shows little phase contrast, suggesting that most of the polarization is oriented in the same direction (see Fig. 4). A characteristic grid of 90° domains is visible for the sample grown on single crystal STO substrate (see Fig. 4a). This is in agreement with previous literature [10, 34]. Some 180° domains can be also seen for PZT on STO substrate, both in phase and amplitude. In the case of the PZT deposited on Si, the presence of domains is not evident.

The presence of switchable polarization was checked by writing artificial domains with opposite directions of polarization. The poling map is shown in the Fig. 5a. The area where a voltage of -10 V was applied on the tip is with polarization towards top surface (polarization UP) and has a light color. The area where a voltage of $+10 \text{ V}$ was applied on the tip is with polarization DOWN (towards the bottom SRO contact) and appears in a dark color in Fig. 5a, b. Comparing the colors of the phase signal in the case of the poled areas in Fig. 5 with the color for unpoled film in Fig. 4 one can have an indication for the dominant

direction of polarization in the unpoled film. In the present case it seems to be UP, at least for the film deposited on STO substrate. The result is in agreement with the XRD finding that the out-of-plane lattice constant is significantly larger than the in-plane one, suggesting dominance of the out-of-plane polarization. The interpretation of the PFM results for the PZT film deposited on Si substrate, by comparing the phase signals in Figs. 4 and 5, is more difficult as the XRD results suggest a mixture of in-plane and out-of-plane polarization with some dominance of the former. This could explain the relative lack of contrast in the phase signal of the unpoled PZT film deposited on the Si substrate (see Fig. 4b). One has to mention that the dominant polarization orientation in the PZT films may depend on the strain imposed by the substrate, the film thickness, the oxygen content, etc. [35, 36] and may change in time, depending on the surrounding atmosphere and storage conditions [37].

The stability of DOWN and UP polarizations was checked by monitoring the poled areas for 120 min after the poling procedure was ended. It was found that the UP polarization is stable for both type of substrates, while the polarization DOWN has a better stability in the case of the film grown on STO substrate. This is visible by comparing the images in Fig. 5 after 30 min and after 120 min. It can be seen in Fig. 5b that the dark regions, corresponding to

Fig. 4 PFM amplitude and phase maps for **a** PZT/SRO/STO structure; **b** PZT/SRO/STO/Si structure

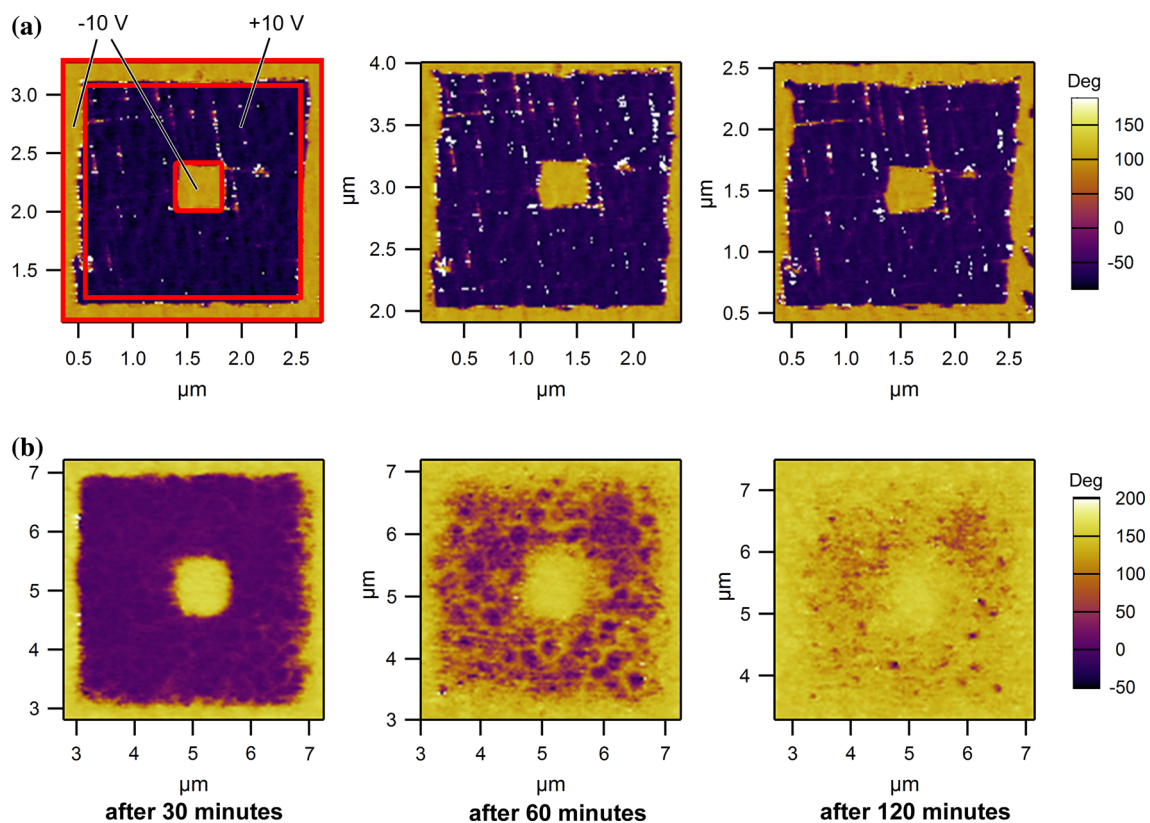
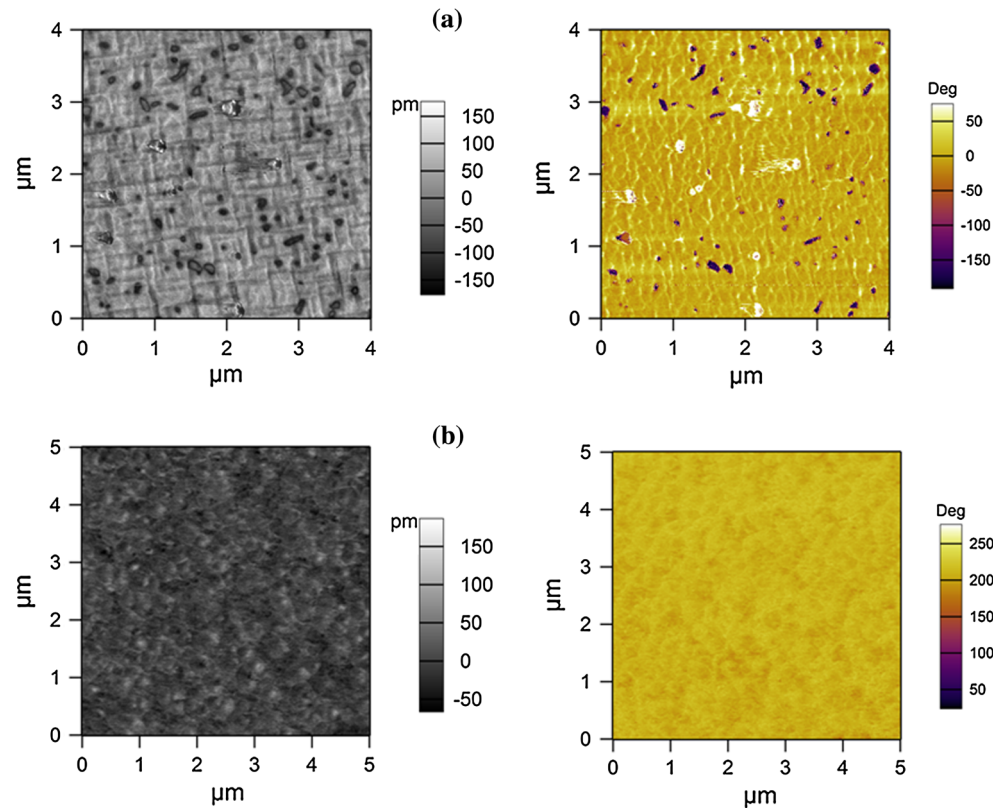


Fig. 5 The time evolution of the artificially written domains with opposite directions of polarization for **a** PZT/SRO/STO structure and **b** PZT/SRO/STO/Si structure

DOWN polarization, changes the contrast (the dark almost disappears after 120 min), while in Fig. 5a the dark regions do not change after 120 min. This result correlates well with the results of XRD investigations, showing that for the PZT film on STO substrate the out-of-plane polarization is favored. The fact that for the PZT film deposited on Si substrate one direction of polarization is not stable can be related to the more defective PZT/SRO interface in this case, as suggested by TEM investigation.

Ferroelectric/electric properties

The results of the hysteresis measurements performed at different temperatures with triangular voltage wave of 1 kHz frequency are shown in Fig. 6. Although the PZT layers deposited on Si and STO substrates are in both cases epitaxial, the P–E characteristics are different. The current loops recorded during the dynamic hysteresis measurements show peaks which are associated to the polarization reversal. The peaks for PZT/SRO/STO are sharper and thus the P–V loops obtained by integrating the I–V loops are more rectangular in this case compared to the PZT films

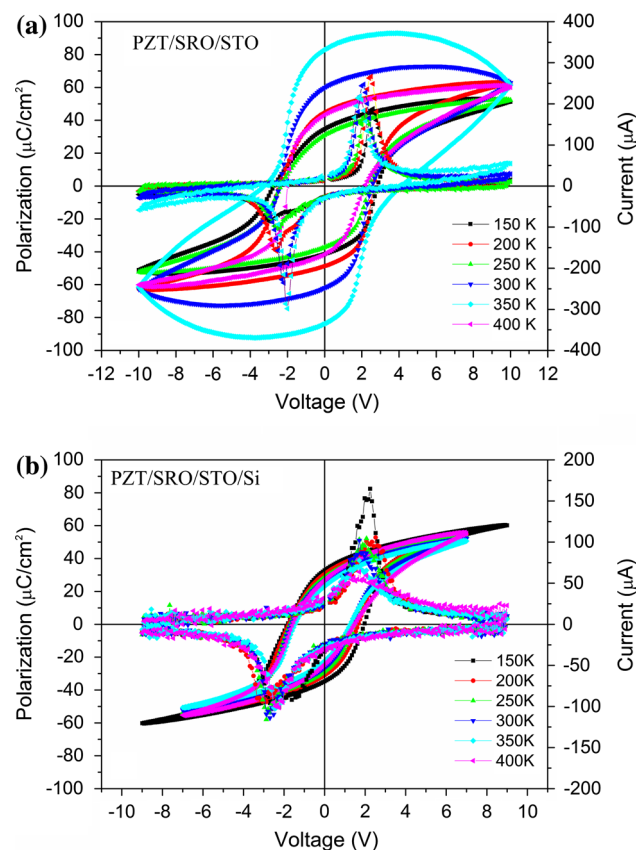


Fig. 6 Hysteresis measured at 1 kHz for **a** PZT/SRO/STO structure and **b** PZT/SRO/STO/Si structure. The characteristics were recorded in the 150–400 K temperature range

deposited on Si substrate. The values for polarization and coercive voltage, at room temperature, are of $48 \mu\text{C}/\text{cm}^2$ and 2.5 V for PZT on single crystal STO, and of $26 \mu\text{C}/\text{cm}^2$ and 1.3 V for PZT deposited on Si substrate with STO buffer layer. Considering that the thicknesses are about the same, one can say that the coercive field is about two times larger for the PZT deposited on STO substrate than for the film deposited on Si. In both cases the loops have a small shift towards negative voltages, of 0.05 V for PZT/SRO/STO and 0.15 V for PZT/SRO/STO/Si, suggesting the presence of an internal electric field oriented towards the bottom SRO electrode. Such a field would, apparently, favor the DOWN orientation of polarization. However, the internal field is much smaller than the coercive one, having thus, only little effect on the polarization orientation. The origin of this internal electric field will be discussed in the next section.

The shape of the hysteresis loops for PZT/SRO/STO/Si structures remains unchanged for the entire temperature range scanned in this work. However, for the PZT/SRO/STO structures the high leakage current present in these samples above 300 K (see Figs. 6a, 8a) is altering the shape of the hysteresis loops with increasing the temperature.

In Fig. 7, the dielectric constant calculated from C–V measurements performed at different temperatures are shown. These measurements were performed at 100 kHz frequency of the a.c. small signal of 50 mV amplitude. It can be seen that the different substrates have an influence on capacitance of the metal–ferroelectric–metal structure, reflected in different values of the dielectric constant. In the case of the PZT/SRO/STO/Si structure, the characteristics are symmetrical with respect to the dielectric constants values, with a small shift towards negative voltages (in agreement with the hysteresis measurements presented above). In the case of the PZT/SRO/STO substrate, there is an asymmetry in the values of dielectric constant for negative and positive polarities, with higher values and sharper peak for negative voltages. This is in accordance with the polarization hysteresis measurements where the value of the negative remnant polarization is greater than positive remnant polarization, suggesting that the polarization compensation mechanism is more effective when negative voltage is applied on the top electrode. As polarization charges are compensated with free charge carriers, it results that for negative voltages on the top electrode these carriers are responding faster leading thus to a larger capacitance value and to a larger dielectric constant (one has to remind that the capacitance measurement is related to the ability of the free carriers in respond to the small amplitude a.c. signal) [38].

Another difference between these two types of structures is related to the values of the dielectric constants as

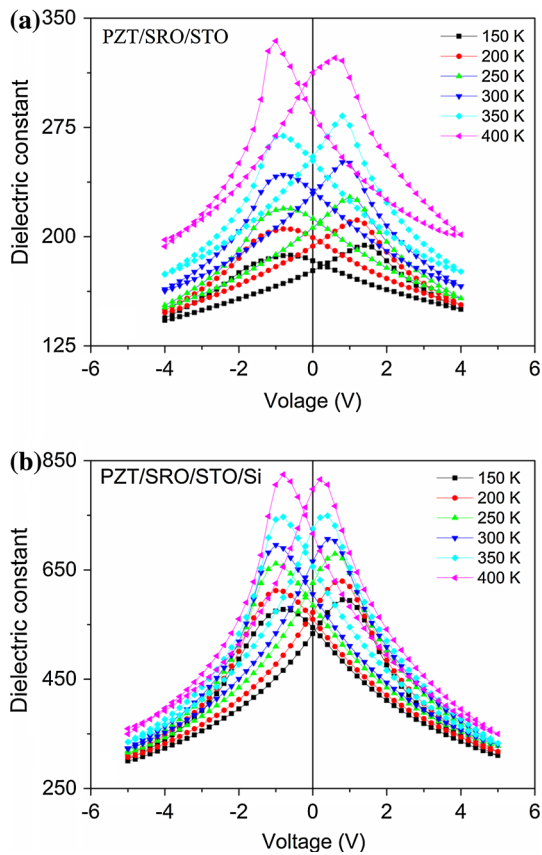


Fig. 7 The voltage dependence of the dielectric constant for **a** PZT/SRO/STO structure and **b** PZT/SRO/STO/Si structures. The characteristics were recorded in the 150–400 K temperature range

measured at the maximum applied voltage, common for both kinds of samples. Thus, at 4 V the dielectric constant of PZT/SRO/STO/Si is two times larger than that measured on PZT/SRO/STO ones. This aspect will be discussed later on.

Current–voltage characteristics have been measured for temperatures between 150 and 400 K in order to investigate the dominant conduction mechanism. The results are presented in Fig. 8 for both types of structures.

The leakage current measurements are affected by the contributions coming from polarization switching or from other transitory phenomena (e.g. releasing of trapped charge, relaxation of other polarization mechanisms not associated to ferroelectricity, etc.) [39]. The currents associated to the polarization switching are largely eliminated by poling the sample before the current measurements are performed. Then the measurement is done with voltages of the same polarity as the one used for poling, which is increased from 0 V to V_{\max} in steps of 0.1 V. A 1 s delay time between changing the voltage and measuring the current is used. The delay time should be large enough to reduce the relaxation currents, but if it is

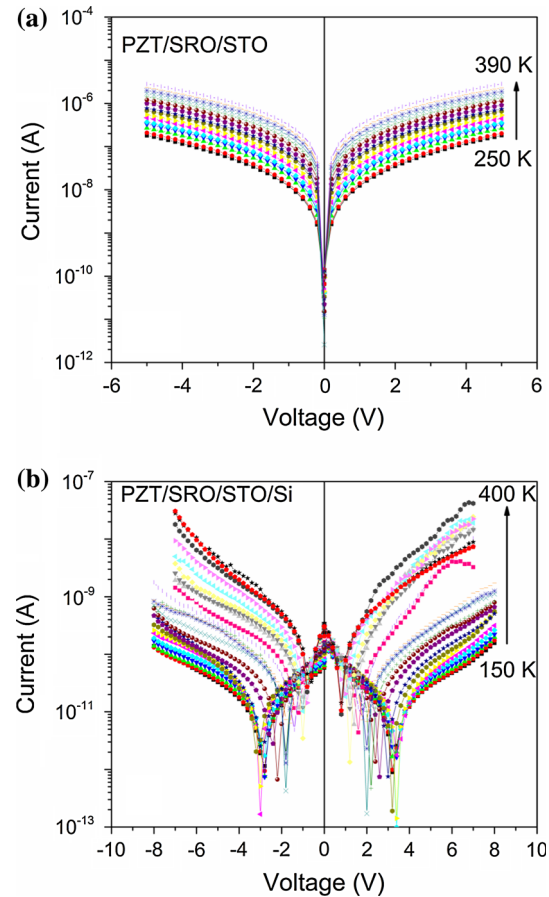


Fig. 8 I–V characteristics for **a** PZT/SRO/STO structure and **b** PZT/SRO/STO/Si structure. The characteristics were recorded in the 150–400 K temperature range

too large a time-dependent breakdown could affect the measurement.

The I–V characteristics are rather symmetric with the voltage polarity and show significant temperature dependence especially for the PZT/SRO/STO/Si structure. In this case, the delay time of 1 s seems to be not enough to completely eliminate the parasitic contributions to the leakage current. This is in agreement with the hysteresis measurements presented in Fig. 6b, showing a more elongated loop for PZT/SRO/STO/Si. This can be explained by the back switching effects associated to a distribution of coercive fields [40]. In other words, for the PZT/SRO/STO structure the switching is sharp, leading to a more rectangular loop, while for PZT/SRO/STO/Si the switching is more gradual.

Discussions

The experimental results presented in the previous section show that the ferroelectric/electric properties of the PZT

films deposited on single crystal STO substrate and on Si substrate with STO buffer layer are somewhat different, due to small differences in the epitaxial quality as revealed by the structural investigations. Summarizing, the remnant polarization and the coercive field are about two times larger, the dielectric constant is about two times smaller, and the leakage current is with almost two orders of magnitude larger for the PZT film grown on single-crystal STO than for the one grown on Si with STO buffer layer. These aspects will be discussed in more detail further on.

The higher values for the remnant polarization in the case of PZT deposited on single-crystal STO substrate could be explained by different stress situations, compressive for STO substrate and tensile for Si substrate. However, previous studies have shown that the polarization value in PZT films is almost insensitive to the strain variations [41]. On the other hand it was shown that structural defects may block ferroelectric domains, leading to lower values of polarizations [42, 43]. It can be the present case, considering that the PZT film deposited on Si with STO buffer layer are more defective than the films deposited on single-crystal STO. The fact that the coercive field is higher for the PZT film deposited on single-crystal STO substrate may be related to the much higher leakage current in this case. The larger density of free carriers may screen the applied electric field [44]. As a result, higher voltages are required to switch the polarization although the thickness of the films deposited on the STO and Si substrates is about the same. Despite the lower value of the remnant polarization, the lower values for the coercive field and for the leakage current are an advantage for the PZT films deposited on Si substrate with STO buffer layers if one wants to use them for applications, as capacitors (e.g. NVM, IR detectors, etc.).

The internal electric field suggested by the shift of the hysteresis loops and C–V characteristics toward negative voltages is significantly smaller than the difference in work functions, which are of 4.6–4.9 eV for SRO and 5.65 eV for Pt [45, 46]. It is possible that the internal electric field has another origin, as for example a small gradient in the strain distribution, as suggested by TEM investigations showing misfit dislocations at the PZT/SRO interface, or a non-homogeneous distribution of some charged defects such as oxygen vacancies acting as donors [47–49]. The different distribution of strain and structural defects may also explain the increase of the dielectric constant in the case of the PZT film deposited on Si substrate with STO buffer layer. Similar results were previously reported in the literature [31–33, 50]. One has to mention here that the data presented in Fig. 7 were obtained as an independent measurement and are not derived from the data presented in Fig. 6. The C–V characteristic derived from the hysteresis loop usually leads to significantly higher values for the

dielectric constant [51]. This is because the hysteresis loop was obtained by a dynamic method, integrating the charge at a frequency of 1 kHz (corresponding to 1 ms), while C–V measurement is quasi-static (slow-increasing d.c. voltage superimposed over a small amplitude a.c. signal). The contribution of the domain walls to the static dielectric constant is very much reduced in this way as the capacitance is measured for a stable value of polarization imposed by the applied d.c. voltage.

The I–V characteristics presented in Fig. 8 will be analyzed assuming that the ferroelectric capacitor can be modeled as a back-to-back connection of two Schottky type diodes [52, 53]. In this way, despite the polarity of the applied voltage on the top Pt contact, one of the diodes will be reverse biased and will limit the current. However, one cannot apply the Richardson–Schottky equation because the mean free path of the charge carriers in PZT is much smaller than the film thickness [54]. Therefore, one has to use the Schottky–Simmons equation, in which the injection is limited by the potential barriers at the electrode interfaces and the drift–diffusion in the film is limited by the carriers mobility. The Schottky–Simmons equation for the current density is given by [55, 56].

$$J = 2q \left(\frac{2\pi m_{\text{eff}} kT}{h^2} \right)^{3/2} \mu E \exp \left(-\frac{q}{kT} \left(\phi_{\text{B}}^0 - \sqrt{\frac{qE_{\text{m}}}{4\pi\epsilon_0\epsilon_{\text{op}}}} \right) \right) \quad (1)$$

where J is the current density, q is the electron charge, h is the Planck's constant, m_{eff} is the effective mass, μ is the mobility, E is the electrical field in the film volume, E_{m} is the maximum electrical field at the electrode interface, k is the Boltzmann's constant, ϵ_0 is the vacuum permittivity, ϵ_{op} is the optical dielectric constant, ϕ_{B}^0 is the interfacial potential barrier height at zero volts. The experimental data were analyzed using the same procedure as previously reported in the literature for ferroelectric epitaxial films with conduction mechanism dominated by thermionic emission [57, 58].

The apparent potential barrier, including the contribution of the ferroelectric polarization, is obtained from the slope of the Arrhenius plot ($\ln(J/T^{3/2}) = f(1000/T)$), as is presented in Fig. 9a for the PZT/SRO/STO structure on positive polarity. The evaluated apparent potential barrier has a linear dependence on \sqrt{V} [57] as shown in Fig. 9b for the PZT/SRO/STO structure. The intercept obtained from the linear fit of the apparent potential barrier as a function of the square root of the applied voltage represents the value of the apparent potential barrier $\phi_{\text{B,P}}^0$ at 0 V (that is a barrier reduced by the presence of the polarization charges just near the electrode interface). For the PZT/SRO/STO structure this was estimated to be 0.171 eV for

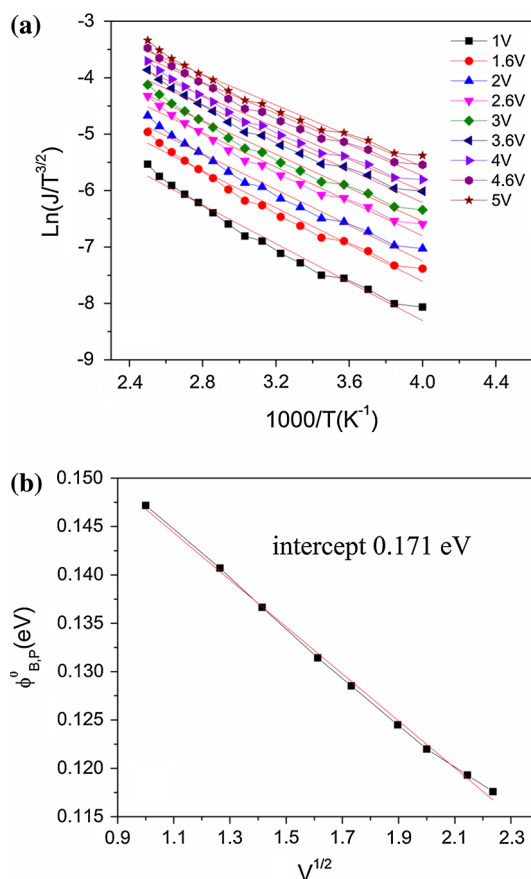


Fig. 9 **a** Arrhenius representation for positive polarity and **b** the apparent potential barrier dependence on $V^{1/2}$, analyzed for PZT/SRO/STO structure

Table 1 The substrate dependence of the remnant polarization, static dielectric constant, optical dielectric constant, apparent potential barrier, and potential barrier height

	P_r ($\mu\text{C}/\text{cm}^2$)	ϵ_{st}	ϵ_{op}	$\phi_{\text{B,P}}^0$ (eV)	ϕ_{B}^0 (eV)
PZT/SRO/STO	50	150	8.4	0.17	0.43
PZT/SRO/STO/Si	26	300	3.9	0.27	0.46

the positive polarity and 0.175 eV for the negative polarity. In the case of PZT/SRO/STO/Si structures, the evaluated potential barriers are of about 0.27 eV. The reduction of the barrier height induced by the ferroelectric polarization is given by $\sqrt{\frac{qP}{4\pi\epsilon_0^2\epsilon_{\text{op}}\epsilon_{\text{st}}}}$, where ϵ_{st} is the static dielectric constant. This term was estimated considering the values of remnant polarization, ϵ_{st} and ϵ_{op} given in Table 1 for PZT/SRO/STO and PZT/SRO/STO/Si structures. The value of optical dielectric constant was estimated from the slope of the $\ln(J/E) = f(E^{1/2})$ representation and the obtained values are comparable with the ones reported in literature [59]. The obtained values for the above polarization term

are 0.26 and 0.19 eV for PZT/SRO/STO and PZT/SRO/STO/Si structures, respectively. Therefore, ϕ_{B}^0 , the potential barrier height at 0 V, is around 0.45 eV for both structures.

Conclusions

Thin films of $\text{PbZr}_{0.52}\text{Ti}_{0.48}\text{O}_3$ have been deposited by PLD on STO and Si substrate. The structural characterizations comprising XRD and TEM prove an epitaxial growth on both substrates. The results of the electrical characterizations including C–V, I–V, and hysteresis measurements suggest that ferroelectric properties as remnant polarization, coercive field, dielectric constant, and leakage current are dependent on the substrate used for the deposition. The remnant polarization and the coercive field are higher for PZT deposited on single-crystal STO, while the dielectric constant is lower. However, all these qualities are of the same order of magnitude on the two types of substrates. The real advantage of the PZT films deposited on Si substrate with STO buffer layer is that they have a much smaller leakage current, almost two orders of magnitude lower compared to the film deposited on single-crystal STO substrate. This fact makes these films attractive for devices integrated directly on Si wafers.

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