

Functional Program Transformation for Parallelisation Using Skeletons

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Abstract It can be challenging to use algorithmic skeletons in parallel program development as it is tedious to manually identify parallel computations in an algorithm and there may be mismatches between the algorithm and skeletons. Also, parallel programs defined using skeletons often employ inefficient intermediate data structures. In this paper, we present a program transformation method to address these issues by using an existing technique called *distillation* to reduce the use of intermediate data structures and an encoding technique to combine the inputs of a program into a single input whose structure matches that of the program. This facilitates automatic identification of skeletons that suit the algorithmic structure of the transformed program.

Keywords Program transformation · Parallelisation · Algorithmic skeletons

1 Introduction

While parallel programming is vital to effectively utilise the computing power, it is challenging as it requires manual analysis of an algorithm to identify potential parallel computations. Parallel program development often uses algorithmic skeletons to hide the complexity of implementing the parallelism from the developer. In this work, we

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are particularly interested in data parallel skeletons, such as *map*, *map-reduce* and *accumulate* [1]. While skeleton-based programming eases the task of the developer, it still requires manual analysis of a program to identify computations that can be defined using the skeletons.

In the case of data parallel skeletons, a given operation is applied in parallel on each element in a given input. Consequently, there is a match between the structure of the input and the recursive structure of the skeleton definition. However, the structure of the data types and the algorithmic structure of a given program may not match with those of the skeletons, which is challenging to resolve [2, 3]. Further, programs that are defined using skeletons often introduce inefficient intermediate data structures [4]. Consider the programs shown in Examples 1 and 2 which compute the product of two matrices and the dot-product of two binary trees, respectively. In Example 1, (*transpose yss*) and (*zipWith* (*) *xs ys*) are intermediate data structures that are subsequently decomposed by *map* and *foldr*, respectively.

```
Example 1 (Matrix multiplication—original program)
```

```
mMul :: [[a]] \rightarrow [[a]] \rightarrow [[a]]
mMul xss yss
where
mMul [] yss
                        = []
                      = (map (transpose yss) (dotp xs)) : (mMul xss yss)
mMul (xs : xss) yss
                       = foldr (+) 0 (zipWith (*) xs ys)
dotp xs ys
transpose xss
                       = transpose' xss
transpose' [] yss
                        = yss
transpose' (xs : xss) yss = transpose' xss (rotate xs yss)
rotate [] vss
                       = vss
rotate (x : xs) []
                    = [x]: (rotate xs yss)
rotate (x : xs) (ys : yss) = (ys ++ [x]) : (rotate xs yss)
```

Even though these programs can be parallelised using the skeletons *map*, *reduce* and *zipWith* (defined over lists and binary trees with parallel implementations), Example 3 retails the original intermediate data structures from Example 1, while Example 4 introduces a new intermediate data structure (*zipWith* (*) *xt yt*) due to the used of skeletons.

[]
(map (dotp xs) (transpose yss)) : (mMul xss yss)
reduce $(+) 0 (zipWith (*) xs ys)$
transpose' xss []
vss
transpose' xss (rotate xs yss)
$zipWith (\lambda x.\lambda ys.(ys ++ [x])) xs yss$
$\begin{bmatrix} 0 \\ r \\ t \\ z \end{bmatrix}$

Example 3 (Matrix multiplication—hand-parallel program using skeletons) *mMul xss yss*

Example 4 (Dot-product of binary trees—hand-parallel program using skeletons) dot P xt yt = reduce (+) 0 (zipWith (*) xt yt)

The dot-product of binary trees program can also be parallelised by evaluating the two recursive calls ($dot P xt_1 yt_1$) and ($dot P xt_2 yt_2$) simultaneously. An example of such a parallel definition of the dot-product program using the Glasgow Parallel Haskell (GpH) is shown in Example 5 and this avoids the use of intermediate data structures.

```
Example 5 (Dot-product of binary trees—hand-parallel program using GpH)

data BTree a ::= E \mid B \mid B \mid BTree \mid a) \quad (BTree \mid a)

dot P :: (BTree a) \rightarrow (BTree \mid a) \rightarrow (BTree \mid a)

dot P xt yt

where

dot P E yt = 0

dot P (B x xt_1 xt_2) E = 0

dot P (B x xt_1 xt_2) (B y yt_1 yt_2) = runEval $ do

z_1 \leftarrow rpar (dot P xt_1 yt_1)

z_2 \leftarrow rseq (dot P xt_2 yt_2)

return (x * y) + z_1 + z_2
```

However, such parallelisation requires manual analysis of a given program which is tedious for non-trivial programs. Also, note that there is no obvious skeleton corresponding to the definition of this program. Therefore, we observe that it is challenging to obtain a parallel version for a given program that uses fewer intermediate data structures and is defined using skeletons.

In this paper, we propose a transformation method with the following aspects to resolve these issues:

- 1. Reduce the number of intermediate data structures in a given program using an existing transformation called *distillation* [5].
- 2. Automatically create a new data type for the program so that the structure of the new data type matches the algorithmic structure of the program.
- 3. Automatically create skeletons that operate over the new data type and identify potential instances of the skeletons in the program. The transformed program can then be executed in parallel using efficient implementations for the skeletons.

In our earlier works, we presented techniques to transform programs defined over polytypic [6] and list inputs [7] individually. In this paper, we integrate the two transformations and present it along with evaluation results. In Sect. 2, we introduce the language used in this work. We present the proposed parallelisation transformation in Sect. 3. In Sect. 4, we present the results of evaluating three example programs that are transformed using our proposed method. In Sect. 5, we present concluding remarks on our transformation method and discuss related work.

2 The Language

The higher-order language used in this work is shown in Definition 1 and uses callby-name evaluation.

Definition 1 (Language grammar) data $T \alpha_1 \dots \alpha_M ::= c_1 t_1^1 \dots t_N^1 | \dots | c_K t_1^K \dots t_N^K$ $t ::= \alpha_m | T t_1 \dots t_M$ Type Declaration Type Component e ::= xVariable Constructor Application $| c e_1 \dots e_N$ $|e_0|$ Function Definition where $f \ p_1^1 \dots p_M^1 \ x_{(M+1)}^1 \dots x_N^1 = e_1 \ \dots \ f \ p_1^K \dots p_M^K \ x_{(M+1)}^K \dots x_N^K = e_K$ Function Call Application $| e_0 e_1$ | let $x_1 = e_1 \dots x_N = e_N$ in e_0 let-Expression λ -Abstraction $\lambda x.e$ $p ::= x \mid c \mid p_1 \dots p_N$ Pattern

A program can contain data type declarations of the form shown in Definition 1. Here, T is the name of the data type, which can be polymorphic, with type parameters $\alpha_1, \ldots, \alpha_M$. A data constructor c_k may have zero or more components, each of which may be a type parameter or a type application. An expression e of type T is denoted by e :: T.

A program can also contain an expression which can be a variable, constructor application, function definition, function call, application, **let**-expression or λ -abstraction. Variables introduced in a function definition, **let**-expression or λ -abstraction are *bound*, while all other variables are *free*. The free variables in an expression *e* are denoted by f v(e). Each constructor has a fixed arity. In an expression $c e_1 \dots e_N$, N must be equal to the arity of the constructor *c*. For ease of presentation, patterns in function definition headers are grouped into two $-p_1^k \dots p_M^k$ are inputs that are pattern-matched, and $x_{(M+1)}^k \dots x_N^k$ are inputs that are not pattern-matched. The series of patterns $p_1^k \dots p_M^k$ in a function definition must be non-overlapping and exhaustive. We use [] and (:) as shorthand notations for the *Nil* and *Cons* constructors of a *cons*-list.

Definition 2 (*Context*) A context *E* is an expression with *holes* in place of subexpressions. $E[e_1, \ldots, e_N]$ is the expression obtained by filling holes in context *E* with the expressions e_1, \ldots, e_N .

3 Parallelisation Transformation

Given a program defined in the language presented in Definition 1, our proposed parallelisation transformation is performed in three stages:

- 1. Apply the *distillation transformation* on the given program to reduce the number of intermediate data structures used. This produces a *distilled program*. (Sect. 3.1)
- 2. Apply the *encoding transformation* on the distilled program to combine the inputs into a data structure that matches the algorithmic structure of the distilled program. This produces an *encoded program*. (Sect. 3.2)

3. Identify potential skeleton instances in the encoded program. This produces an *encoded parallel program*. (Sect. 3.4)

3.1 Distillation Transformation

Given a program in the language from Definition 1, *distillation* [5] is an unfold/foldbased technique that transforms the program to remove intermediate data structures and yields a *distilled program* that can potentially have super-linear speedups. The syntax of a distilled program $de^{\{\}}$ is shown in Definition 3. Here, ρ is the set of variables introduced by **let**-expressions that are created during generalisation. The bound variables of **let**-expressions are not decomposed by pattern-matching in a distilled program. Consequently, $de^{\{\}}$ is an expression that has fewer intermediate data structures.

Definition 3 (Distilled form grammar)			
de^{ρ}	::=	$x de_1^{\rho} \dots de_N^{\rho}$	Variable Application
		$c de_1^{\hat{\rho}} \dots de_N^{\hat{\rho}}$	Constructor Application
		de_0^{ρ}	Function Definition
		where	
		$f p_1^1 \dots p_M^1 x_{(M+1)}^1 \dots x_N^1 = de_1^{\rho} \dots f p_1^K \dots$	$p_M^K x_{(M+1)}^K \dots x_N^K = de_K^\rho$
		$f x_1 \ldots x_M x_{(M+1)} \ldots x_N$	Function Application
		s.t. $\forall x \in \{x_1, \ldots, x_M\} \cdot x \notin \rho$	
		let $x_1 = de_1^{\rho} \dots x_N = de_N^{\rho}$ in $de_0^{\rho \cup \{x_1, \dots, x_N\}}$	let-Expression
		$\lambda x.de^{\rho}$	λ –Abstraction
р	::=	$x \mid c \mid p_1 \dots p_N$	Pattern

Example 6 presents the distilled version of the matrix multiplication program from Example 1 and does not use intermediate data structures.

```
Example 6 (Matrix multiplication—distilled program)
mMul xss yss
where
mMul xss yss = mMul_1 xss yss yss
mMul<sub>1</sub> [] zss yss
                                = [1]
mMul<sub>1</sub> xss [] yss
                                = [1]
mMul_1(xs:xss)(zs:zss)yss = let v = \lambda xs.gxs
                                                where
                                                g []
                                                          = 0
                                                g(x:xs) = x
                                   in (mMul_2 zs xs yss v) : (mMul_1 xss zss yss)
mMul<sub>2</sub> [] xs yss v
                         = []
mMul_2(z:zs) xs yss v = let v' = \lambda xs.g xs
                                         where
                                                    = 0
                                         g []
                                         g(x:xs) = v xs
                            in (mMul_3 xs yss v) : (mMul_2 zs xs yss v')
                            = 0
mMul_3 [] yss v
mMul_3(x:xs)[]v
                            = 0
mMul_3 (x : xs) (ys : yss) v = (x * (v ys)) + (mMul_3 xs yss v)
```

3.2 Encoding Transformation

A distilled program is defined over the original program data types. In order to transform these data types into a structure that reflects the structure of the distilled program, we apply the encoding transformation detailed in this section. In this transformation, we *encode* the pattern-matched arguments of each recursive function f in the distilled program into a single argument which is of a new data type T_f and whose structure reflects the algorithmic structure of function f.

Consider a recursive function f, with arguments $x_1, \ldots, x_M, x_{(M+1)}, \ldots, x_N$, of the form shown in Definition 4 in a distilled program. Here, a function body e_k corresponding to function header $f p_1^k \ldots p_M^k x_{(M+1)}^k \ldots x_N^k$ in the definition of f may contain one or more recursive calls to function f.

Definition 4 (*Recursive function in distilled program*)

 $f x_1 \dots x_M x_{(M+1)} \dots x_N$ where $f p_1^1 \dots p_M^1 x_{(M+1)} \dots x_N = e_1$ \vdots $f p_1^K \dots p_M^K x_{(M+1)} \dots x_N = e_K$ where $\exists k \in \{1, \dots, K\} \cdot e_k = E_k \left[f x_1^1 \dots x_M^1 x_{(M+1)}^1 \dots x_N^1, \dots, f x_1^J \dots x_M^J x_{(M+1)}^J \dots x_N^J \right]$

3.2.1 Encoding Into New Type

The three steps to encode the pattern-matched arguments of function f into a new type are as follows:

1. Declare a new data type for the encoded argument:

First, we declare a new data type T_f for the new encoded argument. This new data type corresponds to the data types of the original pattern-matched arguments of function f. The definition of the new data type T_f is shown in Definition 5.

Definition 5 (*New encoded data type* T_f) **data** $T_f \alpha_1 \dots \alpha_G ::= c_1 T_1^1 \dots T_L^1 (T_f \alpha_1 \dots \alpha_G)_1^1 \dots (T_f \alpha_1 \dots \alpha_G)_J^1$ \vdots

$$c_K T_1^K \dots T_L^K (T_f \alpha_1 \dots \alpha_G)_1^K \dots (T_f \alpha_1 \dots \alpha_G)_J^K$$

where

 $\alpha_1, \ldots, \alpha_G$ are type parameters of the data types of pattern-matched arguments $\forall k \in \{1, \ldots, K\} \cdot c_k$ is a fresh constructor for T_f corresponding to $p_1^k \ldots p_M^k$ of the pattern-matched arguments

$$f p_1^k \dots p_M^k x_{(M+1)} \dots x_N = E_k \begin{bmatrix} f x_1^1 \dots x_M^1 x_1^{(M+1)} \dots x_N^1, \dots, \\ f x_1^J \dots x_M^J x_{(M+1)}^{(M+1)} \dots x_N^J \end{bmatrix}$$

$$\{(z_1 :: T_1^k), \dots, (z_L :: T_L^k)\} = f v(E_k) \setminus \{x_{(M+1)}, \dots, x_N\}$$

Here, a new constructor c_k of the type T_f is created for each set $p_1^k \dots p_M^k$ of the pattern-matched arguments $x_1 \dots x_M$ of function f that are encoded. As stated above, our objective is to encode the arguments of function f into a new type whose structure reflects the recursive structure of f. To achieve this, the components bound by

constructor c_k correspond to the variables in $p_1^k \dots p_M^k$ that occur in the context E_k and the encoded arguments of the recursive calls to function f.

2. Define a function to build encoded argument:

Given a function f of the form shown in Definition 4, we define a function $encode_f$, as shown in Definition 6, to build the encoded argument for function f.

Definition 6 (*Definition of function encode* $_f$)

 $\begin{array}{l} encode_{f} x_{1} \dots x_{M} \\ \textbf{where} \\ encode_{f} p_{1}^{1} \dots p_{M}^{1} &= e_{1}' \\ \vdots & \vdots \\ encode_{f} p_{1}^{K} \dots p_{M}^{K} &= e_{K}' \\ \text{where} \\ \forall k \in \{1, \dots, K\} \cdot e_{k}' = c_{k} \, z_{1}^{k} \dots z_{L}^{k} \, (encode_{f} \, x_{1}^{1} \dots x_{M}^{1}) \dots (encode_{f} \, x_{1}^{J} \dots x_{M}^{J}) \\ f \, p_{1}^{k} \dots p_{M}^{k} \, x_{(M+1)} \dots x_{N} = E_{k} \begin{bmatrix} f \, x_{1}^{1} \dots x_{M}^{1} \, x_{(M+1)}^{1} \dots x_{N}^{1} \\ f \, x_{1}^{1} \dots x_{M}^{J} \, x_{(M+1)}^{J} \dots x_{N}^{J} \\ f \, z_{1}^{k} \dots x_{M}^{L} \end{bmatrix} \\ \\ \left\{ z_{k}^{k}, \dots, z_{L}^{k} \right\} = f \, v(E_{k}) \setminus \{x_{(M+1)}, \dots, x_{N}\} \end{array}$

Here, the original arguments $x_1
dots x_M$ of function f are pattern-matched and consumed by $encode_f$ in the same way as in the definition of f. For each pattern $p_1^k
dots p_M^k$ of the arguments $x_1
dots x_M$, function $encode_f$ uses the corresponding constructor c_k whose components are the variables z_1^k, \dots, z_L^k in $p_1^k \dots p_M^k$ that occur in the context E_k and the encoded arguments of the recursive calls to function f.

3. Transform the distilled program:

After creating the encoded data type T_f and the $encode_f$ function for each function f, we transform the distilled program as shown in Definition 7 by defining a function f', which operates over the encoded argument, corresponding to function f.

Definition 7 (Definition of transformed function over encoded argument)

 $\begin{aligned} f' & x_{(M+1)} \dots x_N \\ \text{where} \\ f' & \left(c_1 z_1^1 \dots z_L^1 x_1^1 \dots x_1^J \right) x_{(M+1)} \dots x_N &= e'_1 \\ \vdots & \vdots \\ f' & \left(c_K z_1^K \dots z_L^K x_K^1 \dots x_K^J \right) x_{(M+1)} \dots x_N &= e'_K \\ \text{where} & \forall k \in \{1, \dots, K\} \cdot e'_k &= E_k \left[f' x_k^1 x_{(M+1)}^1 \dots x_N^1, \dots, f' x_k^J x_{(M+1)}^J \dots x_N^J \right] \\ & f & p_1^k \dots p_M^k x_{(M+1)} \dots x_N = E_k \left[f x_1^1 \dots x_M^1 x_{(M+1)}^1 \dots x_N^1, \dots, f_N^J x_{(M+1)}^J \dots x_N^J \right] \\ \end{aligned}$

The two steps to transform function f into function f' that operates over the encoded argument are:

(a) In each function definition header of f, replace the original pattern-matched arguments with the corresponding pattern of their encoded data type T_f . For instance, a function header $f p_1 \dots p_M x_{(M+1)} \dots x_N$ is transformed to the header $f' p x_{(M+1)} \dots x_N$, where p is the pattern created by $encode_f$ corresponding to the pattern-matched arguments p_1, \dots, p_M .

(b) In each call to function f, replace the original arguments with their corresponding encoded argument. For instance, a call $f x_1 \dots x_M x_{(M+1)} \dots x_N$ is transformed to the function call $f' x x_{(M+1)} \dots x_N$, where x is the encoded argument corresponding to the original arguments x_1, \dots, x_M .

3.2.2 Encoding Into List

From Definition 5, we observe that the new encoded data type T_f that is created is essentially an *n*-ary tree. If we observe that T_f is a linear data type (i.e. n = 1), then we perform the encoding transformation such that the pattern-matched inputs are encoded into a *cons*-list instead of into a new data type. This will allow us to potentially parallelise the encoded program using existing parallel list-based skeletons as discussed in Sect. 3.4. In this section, we present the version of our encoding transformation to combine the inputs into a list of type $[T'_f]$. Note that, if the type T_f created by Definition 5 is linear, then each function body e_k for the recursive function f will contain at most one recursive call. The three steps to encode the pattern-matched inputs of function f are as follows:

1. Declare a new data type for elements of the encoded list:

First, we declare a new data type T'_f for the elements of the encoded list. This new data type corresponds to the data types of the original pattern-matched arguments of function f. The definition of the new data type T'_f is shown in Definition 8.

Definition 8 (New encoded data type T'_f for list) **data** $T'_f \alpha_1 \dots \alpha_G ::= c_1 T_1^1 \dots T_L^1 | \dots | c_K T_1^K \dots T_L^K$ where

 $\alpha_1, \ldots, \alpha_G$ are type parameters of the data types of pattern-matched arguments $\forall k \in \{1, \ldots, K\} \cdot c_k$ is a fresh constructor for T'_f corresponding to $p_1^k \ldots p_M^k$ of the pattern-matched arguments

$$\begin{cases} f \ p_1^k \dots p_M^k \ x_{(M+1)} \dots x_N = e_k \\ \{(z_1 :: T_1^k), \dots, (z_L :: T_L^k)\} = \\ \begin{cases} f \ v(E_k) \setminus \{x_{(M+1)}, \dots, x_N\}, \text{ if } e_k = E_k \left[f \ x_1^k \dots x_M^k \ x_{(M+1)}^k \dots x_N^k \right] \\ f \ v(e_k) \setminus \{x_{(M+1)}, \dots, x_N\}, \text{ otherwise} \end{cases}$$

Here, a new constructor c_k of the type T'_f is created for each set $p_1^k \dots p_M^k$ of the pattern-matched arguments $x_1 \dots x_M$ of function f that are encoded. As stated above, our objective is to encode the arguments of function f into a list where each element contains pattern-matched variables consumed in an iteration of f. To achieve this, the variables bound by constructor c_k correspond to the variables z_1, \dots, z_L in $p_1^k \dots p_M^k$ that occur in the context E_k (if e_k contains a recursive call to f) or the expression e_k (otherwise). Consequently, the type components of constructor c_k are the data types of the variables z_1, \dots, z_L .

2. **Define a function** *encode f* :

Given a recursive function f, we define a function $encode_f$ as shown in Definition 9 to build the encoded list in which each element is of type T'_f .

Definition 9 (Definition of function $encode_f$ for list)

Here, for each pattern $p_1^k \dots p_M^k$ of the pattern-matched arguments, the *encode* f function creates a list element. This element is composed of a new constructor c_k of type T'_f that binds z_1^k, \dots, z_L^k , which are the variables in $p_1^k \dots p_M^k$ that occur in the context E_k (if e_k contains a recursive call to f) or the expression e_k (otherwise). The encoded input of the recursive call $f x_1^k \dots x_M^k x_{(M+1)}^k \dots x_N^k$ is then computed by *encode* $f x_1^k \dots x_M^k$ and appended to the element to build the complete encoded list for function f.

3. Transform the distilled program:

After creating the data type T'_f for the encoded list and the *encode* f function for each function f, we transform the distilled program as shown in Definition 10 by defining a recursive function f', which operates over the encoded list, corresponding to function f.

Definition 10 (Definition of transformed function over encoded list)

 $\begin{aligned} f' x x_{(M+1)} \dots x_N \\ \text{where} \\ f' \left((c_1 z_1^1 \dots z_L^1) : x^1 \right) x_{(M+1)} \dots x_N &= e'_1 \\ \vdots & \vdots \\ f' \left((c_K z_1^K \dots z_L^K) : x^K \right) x_{(M+1)} \dots x_N &= e'_K \\ \text{where} \\ \forall k \in \{1, \dots, K\} \cdot e'_k = \begin{cases} E_k \left[f' x^k x_{(M+1)}^k \dots x_N^k \right], \text{ if } e_k = E_k \left[f x_1^k \dots x_M^k x_{(M+1)}^k \dots x_N^k \right] \\ e_k, & \text{otherwise} \\ \text{where } f p_1^k \dots p_M^k x_{(M+1)} \dots x_N &= e_k \end{cases}$

The two steps to transform function f into function f' that operates over the encoded list are:

- In each function definition header of f, replace the pattern-matched arguments with a pattern to decompose the encoded list, such that the first element in the encoded list is matched with the corresponding pattern of the encoded type T'_f .
- In each call to function f, replace the pattern-matched arguments with their encoding.

3.3 Steps to Apply Encoding Transformation

In summary, the steps to apply the two versions of our encoding transformation on a given program are as follows:

- 1. Apply the distillation transformation on a given program. This produces a *distilled program*.
- 2. For each recursive function f in the distilled program,
 - (a) Create an encoded data type T_f as described in Definition 5. This new data type T_f is essentially an *n*-ary tree.
 - (b) IF n > 1 THEN Apply the encoding transformation in Sect. 3.2.1 to combine the inputs into a new data type T_f whose structure matches that of function f. ELSE Apply the encoding transformation in Sect. 3.2.2 to combine the inputs into a *cons*-list of type [T'_f].

This produces an encoded program.

For the distilled matrix multiplication program in Example 6, we observe that the encoded data types T_{mMul_1} , T_{mMul_2} and T_{mMul_3} for recursive functions $mMul_1$, $mMul_2$ and $mMul_3$ are all linear (i.e. n = 1) as shown below.

```
data T_{mMul_1} a ::= c_1 | c_2 | c_3 [a] [a] (T_{mMul_1} a)

data T_{mMul_2} a ::= c_4 | c_5 (T_{mMul_2} a)

data T_{mMul_3} a ::= c_6 | c_7 | c_8 a [a] (T_{mMul_3} a)
```

Therefore, we apply the encoding transformation presented in Sect. 3.2.2 to obtain an encoded matrix multiplication program that operates over encoded lists of types $[T'_{mMul_1}], [T'_{mMul_2}]$ and $[T'_{mMul_3}]$ as shown in Example 7.

```
Example 7 (Matrix multiplication—encoded program)
data T'_{mMul_1} a ::= c_1 | c_2 | c_3 [a] [a]
data T'_{mMul_2} a ::= c_4 \mid c_5
data T'_{mMul_3} a ::= c_6 | c_7 | c_8 a [a]
encode_{mMul_1} [] zss
                                  = [c_1]
                                  = [c_2]
encode<sub>mMul1</sub> xss []
encode_{mMul_1}(xs:xss)(zs:zss) = [c_3 xs zs] ++ (encode_{mMul_1} xss zss)
encode_{mMul_2} []
                                 = [c_4]
                                 = [c_5] ++ (encode_{mMul_2} xs yss zs)
encode_{mMul_2}(z:zs)
                                  = [c_6]
encode<sub>mMul3</sub> [] yss
encode_{mMul_3}(x:xs)[] = [c_7]
encode_{mMul_3}(x:xs)(ys:yss) = [c_8 x ys] + (encode_{mMul_3} xs yss)
mMul xss yss
where
mMul xss yss = mMul''_{1} (encode_{mMul_{1}} xss yss) yss
mMul'_1(c_1:w) yss
                             = []
mMul'_1(c_2:w) yss = []
mMul'_1((c_3 xs zs): w) yss = let v = \lambda xs.g xs
                                              where
                                              g []
                                                         = 0
                                              g(x:xs) = x
                                 in (mMul'_2 (encode_{mMul_2} zs) xs yss v) : (mMul'_1 w yss)
```

 $mMul'_{2}(c_{4}:w) xs yss v = []$ $mMul'_{2}(c_{5}:w) xs yss v = let v' = \lambda xs.g xs$ where g [] = 0 g (x:xs) = v xsin $(mMul'_{3} (encode_{mMul_{3}} xs yss) v) : (mMul'_{2} w xs yss v')$ $mMul'_{3} (c_{6}:w) v = 0$ $mMul'_{3} (c_{7}:w) v = 0$ $mMul'_{3} ((c_{8} x ys) :w) v = (x * (v ys)) + (mMul'_{3} w v)$

3.4 Parallelisation Using Skeletons

In our work, we are interested in identifying parallel computations that can be modelled using map- and reduce-based skeletons as they are versatile and widely applicable in parallel programming. In this section, we discuss the definitions and parallelisation of list-based and polytypic skeletons that are most relevant to the examples used in this paper.

Using the sequential definitions of these skeletons, we automatically identify their instances in an encoded program and replace them with suitable calls to their parallel counterparts. The rules to achieve this are available in our earlier related work [7], which also discusses in detail the properties of the transformation to encode inputs into a list.

3.4.1 List-Based Skeletons

In this paper, the sequential definitions of list-based map and map-reduce skeletons whose instances we identify in an encoded program are as follows:

```
\begin{array}{ll} map & :: [a] \to (a \to b) \to [b] \\ map [] f & = [] \\ map (x : xs) f = (f x) : (map xs f) \\ map Redr & :: [a] \to (b \to b \to b) \to b \to (a \to b) \to b \\ map Redr [] g v f & = v \\ map Redr (x : xs) g v f = g (f x) (map Redr xs g v f) \end{array}
```

We identify instances of these definitions and replace them with suitable calls to their parallel counterparts in the Eden library [8] that provides implementations of parallel map and map-reduce skeletons in the following forms:

 $\begin{array}{ll} farmB & :: (Trans a, Trans b) \Rightarrow Int \rightarrow (a \rightarrow b) \rightarrow [a] \rightarrow [b] \\ parMapRedr & :: (Trans a, Trans b) \Rightarrow (b \rightarrow b \rightarrow b) \rightarrow b \rightarrow (a \rightarrow b) \rightarrow [a] \rightarrow b \\ parMapRedrI & :: (Trans a, Trans b) \Rightarrow (b \rightarrow b \rightarrow b) \rightarrow (a \rightarrow b) \rightarrow [a] \rightarrow b \end{array}$

3.4.2 Polytypic Skeletons

For the discussions in this paper, we present polytypic reduce and map-reduce skeletons in Examples 8 and 9, respectively, defined over a data type T_f of the form shown in Definition 5. *Example 8* (Reduce skeleton for encoded data type T_f)

 $\begin{aligned} reduce_{f} \left(c_{1} x_{1}^{1} \dots x_{N}^{1}\right) g_{1} \dots g_{K} &= e_{1} \\ \vdots & \vdots \\ reduce_{f} \left(c_{K} x_{1}^{K} \dots x_{N}^{K}\right) g_{1} \dots g_{K} &= e_{K} \\ \text{where } \forall k \in \{1, \dots, K\} \cdot e_{k} = g_{k} z_{1}^{k} \dots z_{L}^{k} \left(reduce_{f} y_{1}^{k} g_{1} \dots g_{K}\right) \dots \left(reduce_{f} y_{J}^{k} g_{1} \dots g_{K}\right) \\ & \left\{y_{1}^{k}, \dots, y_{J}^{k}\right\} = \left\{x \mid (x :: T_{f}) \in \left\{x_{1}^{k}, \dots, x_{N}^{k}\right\}\right\} \\ & \left\{z_{1}^{k}, \dots, z_{L}^{k}\right\} = \left\{x_{1}^{k}, \dots, x_{N}^{k}\right\} \setminus \left\{y_{1}^{k}, \dots, y_{J}^{k}\right\} \end{aligned}$ $Example 9 \text{ (Map-reduce skeleton for encoded data type <math>T_{f}$) mapReduce_{f} $\left(c_{1} x_{1}^{1} \dots x_{N}^{1}\right) g_{1} \dots g_{K} f_{1} \dots f_{M} = e_{1} \\ \vdots & \vdots \\ mapReduce_{f} \left(c_{K} x_{1}^{K} \dots x_{N}^{K}\right) g_{1} \dots g_{K} f_{1} \dots f_{M} = e_{K} \\ \text{where } \forall k \in \{1, \dots, K\} \cdot e_{k} = g_{k} \left(f_{1}^{k} z_{1}^{k}\right) \dots \left(f_{L}^{k} z_{L}^{k}\right) \left(mapReduce_{f} y_{1}^{k} g_{1} \dots g_{K} f_{1} \dots f_{M}\right) \\ & \vdots \\ \left(mapReduce_{f} y_{J}^{k} g_{1} \dots g_{K} f_{1} \dots f_{M}\right) \\ \left(w_{k}^{k} \dots w_{k}^{k}\right) = \left\{w_{k}^{k} \dots w_{k}^{k}\right\}$

$$\{y_1^k, \dots, y_J^k\} = \{x \mid (x :: T_f) \in \{x_1^k, \dots, x_N^k\}\}$$
$$\{z_1^k, \dots, z_L^k\} = \{x_1^k, \dots, x_N^k\} \setminus \{y_1^k, \dots, y_J^k\}$$
$$\{f_1, \dots, f_M\} = \bigcup_{k=1}^K \{f_1^k, \dots, f_L^k\}$$

A simplistic approach to parallelise these polytypic $reduce_f$ and $mapReduce_f$ definitions is by evaluating each recursive call simultaneously as shown in Examples 10 and 11, respectively.

 $\begin{aligned} Example \ 10 \ (\text{Parallel reduce skeleton for type } T_f \text{ using GpH}) \\ reduce_f \ (c_1 \ x_1^1 \dots x_N^1) \ t \ g_1 \dots g_K &= e_1 \\ \vdots & \vdots \\ reduce_f \ (c_K \ x_1^K \dots x_N^K) \ t \ g_1 \dots g_K &= e_K \\ \text{where } \forall k \in \{1, \dots, K\} \cdot e_k = h \ (t \le 0) \\ & \text{where} \\ h \ True \ = g_k \ z_1^k \dots z_L^k \ (reduce_f \ y_1^k \ t \ g_1 \dots g_K) \\ & \vdots \\ (reduce_f \ y_J^k \ t \ g_1 \dots g_K) \\ & h \ False = run Eval \ \$ \ do \\ & w_1^k \leftarrow rpar \ (reduce_f \ y_J^k \ (t-1) \ g_1 \dots g_K) \\ & \vdots \\ & w_J^k \leftarrow rseq \ (reduce_f \ y_J^k \ (t-1) \ g_1 \dots g_K) \\ & \vdots \\ return \ (g_k \ z_1^k \dots z_L^k \ w_1^k \dots w_J^k) \\ & \{y_1^k, \dots, y_J^k\} = \{x \ (x \ :: T_f) \in \{x_1^k, \dots, x_N^k\}\} \\ & \{z_1^k, \dots, z_k^k\} = \{x_1^k, \dots, x_N^k\} \setminus \{y_1^k, \dots, y_d^k\} \end{aligned}$

Example 11 (Parallel map-reduce skeleton for type T_f using GpH)

$$\begin{split} map Reduce_{f} (c_{1} x_{1}^{1} \dots x_{N}^{1}) t g_{1} \dots g_{K} f_{1} \dots f_{M} &= e_{1} \\ \vdots & \vdots \\ map Reduce_{f} (c_{K} x_{1}^{K} \dots x_{N}^{K}) t g_{1} \dots g_{K} f_{1} \dots f_{M} &= e_{K} \\ \text{where} \\ \forall k \in \{1, \dots, K\} \cdot e_{k} = h (t \leq 0) \\ & \text{where} \\ h \ True &= g_{k} (f_{k} z_{1}^{k}) \dots (f_{L}^{k} z_{L}^{k}) (map Reduce_{f} y_{1}^{k} t g_{1} \dots g_{K} f_{1} \dots f_{M}) \\ & \vdots \\ (map Reduce_{f} y_{J}^{k} t g_{1} \dots g_{K} f_{1} \dots f_{M}) \\ h \ False &= runEval \$ do \\ & w_{1}^{k} \leftarrow rpar (map Reduce_{f} y_{1}^{k} (t-1) g_{1} \dots g_{K} f_{1} \dots f_{M}) \\ & \vdots \\ & w_{J}^{k} \leftarrow rseq (map Reduce_{f} y_{J}^{k} (t-1) g_{1} \dots g_{K} f_{1} \dots f_{M}) \\ & \vdots \\ & w_{J}^{k} \leftarrow rseq (map Reduce_{f} y_{J}^{k} (t-1) g_{1} \dots g_{K} f_{1} \dots f_{M}) \\ & f_{1} \dots f_{M} \} \\ & \{y_{1}^{k}, \dots, y_{J}^{k}\} = \{x \mid (x :: T_{f}) \in \{x_{1}^{k}, \dots, x_{N}^{k}\} \} \\ & \{z_{1}^{k}, \dots, z_{L}^{k}\} = \{x_{1}^{k}, \dots, x_{N}^{k}\} \setminus \{y_{1}^{k}, \dots, y_{J}^{k}\} \end{split}$$

Here, t is a threshold value to control the number of parallel threads created by the polytypic skeletons using the *rpar* construct. For an *n*-ary encoded data type T_f (where n > 1), the initial value of t can be determined using the following simple rule-of-thumb where P is the number of processor cores:

IF
$$n = 1$$
 THEN $t = P$ ELSE $t = \lfloor log_n P \rfloor + 1$

Based on our transformation steps presented in Sect. 3.3, if n = 1, then the inputs will be encoded into a list to identify list-based parallel skeletons.

4 Evaluation

In this paper, we present the evaluation of three programs—matrix multiplication, dotproduct of binary trees and maximum prefix sum—to illustrate interesting aspects of our transformation. The programs are evaluated on a 12-core Intel Xeon E5 processor each clocked at 2.7 GHz and 64 GB of main memory. GHC version 7.10.2 is used for the sequential versions of the benchmark programs, the Eden language compiler based on GHC 7.8.2 is used for list-based map and map-reduce skeletons, and the polytypic skeletons are parallelised using the Glasgow Parallel Haskell extension for GHC 7.10.2.

For all parallel versions of a benchmark program, only those skeletons that are present in the top level are executed using their parallel implementations and nested skeletons are executed using their sequential versions. The reason for this approach is to avoid uncontrolled creation of too many threads which we observe to result in inefficient parallel execution where the cost of thread creation and management is greater than the cost of parallel execution.

4.1 Example: Matrix Multiplication

The original sequential, hand-parallel, distilled and encoded versions of the matrix multiplication program were presented in Examples 1, 3, 6 and 7, respectively. Following this, we parallelise the encoded program using the list-based *map* and *mapRedr* skeletons presented in Sect. 3.4.1 and obtain the encoded parallel program shown in Example 12. Here, we observe that the encoded $mMul'_1$ and $mMul'_3$ functions are instances of the *map* and *mapRedr* skeletons and hence are defined using the *farmB* and *parMapRedr1* skeletons from the Eden library. The *farmB* skeleton requires the degree of parallelism to be specified which is usually given as the number of processors (*noPe*).

```
Example 12 (Matrix multplication—encoded parallel program)
```

```
mMul''_1 w yss = farmB noPe f w
                   where
                   f c_1
                               = []
                          = []
                   f c_2
                   f(c_3 xs zs) = let v = \lambda xs.g xs
                                                where
                                                g []
                                                           = 0
                                                g(x:xs) = x
                                   in mMul_2'' (encode<sub>mMul2</sub> zs) xs yss v
mMul'_2(c_4:w) xs yss v = []
mMul_2^{\overline{j}}(c_5:w) xs yss v = let v' = \lambda xs.g xs
                                           where
                                                      = 0
                                           g []
                                           g(x:xs) = v xs
                             in (mMul'_3 (encode_{mMul_3} xs yss) v) : (mMul'_2 w xs yss v')
mMul_3'' w v = parMapRedr1 g f w
                where
                             = (+)
                g
                            = 0
                f c_6
                f c_7
                            = 0
                f(c_8 x ys) = x * (v ys)
```

Figure 1 presents the speedups achieved by the encoded parallel program in comparison with the original, distilled and hand-parallel versions. An input of size NxM denotes the multiplication of matrices of sizes NxM and MxN. When compared to the original program, we observe that the encoded parallel version achieves a positive speedup for all input sizes. For input size 100×1000 , the speedup achieved is 6x-25x more than the speedups achieved for the other input sizes. This is due to the intermediate data structure *transpose yss*, which is of the order of 1000 elements for input size 100×1000 and of the order of 100 elements for the other inputs, that is absent in the encoded parallel program. This can be verified from the comparison with the distilled version, which is also free of intermediate data structures. Hence, the encoded parallel program has a similar speedup for all input sizes when compared to the distilled version.

We also observe that even though the hand-parallel and encoded parallel versions parallelise the equivalent computations in the same fashion as can be seen from Examples 3 and 12, the encoded parallel version is marginally faster than for input sizes



Fig. 1 Speedup of matrix multiplication

 100×100 and 1000×100 , 4×-4 . $5 \times$ faster for input size 250×250 and $15 \times -32 \times$ faster for input size 100×1000 . This is due to the use of intermediate data structures in the hand-parallel version, which has been removed in the encoded parallel version. Further, the hand-parallel version scales better with a higher number of cores the input size 100×1000 . This is because the encoded parallel version achieves better speedup even with fewer cores due to the elimination of intermediate data structures, and hence does not scale as impressively as the hand-parallel version.

4.2 Example: Dot-Product of Binary Trees

The original program to compute the dot-product of binary trees was presented in Example 2. The distilled version of this program remains the same as there are no

intermediate data structures. A hand-parallel version of this program using GpH was shown in Example 5. By following the steps in Sect. 3.3, we observe that the encoded data type $T_{dot P}$ created for function dot P is binary (i.e. n = 2). Hence, we transform the dot product program to obtain an encoded program that operates over the encoded data type $T_{dot P}$. Following this, we can obtain an encoded parallel program using the *reduce*_{dot P} skeleton defined over the encoded type $T_{dot P}$ as shown in Example 13.

```
Example 13 (Dot product of binary trees—encoded parallel program)
```

```
data T_{dot P} a ::= c_1 | c_2 | c_3 a a (T_{dot P} a) (T_{dot P} a)
encode_{dot P} E yt
                                                    = c_1
encode_{dot P} (B x xt<sub>1</sub> xt<sub>2</sub>) E
                                                    = c_2
encode_{dot P} (B x xt<sub>1</sub> xt<sub>2</sub>) (B y yt<sub>1</sub> yt<sub>2</sub>) = c_3 x y (encode_{dot P} xt_1 yt_1) (encode_{dot P} xt_2 yt_2)
reduce_{dot P} c_1 t g_1 g_2 g_3
                                               = g_1
reducedot P c2 t g1 g2 g3
                                              = g_2
reduce_{dot P} (c<sub>3</sub> x y lt rt) t g<sub>1</sub> g<sub>2</sub> g<sub>3</sub> =
                        h(t \leq 0)
                         where
                         h True = g_3 x y (reduce_{dot P} lt t g_1 g_2 g_3) (reduce_{dot P} rt t g_1 g_2 g_3)
                         h False = runEval \$ do
                                         x' \leftarrow rpar (reduce_{dot P} lt (t-1) g_1 g_2 g_3)
                                         y' \leftarrow rseq \ (reduce_{dotP} \ rt \ (t-1) \ g_1 \ g_2 \ g_3)
                                          return (g_3 x y x' y')
dot P'' (encode<sub>dot P</sub> xt yt) t
where
dot P'' w t = reduce_{dot P} w t g_1 g_2 g_3
                    where
                                    = 0
                    g_1
                                  = 0
                    82
                    g_3 x y x' y' = (x * y) + x' + y'
```

Figure 2 presents the speedups of the encoded parallel program compared to the original and the hand-parallel programs. An input size indicated by N denotes the dot-product of two identical balanced balanced binary trees with N nodes each. When compared to the original program we observe that the encoded parallel version achieves a positive speedup of 1.5x-2.6x for all input sizes. The speedup achieved for the different input sizes scales equally for varying numbers of cores. When compared to the hand-parallel version, we observe that both programs achieve similar speedups for all input sizes. This is primarily because both versions essentially parallelise the dot-product computation in a similar fashion as can be observed from their definitions.

Further, we also note from our experiments that if the input trees are not wellbalanced, then speedups achieved by both the encoded parallel and hand-parallel versions are reduced. This is because, from the definitions of these parallel versions, it is evident that their parallelisation and workloads of the parallel threads are dictated by the structure of the input tree. As discussed in other existing work [9], better implementations for *n*-ary tree skeletons can be obtained using tree-contraction [10, 11] approaches that can efficiently operate over unbalanced trees.



Fig. 2 Speedup of dot-product of binary trees

4.3 Example: Maximum Prefix Sum

The original program to compute the maximum prefix sum of a given list is presented in Example 14, where mps_1 computes the maximum prefix sum of list xs using the recursive function mps_2 . Here, we consider max to be a built-in operator.

Example 14 (Maximum prefix sum—original program)

```
mps_1 :: [Int] \rightarrow Int
mps_1 xs
where
mps_1 [] = 0
mps_1 (x : xs) = mps_2 xs x
mps_2 [] v = v
mps_2 (x : xs) v = let v' = x + v
in max v (max v' (mps_2 xs v'))
```

By applying the parallelisation transformation, we encode the input *xs* of the recursive function mps_2 into a list of type $[T'_{mps_2}]$, and the resulting encoded parallel program is presented in Example 15. Here, mps''_2 is found to be an instance of the list-based *accumulate* skeleton [1] and is defined using a call to its parallel implementation (*parAccumulate*).

data $T'_{mps_2} a = c_1 | c_2 a$ encode_{mps2} [] $= [c_1]$ $encode_{mps_2}(x:xs) = [c_2 x] + (encode_{mps_2} xs)$ $mps_1 xs$ where $= mps_{2}'' (encode_{mps_{2}} xs) 0$ $mps_1 xs$ $mps_2'' w v = parAccumulate p \oplus q \otimes w v$ where $p c_1 v$ = v $p(c_2 x) v = v$ Ð = max $q c_1$ = v $q(c_2 x) = x$ \otimes = (+)

Example 16 presents the hand-parallel version of the maximum prefix sum program where the recursive function mps_2 is identified as an instance of the *accumulate* skeleton and is defined using a call to its parallel implementation.

Example 16 (Maximum prefix sum—hand-parallelised program (HPP)) $mps_1 xs$

```
where

mps_1 xs = mps_2 xs 0

mps_2 xs v = parAccumulate p \oplus q \otimes xs v

where

p x v = max v (x + v)

\oplus = max

q = \lambda x.x

\otimes = (+)
```

Figure 3 presents the speedups of the encoded parallel program compared to the original and hand-parallel program. An input size indicated by N denotes the computation of the maximum prefix sum for a list of size N. In comparison to the original program, the encoded parallel version achieves a positive speedup ranging between 2.2x-4.5x. The speedup achieved also increases, though marginally, for increasingly large input sizes, which shows that the encoded parallel version scales well for different input sizes. However, the overall speedup gained does not scale linearly with the number of cores used due to the significant cost of encoding which is a sequential computation.

Further, the encoded parallel and hand-parallel versions parallelise the maximum prefix sum program in the same way using the *accumulate* skeleton, with the hand-parallel version defined over the original input list and the encoded parallel version defined over the encoded list. We observe that the hand-parallel program is marginally faster than the encoded parallel program for all input sizes due to the added cost of encoding inputs in the encoded program. This is an example where encoding inputs may be an overhead when the original input structures already match the algorithmic structure.



Fig. 3 Speedup of maximum prefix sum

5 Conclusion

5.1 Summary

The proposed transformation facilitates automatic identification of polytypic and listbased skeletons in a given program using the existing distillation transformation and a newly-proposed encoding transformation. Distillation provides efficiency by reducing the number of intermediate data structures used. Thus, the speedup from distillation is directly proportional to the sizes of intermediate data structures eliminated. The encoding transformation targets and benefits programs defined over inputs whose structures does not match that of the algorithm of the program. For instance, the speedups for matrix multiplication are impressive and the ones for dot product of binary trees and maximum prefix sum are moderate. This is because the structures of the inputs of the latter programs match the algorithmic structures of the programs, while it is not the case for the matrix multiplication example used here.

Thus, the efficiency achieved for matrix multiplication is partly due to the mismatch in the list-of-lists representation of a matrix compared to the algorithmic structure of the program. Such a representation may be considered unnatural given that there exist methods to match the data type structure with the algorithmic structure [12] and optimised representations of matrices in languages such as Haskell's Repa and NESL. Therefore, we need to further evaluate the encoding transformation on its role in the efficiency achieved, in addition to facilitating the identification of skeleton instances.

5.2 Related Work

A seminal work to derive parallel programs using fusion and tupling methods is presented in [3], where the need to extend them to polytypic and nested data structures is highlighted. In [13], the authors propose a method to systematically derive parallel programs from sequential definitions and automatically create auxiliary functions to define associative operators required for parallel evaluation. However, this method is restricted to a first-order language and functions defined over a single recursive linear data type that has an associative decomposition operator such as +. To address this, [9] presents the *diffusion* transformation to decompose recursive functions into functions that can be described using the *accumulate* skeleton. While diffusion can transform a wide range of functions and can be extended using polytypic *accumulate* skeleton, it is only applicable to functions with one recursive input, and the properties and unit values of skeleton operators are verified and derived manually.

Since calculational approaches to program parallelisation were based on homomorphisms and the third homomorphism theorem on lists [14], this was extended to trees in [15] by decomposing a binary tree into a list of sub-trees called *zipper*, and deriving parallel computations on the zipper structure. Though it addresses tree-based programs, this method requires multiple sequential versions of the program to derive the parallel version. To address polytypic parallel skeletons, [16] presents parallel *map, reduce* and *accumulate* skeletons for n-ary trees which are implicitly converted to binary trees and uses parallel skeletons for binary trees. Despite introducing polytypic parallel skeletons, this approach requires manual check and derivation of the operator associativity and unit values, respectively. As an alternative, [17] proposes an analytical method to transform general recursive functions into a composition of polytypic data parallel skeletons. Even though this method is applicable to a wider range of problems, the transformed programs are defined by composing skeletons and employ multiple intermediate data structures.

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