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# **Abstract**

With the advent of the Internet of Everything, the proliferation of data has put a huge burden on data centers and network bandwidth. To ease the pressure on data centers, edge computing, a new computing paradigm, is gradually gaining attention. Meanwhile, artifcial intelligence services based on deep learning are also thriving. However, such intelligent services are usually deployed in data centers, which cause high latency. The combination of edge computing and artifcial intelligence provides an efective solution to this problem. This new intelligence paradigm is called edge intelligence. In this paper, we focus on edge training and edge inference, the prior training models using local data at the resource-constrained edge devices. The latter deploying models at the edge devices through model compression and inference acceleration. This paper provides a comprehensive survey of existing architectures, technologies, frameworks and implementations in these two areas, and discusses existing challenges, possible solutions and future directions. We believe that this survey will make more researchers aware of edge intelligence.

**Keywords** Edge computing · Deep learning · Edge intelligence · Artifcial intelligence · Edge devices

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## **1 Introduction**

With the constant development of algorithms, big data and computing power, artifcial intelligence (AI) is fourishing in today's era. Among them, deep learning (DL) (Hinton et al. [2006\)](#page-50-0) has yielded breakthrough achievements in various felds, from computer vision (Krizhevsky et al. [2012\)](#page-51-0) to natural language processing (Bengio et al. [2003](#page-48-0)). The rapid development in these felds has produced many DL-based smart application services, which bring great convenience to our lives. However, existing smart application services are often computationally intensive, and these DL-based services are usually deployed in data centers. Such cloud-based DL services have several drawbacks: (1) computation tasks are deployed in data centers, which can impose a huge burden on network bandwidth and data centers; (2) it cannot provide low-latency services; (3) data is uploaded to the cloud, which can be a potential threat to user privacy.

As the extension of cloud computing, edge computing (Shi et al. [2016](#page-55-0)) can efectively alleviate the issues mentioned above. It relieves the pressure on network bandwidth and data centers by sinking part of the computation to the edge. Compared to cloud computing, edge computing has the following advantages: (1) computation is performed locally or at the edge servers, lowering latency; (2) data does not need to be uploaded to the cloud, reducing bandwidth consumption; (3) data is stored at the edge, which protects user privacy. Edge computing offers an effective solution to the key challenges of AI. In fact, the combination of edge computing and AI has formed a new intelligence paradigm, edge intelligence. Edge intelligence deploys a portion of DL services to the edge, improving the speed of data processing, avoiding user privacy leakage and reducing bandwidth consumption.

As far as we know, there exists some efforts related to our work (Zhou et al. [2019b;](#page-57-0) Xu et al. [2020;](#page-56-0) Wang et al. [2020;](#page-56-1) Tang et al. [2020](#page-55-1); Deng et al. [2020](#page-49-0); Yang et al. [2019;](#page-56-2) Kholod et al. [2021;](#page-51-1) Yang et al. [2020a](#page-56-3)). Specifcally, Tang et al. [\(2020](#page-55-1)) provided a comprehensive survey on communication-efficient distributed training. They classified distributed training architectures into three types from the system-level, which are also involved in our work. Yang et al. ([2019](#page-56-2)) provided a comprehensive survey about federated learning (FL), which they classifed as horizontal FL, vertical FL, and federated transfer learning. FL is also an important component of our survey. Kholod et al. [\(2021](#page-51-1)) investigated opensource frameworks for FL. They conducted experiments on three datasets and evaluated the applicability of the FL frameworks to Internet of Things (IoT) systems in terms of fve dimensions: ease of use and deployment, development, analysis capabilities, accuracy, and performance. Yang et al. [\(2020a\)](#page-56-3) presents the main contributions of 35 papers on edge intelligence. Some of these papers are also involved in our survey. The survey of Wang et al. ([2020\)](#page-56-1), Deng et al. [\(2020](#page-49-0)) consists of two parts: (1) how to train and deploy models in resource-constrained edge devices; and (2) how to utilize AI to solve problems in edge computing. The former is the focus of our survey, and the latter is outside the scope of our survey. In addition, their survey was conducted from a macro perspective. Compared to their work, we provide more detailed information and the latest research.

To the best of our knowledge, Zhou et al. [\(2019b\)](#page-57-0) and Xu et al. ([2020\)](#page-56-0) are the two most relevant reviews to our survey. Xu et al. ([2020\)](#page-56-0) investigated the works on edge intelligence from four perspectives: edge caching, edge training, edge inference, and edge offloading. The efforts of these four areas are also involved in our survey. The difference is that we focus on the eforts on edge training and edge inference. For edge caching as well as edge offloading, we focus on their applications to edge intelligence. In addition to this,



<span id="page-2-0"></span>Table 1 The comparison of related surveys **Table 1** The comparison of related surveys we provide a more detailed introduction and the latest research. Zhou et al. ([2019b\)](#page-57-0) introduced the works on edge training and edge inference from a macroscopic perspective. Differently, our investigation is conducted from a microscopic perspective. Moreover, (Zhou et al. [2019b](#page-57-0)) was published in 2019. Therefore, we investigated the most recent studies and provided more detailed information. We compare these efforts in Table [1](#page-2-0).

In this paper, we mainly focus on edge training and edge inference. The former uses federated learning to train models on edge devices. The latter uses techniques such as model compression and inference acceleration to deploy models on edge devices. The remainder of this paper is organized as follows. We introduce the foundations of edge computing and AI in Sects. [2](#page-3-0) and [3,](#page-4-0) respectively. In Sects. [4](#page-5-0) and [5](#page-15-0), we provide a detailed introduction, discussion, and analysis of the techniques, architectures, open-source frameworks and developments in edge training and edge inference, separately. The implementations of edge intelligence is presented in Sect. [6.](#page-39-0) Finally, we provide open challenges, possible solutions and future directions for edge intelligence in Sect. [7](#page-44-0).

# <span id="page-3-0"></span>**2 The foundations of edge computing**

In this section, we introduce some fundamental concepts of edge computing, which consists of four parts: cloud computing, fog computing, edge computing, and cloud-edge collaboration.

- (1) Cloud computing: the National Institute of Standards and Technology (NIST) defned cloud computing as a ubiquitous, easily accessible, and on-demand accessed shared pool of computing resources that can be rapidly deployed with minimal management (Mell and Grance [2011\)](#page-54-0). Cloud computing brings together numerous computing resources and manages them automatically through software. Cloud computing provides fast and secure computing and data storage services that allow everyone to utilize the huge computing resources on the network.
- (2) Fog computing: Bonomi et al. ([2012](#page-48-1)) frst introduced fog computing, defned it as a highly virtualized computing platform that migrates computing tasks from the cloud to network edge for execution. Diferent from cloud computing, fog computing is not composed of powerful servers, but of weaker and more decentralized computing nodes. It is an IoT-oriented distributed computing infrastructure that can signifcantly reduce latency by sinking computation power and data analysis to the edge of the network.
- (3) Edge computing: edge computing is located at the edge of the network that is close to devices. The edge can be any entity from the data source to the data center and provides services such as computation, storage, and data analysis to the devices. It is worth noting that the edge is not the device itself. Although both edge computing and fog computing move computation and storage to the edge of the network, there are certain diferences between the two computation paradigms. Group OCAW et al. [\(2017](#page-50-1)) pointed out that fog computing provides computation, storage, and control services for all requests between the cloud and devices, while edge computing is only for edge devices.
- (4) Cloud-edge collaboration: cloud computing can provide powerful support for computationally intensive tasks such as DL, however it cannot provide low-latency services. Although edge computing can efectively alleviate this problem, it is difcult to run computationally intensive tasks on resource-constrained devices. Collaboration between cloud and edge can provide low-latency DL services. For example, Huang et al. [\(2019](#page-51-2)) split deep neural network (DNN) between cloud, edge and devices, allow-

ing a portion of the computation to be performed at the edge or devices, thus achieving trade-ofs between latency, bandwidth consumption and device energy consumption.

# <span id="page-4-0"></span>**3 The foundations of AI**

In this section, we introduce some subfelds of machine learning and focus on deep neural network.

# **3.1 Machine learning**

Machine learning is a technique that uses algorithms to parse data and make classifcations and predictions about real-world things. Machine learning comes directly from the early days of the AI feld. Its traditional algorithms include decision trees, clustering, Bayesian classifcation, support vector machines (SVM), Adaboost, etc. Here we introduce several subfelds of machine learning: incremental learning, active learning, reinforcement learning, deep learning and deep reinforcement learning.

- (1) Incremental learning: general machine learning models forget what they have learned while they acquire new knowledge, which is known as "catastrophic forgetting". Models can be retrained with the help of data centers. However, this can be time- and energy-consuming. Incremental learning can be an efficient solution to this problem. Models obtained by training with incremental learning can perform well on both old and new tasks. It is worth noting that the concept, incremental learning, is similar to continuous learning and lifelong learning.
- (2) Active learning: supervised learning requires a complete annotated dataset to train the model. Data annotation is costly when the dataset is extremely large. Active learning is an efective way to alleviate this problem. The core idea of active learning is to allow the learning algorithm to proactively propose annotation requests and hand over some relatively difficult annotation tasks to human experts for further annotation. In this way, the costs of data annotation can be efectively decreased.
- (3) Reinforcement learning: reinforcement learning emphasizes how to maximize benefts by taking actions based on the environment. In contrast to supervised learning, reinforcement learning (Bellman [1953\)](#page-48-2) does not require any data. It acquires learning information and updates model parameters by accepting the feedback from the environment. Reinforcement learning is often used in applications such as Go and games, e.g., AlphaGo (Silver et al. [2016\)](#page-55-2) is an implementation based on reinforcement learning.
- (4) Deep learning: as one of the main driving engines of AI development, DL has attracted wide attention of researchers since AlexNet (Krizhevsky et al. [2012\)](#page-51-0) won the ImageNet competition, after which more excellent network models were proposed, such as VGG (Simonyan and Zisserman [2014](#page-55-3)), GoogleNet (Szegedy et al. [2015\)](#page-55-4). DL has made signifcant breakthroughs in many felds such as data mining, machine translation, face recognition, natural language processing, etc. It has solved a number of complicated pattern recognition problems and enabled the rapid development of AI.
- (5) Deep reinforcement learning: DL has a high perceptual ability but lacks a certain degree of decision-making ability. Whereas, reinforcement learning possesses decisionmaking ability but cannot perceive the target well. Deep reinforcement learning (Mnih et al. [2013\)](#page-54-1) integrates the perceptual ability of DL and the decision-making ability of



<span id="page-5-1"></span>**Fig. 1** Three types of common neural networks

reinforcement learning. It can control directly based on the input image. This approach is closer to the pattern of human thought.

#### **3.2 Deep neural network**

DNN can be comprehended as a neural network structure that includes multiple hidden layers. Here, we focus on three structures of DNN: multilayer perceptron (MLP), Convolutional Neural Networks (CNN) and Recurrent Neural Networks (RNN).

The earliest neural network is perceptron, which has an input layer, an output layer and a hidden layer. The input feature vector is transformed through the hidden layer to the output layer, and the results are obtained in the output layer. To ft more complex functions, (McCulloch and Pitts [1943](#page-53-0)) proposed MLP. As shown in Fig. [1](#page-5-1)a, MLP is a series of fully-connected layers. The problem of fully connected DNNs is the excessive number of parameters. To alleviate this problem, LeCun et al. ([1989](#page-52-0)) introduced convolution kernels in CNN. The weights of the same convolution kernel are shared. The image still retains its original position relationship after being convolved. In this way, CNN efectively reduces the number of parameters. CNN is mainly applied to computer vision, but it can also be applied to natural language processing (Zhang and Wallace [2015](#page-57-1)). RNN can represent the changes of time-series, and it is widely applied in natural language processing. In order to better handle time-series tasks, a series of RNN variants had been proposed, such as LSTM (Hochreiter and Schmidhuber [1997\)](#page-50-2), Transformer (Vaswani et al. [2017\)](#page-56-4). Recently, a number of researchers have paid attention to the application of Transformer in computer vision (Dosovitskiy et al. [2020;](#page-49-1) Touvron et al. [2020\)](#page-55-5). All these works obtained good performance.

### <span id="page-5-0"></span>**4 Edge training**

Cloud-based DL requires a complete dataset in the data center for training. However, training tasks in the edge environment are mainly done through the collaboration of multiple participants. Such distributed learning is known as FL, which can efectively save bandwidth and protects user privacy.

There are some diferences between FL and distributed learning (Table [2](#page-6-0)). First, the purpose of distributed learning is to process large amounts of data in parallel, while the purpose of FL is to train models locally and protect user privacy. Second, traditional distributed learning usually works with independent and identically distributed (IID) data, while FL works with non-independent and identically distributed (Non-IID) data. Finally, traditional distributed learning uses high-performance compute nodes for



from client to client

<span id="page-6-0"></span>

Nodes High performance computing nodes Resource-constrained edge nodes

training, while FL uses resource-constrained edge nodes for training. In this section, we focus on training architectures of distributed learning and the development of FL.

# **4.1 Training architecture**

- (1) Parameter server: in the parameter server (PS) architecture (Smola and Narayanamurthy [2010\)](#page-55-6), there are two roles, the server and working nodes. As shown in Fig. [2](#page-7-0)a, frst, the working nodes train the model and update the parameters locally. Then the updated parameters are sent to the server. Finally, the server aggregates the updates to get a global model and distributes the parameters to each node. The main challenges of the PS architecture are communication bottlenecks and single-point-of-failure.
- (2) All-reduce: to avoid single-point-of-failure, All-Reduce is an optional solution (Patarasuk and Yuan [2009\)](#page-54-2). As shown in Fig. [2b](#page-7-0), All-Reduce removes the PS. Each working node communicates with other working nodes and gets updates from all the nodes. Therefore, each node has a consistent global model. The collective communication pattern of All-Reduce prevents it from being utilized for asynchronous communication. Moreover, this solution results in signifcant bandwidth consumption due to frequent communication. In order to reduce bandwidth consumption, Gibiansky [\(2017](#page-50-3)) proposed Ring-All-Reduce. As shown in Fig. [2](#page-7-0)c, they constructed the working nodes into a ring structure, where each node delivers updates to the next node in a clockwise direction. At the same time, it also receives updates from the last node.
- (3) Gossip: in contrast to the PS architecture, the gossip architecture has neither PS nor a consistent global model. As shown in Fig. [2d](#page-7-0), in this architecture, the working nodes randomly (Blot et al. [2016](#page-48-3)) or conditionally (Roy et al. [2019\)](#page-54-3) select several nodes to exchange updates at each iteration. Therefore, in each iteration, all the local models are diferent from each other. However, it guarantees that the fnal model is consistent.
- (4) Neighbor: similar to the gossip architecture, the neighbor architecture does not have a consistent global model either. As shown in Fig. [2e](#page-7-0), in each iteration, each working node communicates only with its neighbor nodes. Therefore, the model on each node is changed in each iteration. Of course, it also ensures the consistency of the fnal model.

# **4.2 Federated learning**

FL was proposed by Google. It is a distributed learning that takes place in mobile edge systems. The most common application scenario of FL (McMahan et al. [2017](#page-53-1)) has the following characteristics: (1) network instability: edge devices are often disconnected; (2) data imbalance: the data volume may be signifcantly diferent between edge devices; (3) Non-IID data: the data distribution of each edge device cannot represent the total data



<span id="page-7-0"></span>**Fig. 2** Five types of training architectures

distribution due to the diferent preferences among users; (4) large number of devices: there are a huge number of devices in the edge.

FL with PS architecture appeared relatively early. Then, FL with peer-to-peer architecture emerged, which reduces bandwidth consumption and avoids the single-point-of-failure compared to FL with PS architecture. We classify these efforts into four categories: (1) communication-efficient FL,  $(2)$  privacy preserving FL,  $(3)$  FL with Non-IID data, and  $(4)$ Decentralized FL. The major efforts of FL are summarized in Table [3.](#page-8-0)

### **4.2.1 Communication-efficient FL**

As shown in Fig. [3,](#page-10-0) for FL with PS architecture, edge devices send updates to the PS and get the global parameters from the PS. However, In the edge environment, network bandwidth resources are limited. Therefore, communication optimization is essential. We categorize the eforts of communication optimization into two types: (1) communication frequency optimization and (2) communication costs optimization. Among them, the optimization of communication frequency avoids frequent update exchanges by controlling communication rounds. The optimization of communication costs reduce the size of the updates in each communication round.

*Communication frequency optimization* McMahan et al. ([2017](#page-53-1)) frst proposed FL and presented the Federated Averaging (FedAvg). They reduced the number of communication rounds by adding additional computations on local devices. Compared to synchronized stochastic gradient descent, FedAvg reduces communication rounds by a factor of 10–100. Wang et al. ([2019](#page-56-5)) proposed a control algorithm which can dynamically adjust the global aggregation frequency to minimize the learning loss under a fxed resource budget. Reisizadeh et al. ([2020](#page-54-4)) proposed a periodic average and quantization (FedPAQ) algorithm. FedPAQ allows clients to perform gradient updates locally, capturing the updates of devices in phases and sending them to the server. Moreover, the information sent to the server is simply a quantifed version of the local information. The periodic average reduces the number of communication rounds and the quantization decreases the costs of each communication round.



<span id="page-8-0"></span>Table 3 The major efforts of Federated Learning **Table 3** The major eforts of Federated Learning





<span id="page-10-0"></span>**Fig. 3** FL with PS Architecture

The aforementioned works randomly selected clients during each training round, which may lead to unreliable client selection and cause unnecessary bandwidth consumption. AbdulRahman et al. ([2020\)](#page-47-0) maximizes the number of users involved in training and reduced the number of discarded communication rounds by evaluating the ability of users to perform FL tasks from multiple perspectives, including CPU, memory, energy and time.

*Communication costs optimization* In contrast to communication frequency optimization, the optimization of communication costs focuses on reducing the size of the updates in each round of communication. Lin et al. ([2017b\)](#page-52-1) discovered that in distributed stochastic gradient descent, up to 99% of the gradient exchanges are unnecessary. Therefore, communication costs can be decreased by reducing redundant gradient exchanges. To achieve this goal, gradient quantization and gradient sparsifcation are widely used. For example, Seide et al. ([2014\)](#page-55-9) used 1-bit quantization to reduce bandwidth consumption in stochastic gradient descent. Aji and Heafeld ([2017\)](#page-48-7) sparsifed the gradient updates by removing a certain percentage of the gradients based on the absolute value of the threshold, and used a global threshold with layer normalization. However, these works either added additional layer normalization or brought a loss of accuracy. Lin et al. ([2017b](#page-52-1)) proposed deep gradient compression (DGC). Compared to the works mentioned above, DGC does not require extra layer normalization and uses momentum correction and local gradient clipping to ensure that the model accuracy is not degraded.

Konečnỳ et al. [\(2016](#page-51-3)) proposed two approaches for reducing the uplink communication bandwidth, structured updates and sketched updates. Structured updates refer to updates that can be learned from parameterized constrained space that uses fewer variables. Sketched updates refer to learning the complete model updates and then compressing them before sending to the server. Different from Konečnỳ et al. [\(2016](#page-51-3)), Caldas et al. [\(2018](#page-48-4))

focused on server-to-client communication. In addition, inspired by the Dropout (Srivastava et al. [2014\)](#page-55-10), they proposed the Federated Dropout, which allows each device locally trains sub-models. These sub-models are subsets of the global model. Thus, local updates can be considered as updates of the global model.

Jiang et al. ([2019\)](#page-51-4) proposed PruneFL, an adaptive distributed parameter pruning method based on FL. PruneFL includes initial pruning and distributed pruning. It also contains a low-complexity adaptive pruning method that achieves a trade-off between accuracy and computation time. Chen et al. ([2019\)](#page-49-2) proposed an asynchronous learning strategy and a temporally weighted aggregation strategy. In the asynchronous learning strategy, DNN layers are divided into shallow layers and deep layers. The parameters of shallow layers are updated more frequently than the parameters of deep layers, which reduce communication costs between the server and clients. In addition, the temporally weighted aggregation strategy can aggregate the local models more efficiently, improving the performance of the model.

#### **4.2.2 Privacy preserving FL**

Although FL protects user privacy to a certain degree, some works have demonstrated that it is possible to obtain certain user information from trained models. For example, Hitaj et al. ([2017\)](#page-50-5) designed a distributed DL attack based on generative adversarial network (GAN). Any user involved in collaborative training can use this attack to generate sensitive information about the victim's device. This attack is efective even when the client uses diferential privacy to obfuscate parameters. The authors claimed that this attack is also effective for FL. Further, Melis et al.  $(2019)$  $(2019)$  attacked to FL. They demonstrated that gradient updates can leak participants' training data and proposed active and passive inference attacks. Despite the fact that FL can average gradient updates, they still successfully exploited gradient updates for the attack.

In FL with PS architecture, privacy threats are from two main sources: (1) PS; and (2) malicious edge nodes. Early works mainly focused on privacy threats from PS (Aono et al. [2017;](#page-48-5) Geyer et al. [2017](#page-50-4); Bonawitz et al. [2017](#page-48-6)). These works mainly protected user privacy through diferential privacy, homomorphic encryption, and secure multi-party computation. Later, researchers used blockchain to eliminate privacy threats from PS and to mitigate privacy threats from malicious nodes to some extent.

*Privacy threats from PS* Shokri and Shmatikov [\(2015](#page-55-11)) proposed a privacy-preserving DL system that allows participants to train on their own datasets and selectively share a portion of the parameters. Sharing parameters can improve model accuracy, but brings privacy loss. The system can adjust the shared parameters to make a trade-of between privacy loss and model accuracy. However, Aono et al. [\(2017](#page-48-5)) found that even if only a portion of the parameters are shared, useful information about the users can still be obtained. Therefore, the authors used homomorphic encryption to protect the participants' information from being leaked to the server.

Geyer et al. ([2017\)](#page-50-4) applied diferential privacy to FL for hiding the contribution of clients participating in training. The algorithm can achieve good convergence performance for a given privacy level, especially when a sufficient number of clients are involved in the training. Bonawitz et al. [\(2017](#page-48-6)) applied secure multi-party computation to FL and proposed an efficient secure aggregation protocol. This method makes it impossible to check individual updates for each client. Truex et al. ([2019\)](#page-55-7) proposed a FL algorithm that combines diferential privacy and secure multi-party computation. In which, diferential

privacy protects user privacy and secure multi-party computation ensures that the information exchange without being protected by diferential privacy is not disclosed.

The works mentioned above only proved the experimental results by simulation, but lacked theoretical analyses. Wei et al. ([2020\)](#page-56-6) provided a theoretical analysis of FL's convergence properties when diferential privacy is used. The authors demonstrated theoretically that there is a trade-off between convergence performance and privacy preservation. They presented NbAFL, a framework that adds noise to the client's parameters before aggregation. In addition, the authors proposed a K-client stochastic scheduling strategy. They discovered that there is an optimal number of clients, K, for a given level of privacy, allowing the model to achieve the best convergence performance.

*Privacy threats from edge nodes* The works mentioned above focused on privacy threats from PS. However, dishonest clients can also impair training. For example, Bagdasaryan et al. [\(2020](#page-48-8)) demonstrated that dishonest clients can be used to attack FL through model replacement. In addition, clients participating in FL have absolute control over their local data. As a result, anomaly detection can't be used to discover malicious nodes. Researchers are already focusing on the application of blockchain in FL to protect privacy from malicious nodes.

Kim et al. ([2019\)](#page-51-5) proposed BlockFL, a blockchain-based architecture, which eliminates the privacy threats and single-point-of-failure posed by PS. Additionally, they introduced incentives to attract clients to engage in training and mitigate privacy threats from dishon-est clients. Similarly, DeepChain Weng et al. [\(2021](#page-56-7)) can provide data confidentiality, computational auditability, and incentives to the parties involved in collaborative training. Taking a step further, differential privacy was introduced in the work of Zhao et al.  $(2020)$  $(2020)$  for further protecting user privacy. Besides, the authors also proposed a normalization technique. This normalization technique outperforms batch normalization (BN) when the data features are under diferential privacy protection.

### **4.2.3 FL with Non‑IID data**

Although there have been some studies focusing on the processing of unbalanced data distribution (Du et al. [2020](#page-49-4), [2021\)](#page-49-5), most of the existing studies on machine learning assume that the data distribution is identical. However, in FL, Non-IID data is even more of a nonnegligible problem.

Li et al. [\(2019c](#page-52-2)) demonstrated that the heterogeneity of the data slows down the conver-gence rate. Similarly, Duan et al. [\(2019](#page-49-6)) proved that unbalanced distributed training data can reduce the accuracy of a FL-trained model. Although the FedAvg algorithm proposed by McMahan et al. ([2017\)](#page-53-1) is robust to a certain degree of Non-IID data, (Zhao et al. [2018a](#page-57-3)) proved that the accuracy of models trained with FedAvg is signifcantly decreased when the training dataset is highly-skewed Non-IID. They demonstrated that the accuracy reduction is caused by weight diferences. The authors quantifed the weight diferences by Earth Mover's distances. In addition, they suggested creating a subset of data that is shared globally across devices to improve training on Non-IID data. However, users may refuse to install unknown data on their own devices due to security concerns.

Yoshida et al.  $(2020)$  $(2020)$  proposed a new learning mechanism, Hybrid-FL, to solve the Non-IID data problem. They assumed that a few clients allow their data to be uploaded to the server in order to form an approximate IID dataset. The IID dataset is utilized to train a model. This model is aggregated with other models trained with Non-IID data. Zhang et al. ([2021b\)](#page-57-5) observed that there are weight diferences between clients with diferent degrees of



(a) Decentralized FL with Neighbor architecture

 $(b)$  Decentralized FL with Gossip architecture

<span id="page-13-0"></span>**Fig. 4** Decentralized FL

Non-IID data and those with IID data. Based on this, CSFedAvg, an efficient FL algorithm, was proposed. They identifed the degree of Non-IID data that clients have by the weight diferences and selected clients with lower degree of Non-IID data to train the model.

All the works mentioned above only considered the efect of local data distribution imbalance. Duan et al.  $(2019)$  $(2019)$ , Duan et al.  $(2020)$  $(2020)$  mitigated the global data imbalance by data augmentation. For local data distribution imbalance, they introduced a mediator, which can rearrange clients training based on the Kullback–Leibler divergence of clients' data distribution. However, the data distribution of clients may disclose their privacy to some extent.

### **4.2.4 Decentralized FL**

FL with PS architecture can protect user privacy to a certain degree. However, this architecture has two drawbacks: (1) it causes communication bottlenecks; (2) it is over-reliant upon the PS, which demands that all clients collectively choose a trusted PS. Moreover, the failure of PS can disrupt the entire training process. To alleviate these problems, some of the existing works discard PS, which can be achieved by blockchain (Weng et al. [2021;](#page-56-7) Kim et al. [2019](#page-51-5); Lu et al. [2019;](#page-53-4) Zhao et al. [2020\)](#page-57-2) and other communication methods (Lalitha et al. [2019;](#page-51-6) Hegedűs et al. [2019;](#page-50-6) Lu et al. [2020](#page-53-2); Hu et al. [2019](#page-51-7)).

As shown in Fig. [4](#page-13-0)a, Lalitha et al. ([2019\)](#page-51-6) distributed all the nodes on a network. Each node communicated with its neighbors to train the model, in this way a decentralized FL was achieved. Hegedűs et al. ([2019\)](#page-50-6) systematically compared the aggregation costs of distributed learning for PS architecture and Gossip architecture and evaluated some additional techniques. They found that the performance of the two architectures are comparable.

Lu et al. [\(2020](#page-53-2)) and Hu et al. [\(2019](#page-51-7)) optimized the decentralized FL from the perspective of communication frequency and communication costs, respectively. In the work of Lu et al. ([2020\)](#page-53-2), clients update the gradients locally for certain rounds before communicating with their neighbors. By this way communication rounds can be efectively reduced. Hu et al. [\(2019](#page-51-7)) partitioned the model into mutually non-overlapping segments. Each node updated by aggregating local segments and segments from the other k nodes. During the training process, each node randomly selects a certain number of other nodes to transmit the model segments. Compared with transmitting the entire model, communication costs are signifcantly reduced.

#### **4.2.5 Open‑source FL frameworks**

Several open-source frameworks have been proposed to facilitate the development of FL. Here we introduce four FL open-source frameworks, which are TensorFlow Federated Framework (TFF), Federated AI Technology Enabler Framework (FATE),<sup>[2](#page-14-1)</sup> Paddle Federated Learning Framework  $(PFL)^3$  $(PFL)^3$  and PySyft Framework.<sup>[4](#page-14-3)</sup>Kholod et al. ([2021\)](#page-51-1) provided a detailed introduction and evaluation of FL frameworks, which is helpful for those interested in FL frameworks.

*TensorFlow federated framework* TFF is an open-source framework proposed by Google for distributed machine learning. TFF was developed to promote open research and experimentation in FL. With TFF, developers can use models and data to simulate the FL algorithms it contains. TFF implements base classes for FedAvg and the Federated Stochastic Gradient Descent algorithm, which are simple implementations of federated evaluation.

TFF's interface can be divided into two layers: (1) FL API: this layer provides a highlevel interface that enables developers to apply all the FL algorithms it contains to existing Tensorfow models. (2) Federated Core API: the core of this layer is a set of low-order interfaces, which allow new FL algorithms to be developed succinctly with a combination of Tensorfow and distributed communication operators. This layer is the basis for developers to build FL.

*Federated AI technology enabler framework* FATE is an open-source project initiated by the AI department of WeBank. To the best of our knowledge, FATE is the most popular FL framework. FATE builds secure computing protocols with multi-party secure computation and homomorphic encryption. Moreover, FATE supports secure computation for diferent kinds of machine learning including logistic regression, DL, transfer learning, etc.

FATE currently supports two deployment methods. (1) Naïve deployment, which includes stand-alone deployment and cluster deployment. Stand-alone deployment helps developers to develop quickly and test FATE. Cluster deployment is for big data scenarios. It is worth noting that migrating from stand-alone deployment to cluster deployment requires only a confguration fle change and no algorithm changes. (2) KubeFATE deployment, with KubeFATE, FATE can be deployed via docker-compose or Kubernetes. Docker-compose is suitable for development or testing, while Kubernetes is suitable for large-scale deployments in production environments.

*Paddle federated learning framework* PFL is an open-source FL framework based on PaddlePaddle, both of which were proposed by Baidu. PFL can process horizontally partitioned and vertically partitioned data, and the algorithms for these two types of data partitioning are implemented in the paddle\_f package and the mpc package, respectively. In PFL, the components for defning FL tasks are as follows.

Components for compiling: (1) FL-strategy: defning the FL strategy; (2) User-Defned-Program: defning the machine learning model structure and training strategy; (3) Distributed-Confg: defning the information about distributed training nodes; (4) FL-Job-Generator: generating FL-jobs for the server side and worker side. FL-jobs are distributed to the server side and worker side for joint training.

<span id="page-14-0"></span><sup>&</sup>lt;sup>1</sup> https://github.com/tensorflow/federated.

<span id="page-14-1"></span><sup>2</sup> [https://github.com/FederatedAI/FATE.](https://github.com/FederatedAI/FATE)

<span id="page-14-2"></span><sup>3</sup> [https://github.com/PaddlePaddle/PaddleFL.](https://github.com/PaddlePaddle/PaddleFL)

<span id="page-14-3"></span><sup>4</sup> [https://github.com/OpenMined/PySyft.](https://github.com/OpenMined/PySyft)

Components for running: (1) FL-Server: a parameter server for model aggregation; (2) FL-Worker: worker nodes that train the model locally; (3) FL-Scheduler: scheduling worker nodes during training and deciding which workers can participate in training.

Notably, PFL requires at least 6GB of RAM and 100GB of hard disk space to run properly (Kholod et al. [2021\)](#page-51-1), which prevents PFL from being used in IoT systems. In addition, PFL is based on PaddlePaddle, a DL framework proposed by Baidu. PaddlePaddle has a smaller user base than Tensorflow and Pytorch, which may be detrimental to the development of PFL.

*PySyft framework* PySyft is an open-source python framework that supports secure, private computation in DL. PySyft integrates FL, secure multi-party computation and diferential privacy into diferent DL frameworks such as PyTorch, Keras or TensorFlow.

In order to extend PySyft to implement FL on mobile or edge devices, some other libraries are necessary such as KotlinSyft (Android) and SwiftSyft (iOS). These libraries belong to the Syft ecosystem. However, these libraries alone cannot connect user data to the real world. The Syft ecosystem can be supported by the Grid ecosystem, which focuses on the deployment, scalability, and other issues that arise when real-world systems are used to process and compute data.

### <span id="page-15-0"></span>**5 Edge inference**

Edge training is achieved through the collaboration between multiple participants. However, edge inference can be performed not only through collaboration between cloud, edge and devices, but also through the model on a single device. In this section, we focus on the deployment and optimization of AI models on edge devices.

### **5.1 Inference architecture**

We divide these inference architectures into three categories based on the inference participants: Solo-inference, Hybrid co-inference, and Peer-to-peer co-inference.

#### **5.1.1 Solo‑inference**

Solo-inference refers to model inference being performed on a single working node. This working node is either device, edge or cloud. Therefore, Solo-inference can be further divided into three types: Device-only, Edge-only, and Cloud-only.

- (1) Device-only: as shown in Fig. [5a](#page-16-0), the inference task is performed entirely at the device. Obviously, this type of inference is only suitable for powerful devices, such as smartphones. In addition, it is necessary to optimize the model using model compression and inference acceleration for successful deployment on the device.
- (2) Edge-only: as shown in the Fig. [5](#page-16-0)b, in this inference architecture, the model is deployed at the edge server. The device collects the data and sends it to the server, which performs inference and returns the results to the device. This type of inference has no performance requirements for the device. Moreover, it has lower latency than the Cloud-only inference architecture.



<span id="page-16-0"></span>**Fig. 5** Three types of Solo-inference

(3) Cloud-only: as shown in the Fig. [5](#page-16-0)c, in this inference architecture, the device collects data and uploads it to the cloud. Then the cloud processes the data and returns the results to the device. It is worth noting that most of existing DL services are based on this inference architecture. Although the cloud can provide powerful arithmetic support for inference tasks, this approach can cause communication bottlenecks and privacy leakage to some extent.

# **5.1.2 Hybrid co‑inference**

Hybrid co-inference allows inference tasks to be performed between the cloud, the edge, and devices. We divide these inference architectures into four categories based on the inference participants: Device-edge co-inference, Edge-cloud co-inference, Device-cloud coinference and Device-edge-cloud co-inference.

- (1) Device-edge co-inference: as shown in the Fig. [6](#page-17-0)a, in this inference architecture, the device frst performs a portion of the inference task and then sends the results to the edge server for further inference. This approach can efficiently utilize the resources of edge devices.
- (2) Edge-cloud co-inference: as shown in the Fig. [6](#page-17-0)b, in this inference architecture, the device only collects data and sends it to the edge server. Then, the edge server performs the initial inference and sends the results to the cloud to perform the remaining inference. Compared to the Cloud-only architecture, this approach alleviates the communication bottlenecks to some extent.
- (3) Device-cloud co-inference: as shown in the Fig. [6c](#page-17-0), in this inference architecture, the device and the cloud perform the inference task together. Compared with Cloud-only architecture, it is more benefcial to protect user privacy.
- (4) Device-edge-cloud co-inference: as shown in the Fig. [6](#page-17-0)d, in this inference architecture, the computation resources of cloud, edge and devices are all efectively utilized. In addition, the user's privacy is protected to a certain extent.

## **5.1.3 Peer‑to‑peer co‑inference**

Diferent from Hybrid co-inference, the participants in Peer-to-peer co-inference are all peer nodes. We divide these inference architectures into two categories based on the diference of the participants: Co-inference between devices and Co-inference between processors.



<span id="page-17-0"></span>**Fig. 6** Four types of Hybrid co-inference



<span id="page-17-1"></span>**Fig. 7** Two types of Peer-to-peer co-inference



**(a)** Co-inference between devices **(b)** Co-inference between processors

- (1) Co-inference between devices: as shown in Fig. [7](#page-17-1)a, this inference architecture discards the edge and cloud, which cause computation burden on devices. However, diferent from Device-only architecture, it is not necessary for the devices in this architecture to be powerful, as collaboration between multiple devices can speed up convergence.
- (2) Co-inference between processors: as shown in Fig. [7b](#page-17-1), this inference architecture splits the inference task among diferent processors in the same device, thus reducing the energy consumption caused by long-term use of the GPU.

# **5.2 Model compression**

The improvement of model performance is typically accompanied by the increasing of its depth or width. In the training phase, complicated DNN can be well-trained with the help of data centers. However, it is difficult to deploy such DNN to edge devices. The reason for this comes from two main sources. On the one hand, the model occupies too much storage. On the other hand, model inference consumes too much computation resources. Much of the existing eforts used model compression to reduce storage and accelerate inference. The efforts in this area mainly fall into five categories: model pruning, low-rank approximation, parameter quantization, knowledge distillation, and lightweight model design. We summarize the main eforts of model compression in Table [4.](#page-18-0)

# **5.2.1 Model pruning**

Denil et al. ([2013\)](#page-49-7) demonstrated that most of the parameters in neural networks are redundant. They trained several networks by learning a few weights and predicting the remaining

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<span id="page-20-0"></span>**Fig. 8** Unstructured pruning

ones. The main idea of model pruning is to transform a dense network into a sparse network by removing redundant parameters from the network. According to the type of pruning, the existing eforts can be divided into unstructured pruning and structured pruning. Unstructured pruning can achieve a high compression rate, but it causes irregular memory access. In contrast, structured pruning can avoid this problem. However, it cannot achieve the same compression rate as unstructured pruning. Existing studies focus on structured pruning.

*Unstructured pruning* The earliest work on unstructured pruning can be traced back to LeCun et al. ([1990\)](#page-52-13). They used the Hessian matrix to represent the contribution of parameters and then removed parameters with less contribution. They demonstrated that this pruning method is more accurate than size-based pruning. To simplify the computation, this work assumed the Hessian matrix as a diagonal matrix, which can lead to incorrect weight removal. Diferent from LeCun et al. ([1990\)](#page-52-13), Hassibi et al. ([1993\)](#page-50-13) did not impose any restrictions on the Hessian matrix and achieved better efects. However, these methods are mainly based on second-order derivatives, which can cause expensive computation costs. Therefore, they are not suitable for deep models.

As shown in Fig. [8,](#page-20-0) Han et al. ([2015b](#page-50-7)) proposed a threshold-based weight pruning method. First, the weights of the connections are obtained by training the network. Then connections whose weights are below a certain threshold are removed. Finally, the pruned network is retrained to fne-tune the weights of the remaining connections. This approach achieved  $9\times$  and  $13\times$  pruning rates for AlexNet and VGG-16, respectively, while maintaining accuracy. It is highly efective for network compression. However, it has several drawbacks: (1) it consumes a lot of time and computing resources; (2) the authors assumed that connections with lower weights contribute less, which may lead to important connections being removed; (3) it leads to irregular memory access, so it only compresses the model and does not speed up the inference; (4) they set a threshold for all layers, which may not be optimal for a single layer.

Several works improved the work of Han et al. [\(2015b\)](#page-50-7) from different perspectives. Guo et al. [\(2016](#page-50-8)) proposed dynamic network surgery. This method contains two operations, pruning and splicing. Among them, the splicing is to prevent important connections from being removed. Molchanov et al. ([2016\)](#page-54-5) used a new criterion based on Taylor expansion to measure the importance of connections and showed better performance than other criterias. Han et al.  $(2016)$  $(2016)$  proposed an efficient inference engine (EIE) to accelerate the



<span id="page-21-0"></span>**Fig. 9** Three levels of pruning

inference of compressed models. Manessi et al.  $(2018)$  $(2018)$  jointly optimized thresholds and network weights to achieve a higher compression rate. Moreover, since each layer has a threshold, a single layer can be optimized independently of the other layers. Building on Han et al. ([2015b\)](#page-50-7), Han et al. [\(2015a\)](#page-50-15) further compressed DNN with quantization and huffman coding. The storage required for AlexNet and VGG-16 was reduced by a factor of 35 and 49, respectively. However, hufman coding can cause inconvenience to the inference phase because its decoding process brings additional computation overhead.

The methods mentioned above reduced the model size by pruning unimportant parameters of the model, but the energy consumption in the inference phase was not considered. Yang et al. ([2017\)](#page-56-8) argued that although the model parameters are mainly derived from the fully connected layers, the convolutional layers account for most of the energy consumption. Thus, the amount of weights does not serve as a direct indicator of energy consumption. Therefore, they proposed an energy-aware pruning approach. It can identify the most energy-consuming part of the CNN for energy-saving.

*Structured pruning* The model obtained after structured pruning remains the same in terms of network structure. As a result, it can be perfectly supported by existing DL libraries. According to the pruning level, the existing eforts can be divided into four categories: layer-level pruning, flter-level pruning, kernel-level pruning and intra-kernel pruning. It is worth noting that flter-level pruning and channel pruning are the same concept. The reason for this is that when a flter is pruned, its corresponding channel is also trimmed (as shown in Fig. [9b](#page-21-0)). Since intra-kernel pruning has been less studied, here we mainly present the first three pruning efforts.

- (1) Layer-level pruning Huang et al. [\(2016\)](#page-51-11) proposed stochastic depth. During training, they randomly skipped some layers to shorten the network. The network with deeper layers was used for testing. This simple and efective method signifcantly reduces the training time and testing error. Wen et al. [\(2016](#page-56-10)) proposed a structural sparse learning (SSL) method to regularize the structure of DNN. Specifcally, during training, SSL learns a compact structure directly through Group Lasso, which can reduce the number of layers in the network. Li et al. ([2018a\)](#page-52-3) proposed DeepRebirth. The authors found that layers with few parameters consume a lot of computation time due to memory access speed and other factors. They divided the network layers into tensor and non-tensor layers. By merging the tensor and non-tensor layers, the run-time can be signifcantly decreased. Layer-level pruning does not cause irregular memory access. However, the whole layer is pruned, so layer-level pruning is not fexible. In contrast, flter-level pruning has great fexibility and ease-of-implementation (Liu et al. [2017](#page-53-10)).
- (2) Filter-level pruning The key of flter-level pruning is to measure the importance of flters. Li et al. [\(2016b](#page-52-4)) proposed flter-level pruning. They used the absolute value of weights to measure the importance of flters. Liu et al. [\(2017\)](#page-53-10) used the scaling factor

of the BN layer to evaluate the importance of flters. Specifcally, they performed L1 regularization on the scaling factor and removed channels with a scaling factor of 0 after regularization. Luo et al.  $(2017)$  $(2017)$  presented ThiNet, an efficient and uniform CNN framework. It crops channels by minimizing feature reconstruction errors. Similarly, (He et al. [2017\)](#page-50-16) also performed channel selection by minimizing the feature reconstruction error. The diference is that the former used a greedy algorithm for channel selection, while the latter used Lasso regression for channel selection. Luo et al. ([2017](#page-53-5)) and He et al. ([2017\)](#page-50-16) only considered the importance of neurons in a single layer or two consecutive layers. However, trimming neurons that seem to be unimportant may have an infuence on the response output of subsequent layers. Therefore, the importance of neurons needs to be considered in a holistic manner. Yu et al. [\(2018\)](#page-57-6) demonstrated that greedy layer-by-layer pruning leads to severe reconstruction error propagation. In addition, they proposed the Neuronal Importance Score Propagation (NISP). Specifcally, they defned the second-to-last layer before classifcation as the fnal response layer (FRL) and decided the importance of each feature in the FRL by feature selection. Then, the importance scores were back-propagated to the neurons in all layers. Finally, unimportant neurons were cropped. You et al. [\(2019](#page-57-7)) estimated the importance of the global flters by Taylor expansion. Specifcally, they multiplied the output of the CNN module with the channel scaling factor. When the channel scaling factor is set to 0, the corresponding flter is removed. Taylor expansion was used to estimate the change in the loss function and rank the importance of the global flters. Finally, unimportant flters are removed. All the eforts mentioned above treated pruning and fne-tuning as two independent steps. In contrast, (Luo and Wu [2020](#page-53-6)) combined the two steps and proposed an end-to-end training system, AutoPruner. They proved that the two steps can be mutually benefcial. Specifcally, they took the activation of the previous layer as input, which resulted in a binary code. Then, flters corresponding to the value of 0 in the binary code were removed. Zuo et al. [\(2020](#page-58-0)) found that previous flter-level pruning corrupted the network capacity. To cope with this problem, they proposed a flter-level pruning method with no damage to the network capacity. Specifcally, they pruned redundant flters in the network. Then, new feature maps are generated based on the remaining feature maps to recover the original capabilities.

(3) Kernel-level pruning Filter-level pruning allows pruning the flters and their corresponding channels for compressing and accelerating the CNN. However, it causes dimensionality mismatch in multi-branch networks. As shown in Fig. [10](#page-23-0), in which n denotes the number of flters that are pruned. In addition, the kernels in the flter may still have an important role even if the flter is regarded as redundant. Therefore, flter-level pruning may result in loss of accuracy of the model (Zhu et al. [2021\)](#page-58-1). This was also demonstrated by both (Anwar et al. [2017;](#page-48-9) Zuo et al. [2020\)](#page-58-0). Anwar and Sung ([2016\)](#page-48-10) proposed kernel-level pruning for reducing model complexity. Anwar et al. ([2017\)](#page-48-9) explored flter-level, kernel-level, and intra-kernel pruning. They demonstrated that the expressive ability of the model would be impaired when flter-level pruning is applied in a high ratio. Lin et al. ([2018a\)](#page-52-5) used synaptic strength to measure the importance of connections. The non-important connections are identifed by sparse regularization. Then connections with lower synaptic strength are pruned to generate kernel-level sparse CNN. Li et al. ([2019d\)](#page-52-6) combined kernel-sparsity with entropy to measure the importance of feature maps. In addition, they utilized kernel clustering to reduce the number of kernels. Zhu et al. ([2021\)](#page-58-1) designed a progressive kernel-level pruning. Specifcally, they pruned kernels in both flters and layers, which can avoid the local optimum that occurs when pruning directly in the layers.



<span id="page-23-0"></span>**Fig. 10** The illustration of channel mismatch problem

#### **5.2.2 Low‑rank approximation**

Decomposing a matrix into two smaller matrices can improve computation efficiency. For example, a  $m \times k$ -dimension weight matrix W can be decomposed into  $m \times d$  and  $d \times k$ -dimension matrices. The computation complexity of the matrix W is  $O(m \times k)$ , while the computation complexity of the decomposed matrix is  $O(m \times d + d \times k)$ . The computation can be efectively decreased by controlling the value of d. Tensor can be decomposed if we extend this idea to multi-dimensional space. The existing decomposition methods can be divided into four categories: Tensor Train decomposition, Singular Value Decomposition (SVD), Canonical Polyadic (CP) Decomposition and Tucker decomposition.

A part of the early eforts focused on the decomposition of general DNN. For example, to reduce the parameters in DNN, Sainath et al. [\(2013](#page-54-10)) decomposed the fnal weight layer of DNN with low-rank matrix. Similarly, Xue et al. ([2013](#page-56-11)) used SVD to decompose the weight matrices in DNN for reducing the model size while maintaining accuracy.

With the development of CNN, this method has been widely applied in CNN. Rigamonti et al. ([2013\)](#page-54-11) frst proposed to accelerate convolution operations with low-rank approximation. Denton et al. [\(2014](#page-49-8)) used low-rank approximation and flter clustering to achieve the 2-3x speed-up in the frst convolutional layer. Similarly, Jaderberg et al. [\(2014](#page-51-8)) decomposed the  $w \times h$ -dimension convolutional layer matrix of into  $w \times 1$ -dimension matrix and  $1 \times h$ -dimension matrix. Lebedev et al. ([2014\)](#page-52-7) used CP decomposition to decompose a convolutional layer into multiple layers of low complexity. In contrast to the methods mentioned above, Novikov et al. [\(2015](#page-54-12)) mainly targeted the fully-connected layer. They converted the dense weight matrix in the fully-connected layer to the Tensor Train format. The expressive power of the original network is preserved while reducing the parameters.

Denton et al. ([2014\)](#page-49-8), Jaderberg et al. [\(2014](#page-51-8)) and Lebedev et al. ([2014\)](#page-52-7) only focused on the decomposition of one or several layers. These methods show good acceleration on shallow models, but lack acceleration on deep models. Zhang et al. ([2015\)](#page-57-15) proposed a method to compress and accelerate deep CNN. They considered non-linear units and reduced the error accumulation when using low-rank approximation for multi-layer CNN. This method achieved a 4x full-model speed-up with only a 0.3% increase in top-5 error for the ImageNet classifcation task. Kim et al. ([2015\)](#page-51-9) achieved acceleration of the entire model with Tucker decomposition. First, they used Variational Bayesian Matrix Factorization for rank selection. Then Tucker decomposition was performed on the kernel tensors. Finally, the entire model was fne-tuned. Block term decomposition (BTD) based on low-rank and group sparse was proposed in Wang and Cheng ([2016\)](#page-56-12). For convolutional layers, each kernel tensor is decomposed into the sum of a few low-rank tensors. These low-rank tensors are used to replace the original tensor in the convolutional layer. Astrid and Lee [\(2017](#page-48-11)) successfully applied CP decomposition for full model compression. Due to the instability of CP decomposition, previous methods (Lebedev et al. [2014](#page-52-7)) did not successfully compress the entire CNN with CP decomposition. The authors argued that CP decomposition of the entire CNN would lead to error accumulation. They overcame the instability of CP decomposition by iterative fne-tuning.

Most of the aforementioned efforts focused on accelerating the convolutional layers (Denton et al. [2014](#page-49-8); Jaderberg et al. [2014;](#page-51-8) Lebedev et al. [2014\)](#page-52-7) or compressing the fully connected layers (Novikov et al. [2015](#page-54-12)) instead of aiming for joint optimization. Lin et al. ([2018b\)](#page-52-8) proposed a holistic CNN compression framework which acts on both the convolutional and fully connected layers. Specifcally, they presented a global CNN compression method based on low-rank decomposition and knowledge transfer. They used a low-rank decomposition with a closed-form solver to accelerate the convolution computation and reduce the memory overhead.

To further compress the network, Swaminathan et al. ([2020\)](#page-55-12) introduced pruning techniques. They considered the importance of neurons in the low-rank decomposition and achieved better compression rate by keeping the unimportant neurons at low-rank. The above decomposition methods were trained in a decomposition manner. To further accelerate inference, Lee et al. ([2021\)](#page-52-9) proposed an alternating tensor compose-decompose (ATCD) method for training low-rank CNN. For example, for a rank-1 CNN, in the training phase, the 3-D flters of rank-1 CNN are decomposed into one-dimension vectors to form 3-D flters again. In the test phase, the 3-D flters are permanently decomposed into onedimension vectors. This allows the rank-1 CNN to maintain its original accuracy, but with the same inference speed as the one-dimension vector.

#### **5.2.3 Parameter quantization**

Parameter quantization is an efective method for network compression and acceleration and is widely used in the feld of image compression. According to the nature of these works, we divide these eforts into two main parts: vector quantization and low-bit quantization.

*Vector quantization* The main idea of vector quantization is to divide the weights into groups and each group shares the same weight. Gong et al. ([2014](#page-50-9)) used vector quantization for network compression. They focused on operating on the fully connected layers. Therefore, this work does not involve inference acceleration. The authors found that 8-16 compression ratio can be achieved by simply using the K-means algorithm for quantization with no more than 0.5% loss in top-5 accuracy. Chen et al. ([2015b](#page-49-9)) proposed HashNets. First, they used a hash function to classify the connection weights. Then, connections that were grouped into the same hash bucket shared a common weight. Chen et al. [\(2016\)](#page-49-12) further extended this idea. They transformed the flter weights to the frequency domain. Then a hash function was used to group the parameters in the frequency domain.

The efforts mentioned above compressed the model without accelerating the inference. Wu et al.  $(2016)$  proposed a product quantization-based approach to simultaneously accelerate and compress CNN. They quantifed both convolutional layers and fully connected layers by minimizing the response error.

*Low-bit quantization* In general, the parameters of neural networks are represented by 32-bit foating-point numbers. In fact, it is not necessary to maintain such a high accuracy. Quantifcation can reduce the storage at the expense of accuracy. Therefore, the model size can be reduced while maintaining accuracy by rationally quantizing the network.

In the inference phase, foating-point multiplication consumes a lot of time and computation resources, while fixed-point quantization can effectively alleviate this problem. There are two main reasons for this. First, fxed-point computation is typically faster than foating-point and consumes much less energy and hardware resources. In addition, lower precision data representation reduces memory footprint, allowing larger models to be deployed on resource-constrained devices.

Holi and Hwang ([1993\)](#page-50-17) frst provided a theoretical analysis of the error caused by the fnite precision computation. They demonstrated that 8–16 bits of quantization is sufficient to train a small neural network. However, Chen et al.  $(2014)$  $(2014)$  $(2014)$  found that although 16-bit fxed point format can meet the need for inference to a great extent, it may lead to a decrease in accuracy or even prevent the model from converging. In a further step, Gupta et al. ([2015](#page-50-18)) presented stochastic rounding, which allows training DNN using 16-bit fxed point with almost no degradation in classifcation accuracy.

Binary quantization, as a special case of low-bit quantization, can theoretically achieve 32× compression rate. By limiting the weights to two values (e.g., 1 or  $-1$ ), the multiplication operations can be replaced with additive operations. Since the multiplier is the most computationally resource-intensive component, binarization operation can bring signifcant benefts to the hardware used for DL (Courbariaux et al. [2015\)](#page-49-10). Soudry et al. ([2014](#page-55-13)) proposed the expectation back propagation (EBP) algorithm. They binarized the network by Variational Bayesian and proved the high performance of the binary network trained with EBP. Courbariaux et al. ([2015](#page-49-10)) presented BinaryConnect. They constrained the weights to 1 or  $-1$ , which eliminates the multiplication operation in the forward propagation and thus accelerates the training process. However, experi-ments in Rastegari et al. ([2016](#page-54-7)) showed that this method cannot be applied to largescale datasets.

If only the weights are quantifed, time-consuming arithmetic operations are still required. Courbariaux et al. [\(2016](#page-49-11)) further extended the work of Courbariaux et al. [\(2015](#page-49-10)). They quantifed both activations and weights. In this case, bit-wise operations can be used in forward-propagation, thus further accelerating the inference. A similar approach was adopted by Rastegari et al. ([2016\)](#page-54-7). In Courbariaux et al. ([2016\)](#page-49-11) and Rastegari et al. ([2016\)](#page-54-7), although the weights are binary, the gradients are full precision. Therefore, bit-wise

operations cannot be used in the back-propagation. This means that during the training process, Courbariaux et al. [\(2016](#page-49-11)) and Rastegari et al. ([2016\)](#page-54-7) would spend a lot of time in the back-propagation. To further speed up the inference, the gradients were also quantifed in Zhou et al. [\(2016](#page-57-8)). Specifcally, the gradients were randomly quantifed to low-bit during back-propagation. Since the weights, activations, and gradients during forward-propagation and back-propagation are all low-bit, bit-wise operations can be used during the whole training process.

Although the network after low-bit quantization can lead to high compression rate, this method leads to a decrease in accuracy. For example, the test results of binarized neural network (Courbariaux et al. [2016](#page-49-11)) on the ImageNet dataset showed that the diference in top-1 accuracy between real-valued ResNet-18 and binary ResNet-18 is a staggering 28%. Therefore, many existing works have focused on how to maintain model accuracy after low-bit quantization. Li et al. [\(2016a\)](#page-52-10) proposed ternary weight networks (TWN). They restricted the weights to 1, 0, or  $-1$  thus achieving a trade-off between accuracy and compression ratio. Zhou et al. [\(2017](#page-57-9)) presented incremental network quantization (INQ), which can convert a pre-trained full-precision network into a lossless low-precision network. INQ contains three processes: weight partition, group-wise quantization, and retraining. And it can achieve fve-bit lossless quantization. Mishra et al. ([2017\)](#page-54-6) compensated the accuracy degradation caused by low-bit quantization through increasing the width of the network. Lin et al. [\(2017a](#page-52-11)) proposed ABC-Net. They used a linear combination of multiple binary weights to approximate full precision weights. Kim et al. [\(2018](#page-51-10)) combined low-bit quantization and knowledge distillation. They proved that knowledge distillation can signifcantly improve the accuracy of low-bit networks. Hu et al. [\(2018b\)](#page-51-12) presented a hash-based training method for binary networks. They revealed the tight connection between inner product hashing and binary networks. In addition, the authors demonstrated that the network parameter binarization problem can be converted into a hash problem. This method can efectively reduce the accuracy loss.

Rastegari et al. ([2016\)](#page-54-7) proposed the binary weight network (BWN) and XNOR-Net. They introduced a real-valued scaling factor to adjust the output of the binary convolution, which can compensate the error caused by the binary quantization to some extent. On the foundation of XNOR-Net, Bulat and Tzimiropoulos [\(2019](#page-48-12)) proposed XNOR-Net++. They fused the activation and weight scale factor into a single factor, which is learned by back-propagation. Compared with XNOR-Net, the accuracy of XNOR-Net++ is improved by 6%. Similar to Bulat and Tzimiropoulos [\(2019](#page-48-12)), Rastegari et al. [\(2016](#page-54-7)) and Martinez et al. [\(2020](#page-53-7)) improved the performance of the network by rescaling the output of the binary convolution. Specifcally, before binarization, they used the real-valued activation of the binary network to compute a scaling factor. After applying the binary convolution, this scaling factor was utilized to adjust the activations. Compared to XNORNet++, this method improves 8.3% accuracy on Top-1 error.

#### **5.2.4 Knowledge distillation**

The core idea of knowledge distillation is to train a lightweight neural network with the help of a large neural network. The large neural network is called the teacher network, and the lightweight neural network is called the student network. Knowledge distillation is a learning process for the student network. What knowledge to learn and how to learn the knowledge are two cores of knowledge distillation. According to the number of



<span id="page-27-0"></span>**Fig. 11** Knowledge distillation

participants in the knowledge distillation, we classify these eforts into three categories: three-paticipant distillation, two-paticipant distillation and self-distillation.

Long before the concept of knowledge distillation was introduced, Buciluǎ et al. [\(2006](#page-48-13)) attempted to have a small model learn the representation of a large model. Specifcally, they used a data generation algorithm to obtain unlabeled data and labeled them with a well-trained teacher network. These data contain the knowledge of the teacher network. The student network trained with these data performs better than the one trained with original data. However, this approach is not suitable for deep models. Ba and Caruana [\(2013](#page-48-14)) extended this work to a shallow and wide student network. In which, the output of the teacher network was used to train the student network. The trained student network has similar performance to the teacher network. Hinton et al. [\(2015](#page-50-10)) frst proposed knowledge distillation. As shown in Fig. [11,](#page-27-0) they obtained soft labels utilizing a well-trained teacher network. Then, both soft and true labels are used to train a small network. Sau and Balasubramanian ([2016\)](#page-54-13) argued that the knowledge from a single teacher may be restricted. Building on this idea, they proposed a noise-based regularization method to mimic the guidance of multiple teachers.

Besides mimicking the output of teachers, some efforts suggest learning the hidden features of the teacher network. For example, Romero et al. ([2014\)](#page-54-8) presented FitNets. They used intermediate representations of the teacher network to train the student network. Yim et al. [\(2017](#page-57-16)) argued that simply learning the intermediate representations of the teacher network may constrain the performance of the student network. Therefore, they defned distillation knowledge as a feature relationship between layers and utilized the fow of solution procedure (FSP) matrix to represent it. Zagoruyko and Komodakis [\(2016a](#page-57-10)) introduced attention mechanisms to knowledge distillation. The attention map of the teacher network is utilized to train the student network, which signifcantly improved the performance of the student network. Kim et al. ([2018\)](#page-51-10) suggested learning the interpreted information of the teacher network. They used two convolutional modules as paraphraser and translator. The paraphraser extracted the features of the teacher network as teacher factors and the translator extracted the features of the student network as student factors. The translator of the student network can help the student understand the teacher factors of the teacher network.

Considering the relevance of the generalization performance of a classifer to its decision boundary, Heo et al. [\(2019](#page-50-11)) defned distillation knowledge as the decision boundary. Specifcally, they used self-adversarial techniques to move the original samples near the decision boundary. The moved samples are called boundary support samples (BSS), which contain information about the decision boundary. Training the student network with BSS allows the student network to better learn the decision boundary of the teacher network.

All the eforts mentioned above required two steps to obtain a lightweight student model: (1) training a complex teacher network. (2) using the teacher network to train the student network. This process is time-consuming. Besides that, it is difficult to find a suitable teacher. For the former, several efforts proposed to train the teacher network and the student network together. For example, Mishra and Marr [\(2017](#page-54-14)) presented a scheme to jointly train the teacher network and the student network. Similarly, Zhou et al. [\(2018](#page-57-11)) argued that the knowledge of the teacher network exists not only in the fnal output, but also in the training process of the entire network. Based on this idea, they presented Rocket Launcing, which consists of a light net and a booster net. Specifcally, in the training phase, the light net and the booster net are trained on the same task. Meanwhile, the light net continuously learns the knowledge of the booster net, thus improving the performance of the light net. Zhang et al. ([2019\)](#page-57-13) proposed self-distillation. Specifcally, they added several branches to the network, which were trained with the help of the main network.

Although (Mishra and Marr [2017](#page-54-14); Zhang et al. [2019](#page-57-13); Zhou et al. [2018](#page-57-11)) improved the time-consuming process of knowledge distillation, they did not address the impact of the teacher-student performance gap on training student networks. To solve this problem, Mirzadeh et al. ([2020\)](#page-54-9) introduced a teaching assistant network. Their experiments showed that the performance of the student network would decrease if the gap between teacher and student is relatively large. To bridge the gap between the student network and the teacher network, they introduced the intermediate model (i.e., the teaching assistant network). Unlike the work of Mirzadeh et al. [2020](#page-54-9), Zhang et al. [2018c](#page-57-12) improved network performance through mutual learning of student networks. This work found that a powerful teacher network is not essential and that the student network obtained by mutual learning performs better than the student network guided by the teacher network. With this approach, student networks can be trained well while eliminating the impact of performance gaps between networks on training.

#### **5.2.5 Lightweight model design**

Much of the existing works improved the performance of the network by increasing the width and depth of the network (Szegedy et al. [2015](#page-55-4); He et al. [2016\)](#page-50-19), which makes it diffcult to deploy the model on resource-constrained devices. To enable the model to be deployed on edge devices, an increasing amount of works has focused on designing efficient and lightweight network model.  $1 \times 1$  convolution, group convolution and depth-wise separable convolution have been widely used in lightweight model design because they can efectively reduce foating-point operations per second (FLOPs).

Lin et al. ([2013\)](#page-52-12) proposed the Network-In-Network (NIN). To reduce the computation complexity, they suggested using  $1 \times 1$  convolution and replacing the fully connected layers with global average pooling layers. GoogleNet Szegedy et al. ([2015\)](#page-55-4) and ResNet He et al. ([2016\)](#page-50-19) both adopt these ideas. In addition, the earliest lightweight model, SqueezeNet Ian-dola et al. ([2016\)](#page-51-13), also made extensive use of  $1 \times 1$  convolution instead of  $3 \times 3$  convolution.

Howard et al. ([2017\)](#page-50-12) proposed depth-wise separable convolution and MobileNetV1. Depth-wise separable convolution is composed of depth-wise convolution and pointwise convolution. The depth-wise convolution convolves each channel of the input image separately, and the point-wise convolution uses  $1 \times 1$  convolution kernels to convolve the feature maps generated by the depth-wise convolution. Compared with common convolution, depth-wise separable convolution reduces the parameters by a factor of 9. Guo et al. ([2018a](#page-50-20)) analyzed the mathematical relationship between common convolution and depthwise separable convolution. They proved theoretically that common convolution and depth-wise separable convolution are similar in effect. Likewise, Xception (Chollet [2017\)](#page-49-14) also used depth-separable convolution. However, this work aimed at improving network performance rather than reducing parameters. On the basis of MobileNetV1, Sandler et al. [\(2018](#page-54-15)) proposed MobileNetV2. They introduced an inverted residual structure with a linear bottleneck. Compared with MobileNetV1, the accuracy is improved and the model size is further reduced. In a further step, Howard et al. ([2019\)](#page-50-21) proposed MobileNetV3. They improved blocks by updating the activation function and introducing the Squeeze-and-Excite module (Hu et al. [2018a](#page-51-14)). MobileNetV3-Large improves the accuracy by 3.2% over MobileNetV2 in the ImageNet classifcation task, while reducing the 20% latency.

Group convolution is a method between common convolution and depth-wise separable convolution. Given a group number n, it can reduce the number of parameters by a factor of n. The earliest use of group convolution can be traced back to AlexNet (Krizhevsky et al. [2012\)](#page-51-0). Xie et al. [\(2017](#page-56-13)) also used group convolution and proposed ResNeXt. However, the aim of ResNeXt is to improve performance instead of reducing parameters. Although  $1 \times 1$  convolution has been proven its ability to reduce model complexity by a large number of works (Szegedy et al. ([2015\)](#page-55-4), He et al. ([2016\)](#page-50-19), Iandola et al. ([2016\)](#page-51-13), Lin et al.  $(2013)$  $(2013)$ ), Zhang et al.  $(2018b)$  argued that dense  $1 \times 1$  convolution can degrade the performance of the model. Based on this idea, they used group convolution to reduce the complexity of  $1 \times 1$  convolution. In addition, the authors proposed channel shuffle to fuse the feature information of diferent groups. The works mentioned above mainly designed lightweight models by reducing FLOPs. However, Ma et al. ([2018\)](#page-53-8) pointed out that direct metrics (e.g., speed) rather than indirect metrics (e.g., FLOPs) should be considered when designing network architectures. Based on this, the authors proposed four guidelines for designing network architectures and a new network, ShuffleNetV2.

Previous works improved the performance of the model by increasing the depth (He et al. [2016](#page-50-19)) and width (Zagoruyko and Komodakis [2016b](#page-57-17)) of the network. Tan and Le ([2019\)](#page-55-14) investigated the efects of width, depth, and image resolution on the network and presented EfficientNet, where EfficientNet-B7 is 8.4× smaller and 6.1× faster compared to the model with the highest accuracy back then. However, the increase in image resolution leads to signifcant memory usage. To address this problem, Tan and Le ([2021\)](#page-55-15) proposed a progressive learning strategy that adjusts the regularization factor according to the image size.

Most of the networks mentioned above are designed by humans. In addition, there are some works that used Neural Network Architecture Search (NAS) to build networks. For example, EfficientNet-B0 in Tan and Le  $(2019)$  $(2019)$  is designed through NAS. Since NAS consumes a lot of resources and these works are not oriented towards resource-constrained edge devices, we do not present it here. For readers who are interested in NAS, you can view (Elsken et al. [2019](#page-49-15); Wistuba et al. [2019](#page-56-14)).

#### **5.3 Inference acceleration**

Due to the limited energy of edge devices, it is important to accelerate inference thus reducing latency and lowering energy consumption. According to the diference of these efforts, we divide them into three categories: model partition, edge caching, and conditional computation. Among them, the conditional computation can be further divided into model early exit, input fltering, and model selection. We summarize the works on inference acceleration in Table [5.](#page-31-0)

#### **5.3.1 Model partition**

The general DNN services are provided through the cloud, which can result in high bandwidth usage and privacy concerns. Kang et al. [\(2017b\)](#page-51-15) evaluated DNN services on both cloud and edge devices. They found that uploading data to the cloud is the largest bottleneck for DNN services due to the huge data transfer load. Although edge computing can reduce transmission overhead, compute-intensive tasks are difficult to be deployed on resource-constrained devices. By partitioning DNN between cloud, edge and devices, the requirements of DNN services in terms of energy consumption, latency and throughput can be met. According to the diference of DNN partition, we classify these works into the following two categories: (1) hybrid partition and (2) peer-to-peer partition.

*Hybrid partition* As shown in Fig. [12](#page-33-0), Hybrid partition consists of partition between device-edge, partition between device-cloud and partition between device-edge-cloud. A key point of these eforts is how to determine the optimal partition point. Kang et al. ([2017b\)](#page-51-15) proposed Neurosurgeon, which is capable of predicting the latency and energy consumption of each DNN layer without performing DNN. Based on these predictions, the current bandwidth and data center load level, an optimal DNN partition point that meets the energy consumption and latency requirements can be found. Ko et al. ([2018\)](#page-51-16) partitioned the DNN at the end of the convolutional layers. Moreover, they reduced the energy consumption of the edge and the bandwidth consumption during transmission by performing lossy encoding on the feature map. Finally, they retrained DNN on the host after DNN partition to mitigate the accuracy degradation caused by lossy coding. Similarly, Li et al. ([2018b\)](#page-52-14) also compressed the feature map before it is transferred to the cloud. Moreover, for diferent models and bandwidth situations, they found a correct splitting point using integer linear programming.

Besides fnding the optimal DNN partition point, there are many challenges for DNN partition. For example, the instability of the network, the scheduling problem when multiple devices request DNN services, and the impact of user mobility on DNN services. Several works optimized model partition with these perspectives. Considering the real-world network environments, Li et al. [\(2019b\)](#page-52-15) designed two strategies for static and dynamic environments. Moreover, to further improve the performance, they combined model partition with model-early-exit and proposed Edgent, which can maximize the inference accuracy while satisfying the latency requirement. DeePar (Huang et al. [2019\)](#page-51-2) divided DNN between cloud, edge and devices. They considered not only the DNN partition optimization problem, but also the multi-task scheduling problem. To reduce the delay caused by the mobility of users to the DNN partition, Tian et al.  $(2021)$  $(2021)$  proposed a mobility-included DNN partition offloading algorithm (MDPO). It is worth noting that MDPO is applicable not only to DNN with chain topology [e.g., AlexNet (Krizhevsky et al. [2012\)](#page-51-0)], but also to DNN with directed acyclic graph topology [e.g., ResNet (He et al. [2016](#page-50-19))].

<span id="page-31-0"></span>





<span id="page-33-0"></span>**Fig. 12** Five types of DNN partition

*Peer-to-peer partition* As shown in Fig. [12](#page-33-0), Peer-to-peer partition consists of partition between devices and between processors. It discards the edge and cloud, partitioning DNN between devices or between processors. For example, Mao et al. [\(2017a\)](#page-53-11) partitioned the trained DNN model onto multiple devices to accelerate inference. In the computing cluster, the device carrying the test data is the group owner and the other devices act as working nodes. Moreover, they proposed two partition schemes according to the diference of convolutional layers and fully-connected layers. Mao et al. [\(2017b\)](#page-53-12) proposed greedy two dimensional Partition (GTDP), which can adaptively partition a DNN to diferent devices according to the resources of mobile devices. In addition, they presented structured model compact deployment (SMCD), which utilizes structured sparse pruning to accelerate DNN execution. Zhou et al.  $(2019a)$  traded off the computation and communication capabilities of devices and designed a dynamic programming algorithm to fnd the optimal partition policy under the criterion of minimizing the system response time.

Zhao et al. [\(2018b](#page-57-19)) proposed fusion tile partitioning (FTP) to divide CNN into independent tasks. Additionally, they considered the allocation of FTP in dynamic scenarios and presented a new scheduling scheme to maximize the reuse of overlapping data between adjacent FTP partitions. However, they did not take into account weight partitioning. This is an important factor because it is not possible to store a large amount of weight data on a single resource-constrained edge device. On the basis of Zhao et al. ([2018b\)](#page-57-19) and Stahl et al. ([2021\)](#page-55-17) considered weight partitioning. In addition, they proposed an integer linear programming (ILP) approach to fnd the optimal CNN partition thus minimizing the communication requirements.

Unlike the above works, Lane et al.  $(2016)$  $(2016)$  partitioned and assigned the network to local heterogeneous processors. They proposed two strategies: runtime layer compression (RLC) and deep architecture decomposition (DAD). First, they compressed the DNN with SVD. Then, the compressed model was partitioned and assigned to specifc processors for computing.



<span id="page-34-0"></span>**Fig. 13** The illustration of computation results cache

## **5.3.2 Edge caching**

There are two types of redundancy in edge environments (Xu et al. [2020\)](#page-56-0): data redundancy and computation redundancy. Data redundancy is caused by users repeatedly accessing data with high prevalence, while computation redundancy is caused by users requesting the same service. Redundancy can be mitigated by caching data or computation results in edge servers. There exists a large amount of research in edge caching, including cache deployment, cache replacement, etc. Here, we focus on the application of edge caching in edge intelligence. According to the nature of the works, we classify these efforts into three categories: (1) data cache, (2) computation results cache, and (3) research on caching mechanisms.

*Data cache* A large amount of research in edge caching has been focusing on real-time mobile vision. These works usually utilize DNN to accurately detect the object in the initial frame. Then the tracking algorithm is used to track subsequent frames that are cached locally. Glimpse Chen et al. ([2015a\)](#page-49-16) first applied the cache techniques to DNN inference. Glimpse selectively sends some key frames to the server, which marks the objects in the key frames. The local device caches a portion of the subsequent frames. Then the target objects in the subsequent frames are tracked based on the results returned by the server. To enable fast detecting targets to maintain tracking accuracy, (Liu et al. [2019](#page-53-13)) designed a dynamic regions of interest (ROI) encoding technique and a parallel stream and inference method to reduce transmission delay. In addition, they proposed a moving vector-based target tracking technique.

All the efforts mentioned above offload some tasks to the cloud or edge servers. However, this is accompanied by the transmission delay. Diferent from them, MARLIN (Api-charttrisorn et al. [2019\)](#page-48-15) does not offload tasks to the cloud or edge. It contains two parts: a target detector and a target tracker. First, MARLIN determines the location of targets based on the initial input frame. Then subsequent frames are cached in the target tracker and the targets are continuously tracked in the subsequent frames. DNN is re-triggered when the targets in the frames change signifcantly. However, in MARLIN, the target detector and target tracker run sequentially, which is not suitable for scenarios where the video content changes frequently. Diferent from MARLIN, AdaVP (Liu et al. [2020](#page-53-14)) runs the target detector and the target tracker in parallel. Among them, the target detector corrects the error of the target tracker. They designed two models as target detectors and dynamically selected a model for tracking based on the environment.

*Computation results cache* Since there are limited targets in the same environment, the images captured by mobile devices are often very similar. The core idea of computationresults-cache is that image features are stored at the local edge server. Therefore, when the local device requests the DNN service, the server only needs to match the image features and return results (Fig. [13](#page-34-0)).



<span id="page-35-0"></span>**Fig. 14** The illustration of model early exit

Drolia et al. [\(2017b\)](#page-49-17) cached the computation results at the edge server. As shown in Fig. [13](#page-34-0), when a user requests DNN services, the edge device uploads the captured images to the edge server. Then, the server extracts the image features and fnds the best match in the cache. For further accelerating inference, Drolia et al.  $(2017a)$  $(2017a)$  suggested utilizing the available resources of the device to reduce the communication latency. Specifcally, they used a Markov model to predict the user's next request. The computation results are stored on the device in advance. In addition, they could dynamically adjust the parameters and cache contents based on the results predicted by the Markov model.

*Research on caching mechanisms* To reuse computation results more efficiently, a portion of the research focused on designing schemes and mechanisms for edge caching. To enable reusing computation results according to the similarity of the input information, Guo et al. [\(2018b](#page-50-22)) proposed two schemes: adaptive locally sensitive hashing (A-LSH) and homogenized kNN (H-kNN). The former enables fast and constant lookup, and the latter guarantees highly accurate computation reuse. Xu et al. [\(2018](#page-56-18)) proposed a new caching mechanism. They used the input video frames as keys and the reusable regions in feature maps as values. During the CNN runtime, the cached reusable regions are used to replace the CNN computation.

#### **5.3.3 Conditional computation**

The main idea of conditional computation is to selectively perform DNN inference for reducing computation. We divide this part of the works into three parts: model early exit, input fltering, and model selection.

*Model early exit* The key to model-early-exit is determining when the sample exits from the network. The concept of model-early-exit was frst introduced in Teerapittayanon et al. ([2016\)](#page-55-18). As shown in Fig. [14](#page-35-0), they added several branches at specifc layers. When the confdence of the inferred result is high enough, the sample is allowed to exit from the network early through the branches. They used the entropy of the classifcation result to determine whether the sample can exit from the network. When the entropy is less than a pre-defned threshold, it means that the inferred result is trustworthy. At this point, the sample exits from the network. Diferent from the work of Teerapittayanon et al. ([2016\)](#page-55-18), Panda et al. ([2017\)](#page-54-16) compared the maximum confdence of the prediction with the threshold. If the confdence is greater than the threshold, the prediction is considered as reliable. Similarly, Laskaridis et al. ([2020\)](#page-52-16) also used the comparison of confidence and the threshold for determining if the sample should exit from the network. The authors combined model-early-exit



<span id="page-36-0"></span>**Fig. 15** The workfow of Noscope

with model partition. They set up an analyzer and a dynamic scheduler to fnd the optimal partition points and exit points. First, the analyzer analyzes the metrics of the model. Then the dynamic scheduler selects an optimal result based on the current situation. Finally, the results that prioritize satisfying the most important optimization objective are found according to the importance of the objectives.

The thresholds in the works mentioned above are user-defned, while (Lo et al. [2017](#page-53-15)) defned thresholds using a statistical method. They added an Authentic Operation (AO) unit to the network. The AO unit sets diferent thresholds for diferent DNN output classes. These thresholds determine whether the samples need to be transferred to the server for further inference. Moreover, they set up corresponding strategies according to the stability of the channel. When the channel is unstable, the input samples are traversed across the entire network at the edge for maintaining accuracy. Otherwise, more workload is transferred to the main network at the cloud.

Laskaridis et al. [\(2020](#page-52-16)), Lo et al. ([2017\)](#page-53-15), Panda et al. ([2017\)](#page-54-16) and Teerapittayanon et al. ([2016\)](#page-55-18) determined whether the current sample needs to be further inferred by confdence and threshold. Unlike the above works, Bolukbasi et al. ([2017\)](#page-48-16) introduced a decision function for determining when the current sample exits from the network. Moreover, they introduced model selection to make trade-offs in terms of inference time and accuracy.

Although DNN model with multiple exit points can dynamically select the best exit point for each sample, each exit point incurs additional computation complexity. Lee et al. ([2020\)](#page-52-18) investigated how the number of exit points afects model performance. They found that when using models with a single exit point, higher accuracy can be obtained with fewer calculations.

*Input filtering* Similar to the model-early-exit, input filtering speeds up inference by selectively processing data. According to the diference of these works, we divide the efforts into two categories:  $(1)$  Filtering of video data;  $(2)$  Filtering in edge offloading.

(1) Filtering of video data As shown in Fig.[15,](#page-36-0) Noscope (Kang et al. [2017a](#page-51-18)) contains a specialized DNN and a diference detector. The specialized DNN abandons the generalization capability and performs inference only in the specifed task. The diference detector checks the diferences between frames. Frames with little or no change are skipped to speed up inference. Hsieh et al. [\(2018\)](#page-51-19) proposed a two-stage fltering system, Focus. In the input stage, Focus uses a lightweight CNN to classify the objects.

Then, similar objects are clustered and the top-k results are used to index the cluster centers. In the query phase, Focus uses a complicated CNN for further inference and return frames containing the query target. Zhang et al. [\(2018a](#page-57-22)) and Zhang et al. [\(2020](#page-57-21)) proposed a three-stage fltering system, FFS-VA. It contains three models: a streamspecialized diference detector (SDD), a stream-specialized network model (SNM) and a Tiny-YOLO-Voc model. Among them, the SDD is used to remove frames that only contain background. The SNM is used to identify frames that contain target objects. The Tiny-YOLO-Voc is used to flter frames where the confdence of target objects is less than a pre-defned threshold. The above methods only fltered the input of a single video. Unlike the above methods, Jain et al. [\(2020\)](#page-51-20) and Jain et al. [\(2018](#page-51-21)) built a crosscamera spatio-temporal correlation model from unlabeled video data. In the inference phase, they used this model to flter frames that have no spatio-temporal correlation with the current location of the query entity, thus reducing inference.

 $(2)$  Filtering in edge offloading Besides the above efforts, some works applied input filtering in edge environments to determine which data needs to be offloaded. Wang et al. ([2018a](#page-56-15)) used MobileNet and SVM to flter irrelevant frames on Unmanned Aerial Vehicles, thus saving bandwidth when offloading. However, they did not optimize for the multi-tenant problem. In a further step, FilterForward (Canel et al. [2019\)](#page-48-17) set up micro-classifers (MCs) at the edge to flter irrelevant frames. Moreover, FilterForward can run tens of micro-classifers simultaneously, which compensates for the shortcoming of Wang et al. [\(2018a\)](#page-56-15). The works mentioned above performs fltering at the edge. Pakha et al. [\(2018\)](#page-54-17) allowed the server to decide which frames to be fltered. In this work, the client sends images with diferent resolution to the server, which processes the images and returns the results to the client. Specifcally, the client sends a lowresolution frame to the server. If the target object is not detected in the low-resolution frame, the frame is fltered. On the contrary, if the target object is detected with a certain degree of confdence, the regions of interest are cropped and sent to the server at a high resolution.

*Model selection* The main idea of model selection is to adaptively select the most suitable DNN for inference tasks according to factors such as inference difficulty, accuracy requirements and inference time. According to the nature of these eforts, we divide the eforts in this area into two categories: (1) Selection of independent model; (2) Selection of partial model.

(1) Selection of independent model Park et al. ([2015](#page-54-18)) is the earliest work on model selection. For a given sample, they frst performed inference with a light-weight DNN. When the confdence is lower than a pre-defned threshold, a complex DNN is used for inference. Stamoulis et al. [\(2019](#page-55-19)) treated model selection as a hyperparameter optimization problem under hardware constraints and solved it with Bayesian optimization. Marco et al. ([2019](#page-53-16)) and Taylor et al. ([2018](#page-55-20)) trained a predictor with machine learning. For given input samples, it is able to dynamically select the optimal DNN according to the desired accuracy and inference time. MobiSR Lee et al. ([2019](#page-52-19)) generates a lighter super-resolution (SR) model based on the original SR model with model compression. Then, an appropriate model is selected according to the difficulty of upgrading the low-resolution input image and the load of the computing engine. Yang et al. [\(2020b](#page-56-16)) proposed RANet, an adaptive network, which contains subnets with diferent input resolution. The samples are identifed starting from the smallest subnet and can be

exited early if the confdence of the sample is greater than the threshold. Otherwise, the next-level subnet fuse the features of the current subnet and continue to identify samples.

(2) Selection of partial model All of the works mentioned above select independent model for inference. Although this approach can speed up inference, the total size of the models and the training time also increase signifcantly. To alleviate this problem, Tann et al. ([2016](#page-55-21)) used incremental learning to train the network and dynamically adjusted the channels in the network according to multiple factors such as response time, power, and accuracy targets. They devised a method to determine the appropriate network width and dynamically adjust the network when inference errors occur. Unlike the above works, only the weights of a complete network need to be stored in this work. Similarly, Panda et al. ([2016](#page-54-19)) constructed a tree of classifers, where the shallow classifcation network classifes the common features and selectively activates subsequent networks based on the features of the samples. Wang et al. ([2018b\)](#page-56-17) proposed SkipNet. They added gating modules to the network, which maps the activation of the previous layer as a binary to decide whether to skip the next layer.

## **5.3.4 Open‑source frameworks for inference accleration**

- (1) *TensorRT* TensorRT<sup>[5](#page-38-0)</sup> is a high-performance inference framework proposed by NVIDIA that enables well-trained DL models to be deployed on NVIDIA GPUs with low latency and high throughput. Tensor RT is a  $C++$  library that provides  $C++$  API and Python API. In addition, it supports mainstream DL frameworks such as Cafe, Tensorfow, Pytorch, MxNet, etc. It is necessary to obtain a runtime engine with TensorRT in order to deploy a trained model. It is worth noting that for network models built with Cafe and Tensorfow, TensorRT maps the layers in them and optimizes them for NVIDIA's GPUs. However, models trained with other frameworks have to be frst converted to a generic ONNX model and then parsed by TensorRT. ONNX is a fle format designed for machine learning to store trained models. It enables diferent DL frameworks to store models in the same format.
- (2) *Tensorflow Lite* Tensorflow Lite<sup>[6](#page-38-1)</sup> is a lightweight solution for embedded and mobile devices proposed by Google. With Tensorfow Lite, developers can run Tensorfow models on embedded, mobile and IoT devices. Tensorfow Lite is composed of two main components: (1) Tensorfow Lite interpreter: it allows optimized models to be run on diferent types of hardware such as cell phones and embedded devices. (2) Tensorfow Lite Converter: it reduces binary fle size and improves performance, thus optimizing the model for the interpreter. In addition to model-specifc optimizations, Tensorfow Lite supports the Andriod neural network API to take full advantage of available hardware accelerators of mobile devices. When the hardware accelerator is unavailable, Tensorfow Lite uses the CPU for computation, ensuring that the model can still be run on a large number of devices.
- (3) *Paddle Lite* Paddle Lite<sup>7</sup> is a high-performance, lightweight, flexible and easy-to-extend DL framework that supports multiple hardware platforms including mobile, embedded and server-side. It is proposed by Baidu and provides three APIs including C++, Java,

<span id="page-38-0"></span><sup>5</sup> [https://github.com/NVIDIA/TensorRT.](https://github.com/NVIDIA/TensorRT)

<span id="page-38-1"></span><sup>6</sup> [https://github.com/tensorfow/tfite-micro](https://github.com/tensorflow/tflite-micro).

<span id="page-38-2"></span><sup>7</sup> <https://github.com/PaddlePaddle/Paddle-Lite>.

and Python. Paddle Lite optimizes machine learning on mobile devices, compressing model and binary fle sizes and accelerating inference. It is worth noting that Paddle Lite directly supports the model fle format saved by PaddlePaddle. For models built with other DL frameworks (e.g. Tensorflow, Pytorch), X2Paddle<sup>[8](#page-39-1)</sup> needs to be used to convert the model fles to PaddlePaddle format. Paddle Lite supports multiple types of computational graph optimization including operator fusion, computation pruning, memory optimization, and quantization computation. In addition, it also supports hybrid scheduling between diferent hardware, which can make full use of various hardware resources.

# <span id="page-39-0"></span>**6 AI appllications on the edge**

In the above sections, we have introduced the techniques, architectures and open-source frameworks of edge training and edge inference. In this section, we introduce the eforts of edge training and edge inference from an application perspective. We summarize these efforts in Table [6.](#page-40-0)

# **6.1 Training models on the edge**

## **6.1.1 FL for healthcare**

The development of DL has brought revolutionary changes in disease diagnosis. However, a large amount of data is necessary for getting a well-trained DL model. However, it is diffcult to share data among various medical institutions due to privacy issues. FL can get a well-trained model while protecting patients' privacy. Thus, it can facilitate the development of AI in the medical feld. For example, with the help of FL, Sheller et al. [\(2018](#page-55-22)) trained a brain tumor segmentation model with data from multiple medical institutions. The model performs similarly to a model trained with centralized data. Further, to avoid the privacy threat posed by PS. Roy et al. [\(2019](#page-54-3)) proposed BrainTorrent, a peer-to-peer FL framework for medical collaboration. In addition, they found that the server-less BrainTorrent approach outperforms the traditional server-based approach.

# **6.1.2 FL for recommendation system**

Traditional recommendation system models are trained with centrally stored user behavior data. However, centralized data storage may lead to privacy issues. FL can provide users with personalized recommendations while protecting their privacy. For example, by using FL, Qi et al. ([2020\)](#page-54-20) trained a news recommendation model while keeping private data on the user's device. In addition, they further protected user privacy by adding noise to the gradients. Extensive experiments on real-world datasets showed that the method can achieve a performance comparable to that of the state-of-the-art news recommendation methods at that time. Similarly, with the help of FL, Hartmann et al. [\(2019](#page-50-23)) improved the suggestion ranking of Firefox URL without collecting users' data, which allows the users to type about half a character less before selecting an item.

<span id="page-39-1"></span><sup>8</sup> <https://github.com/PaddlePaddle/X2Paddle>.

<span id="page-40-0"></span>

#### **6.1.3 FL for industrial internet of things**

Training AI models usually requires large amounts of high-quality labeled data. However, it is often difficult and expensive to collect data in industrial scenarios. Obviously, collecting data from diferent clients to train the model is a good solution. However, data sharing is not widely used in practical industrial scenarios due to potential conficts of interest. FL can get a well-trained model without leaking clients' data. Therefore, some existing works used FL to solve the data island problem that exists in industrial scenarios. For example, Zhang et al. [\(2021a](#page-57-23)) proposed a FL-based approach for mechanical fault diagnosis. Specifcally, clients train local models, and the server aggregates local models to obtain a global model. Gradients rather than data are exchanged between clients and the server. In addition, they proposed a self-supervised learning strategy to improve the performance of the model.

Edge devices failing can have a serious impact on industrial production. Moreover, edge devices often contain private information about users. In order to detect anomalous devices while protecting user privacy, Liu et al.  $(2021)$  $(2021)$  proposed an on-device FL-based anomaly detection framework for sensing time-series data in IoT environments. In addition, they proposed a gradient compression strategy based on top-k selection for improving communication efficiency. The framework detects anomalies accurately and timely while reducing communication overhead by 50% compared to FL without a gradient compression policy.

The two eforts mentioned above utilizes the PS architecture-based FL. Diferent from them, (Savazzi et al. [2020\)](#page-55-8) revisited the FedAvg algorithm in the context of industrial IoT and proposed several FedAvg algorithms based on the Gossip protocol, which provide reliable robustness and scalability.

## **6.1.4 FL for vehicular networks**

The mission of vehicular network requires a huge amount of road test data, which is transmitted to the cloud for processing. However, the large amount of data transmission can cause communication delays. In addition, there are a long business process and complex environments in vehicle networks. Traffic conditions vary greatly from location to location. Therefore, the development of secure transportation is limited due to the data island problem. Samarakoon et al. [\(2020](#page-54-21)) applied FL to solve joint power and resource allocation problems in vehicular networks. First, vehicles train local models and transmit them to the roadside units. The roadside units aggregate the local models transmitted by each vehicle to obtain a global model and then return it to the vehicles. With FL, vehicles can predict the queue tail distribution locally without sharing actual queue length samples, which reduces unnecessary overheads. Based on the queue information, resource allocation can be optimized better. The method's accuracy is comparable to that of a centralized solution. Moreover, the amount of data exchanged can be reduced by 79%.

### **6.2 Model inference on the edge**

### **6.2.1 Collaborative inference**

(1) *Co-inference for object tracking* The development of DL has greatly enhanced the accuracy of object tracking. However, due to the computationally intensive nature of DL, it is difcult to perform target tracking tasks on resource-constrained edge devices. Although the cloud or edge can provide arithmetic support to the devices, cloud-based or edge-based inference sufers from unbearable communication latency. Chen et al. ([2015a](#page-49-16)) utilized cloud-device co-inference for target tracking. Specifcally, they performed target tracking algorithms at the device and maintained accuracy with a cloud-based object detection model. Further, Liu et al. ([2019\)](#page-53-13) designed a system and proposed dynamic ROI encoding techniques and a parallel stream inference method to further reduce latency. The system can improve detection accuracy by 20.2–34.8%, while only 2.24 ms latency is required for target tracking on AR devices. The two studies mentioned above do not optimize small object detection. Du et al. [\(2020b](#page-49-19)) presented DNN-Driven Streaming (DDS). Specifcally, DDS allows the server to determine the areas of the frame that need to be re-encoded. Then, the camera recodes the areas containing small objects and offloads them to the server for inference. Compared to the solutions available at the time, DDS maintains higher accuracy and reduces bandwidth usage by 59%. Although DDS improves the small object detection accuracy, it increases the latency of object detection. To alleviate this problem, Wang et al. ([2021](#page-56-19)) presented EdgeDuet. Specifcally, EdgeDuet performs large object detection on low-resolution video frames with a lightweight model, while offloading high quality video frames for small object detection. EdgeDuet offers a 233% improvement in accuracy over local object detection on small object detection tasks. In addition, it improves total latency by  $44.7\%$  and end-to-end latency by  $34.2\%$  over the state-of-the-art offloading strategy.

- (2) *Co-inference for industrial internet of things* With the rapid development of IOT and network infrastructures, there are a large number of sensors in industrial scenarios, resulting in a surge in data volume. If all these data are offloaded to the cloud for processing, it would put a huge burden on network bandwidth and servers. Therefore, it is necessary to offload some data to the edge for processing. Some existing works utilize co-inference between device-edge-cloud to ease the pressure on data centers. Li et al. [\(2018c](#page-52-17)) designed DeepIns, a manufacturing inspection system. To enable real-time data processing, they used edge servers and cloud servers as exit points, speeding up response time and reducing network trafc. Zeng et al. ([2019](#page-57-20)) combined model partition and model-early-exit and proposed Boomerang. They found optimal partition points and exit points with deep reinforcement learning, achieving maximum accuracy while meeting latency. The efforts mentioned above utilize cloud-edgedevice co-inference to accelerate inference. Unlike them, Zhou et al. [\(2019a](#page-57-18)) proposed AAIOT, which can allocate inference tasks to each device in multi-layer IoT systems, accelerating inference through collaboration between devices. Similarly, Zhao et al. [\(2018b\)](#page-57-19) proposed DeepThings, a framework that can adaptively allocate inference tasks on resource-constrained IoT devices. DeepThings enables 1.7×–2.2× inference acceleration on 2–6 edge devices with less than 23 MB memory.
- (3) *Co-inference for continuous mobile vision* There are a huge number of cameras in modern cities, which are generating a huge amount of video data. DL-based video analysis can significantly improve the efficiency of video processing. However, uploading such a huge amount of data to the cloud would put a tremendous strain on the network infrastructure. Obviously, how to perform video analysis efficiently and quickly has become an important issue. Wang et al. ([2018a](#page-56-15)) utilized MobileNet and SVM to flter irrelevant video frames on drones, thus reducing network bandwidth consumption and accelerating video analysis. As a further step, Canel et al.  $(2019)$  $(2019)$  proposed FilterForward. Specifcally, they utilized small neural networks rather than SVMs to determine whether frames should be fltered, which improves the accuracy of flter-

ing. In addition, they optimized for the multi-tenancy problem. FilterForward reduces bandwidth usage by an order of magnitude while maintaining accuracy. In order to be able to fnd missing people in large, crowded cities, Yi et al. ([2020\)](#page-56-20) designed EagleEye, which contains several complex DNNs including face detection network, resolution enhancement network, and face recognition network. To provide low latency services, they developed Content-Adaptive Parallel Execution, using devices and cloud to optimize multi-DNN model execution pipeline. Compared to naïve execution, EagleEye reduces latency by 9.07x while offloading only 108k of data.

### **6.2.2 Solo inference**

- (1) *Solo-inference for smart driving* Due to transmission latency, cloud-based or edgebased inference cannot provide real-time detection for smart driving. Therefore, it is necessary for devices to participate in inference. There are many eforts that applied the works mentioned in the above sections for smart driving. For example, to assist people with driving, Chen et al. [\(2019a](#page-48-18)) presented a shallow model called concatenated feature pyramid network for real-time embedded traffic flow prediction. They proposed concatenated blocks to reduce the number of convolutional layers and connections. Dwisnanto Putro et al. ([2020](#page-49-20)) proposed an eyes detector that can be run in real time on the CPU for detecting driver's fatigue states. It consists of two modules, a backbone network for feature extraction and a detection module for predicting eye regions. The eyes detector can reach a detection speed of 467 frames per second on the Intel-I5-6600.
- (2) *Solo-inference for object recognition* Since there are limited objects in the edge environments, duplication of recognition tasks in the same scene may occur. Drolia et al. [\(2017b\)](#page-49-17) cached the computation results in the edge server. When the client sends images to the edge server, the edge server performs feature extraction and feature matching on the sent images. For objects that are already cached in the edge server, recognition results are returned directly. To be able to accurately identify prescription pills on mobile devices, Zeng et al. [\(2017\)](#page-57-24) developed a pills identifcation system named MobileDeepPill. They utilized a multi-CNNs model to collectively capture the shape, color, and imprints features of the pills. In addition, for speeding up the inference, they utilized a knowledge distillation-based model compression technique to reduce the number of parameters and FLOPs of the multi-CNNs model. However, MobileDeepPill simply extracts diferent features without considering the complementary relationship between diferent features. Ling et al. ([2020](#page-52-20)) proposed two training strategies, Batch ALL and Batch Hard, to allow diferent features to compensate each other for better recognizing hard samples.
- (3) *Solo-inference for human activity recognition* With the popularity of smart devices, there has been a tremendous growth in the data collected by smart devices. However, these data contain the personal information of the user. To protect user privacy, it is necessary to process the data from smart devices locally. Almaslukh et al. ([2018](#page-48-20)) designed an efficient and lightweight model for smartphone-based human activity recognition. They used time-domain statistical features to extract more distinguishable features. In addition, they explored a new data augmentation method, which can improve the accuracy of smartphone-based human activity recognition. Sensors collect a large amount of data in the edge environment. However, these data are difficult to interpret and label. For providing human activity recognition labels to sensor data, Radu and

Henne ([2019](#page-54-22)) proposed a framework, Vision2Sensor, which synchronizes the visual images captured by the camera with the sensor data to generate labels. The system can be run on embedded devices in real time, thus protecting user privacy.

# <span id="page-44-0"></span>**7 Open challenges and future directions**

From the discussion above, we can see that the benefts of edge intelligence are obvious. It relieves the pressure on data centers and brings intelligent services down to the edge. However, there are still many challenges on the way to achieve edge intelligence. In this section, we present open challenges, possible solutions and future directions for edge intelligence.

#### **7.1 The specialties of data in edge environments**

Unlike the data in data centers, data in edge environments often have the following characteristics: (1) Persistence of data; (2) Unlabeled data; (3) Non-IID data; (4) Data with noise. We provide possible solutions for the specialties of data in edge environments.

- (1) Persistence of data: in the edge environment, data is continuously generated. For supervised learning, the models are trained with pre-collected static datasets and cannot learn new knowledge when the training is over. Clearly, models cannot be retrained on resource-constrained edge devices. In addition, it is not possible to get all the training data at once in edge environments. For this situation, we believe that incremental learning may be a solution. Actually, there are already some eforts to explore online incremental learning on edge devices, which requires less computational resources than training models from scratch. In addition, models trained with incremental learning are able to perform equally well on new tasks and old ones. For example, Li et al. [\(2019a](#page-52-21)) proposed the RILOD system, which progressively trains an existing object detection model so that it can detect new targets without compromising its original capabilities. In addition, they designed a real-time dataset construction pipeline for collecting training images and automatically labeling images. Experiments have shown that RILOD can learn to detect a new object in a few minutes. With incremental learning, the problem of persistence of edge data can be solved to a great extent. In addition, the model can be extended without hurting its original capabilities. However, there are still some challenges with incremental learning on edge devices, such as hardware. Traditional hardware platforms such as CPUs and GPUs are not able to handle DNNs on edge devices efficiently. CPU cannot be used for parallel computing and therefore is computationally inefficient. GPU can provide high throughput but its low energy efficiency prevents it from being deployed on edge devices. In fact, there has been some eforts to design efficient hardware architectures. For example, (Luo and Yu [2021](#page-53-18)) proposed a compute-in-memory-based accelerator, AILC, for on-chip incremental learning using spin-transfer-torque magnetic random access memory (STT-MRAM) technology.
- (2) Unlabeled data: for supervised learning, a complete labeled dataset is required for training. However, data in edge environments is often unlabeled. Active learning may be a solution to alleviate this problem. It allows learning algorithms to proactively make annotation requests and hand over the fltered data to human experts for further annotation. Although it can reduce the costs of data annotation, human annotations are still needed. In fact, several works have attempted to utilize active learning to solve

the problem of unlabeled data in edge environments. For example, Diethe et al. [\(2016](#page-49-21)) proposed hierarchical Bayesian active learning methods to label data in smart home. Shahmohammadi et al. [\(2017\)](#page-55-23) used active learning to label the sensor data collected by the smartwatch for human activity recognition. In addition, unsupervised learning is also an optional solution. Unlike supervised learning, it is able to learn from unlabeled data. Janjua et al. ([2019\)](#page-51-22) proposed IRESE, a rare-event detection system that processes input data utilizing unsupervised machine learning. It achieves over 90% accuracy in detecting audio rare events such as gunshot, scream, and siren.

- (3) Non-IID data: models trained with IID data can learn the data distribution characteristics well. However, the data generated by edge devices is usually Non-IID, which means the data distribution of each edge device cannot represent the total data distribution. A portion of existing research has been focusing on how to process Non-IID data in FL. For example, Zhao et al. [\(2018a\)](#page-57-3) suggested creating a subset of data that is shared globally to improve the training on Non-IID data. Yoshida et al. ([2020\)](#page-57-4) assumed that a few clients allow their data to be uploaded to the server in order to form an approximate IID dataset. The collected IID dataset was utilized to train a model. Then the model is aggregated with other local models trained with Non-IID data.
- (4) Data with noise: for IoT devices in diferent edge environments, the noise in environments may afect the data and consequently the model performance. We believe that input fltering may be a good solution. For example, Li et al. [\(2020\)](#page-52-22) fltered invalid data in IoT environments, thus avoiding its impact on sentiment detection. However, when there is too much noise in the edge environment, a small number of high-quality labeled samples may not be enough to train a model. In this situation, we believe that the following technologies may be potential solutions. First, data augmentation, which can enhance the generalization ability of the network by increasing the number of samples. For example, Mathur et al. ([2018\)](#page-53-19) used data augmentation to address noise pollution caused by sensor heterogeneity. Second, few-shot learning, which can improve the accuracy when the number of samples in a category is small. For example, Lungu et al. ([2020\)](#page-53-20) proposed an improved Siamese Networks for few-shot learning. In a 5-way, 1-shot classifcation task, this network improves accuracy by up to 22% on average across four datasets.

#### **7.2 Privacy and security issues**

For cloud-based DL services, data needs to be uploaded to the cloud for inference, which can be a threat to user privacy. Moreover, for organizations like medical centers, they need to protect the privacy of patients. Therefore, it is not possible to exchange data with other organizations to train models. FL can be a good solution to this problem. For example, Sheller et al. ([2018](#page-55-22)) used data from multiple institutions to train a brain tumor segmentation model without sharing data. However, one of the disadvantages of FL with PS architecture is the dependence on PS. For application scenarios like smart home, it is not difcult to fnd a trusted third party as PS. However, for collaboration between medical centers, it is difficult to find a trusted third party (Roy et al. [2019\)](#page-54-3). Decentralized FL can be a good solution to this problem, which is mainly achieved through blockchain (Weng et al. [2021;](#page-56-7) Kim et al. [2019\)](#page-51-5) or other communica-tion methods (Lu et al. [2020](#page-53-2); Hu et al. [2019\)](#page-51-7). For example, Roy et al. [\(2019\)](#page-54-3) proposed a peerto-peer FL framework for medical collaboration, which does not require a trusted third party to aggregate local models.

### **7.3 Scheduling and trade‑ofs of DNN services**

There are often multiple DNN service requests in edge enviroments. For example, requests from vehicles in autonomous driving for services such as object detection and path planning. In this case, multiple DNN programs compete for limited resources. Therefore, service providers need to optimize the scheduling of DNN services. There have been works exploring the DNN scheduling problem in edge environments. For example, Huang et al. [\(2019\)](#page-51-2) designed an efficient heuristic online algorithm for multitask scheduling.

In addition, various factors such as model accuracy, inference time need to be traded of in order to reduce resource consumption. We believe that model selection and model-earlyexit are optional solutions. The former selects the most suitable model by measuring various factors such as accuracy and inference time (Taylor et al. [2018\)](#page-55-20). This method is suitable for devices with large storage such as smartphones since the weights of multiple models need to be stored. Similarly, the latter determines which layer to exit for samples by measuring the difficulty of inference (Teerapittayanon et al. [2016\)](#page-55-18). The difference is that for the latter, only the weights of one model need to be stored, which makes it suitable for devices with small storage. In fact, quite a few efforts have been made to use both techniques in edge environ-ments to achieve the accuracy-inference time trade-off. Bolukbasi et al. [\(2017\)](#page-48-16) utilized model selection to make a trade-off in terms of inference time and accuracy. Zeng et al. [\(2019](#page-57-20)) achieved maximum accuracy while meeting the inference time by utilizing model partition and model-early-exit.

## **7.4 Incentives and penalties**

Many existing works on FL suffer from several problems: (1) device resources are inevitably consumed when users participate in collaborative training, which may lower the willingness of users to engage in training; (2) users, as providers of the data, may refuse to participate in training due to privacy concerns; (3) many of existing works on FL considered only privacy threats from PS (Bonawitz et al. [2017;](#page-48-6) Geyer et al. [2017](#page-50-4); Truex et al. [2019;](#page-55-7) Wei et al. [2020](#page-56-6)). However, there are likely to be malicious nodes during the training process, which can seriously disrupt the training. Moreover, in FL, local nodes have absolute control over their own data. Therefore, anomaly detection cannot be used for checking malicious nodes. Blockchain can be a promising solution for the above problems. For example, in FLChain (Bao et al. [2019](#page-48-21)), honest users can earn more partition profts with a well-trained model, and malicious users are promptly detected and severely penalized.

#### **7.5 The development of training models dynamically**

In order to deploy the model to edge devices, two training methods can be used: dynamic training and static training. Among them, static training means that once training is complete, the model cannot be fne-tuned. It is dependent on high performance computing nodes. Dynamic training refers to training with computation resources of edge nodes. Due to the specifcity of the data generated at the edge, models obtained by static training may not be available for edge environments all the time. On the contrary, the model obtained by dynamic training can be applied to the edge environments well. Moreover, when the data in the environment changes, the model can be adjusted by fne-tuning. It is obvious that the development of dynamic training plays a crucial role in the implementation of edge intelligence.

## **7.6 The development of cloud‑edge‑device collaborative inference**

A part of the existing works (Mao et al. [2017a](#page-53-11), [b](#page-53-12); Zhou et al. [2019a\)](#page-57-18) performed inference completely on devices for avoiding transmission latency. We believe that edge computing, as an extension of cloud computing, can provide low latency to the cloud. Moreover, the cloud can provide computing support to the edge. The collaboration between cloud, edge and devices can achieve a great trade-of between latency, inference accuracy and energy consumption. Especially in the future 6G era, powerful network bandwidth and communication capability can provide strong support for the collaboration between cloud, edge and devices.

## **7.7 The extension of edge intelligence**

From the discussion in the above sections, we can see that edge intelligence can provide better services in several aspects such as user privacy, communication bandwidth, and transmission latency. However, intelligent services can be deployed on more fne-grained devices. Tiny machine learning (TinyML) (Sanchez-Iborra and Skarmeta [2020;](#page-54-23) Banbury et al. [2020\)](#page-48-22), as an extension of edge intelligence, can be deployed on micro-controllers. This intelligence paradigm can provide more reliable intelligent services. Although research on TinyML is still in its initial stage, we believe that only through the collaboration between cloud-based intelligence, edge intelligence and TinyML can we efectively build a complete intelligence eco-system.

# **8 Conclusion**

Edge computing, as the extension of cloud computing, is promising to bring computeintensive DL services down to the edge. The combination of AI and edge computing has produced a new paradigm, edge intelligence, which is gradually attracting the attention of researchers in academia and industry. In this paper, we provided a detailed introduction of techniques, architectures, frameworks and implementations for edge training and edge inference, and compared their advantages and disadvantages from multiple perspectives. In addition, we discussed the challenges, possible solutions and future directions of edge intelligence. We believe that in the near future, with the rapid development of AI and edge computing, more and more researchers will be dedicated to implementing edge intelligence.

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