Special section on model checking

Pragmatics of model checking: an STTT special section[∗]

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Abstract. Since its discovery in the early 1980s, model checking has emerged as one of the most exciting areas in the field of formal verification of system correctness. The chief reason for this enthusiasm resides in the fact that model checkers establish in a fully automated manner whether or not a system satisfies formal requirements; users need not construct proofs. Until the early 1990s, however, the practical impact of model checking was modest, in large part because the *state-explosion* problem limited the applicability of the technology. Recent advances in the field have dramatically expanded the scope of model checking, however, and interest in it on the part of practitioners is growing. This special section surveys several recent approaches to attacking the state explosion that are supporting the continued advancement of model checking as a viable technology for improved system design.

Key words: Model checking – Temporal logic – State explosion – System verification

1 Introduction

Model checking [20, 22, 57] has emerged as one of the most promising developments of the past two decades in the area of formal methods for system correctness. As the term suggests, in model checking one checks whether a mathematically precise description of a system is a model of, or satisfies, a mathematically precise description of its requirements. The crucial insight underpinning the interest in model checking is that when the system

descriptions are *finite-state*, and the requirements are expressed in temporal logic [33, 52], then the satisfaction check can be performed algorithmically, without requiring interaction with the user. This contrasts with more traditional approaches to system verification, which rely on user-generated proofs that systems meet their requirements. Indeed, the term "model checking" has become virtually synonymous with the (more precise) phrase "automatic temporal-logic model checking," although other automated analyses, including checks of refinement relations between systems [10, 12, 18, 27, 41, 45, 55, 60], may also be seen as instances of the general notion. In the remainder of this note we use "model checking" to mean "automatic temporal-logic model checking."

Despite the obvious appeal of model checking it has only been within the past five years that it has begun attracting serious attention from engineers, such as hardware and communications-protocol designers, who build finite-state systems. In addition to pedagogical and cultural reasons, a key technical difficulty has impeded the uptake of this technology: the state-explosion problem. State explosion results from the fact that, in general, the size of a state space of a system grows exponentially in the size of the system description. This phenomenon has several causes, two of which include the following.

- If the system contains data elements (variables, latches, etc.) then the number of states is usually exponential in the number of such elements.
- If a system consists of several parallel components, then the number of system states is usually proportional to the product of the sizes of the state spaces of the individual processes.

Consequently, while the best traditional model-checking algorithms [22, 29, 57, 63] are linear in the number of states of a system, their applicability is severely restricted by the prohibitive number of states systems can have.

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Over the past decade, researchers have begun to develop effective techniques for ameliorating the consequences of state explosion. Some of these approaches have been implemented in different verification tools and exercised on various case studies, with promising results [26]. Industrial interest, especially in the hardware community but also in the telecommunications and embedded systems areas, has grown significantly [42, 58] with companies such as Cadence, Chrysalis, CS Verilog, and Telelogic marketing model checkers as part of their system design automation packages, and other companies such as Intel, Lucent, and HP supporting internal model-checking groups.

Despite this developing industrial interest, however, model checking represents only a small part of overall industrial and governmental investment in system development, and new methods for coping with state explosion are needed for the technology to continue its advance. This special section surveys some of the recent promising approaches to this problem. Each of the topics presented has a sound mathematical basis, has been implemented in a tool, and has been used to undertake at least one significant case study.

The remainder of this note sets the stage for the papers to come by providing some general background in temporal logic and model checking. The next section reviews basic definitions needed to understand temporal logics and their use in specifying system requirements. The following section then describes the classic modelchecking algorithm of [22] and points out how state explosion affects its behavior. The paper closes with brief descriptions of the results presented in the rest of the special section.

2 Temporal logic and state machines

The temporal-logic model-checking problem may be phrased abstractly as follows.

- Given: A system description and a formula in temporal logic.
- Determine: Whether or not the system satisfies the formula.

Model-checking algorithms take two inputs $-$ a system and a formula – and return a yes/no answer, depending on whether or not the system satisfies ("is a model of") the formula. (Some model checkers also return diagnostic information in the event the answer is "no".) Generally speaking, model checking is only guaranteed to be decidable if the system in question contains at most a finite number of reachable states, although for some classes of infinite-state systems the problem may also be solved automatically [3–5, 17, 40].

Model-checking tools typically provide users with a programming-like notation for describing systems. The tool then"compiles"system descriptions into (finite-)state machines that are given, together with the temporal logic formula, to the model-checking algorithm. The intention behind the state machines is that they encode all the possible states and execution steps a system may exercise as it runs; the algorithms use this information together with the semantics of the temporal logic to calculate their answers.

2.1 State machines

In the model-checking literature one may find two major classes of state machines: Kripke structures, and labeled transition systems. We present the mathematical definitions of these concepts and discuss briefly the kinds of systems they are generally used to model.

Kripke structures A Kripke structure contains five kinds of information; hence, mathematically, a Kripke structure may be represented as a quintuple $\langle \mathcal{S}, \mathcal{P}, \ell, \longrightarrow, s_I \rangle$. These five components have the following interpretation.

- $-$ S is a set of states.
- $-$ P is a set of *atomic propositions* that may be true in some states and false in others.
- $\ell : S \longrightarrow 2^{\mathcal{P}}$, the *labeling function*, maps states to sets of atomic propositions satisfied by the states. So if $p \in$ $\ell(s)$ then proposition p holds in state s, and doesn't hold otherwise.
- $-\longrightarrow \subset \mathcal{S}\times\mathcal{S}$, the transition relation, indicates which execution steps are possible. Intuitively, if $\langle s, s' \rangle \in \longrightarrow$ then when the system is in state s it may perform an execution step and evolve to state s' . Following the usual notational convention, we write $s \rightarrow s'$ in lieu of $\langle s, s' \rangle \in \longrightarrow$.
- $s_I \in \mathcal{S}$ is the designated start state.

One may find variants of this definition in the literature: sometimes P is left implicit, and in some situations a nonempty set of start states, rather than a single start state, is specified. It is also common to require that the transition relation \longrightarrow be *total*, i.e., that every state has at least one outgoing transition. Some Kripkestructure-based formalisms are equipped with *fairness* constraints that disallow certain pathological execution sequences [52].

Kripke structures generally model systems that are closed in the sense that they execute without interacting with their environments. The atomic propositions typically refer to artifacts, such as values of variables, coming from the (user-level) system-modeling notation. For example, in the UNITY language [19] one represents systems as collections of if-then statements whose bodies assign values to variables. If x is such a variable, an example atomic proposition might be $x > 0$, which is true in some states and false in others.

Labeled transition systems Labeled transition systems differ from Kripke structures by allowing transitions as well as states to bear labels. The set of transition labels, or actions, must therefore be part of the description of a labeled transition system. Mathematically, a labeled transition system is a sextuple $\langle \mathcal{S}, \mathcal{P}, \mathcal{A}, \ell, \longrightarrow, s_I \rangle$.

- The interpretation attached to S, P, ℓ , and s_I are the same as for Kripke structures.
- $-$ A is a set of *actions*.
- The transition relation $\longrightarrow \subseteq \mathcal{S} \times \mathcal{A} \times \mathcal{S}$ labels transitions with actions. Intuitively, if $\langle s, a, s' \rangle \in \longrightarrow$, and if the system is in state s , then it may perform an execution step and evolve to state s' provided that the environment enables action a. Traditionally, one writes $s \stackrel{a}{\longrightarrow} s'$ in lieu of $\langle s, a, s' \rangle \in \longrightarrow$.

As with Kripke structures, one may find minor variations of this definition in the literature. In process algebras [7, 11, 46, 54], for instance, the set P is typically taken to be empty, or to contain only the propositions tt (for "true") and ff (for "false") which hold of all and no states, respectively. In both cases P and ℓ are omitted, and labeled transition systems become quadruples. Also, the start state s_I is sometime left out, in which case the labeled transition system actually defines a collection of systems differentiated on the basis of which state is the start state.

In contrast with Kripke structures, labeled transition systems are usually used to model open systems that interact with their environments. The actions encode these interactions and are often interpreted as communications, i.e., as sends and receives. So if $s \stackrel{a}{\longrightarrow} s'$ and a is viewed as reception of an input, then the transition can "fire" only if the environment enables the input by providing the corresponding output. Mathematically, Kripke structures may be seen as labeled transition systems whose action set A contains exactly one element.

2.2 Temporal logics

Temporal logics provide constructs for describing the properties systems have as they evolve over time. Different logics are generally used for Kripke structures and labeled transition systems, with notations like Computation Tree Logic (CTL) [22] and Linear-Time Temporal Logic (LTL) [52, 56] being favored for the former and the modal mu-calculus (also known as the propositional mucalculus) [49, 61] being used for the latter.

2.2.1 Logics for Kripke structures

Temporal logics for Kripke structures traditionally provide operators for describing properties of individual execution sequences of systems together with mechanisms for quantifying over such sequences. The number of such logics is vast, and a summary of them all lies beyond the scope of this note; the interested reader may consult the survey paper of Emerson [33] for an overview. Here we focus on the temporal logic CTL[∗] [35] and various of its sublogics; these have received the bulk of attention from tool builders in the Kripke-structure community.

CTL[∗] formulas fall into two categories. Path formulas permit properties to be defined for execution sequences (i.e., sequences of states) in a given Kripke structure, while *state* formulas allow properties to be defined for states. State formulas are the ones used to define properties of systems, the idea being that if a system's start state satisfies a state formula, then the system is deemed to have the given property.

Path formulas have the following form.

- Any state formula S is also a path formula; the intention is that a path satisfies S if the initial state on the path does.
- If P is a path formula, then so is $\neg P$, the "negation" of P. A path satisfies $\neg P$ if it does not satisfy P.
- If P and Q are path formulas, then so is $P \wedge Q$, the "conjunction" of P and Q. A path satisfies $P \wedge Q$ if it satisfies both P and Q.
- If P is a path formula, then XP is a path formula. X is often referred to as the "next-state" operator; a path satisfies XP if the suffix, or "tail", starting with the state after the initial one satisfies P.
- If P and Q are path formulas, then so is $P \cup Q$. A path satisfies $P \cup Q$ if it keeps P true "until" it makes Q true. More precisely, the path satisfies $P \cup Q$ if some suffix of the path satisfies Q and every suffix up to this one satisfies P.

In addition to these operators, formulas typically use the following derived constructs.

- Additional propositional connectives such as ∨ ("or") and \rightarrow ("implies") can be encoded in terms of \neg and \wedge . Specifically, $P \vee Q$ may be rendered as $\neg((\neg P) \wedge Q)$ $(\neg Q)$) ("it is not the case that both P and Q are false") and $P \to Q$ as $(\neg P) \lor Q$.
- $\mathsf{F} P$ holds of paths in which path formula P eventually becomes true. It may be encoded as $tt \cup P$ ("true") holds until P does"). Here tt, a formula that holds of any path, is defined as $S \vee (\neg S)$ for an arbitrary state formula S.
- $\mathsf{G} P$ holds of paths in which P remains true forever. This may be defined as \neg F(\neg P) ("it is not true that P is eventually false").

The operators X, U, F and G are often called path modalities; their meanings are illustrated in Fig. 1. In the literature one sometimes finds XP, FP and GP written as $\bigcirc P$, $\diamond P$ and $\Box P$, respectively.

State formulas are intended to be interpreted with respect to states in a Kripke structure. They may be defined as follows, where P , a parameter to the definition, contains the set of atomic propositions.

- Any atomic proposition $p \in \mathcal{P}$ is a state formula. A state satisfies such a p if p belongs to the set that the labeling in the Kripke structure assigns to the state.
- If S is a state formula then so is its negation, $\neg S$. A state satisfies $\neg S$ if it does not satisfy S.

Fig. 1. An illustration of the path modalities

- If S and T are state formulas then so is their conjunction, $S \wedge T$. A state satisfies $S \wedge T$ if it satisfies both S and T .
- If P is a path formula then AP is a state formula. Intuitively, a state satisfies AP if every execution path beginning at the state satisfies path formula P.

The following derived constructs are also frequently used.

- Propositional operators such as \vee and \rightarrow may be encoded in the same way that their path-formula counterparts are.
- $-$ E P holds of states having some execution path emanating from them that satisfies path formula P . $E P$ may be encoded as $\neg A(\neg P)$ ("it is not the case that every path violates P ").

The operators A and E are often called *path quantifiers*. Figure 2 illustrates the meaning of the A quantifier.

As stated previously, CTL[∗] state formulas are the ones used to define properties of Kripke structures; a Kripke structure satisfies such a formula if its start state does. Sample properties, and their natural language equivalents, include the following; propositions in italics denote atomic propositions.

 $- AG(\neg inCS1 \vee \neg inCS2)$

"It is always the case that $inCS1$ (which would hold when e.g., process 1 is in its critical section) or $inCS2$ is false.

 $- AG(sent \rightarrow X((\neg sent) \cup received))$

"It is always the case that if a message has just been sent, then another cannot be sent until a message has been received."

 $-$ AGEF $reset$

"It is always the case that the reset state is reachable."

Fig. 2. An illustration of the A path quantifier

The intuitive accounts of the operators given above can be formalized mathematically by giving a relation indicating when sequences of states satisfy path formulas and states satisfy state formulas. In order to do this we need to make precise the notion of "execution sequence". Some auxiliary notation on infinite sequences proves useful.

Definition 1. Let $\mathcal{K} = \langle \mathcal{S}, \mathcal{P}, \ell, \longrightarrow, s_I \rangle$ be a Kripke structure such that \longrightarrow is total, let S^{ω} be the set of infinite sequences of states, and let $\sigma = s_0 s_1 s_2 \dots$ be an element of S^{ω} .

- 1. $\sigma[i] \in \mathcal{S}$ is defined to be s_i .
- 2. $\sigma(i) \in \mathcal{S}^{\omega}$ is the infinite sequence $s_i s_{i+1} \ldots$.
- 3. σ is an execution sequence emanating from s if $s_0 = s$ and for all $i \geq 0$, $s_i \longrightarrow s_{i+1}$. That is, s must be the initial state in the sequence, and a state is reachable from its predecessor via an execution step. We use $E_{\mathcal{K}}(s) \subseteq \mathcal{S}^{\omega}$ to represent the set of execution sequences emanating from s in Kripke structure K .

Given a Kripke structure $\mathcal{K} = \langle \mathcal{S}, \mathcal{P}, \ell, \longrightarrow, s_I \rangle$ we now define a relation $\models_{\mathcal{K}}$ relating paths (i.e., elements of \mathcal{S}^{ω}) to path formulas and states to state formulas. If $\sigma \models_K P$ then σ is deemed to satisfy P, and likewise for $s \models_{\mathcal{K}} S$. The definition is given below; in what follows p is an atomic proposition, P and Q are assumed to be path formulas, and S and T are state formulas.

Finally, we write $\mathcal{K} \models S$ if $s_I \models_{\mathcal{K}} S$; a Kripke structure satisfies a state formula exactly when its initial state does.

Sublogics of CTL[∗] We close this section by commenting on fragments of CTL[∗] that appear frequently in the literature. Linear-time temporal logic, or LTL [33, 52], consists of path formulas containing no occurrences of the path quantifiers A and E. A system satisfies such a formula P if it satisfies the CTL[∗] state formula A P . Computation Tree Logic, or CTL [22], formulas require that every path modality (X, U, F) or G be immediately preceded by a path quantifier (A or E). For example, the formula FGp is an LTL formula, but $FEGp$ is not, owing to the presence of the E operator. AFEG p is a CTL formula, but $\mathsf{AFG}\,p$ is not, since the G operator is not immediately preceded by either an A or an E.

As LTL formulas may only quantify over the executions leaving the start state of a system, they often prove easier to formulate and conceptualize; consequently, most of the non-model-checking literature on temporal logic deals with LTL. Until relatively recently [48], however, effective model-checking tools have not existed for LTL, owing to worries about the inefficiency of the modelchecking procedures for it [51, 63]. On the other hand, efficient model-checking algorithms for CTL and similar logics were developed in the early 1980s [21, 57], and consequently the earliest model-checkers supported these "pure branching-time" logics. Most current model checkers for Kripke structures still provide CTL as their temporal logic for this reason. For a discussion on the relative merits of branching-time and linear-time temporal logics, the interested reader is referred to [35, 37, 50].

2.2.2 Logics for labeled transition systems

The *modal mu-calculus* [49, 61], the logic most frequently used in the setting of labeled transition systems, differs substantially in form from the temporal logics discussed above. Specifically, it replaces the two-level syntax of CTL[∗] with a single syntax of state formulas, and instead of a fixed collection of path modalities it provides a family of action-labeled next-state operators together with a general facility for defining properties recursively.

The definition of the mu-calculus is parameterized with respect to three sets: a set P of atomic propositions, a set A of actions, and a set X of propositional variables. These sets are assumed to be disjoint. Formulas are intended to describe properties of states in a given labeled transition system, and may have one of the following forms.

- Any atomic proposition $p \in \mathcal{P}$ is a mu-calculus formula. A state satisfies such a formula if the state labeling in the labeled transition system indicates this is the case.
- Any variable $x \in \mathcal{X}$ is a mu-calculus formula. To interpret formulas of this form, one needs an environment

indicating what variables mean. Assuming such an environment is given, a state satisfies a variable if the environment indicates this is the case.

- If S is a mu-calculus formula, then so is its negation, $\neg S$. A state satisfies $\neg S$ if it does not satisfy S.
- If S and T are mu-calculus formulas, then their conjunction, $S \wedge T$, is a formula. A state satisfies $S \wedge T$ if it satisfies both S and T .
- If $a \in \mathcal{A}$ is an action and S is a mu-calculus formula, then $[a]S$ is a mu-calculus formula. A state satisfies $[a]$ S if all the *a*-transitions emanating from the state lead to states in which S holds. Figure 3 illustrates the meaning of $[a]$ *S*.
- If $x \in \mathcal{X}$ is a variable and S is a mu-calculus formula, then $\mu x.S$ is a mu-calculus formula. This formula is intended to represent a "recursively defined" formula that "solves" the equation $x = S$.

Fig. 3. The meaning of mu-calculus formula $[a]S$

In addition to adhering to the forms outlined above wellformed mu-calculus formulas must satisfy two additional constraints. First, a formula must be closed: every occurrence of a variable x must fall within the scope of a μx construct. Second, in formulas of the form $\mu x.S$ any (free) occurrence x within S must be *positive* in the sense that it falls within the scope of an even number of negations. The first requirement ensures that variables are only used in support of recursive definitions. The second restriction is necessary to ensure that equations such as $x = S$ indeed have solutions.

The following derived constructs also prove useful.

- The propositional operators tt, ff, \vee and \rightarrow are encoded in the usual fashion.
- $-\langle a \rangle S$ holds of a state if it has an outgoing transition labeled by a and leading to a state satisfying S . It may be encoded as $\neg[a](\neg S)$ ("it is not the case that every a-transition leads to a state violating S ").
- $\nu x.S$ represents another "solution" to the equation $x = S$. It may be encoded as $\neg \mu x.(\neg S[\neg x/x])$, where $S[\neg x/x]$ represents the formula obtained by replacing all (free) occurrences of x in S by $\neg x$. We comment on $\nu x.S$ and its relationship with $\mu x.S$ below.

The [a] and $\langle a \rangle$ constructs are *single-step* modalities in that they only permit the expression of properties that look one execution step into the future. Nevertheless, the mu-calculus turns out to be more expressive than other known temporal logics. This expressiveness results from the inclusion of the μ construct, which is often referred to as a *least fixpoint* operator. Semantically, $\mu x.S$ represents the "least solution" to the equation $x = S$, where "least" means "satisfied by the fewest possible states." Intuitively, at least for finite-state systems, $\mu x.S$ may be thought of as an infinitary disjunction,

$$
S_0 \vee S_1 \vee S_2 \vee \cdots,
$$

where S_0 represents the formula ff that holds of no states, and S_{i+1} is $S[S_i/x]$ (i.e., S with all free occurrences of x replaced by S_i). As an example, consider the formula $\mu x. [a]x$. According to the interpretation just given, this is equivalent to

$$
\mathsf{ff} \vee [a] \mathsf{ff} \vee [a][a] \mathsf{ff} \vee \cdot.
$$

A state satisfying this formula must satisfy either ff (which no state can satisfy); or $[a]$ ff, which holds if the state has no a-transitions (this is the only way that "every a-transition" can lead to a state satisfying ff); or $[a][a]$ ff (i.e., every a-transition leads to a state having no atransitions); etc. So for this formula to hold of a state in a finite-state system, the state must be incapable of an infinite sequence of a-transitions. The set of states satisfying this property may be seen as a "solution" to $x = [a]x$.

By way of contrast, $\nu x.S$ represents the greatest solution to $x = S$ and is consequently referred to as the greatest fixpoint operator. Using the definition ν together with DeMorgan's laws, one sees that in the case of finite-state systems, $\nu x.S$ can be viewed as an infinite conjunction,

$$
\hat{S}_0 \wedge \hat{S}_1 \wedge \hat{S}_2 \wedge \cdots,
$$

where \hat{S}_0 is tt and $\hat{S}_{i+1} = S[\hat{S}_i/x]$. As an example, consider $\nu x. [a]x$. A state in a finite-state system that satisfies this formula must satisfy:

$$
\mathsf{tt} \wedge [a] \mathsf{tt} \wedge [a] [a] \mathsf{tt} \wedge \cdots
$$

So a state satisfying this formula satisfies tt (which every state does); and $[a]$ tt (i.e., every a-transition must lead to a state satisfying tt, but every state has this property); etc. It turns out that $\nu x. [a]x$ is equivalent to tt in that every state can satisfy it. Like $\mu x. [a]x$, this may be seen as a reasonable "solution" to $x = [a]x$.

The following examples illustrate the kinds of properties one can express in the mu-calculus.

$$
- \mu x. [login] x
$$

"Only finitely many login attempts are possible."

 $- \nu x.$ ([receive]ff \wedge [idle]x)

"A receive becomes enabled only after a send action occurs." (Here we assume that the set of actions includes only send, receive and idle.) Recall that a state satisfies $[a]$ ff exactly when it has no outgoing a-transitions.

The formal semantics of the mu-calculus is given in terms of a function, $[-\]_{\mathcal{L}}$, mapping formulas to sets of state in a labeled transition system \mathcal{L} . Intuitively, $\llbracket S \rrbracket_{\mathcal{L}}$ returns the set of states in $\mathcal L$ satisfying S. In order to define $\llbracket - \rrbracket_{\mathcal{L}}$ inductively on the structure of formulas, we need

to cater for free occurrences of variables that arise when giving the semantics of $\mu x.S$ in terms of S. This may be done by augmenting $\llbracket - \rrbracket_{\mathcal{L}}$ so that it also takes a second argument, e, which is an environment assigning meanings to free variables. Thus $\llbracket S \rrbracket_{\mathcal{L}} e$ returns a set of states satisfying S , with e supplying the interpretation of any free variables in S. The following notions make this precise.

Definition 2. Let $\mathcal{L} = \langle \mathcal{S}, \mathcal{P}, \mathcal{A}, \ell, \longrightarrow, s_I \rangle$ be a labeled transition system, and let X be a set of propositional variables.

1. Let $s \in \mathcal{S}$ and $a \in \mathcal{A}$ be a state and action, respectively. Then $\{s \stackrel{a}{\longrightarrow} \bullet\} \subseteq \mathcal{S}$ is the set of states reachable from s via an a-transition:

$$
\{s \xrightarrow{a} \bullet\} = \{s' \in \mathcal{S} \mid s \xrightarrow{a} s'\}.
$$

- 2. An environment over $\mathcal X$ and $\mathcal L$ is a function $e \in \mathcal X \longrightarrow$ 2^S mapping variables to sets of states.
- 3. Let e be an environment over $\mathcal X$ and $\mathcal L$, and let $x \in \mathcal X$ and $S' \subset S$ be a variable and set of states, respectively. Then $e[x \mapsto S']$ is a new environment defined as follows.

$$
(e[x \mapsto \mathcal{S}']) (y) = \begin{cases} \mathcal{S}' & \text{if } x = y \\ e(y) & \text{otherwise} \end{cases}
$$

In other words, $e[x \mapsto S']$ behaves exactly like e except on argument x, in which case it returns S' .

Let $\mathcal{L} = \langle \mathcal{S}, \mathcal{P}, \mathcal{A}, \ell, \longrightarrow, s_I \rangle$ and e be an environment over $\mathcal X$ and $\mathcal L$. We may now define $\llbracket - \rrbracket_{\mathcal L}$ as follows.

$$
\begin{aligned}\n[p]_{\mathcal{L}}e &= \ell(p) \\
[x]_{\mathcal{L}}e &= e(x) \\
[\neg S]_{\mathcal{L}}e &= \mathcal{S} - ([S]_{\mathcal{L}}e) \\
[S \wedge T]_{\mathcal{L}}e &= ([S]_{\mathcal{L}}e) \cap ([T]_{\mathcal{L}}e) \\
[[a]S]_{\mathcal{L}}e &= \{ s \in \mathcal{S} \mid \forall s' \in \{ s \xrightarrow{a} \bullet \} . s' \in [S]_{\mathcal{L}}e \} \\
[xx.S]_{\mathcal{L}}e &= \bigcap \{ S' \subseteq \mathcal{S} \mid [S]_{\mathcal{L}}(e[x \mapsto S']) \subseteq \mathcal{S}' \}\n\end{aligned}
$$

The definition of $[\![\mu x.S]\!]_\mathcal{L} e$ relies on results from lattice theory, and we comment more on it here. Given formula $\mu x.S$ and environment e , we may define a function $f_e: 2^{\dot{S}} \longrightarrow 2^S$ as follows.

$$
f_e(\mathcal{S}')=[\![S]\!]_\mathcal{L}e[x\mapsto \mathcal{S}']
$$

Because of the syntactic restrictions on x in S (occurrences of x must appear within the scope of an even number of negations), f_e is guaranteed to be *monotonic*: if $S' \subseteq S''$ then $f_e(S') \subseteq f_e(S'')$. In addition, 2^S forms a complete lattice with respect to the subset ordering: any set $\mathcal{T} \subseteq 2^{\mathcal{S}}$ of subsets of \mathcal{S} has a greatest and least upper bound given by $\bigcup \mathcal{T}$ and $\bigcap \mathcal{T}$, respectively. Consequently, the Tarski-Knaster Theorem from lattice theory [62] ensures that f_e has a least fixpoint $\mu f_e \subseteq S$ and greatest fixpoint $\nu f_e \subseteq \mathcal{S}$ given as follows.

$$
\mu f_e = \bigcap \{ S' \subseteq S \mid f_e(S') \subseteq S' \}
$$

$$
\nu f_e = \bigcup \{ S' \subseteq S \mid S' \subseteq f_e(S') \}
$$

That is, μf_e is the smallest set of states that is a "solution" to the "equation" $x = f_e(x)$ in the sense that $f_e(\mu f_e) = \mu f_e$; similarly, νf_e is the largest such solution. From the definition of $[\mu x.S]_{\mathcal{L}}e$, one may see that

$$
[\![\mu x.S]\!]_{\mathcal{L}}e = \mu f_e;
$$

consequently, $\mu x.S$ may be seen as the "smallest," or "least permissive," property satisfying the equation $x = S$. A similar line of reasoning establishes the dual result for $\nu x.S$: it is the "most permissive" property satisfying $x = S$.

When a formula S contains no free variables, it follows that the meaning of S cannot depend on an environment; formally, $\llbracket S \rrbracket_{\mathcal{L}} e = \llbracket S \rrbracket_{\mathcal{L}} e'$ for any environments e and e'. In this case we write $\llbracket S \rrbracket_{\mathcal{L}}$ to represent this set of states. We also write $\mathcal{L} \models S$ if $s_I \in \llbracket S \rrbracket_{\mathcal{L}}$; so a labeled transition system satisfies formula S exactly when its start state does.

Researchers have identified useful variants and fragments of the mu-calculus that we briefly touch on here. Some accounts label the $[-]$ and $\langle - \rangle$ modalities with sets of actions rather than single actions [61]. Assuming that $A \subseteq \mathcal{A}$ is such a set, a state satisfies $[A]S$ if every transition labeled by an action in A leads to a state satisfying S, and dually for $\langle A \rangle$ S. Other accounts use systems of equations to define formulas rather than the linear syntax presented here [6]. Specifically, a system such as:

$$
\min \begin{Bmatrix} X_1 = S_1 \\ \vdots \\ X_n = S_n \end{Bmatrix}
$$

defines n formulas, one for each equation, where the min indicates that "smallest" solutions are intended. This notation is no more expressive than the one given here, although it can be more concise. Finally, the full mucalculus presents efficiency problems for model checking [13], and researchers have thus focused on the development of algorithms for fragments of the logic. The alternation-free fragment forbids formulas of the following form:

$$
\mu x.(\cdots \nu y.S \cdots)
$$

where both x and y appear free in S . In essence, a formula of this form introduces a "mutual dependency" between x and y in that assigning a meaning to S requires values for both x and y. If the type of fixpoint for x and y is the same this does not present problems; however, if (as in the example) one fixpoint is greatest while the other is least, then the time-complexity of model checking can be affected. The alternation-free mu-calculus [38] forbids such situations by requiring that fixpoints of different types be strictly nested within one another. Another fragment, L_2 [8, 36], copes with problems of alternation by restricting the ways in which variables may appear in the context of the \land and $\left[-\right]$ operators.

As was remarked at the end of Sect. 2.1, Kripke structures may be seen as labeled transition systems whose action set A contains only one element. With A thus restricted, one may then compare the expressive power of CTL[∗] and the mu-calculus. The latter turns out to be strictly more expressive [33], and effective translation procedures exist from CTL^* to the $(L_2 \text{ fragment})$ of the) mu-calculus [9, 31, 36]. It also turns out that the CTL fragment of CTL[∗] can be efficiently encoded in the alternation-free mu-calculus [34].

3 Traditional model checking

This section explains the basic workings of traditional model-checking procedures by examining in some detail the classic algorithm for CTL presented in [22].

3.1 The Syntax of CTL

We begin by providing a more systematic account of the syntax of CTL than the one given in Sect. 2.2.1. As CTL formulas are special cases of CTL[∗] formulas, the reader is referred to Sect. 2.2.1 for a discussion of their semantics.

The syntax of CTL is parameterized with respect to a set P of atomic propositions. CTL formulas then have the following form.

- If $p \in \mathcal{P}$ then p is a CTL formula.
- If S is a CTL formula then $\neg S$ and AXS are CTL formulas.
- If S and T are CTL formulas then $S \wedge T$, $\mathsf{A}(S \cup T)$ and $E(S \cup T)$ are CTL formulas.

It should be noted that CTL formulas are all *state* formulas in the parlance of CTL[∗]. Also, both the $E(_U)$ and the $A(\underline{\hspace{1ex}}\cup\underline{\hspace{1ex}})$ operators are needed; one cannot be encoded in terms of the other.

3.2 CTL model checking

A CTL model-checking algorithm takes two inputs: a Kripke structure $\mathcal{K} = \langle \mathcal{S}, \mathcal{P}, \ell, \longrightarrow, s_I \rangle$, and a CTL formula S. It returns the Boolean true if $\mathcal{K} \models S$ and false otherwise. In what follows, we assume that K is fixed.

The model-checking procedure of [22] follows a "global strategy" in that it calculates *all* states satisfying formula S. Then $\mathcal{K} \models S$ exactly when s_I is in this set of states. For notational convenience we write $\llbracket S \rrbracket_{\mathcal{K}} \subseteq \mathcal{S}$ to represent the set of states satisfying formula S; so $s \in$ $\llbracket S \rrbracket_{\mathcal{K}}$ if and only if $s \models_{\mathcal{K}} S$. We may therefore characterize the model checker [22] as consisting of two steps.

- Calculate $\llbracket S \rrbracket_{\mathcal{K}}$.
- Determine whether or not $s_I \in [S]_{\mathcal{K}}$.

Clearly, the difficult step involves the calculation of $\llbracket S \rrbracket_{\mathcal{K}}$. The procedure is syntax-directed; it uses the structure of formula S to guide the computation. The specific strategy may be summarized as follows.¹

- For each immediate subformula T of S , recursively calculate $[T]_{\mathcal{K}}$.
- Use the topmost operator of S to compute $\llbracket S \rrbracket_{\mathcal{K}}$ from the $[T]_K$.

Most of the CTL operators present no difficulty and are summarized below.

$$
S = p : [S]_{\mathcal{K}} = \ell(p)
$$

\n
$$
S = \neg T : [S]_{\mathcal{K}} = \mathcal{S} - [T]_{\mathcal{K}}
$$

\n
$$
S = T \wedge U : [S]_{\mathcal{K}} = [T]_{\mathcal{K}} \cap [U]_{\mathcal{K}}
$$

The set difference operator − and the intersection operator ∩ can be implemented in obvious ways in time proportional to the sizes of the sets being manipulated.

The AX operator is only slightly more difficult to handle. Suppose that $S = AXT$ and that $[T]_K$ has been computed. To calculate $[S]_K$, we scan through all states in $s \in \mathcal{S}$; for each such s, we traverse each transition emanating from s and check whether the target state t is in $[T]_K$. If this holds for each such t then s may be added to $[[S]]_K$; otherwise, it is omitted. In general, computing $[[S]]_K$ in this case requires that each state in K be visited and that each transition be traversed once.

The complicated operators to handle are $A(\underline{\hspace{0.2cm}}\cup \underline{\hspace{0.2cm}})$ and $E(\underline{\hspace{0.1cm}}\cup \underline{\hspace{0.1cm}})$; as the issues are similar for the two, we only study the former in depth. So assume that $S = A(T \cup U)$ and that $[T]_K$ and $[U]_K$ have been computed. An iterative, approximation-based procedure is then used to compute $\llbracket S \rrbracket_{\mathcal{K}}$. Roughly speaking, the routine generates a series S_0, S_1, \ldots of approximations to $\llbracket S \rrbracket_{\mathcal{K}}$. Each S_i contains more states than its predecessor, and each is guaranteed to contain only states that are also in $\llbracket S \rrbracket_{\mathcal{K}}$. More formally, assuming S_n is the most recently generated approximation, the invariants maintained are the following.

- 1. For all i with $0 \leq i < n, S_i \subset S_{i+1}$.
- 2. For all i with $0 \leq i \leq n, S_i \subseteq \llbracket S \rrbracket_{\mathcal{K}}$.

The procedure stops when no more states can be added to the most recent approximation F_n without invalidating invariant 2. The S_i are calculated as follows.

- 1. S_0 is \emptyset
- 2. From the definition of the semantics of A and U, every state that satisfies U is guaranteed to satisfy $A(T \cup U)$. Consequently, S_1 is set to $[[U]]_K$, if it is nonempty. If it is empty, we stop and set $[S]_{\mathcal{K}} = S_0 = \emptyset$.
- 3. For $i \geq 2$, we generate S_{i+1} from S_i by doing the following.
	- (a)We calculate the set

$$
S_i' = \{ s \in \mathcal{S} \mid s \in [T]_K \land s \notin S_i
$$

$$
\land \forall t \in \{ t \in \mathcal{S} \mid s \longrightarrow t \} . t \in S_i \}
$$

Informally, S_i' contains those states that satisfy T and are not in S_i but all of whose transitions lead to states in S_i .

(b) If S_i' is empty we set halt, setting $[\![S]\!]_{\mathcal{K}} = S_i$.

(c) Otherwise, we set $S_{i+1} = S_i \cup S'_i$.

Figure 4 illustrates how this works.

Fig. 4. Calculating $[A(T \cup U)]$

The key insight in the algorithm involves the efficient calculation of set S_i' in step 3a. A naive approach would require scanning the entire Kripke structure K . Using such a scheme would lead to a worst-case running time quadratic in the size of K to calculate $[A(T \cup U)]_{\mathcal{K}}$, since the number of approximations is bounded by the number of states in K and the generation of each approximation would require processing every state and transition in K . It turns out, however, that the computation of set S_i' can be amortized over the computation of the preceding approximations by remembering, for each state, how many of its transitions lead to states not contained in the current approximation. When this number becomes 0 for a state $s \in [T]_{\mathcal{K}}$, s has all its transitions contained in the current approximation S_i and should then be added to S_i' . As this set will then be merged into S_{i+1} , states having a transition into s must have their counts decremented. It may be shown that each transition is traversed once in this fashion throughout the calculation of all the approximations, and hence the computation of $[A(T \cup U)]$ can be done in time proportional to the size of K .

Implementing this approach requires some additional storage in the form of a counter for each state (which may be reused), and the data structure for K must allow transitions to be traversed backwards from target to source so that counters can be decremented efficiently. At the beginning of the approximation procedure the counters must also be initialized; this requires a scan of K .

 1 It should be noted that this account of the algorithm differs in style, although not in substance, from the one given in [22].

3.3 Practical impacts

The previous algorithm may be shown to have a running time of $O(|\mathcal{K}|\cdot|S|)$, where $|\mathcal{K}|$ is the total number of states and transitions in the system and $|S|$ is the number of subformulas of S. In practice, the latter quantity is much smaller than the former, and thus the routine is essentially linear in the size of K . The efficiency of the algorithm led Clarke and colleagues to experiment with model checking on actual systems; the EMC system was built and used to analyze a variety of hardware designs [14, 15]. At the same time, Sifakis and coworkers built the CE-SAR/XESAR model checker and applied it to the analysis of several communications protocols [57, 59]. It may be safely said that these groups' work marked the emergence of model-checking as an interesting and useful technology for system design.

Nevertheless, the limitations of the traditional approach to model checking quickly became apparent, because even though the algorithms exhibited running times proportional to the size of the state machines, in practice these state machines can be enormous. For example, a (quite small) hardware design containing only 100 latches could have 2^{100} , or over 10^{30} , states. Thus, while it generated excitement in the formal methods research community in the 1980s, model checking did not attract much attention from practicing engineers during this time.

4 The special section

In the 1990s, advances in model-checking technology have improved its practical utility to the point that it is becoming commercialized. These developments have come about because of research aimed at alleviating the stateexplosion problem referred to in the introduction.

The papers in this special section describe approaches to coping with state explosion. The methods presented may be categorized on the following basis.

- Some techniques use compact data structures or efficient search techniques to limit the storage needed to analyze a Kripke structure or labeled transition system.
- Other techniques attempt to exploit structure in system descriptions before they are converted into Kripke structures or labeled transition systems in order to reduce the sizes of the structures analyzed.

A number of other promising directions exist besides the ones addressed in this special section; these include abstraction-based approaches [25, 43, 44], the exploitation of system symmetries and architectures [23, 24, 39], and the use of semantic quotienting to eliminate redundant states [32]. Interested readers may consult [26, 28] for more complete overviews of research in the area.

The first paper in the section, "Model checking: a hardware design perspective," describes how model checking may be used in traditional hardware design methodologies as a means of getting early feedback on the correctness of a design. The model-checking technology the authors describe uses *ordered binary decision* diagrams [16] (OBDDs) to encode the Kripke structures associated with designs. OBDDs afford extremely compact representations of state spaces and transition relations; the authors discuss how OBDDs can significantly expand the scope of designs that may be analyzed using CTL model checking.

The second paper, "A minimized automaton representation of reachable states," presents an alternative means of efficiently encoding state spaces that is based on the use of minimized deterministic automata. The authors show how operations on sets of states may be implemented efficiently, and they give a wide array of experimental results for their technique, which they have implemented in the SPIN verification tool [47].

The next paper, "State space reduction using partial order techniques," describes the use of partial order information as a means of eliminating redundant states during LTL model checking. The name of the method derives from the fact that when a system consists of several parallel components, the transitions of individual subsystems may be *independent* in the sense that the occurrence of one does not affect the other, and vice versa. When this is the case, instead of considering the states resulting from interleaving these transitions in all possible ways, one may instead consider only one interleaving without sacrificing correctness. The paper describes the mechanics of partial-order reduction and discusses practical experience with the technique in the context of the SPIN model checker [48].

"Partial model checking of modal equations" also advocates an analysis of the "concurrency structure" of a system in order to reduce state-space size. The basic approach is to use the labeled transition systems for individual components to "factor" a mu-calculus formula into a formula that the rest of the system needs to satisfy for the whole system to be correct. Consequently, the global labeled transition system for the whole system need never be constructed. The paper describes several heuristics for reducing the sizes of the intermediate mu-calculus formulas generated and presents experimental results obtained from an implementation of the method.

The final paper in the section, "Local model checking and protocol analysis," describes a local, or on-the- $\mathit{fly},$ technique for comparing labeled transition systems to formulas in the alternation-free modal mu-calculus. The hallmark of local algorithms is that they compute partial semantics of subformulas in a demand-driven manner; this stands in contrast to traditional, global techniques. This demand-driven aspect ensures that irrelevant information is not computed; it also enables the labeled transition system itself to be computed in a demand-driven manner. The potential savings of this technique are analyzed in the context of a case study involving a real-time modification to the Ethernet protocol.

References

- 1. Twelfth Annual ACM Symposium on Principles of Programming Languages (POPL '85). New Orleans, Louisiana, ACM Press, January 1985
- 2. Symposium on Logic in Computer Science (LICS '86). Cambridge, Massachusetts, IEEE Computer Society Press, June 1986
- 3. Abdulla, P.A., Jonsson, B.: Verifying programs with unreliable channels. Information and Computation 127(2): 91–101, 1996
- 4. Alur, R., Courcoubetis, C., Dill, D.: Model-checking in dense real time. Information and Computation 104(1): 2–34, 1993
- 5. Alur, R., Courcoubetis, C., Henzinger, T., Halbwachs, N., Ho, P.-H., Nicollin, X., Olivero, A., Sifakis, J., Yovine, S.: The algorithmic analysis of hybrid systems. Theoretical Computer Science 138(1): 3–34, 1995
- 6. Andersen, H.R.: Model checking and boolean graphs. Theoretical Computer Science 126(1): 3–30, 1994
- 7. Baeten, J.C.M. (ed): Applications of Process Algebra. Cambridge Tracts in Theoretical Computer Science Vol. 17. Cambridge University Press, Cambridge, England, 1990
- 8. Bhat, G., Cleaveland, R.: Efficient local model checking for fragments of the modal μ -calculus. In: Margaria and Steffen [53], pp. 107–126
- 9. Bhat, G., Cleaveland, R.: Efficient model checking via the equational μ -calculus. In: Eleventh Annual Symposium on Logic in Computer Science (LICS '96). New Brunswick, New Jersey: IEEE Computer Society Press, 1996, pp. 304–312
- 10. Bloom, B., Paige, R.: Transformational design and implementation of a new efficient solution to the ready simulation problem. Science of Computer Programming 24(3): 189–220, 1995
- 11. Bolognesi, T., Brinksma, E.: Introduction to the ISO specification language LOTOS. Computer Networks and ISDN Systems 14: 25–59, 1987
- 12. Bouali, A., Ressouche, A., Roy, V., de Simone, R.: The fc2tools set. In: Margaria and Steffen [53], pp. 396–397
- 13. Browne, A., Clarke, E., Jha, S., Long, D., Marrero, W.: An improved algorithm for the evaluation of fixpoint expressions. Theoretical Computer Science 178(1–2): 237–255, 1997
- 14. Browne, M.C., Clarke, E.M., Dill, D.: Automatic circuit verification using temporal logic: Two new examples. In: Formal Aspects of VLSI Design, 1985
- 15. Browne, M.C., Clarke, E.M., Dill, D., Mishra, B.: Automatic verification of sequential circuits using temporal logic. IEEE Transactions on Computing C-35(12): 1035–1044, 1986
- 16. Bryant, R.E.: Graph-based algorithms for boolean function manipulation. IEEE Transactions on Computers C-35(8), 1986
- 17. Burkart, O., Steffen, B.: Model-checking the full-modal mucalculus for infinite sequential processes. In: Degano, P., Gorrieri, R., Marchetti-Spaccamela, A. (eds.): Automata, Languages and Programming (ICALP '97), Bologna, Italy, July 1997. LNCS 1256. Berlin, Heidelberg, New York: Springer-Verlag, 1997, pp. 419–429. Full version to appear in Theoretical Computer Science
- 18. Celikkan, U., Cleaveland, R.: Generating diagnostic information for behavioral preorders. Distributed Computing 9: 61–75, 1995
- 19. Chandy, K.M., Misra, J.: Parallel Program Design: A Foundation. Addison-Wesley, Reading, Massachusetts, 1988
- 20. Clarke, E.M., Emerson, E.A.: Synthesis of synchronization skeletons for branching time temporal logic. In: Kozen, D. (ed.): Logics of Programs: Workshop, Yorktown Heights, May 1981. LNCS 131. Berlin, Heidelberg, New York: Springer-Verlag, 1981, pp. 52–71
- 21. Clarke, E.M., Emerson, E.A., Sistla, A.P.: Automatic verification of finite state concurrent systems using temporal logic specifications: A practical approach. In: Tenth Annual ACM Symposium on Principles of Programming Languages (POPL '83). Austin, Texas: ACM Press, 1983, pp. 117–126
- 22. Clarke, E.M., Emerson, E.A., Sistla, A.P.: Automatic verification of finite-state concurrent systems using temporal logic

specifications. ACM Transactions on Programming Languages and Systems 8(2): 244–263, 1986

- 23. Clarke, E.M., Filkorn, T., Jha, S.: Exploiting symmetry in model checking. In: Courcoubetis [30], pp. 450–462
- 24. Clarke, E.M., Grumberg, O., Jha, S.: Verifying parameterized networks. ACM Transactions on Programming Languages and Systems 19(5): 726–750, 1997
- 25. Clarke, E.M., Grumberg, O., Long, D.E.: Model checking and abstraction. ACM Transactions on Programming Languages and Systems 16(5): 1512–1542, 1994
- 26. Clarke, E.M., Wing, J.M.: Formal methods: state of the art and future directions. ACM Computing Surveys 28(4): 626– 643, 1996
- 27. Cleaveland, R., Hennessy, M.C.B.: Testing equivalence as a bisimulation equivalence. Formal Aspects of Computing 5: 1–20, 1993
- 28. Cleaveland, R., Smolka, S.: Strategic directions in concurrency research. ACM Computing Surveys 28(4): 607–625, 1996
- 29. Cleaveland, R., Steffen, B.: A linear-time model-checking algorithm for the alternation-free modal mu-calculus. Formal Methods in System Design 2: 121–147, 1993
- 30. Courcoubetis, C. (ed): Computer Aided Verification (CAV '93), Elounda, Greece, June/July 1993. LNCS 697. Berlin, Heidelberg, New York: Springer-Verlag, 1993
- 31. Dam, M.: CTL[∗] and ECTL[∗] as fragments of the modal mucalculus. Theoretical Computer Science 126(1): 77–96, 1994
- 32. Elseaidy, W., Cleaveland, R., Baugh Jr., J.W.: Modeling and verifying active structural control systems. Science of Computer Programming 29(1–2): 99–122, 1997
- 33. Emerson, E.A.: Temporal and modal logic. In: van Leeuwen, J. (ed.): Handbook of Theoretical Computer Science Vol. B. North-Holland, 1990, pp. 995–1072
- 34. Emerson, E.A., Clarke, E.M.: Characterizing correctness properties of parallel programs as fixpoints. In: de Bakker, J., van Leeuwen, J. (eds.): Automata, Languages and Programming (ICALP '80), Utrecht, July 1980. LNCS 85. Berlin, Heidelberg, New York: Springer-Verlag, 1980, pp. 169–181
- 35. Emerson, E.A., Halpern, J.Y.: 'Sometime' and 'not never' revisited: On branching versus linear time temporal logic. Journal of the Association for Computing Machinery 33(1): 151–178, 1986
- 36. Emerson, E.A., Jutla, C., Sistla, A.P.: On model-checking for fragments of μ -calculus. In: Courcoubetis [30], pp. 385–396
- 37. Emerson, E.A., Lei, C.-L.: Modalities for model checking: Branching time strikes back. In: Twelfth Annual ACM Symposium on Principles of Programming Languages (POPL '85) [1], pp. 84–96
- 38. Emerson, E.A., Lei, C.-L.: Efficient model checking in fragments of the propositional mu-calculus. In: Symposium on Logic in Computer Science (LICS '86) [2], pp. 267–278
- 39. Emerson, E.A., Sistla, A.P.: Symmetry and model checking. In: Courcoubetis [30], pp. 463–478
- 40. Esparza, J.: Decidability of model-checking for infinite-state concurrent systems. Acta Informatica 34: 85–107, 1997
- 41. Fernandez, J.-C.: An implementation of an efficient algorithm for bisimulation equivalence. Science of Computer Programming 13: 219–236, 1989/1990
- 42. Goering, R.: Model checking expands verification's scope. EE Times. Issue 939, February 3, 1997
- 43. Graf, S.: Characterization of a sequentially consistent memory and verification of a cache memory by abstraction. Distributed Computing 12(2+3): 75–99, 1999
- 44. Graf, S., Saidi, H.: Construction of abstract state graphs with PVS. In: Grumberg, O. (ed.): Computer Aided Verification (CAV '97), Haifa, Israel, June 1997. LNCS 1254. Berlin, Heidelberg, New York: Springer-Verlag, 1997, pp. 72–83
- 45. Groote, J.F., Vaandrager, F.: An efficient algorithm for branching bisimulation and stuttering equivalence. In: Paterson, M.S. (ed.): Automata, Languages and Programming (ICALP '90), Warwick, England, July 1990. LNCS 443. Berlin, Heidelberg, New York: Springer-Verlag, 1990, pp. 626–638
- 46. Hoare, C.A.R.: Communicating Sequential Processes. Prentice-Hall, London, 1985
- 47. Holzmann, G.J.: Design and Validation of Computer Proto-

cols. Prentice-Hall, 1991

- 48. Holzmann, G.J., Peled, D.: The state of SPIN. In: Alur, R., Henzinger, T. (eds.): Computer Aided Verification (CAV '96), New Brunswick, New Jersey, July 1996. LNCS 1102. Berlin, Heidelberg, New York: Springer-Verlag, 1996, pp. 385–389
- 49. Kozen, D.: Results on the propositional μ -calculus. Theoretical Computer Science 27(3): 333–354, 1983
- 50. Lamport, L.: 'sometimes' is sometimes 'not never'. In: Seventh Annual ACM Symposium on Principles of Programming Languages (POPL '80). Las Vegas, Nevada: ACM Press, 1980, pp. 174–185
- 51. Lichtenstein, O., Pnueli, A.: Checking that finite state concurrent programs satisfy their linear specification. In: Twelfth Annual ACM Symposium on Principles of Programming Languages (POPL '85) [1], pp. 97–107
- 52. Manna, Z., Pnueli, A.: The Temporal Logic of Reactive and Concurrent Systems. Berlin Berlin, Heidelberg, New York: Springer-Verlag, 1992
- 53. Margaria, T., Steffen, B. (eds.): Tools and Algorithms for the Construction and Analysis of Systems (TACAS '96), Passau, Germany 1996. LNCS 1055. Berlin, Heidelberg, New York: Springer-Verlag, 1996
- 54. Milner, R.: Communication and Concurrency. Prentice-Hall, London, 1989
- 55. Paige, R., Tarjan, R.E.: Three partition refinement algorithms. SIAM Journal of Computing 16(6): 973–989, 1987
- 56. Pnueli, A.: The temporal logic of programs. In: 18^{th} Annual Symposium on Foundations of Computer Science, Providence, Rhode Island, October/November 1977. IEEE, pp. 46–57
- 57. Queille, J.P., Sifakis, J.: Specification and verification of concurrent systems in CESAR. In: Dezani-Ciancaglini, M., Montanari, U. (eds.): Proceedings of the International Symposium in Programming. Turin, April 1982. LNCS 137. Berlin, Heidelberg, New York: Springer-Verlag, 1982, pp. 337–351
- 58. Rathje, T., Sandler, S.: CPU formal verification receives a boost. EE Times, 1996. Issue 927, November 11, 1996
- 59. Richier, J., Rodgriguez, C., Sifakis, J., Voiron, J.: Verification in XESAR of the sliding window protocol. In: Proceedings of the IFIP Symposium on Protocol Specification, Testing and Verification, Zurich, May 1987. North-Holland, pp. 235–250
- 60. Roscoe, A.W.: Model-checking CSP. In: Roscoe, A.W. (ed.): A Classical Mind: Essays in Honour of CAR Hoare. Prentice-Hall, chapter 21, pp. 353–378, February 1994
- 61. Stirling, C.: Modal and temporal logics. In: Abramsky, S., Gabbay, D., Maibaum, T.S.E. (eds.): Handbook of Logic in Computer Science, Vol. 2. Oxford University Press, pp. 477– 563, 1992
- 62. Tarski, A.: A lattice-theoretical fixpoint theorem and its applications. Pacific Journal of Mathematics 25(2): 285–309, 1955
- 63. Vardi, M., Wolper, P.: An automata-theoretic approach to automatic program verification. In: Symposium on Logic in Computer Science (LICS '86) [2], pp. 332–344