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Saving constraint checks in maintaining coarse-grained generalized arc consistency

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Abstract Constraint check plays a central role in establishing generalized arc consistency which is widely used to solve constraint satisfaction problems. In this paper, we propose a new generalized arc consistency algorithm, called GTR, which ensures that the tuples that have been checked to be allowed by a constraint will never be checked again. For each constraint, GTR maintains a dynamic list of the tuples that were checked to be allowed by this constraint and check their validities to identify some values with supports. It is equipped with a mechanism avoiding redundant validity checks. The basic GAC3 algorithm is employed to find a support for the rest values and to add new tuples to the dynamic list. The experiments show that maintaining GTR during search saves a number of constraint checks. It also brings some improvements over cpu time while solving some CSPs with tight constraints.

Keywords Constraint satisfaction · Local consistency · Backtracking

1 Introduction

Constraint check plays a central role in arc consistency (AC) [16]. Maintaining arc consistency (MAC) [15, 18] is widely used to solve binary constraint satisfaction problems (CSP). In non-binary CSPs, AC is replaced by generalized arc consistency (GAC) [4]. An efficient MGAC algorithm

usually has two features: (1) the GAC algorithm it uses is efficient, (2) it maintains few data structure during search. GAC algorithms can be classified into the coarse-grained and the fine-grained. The coarse-grained algorithms, such as AC3 and its improvements, are based on constraintoriented propagation schemes. The latter [1, 3, 17] are based on value-oriented propagation schemes. The fine-grained algorithms usually maintain elaborate data structures during search, so the coarse-grained GAC algorithms are more efficient and more popular when being used in search. The original AC3 (GAC3) algorithm has the worst-case time complexity $O(ed^3)$ ($O(er^3d^{r+1})$). By recording last supports, AC3.1 algorithm avoids some repeated constraint checks and it has an optimal worst-case time complexity $O(ed^2)$ [5], but MAC3.1 is inefficient due to its heavy data structure. MAC3.2 [9] explores multi-directional supports, but it still maintains heavy data structures during search. MAC3r algorithm [14] explores residue supports which are not maintained during search, so it costs less time than MAC3 and MAC3.1. Making use of multi-directional residues, AC3rm algorithm [10] improves AC3r. MAC3rm is efficient to solve CSPs, although its worst-case complexity is $O(ed^3)$. This is because MAC3rm also maintains few data structure during search. Exploring multiple residues, MAC3rm2 [11] is more efficient than MAC3rm. All the coarse-grained AC algorithms can be extended to GAC versions.

In some problems, we may face tight constraints, such as the constraint C_{10} in *mknap-1-3* has a tightness higher than 0.99 and the weightedSum constraints in *magicSquare*¹ problems have tightness higher than 0.95. The GAC algorithms usually execute much more constraint checks to

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¹The two instances are downloaded from http://www.cril.univ-artois. fr/~lecoutre/benchmarks.html.

enforce GAC on these tight constraints than dealing with loose constraints. For example, the mknap-1-3 instance contains eleven 20-arity constraints and only one of them, C_{10} , has tightness higher than 0.99, the tightnesses of the other constraints are between 0.25 and 0.47. We use MGAC3rm2 to solve mknap-1-3 and observe that the number of constraint checks executed on C_{10} is 1,067,710, whereas the total number on all the other constraints is 40,744. The result indicates that we should pay more attention to GAC algorithms for tight constraints, especially for those processed by a generic GAC algorithm. Although saving constraint checks does not always save time [20], it is important to save constraint checks in MGAC when constraint checks are relatively expensive. Some constraint checks are repeated in coarse-grained MGAC and [14] we identify two types of repeated constraint checks:

- Positive repeat: a constraint check for a tuple τ on a constraint *c* is performed and it is allowed by *c*. Later in the search, τ is still valid and it is checked again. Such repeated constraint checks are called positive repeats.
- Negative repeat: a constraint check for a tuple τ on a constraint c is performed and it is disallowed by c. Later in the search, τ is still valid and it is checked again. Such repeated constraint checks are called negative repeats.

In this paper, we propose a new generic GAC algorithm, called growing tabular reduction (GTR), which avoids all positive repeats and some of the negative repeats. It ensures that a tuple on a constraint c will never be checked again if it has been checked to be allowed by c. For each constraint c, it maintains a dynamic list recording the tuples that have been checked to be allowed by c. It first iterates over all active tuples in the list and checks their validities. A tuple is valid iff all the values in the tuple are present in the domains of the corresponding variables. If a tuple in the list is valid, all the values in this tuple have supports on c. Secondly, it uses constraint checks to seek supports for those values that have not found a support. If a new tuple is found to be allowed by c, it will be added into the list of c. There is extensive redundant work if we check all tuples in the list, so we propose a method to avoid redundant validity checks in maintaining GTR during search. An improved version, GTR2, is also introduced. The experimental results show that, compared with the classical coarse-grained MGAC algorithms, MGTR algorithms are not efficient on binary instances, but they save both constraint checks and cpu time on the non-binary instances with larger arity constraint that are tight.

This paper is organized as follows. Section 2 provides some technical background about CSP. The GTR algorithm and its improvement are introduced in Section 3. Section 4 presents the discussions and some related works. The experimental results and the analysis are in Section 5. Finally, conclusion and future work are in Section 6.

2 Background

A constraint satisfaction problem (CSP) P is a triple P = $\langle X, D, C \rangle$ where X is a set of n variables $X = \{x_1, x_2...x_n\},\$ D is a set of domains $D = \{dom(x_1), dom(x_2)...dom(x_n)\}$ where $dom(x_i)$ is a finite set of possible values for variable x_i , C is a set of e constraints $C = \{c_1, c_2 \dots c_e\}$. A constraint c consists of two parts, an ordered set of variables $scp(c) = \{x_{i1}, x_{i2} \dots x_{ir}\}$ and a subset of the Cartesian product $dom(x_{i1}) \times dom(x_{i2}) \times ... \times dom(x_{ir})$ that specifies the allowed (or disallowed) combinations of values for the variables $\{x_{i1}, x_{i2} \dots x_{ir}\}$. |scp(c)| is the arity of c. We use r to denote the arity of a constraint and d to denote the domain size of a variable. An element of $dom(x_{i1}) \times$ $dom(x_{i2}) \times ... \times dom(x_{ir})$ is called a tuple on scp(c), denoted by τ . $\tau[x]$ is the value of x in τ . The tightness of a constraint c is t/d^r , where t is the number of disallowed tuples on scp(c) and d^r is the number of all tuples on scp(c). Verifying if a given tuple is allowed by a constraint is called a constraint check and verifying if a given tuple is valid is called a validity check (x, a) denotes the value a for variable x.

Definition 1 (Generalized arc consistency [4]) Given a CSP $P = \langle X, D, C \rangle$, a constraint $c \in C$, and a variable $x \in scp(c)$,

- A value (x, a) is consistent with c iff there exists a valid tuple τ allowed by c and τ[x] = a. τ is called a support for (x, a) on c.
- A constraint *c* is generalized arc consistent iff $\forall x \in scp(c), dom(x) \neq \emptyset$ and $\forall a \in dom(x), (x, a)$ is consistent with *c*.
- *P* is generalized arc consistent iff all the constraints of *C* are generalized arc consistent.

To establish GAC on a constraint, GAC algorithms seek a support for every value (x, a) on the constraints involving x and remove those values without any support on these constraints. If the domain of a variable is empty, GAC fails. To seek a support for a value on a constraint, the GACvalid scheme, iterating over valid tuples to find an allowed one, is a universal technique for all kinds of constraints. The MGAC algorithm, maintaining generalized arc consistency during backtracking search, is the most popular technique to solve hard CSPs. It builds up a search tree from level 0 to level *n*, where *n* is the number of variables. At each node of the search tree, a variable *x* and a value *a* in dom(x) are selected and a GAC algorithm is used to propagate the assignment. At level 0, GAC is usually enforced to preprocess the problem before searching starts. A deadend is reached if the propagation fails, then a backtracking occurs.

The classical GAC3rm algorithm is recalled here. We present the constraint-oriented version of GAC3rm in algorithm 1 and algorithm 2. The changedVars stores all the variables in scp(c) whose domains are changed during current invocation of algorithm 2. The gacValues(x) records the values in dom(x), which have already found a support. Residue supports are used at line 6 in algorithm 2 and multidirectional residue is implemented at lines 14 to 16. The residue(x, a, c) is a residue support for (x, a) on c, which was found as the support for (x, a). If residue(x, a, c) is valid, (x, a) still has a support on c; otherwise, the FINDSUPPORT procedure is called to find a new support for (x, a). FIND-SUPPORT iterates over all valid tuples involving (x, a) on cand check whether they are allowed by c. If an allowed one is found, it returns the tuple as the new support; otherwise, it returns NULL.

Algorithm	1	GAC3
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1:	initialize Q ;
2:	while Q is not empty do
3:	$c \leftarrow$ the first constraint in Q ;
4:	remove c from Q ;
5:	$changedVars \leftarrow \text{REVISE}(c);$
6:	if changedVars = FAIL then
7:	return FAIL;
8:	for each variable x in changedVars do
9:	add all constraints involving x except c into Q ;

10: return *SUCCESS*;

Algorithm 2 GAC3RM(c:constraint)

1:	for each variable $x \in scp(c)$ and x is not assigned do
2:	$gacValues(x) \leftarrow \emptyset;$
3:	$changedVars \leftarrow \emptyset;$
4:	for each variable $x \in scp(c)$ and x is not assigned do
5:	for each value $a \in dom(x) \setminus gacValues(x)$ do
6:	if $residue(x, a, c)$ is invalid then
7:	$\tau \leftarrow \text{FINDSUPPORT}(x, a, c);$
8:	if $\tau = NULL$ then
9:	remove a from $dom(x)$;
10:	if $dom(x) = \emptyset$ then
11:	return FAIL;
12:	$changedVars \leftarrow changedVars \bigcup \{x\};$
13:	else
14:	for each $x' \in scp(c)$ and x' is not
	assigned do
15:	$gacValues(x') \leftarrow gacValues(x') \bigcup$
	$\{\tau[x']\};$
16:	<i>residue</i> ($x', \tau[x'], c$) $\leftarrow \tau$;
17:	return changedVars;

3 Growing tabular reduction: saving constraint checks during search

Before introducing the GTR algorithm, we give an example of positive repeats and negative repeats in MGAC3rm. Given a constraint c, $x \in scp(c)$ and $a \in dom(x)$, τ_1 , τ_2 , τ_3 , τ_4 , and τ_5 are tuples involving (x, a) on scp(c), where τ_3 and τ_5 satisfy c. At the beginning, all the five tuples are valid. After checking τ_1 , τ_2 , and τ_3 , GAC3rm finds τ_3 as the support for (x, a) and records it as the residue support. (x, a) has a support as long as τ_3 is still valid. Later in the search, assuming τ_3 loses its validity due to some assignment and τ_1 , τ_2 , τ_4 , and τ_5 are still valid, so during the search for another support, τ_1 and τ_2 are checked again. These constraint checks on τ_1 and τ_2 are negative repeats. After searching, τ_5 is found as a new support and it is recorded as new residue support for (x, a). The old residue support τ_3 is discarded. Later in the search, a backtracking occurs, assuming τ_5 loses its validity and τ_3 becomes valid again. Now, GAC3rm will seek a new support for (x, a) because τ_5 is no longer valid. It will check all valid tuples, so τ_3 is checked again. This constraint check on τ_3 is a positive repeat. Before this positive repeat on τ_3 , if τ_1 and τ_2 are still valid, then there are another two negative repeats. If we did not discard the old residue support τ_3 when it became invalid and restore it after it becomes valid again, then this positive repeat will be avoided and the corresponding two negative repeats are also avoided.

In this section, we propose a new coarse-grained GAC algorithm avoiding all positive repeats, named growing tabular reduction (GTR). The GTR algorithm maintains a dynamic list of the tuples for each constraint c, which have been checked to be allowed by c. The algorithm contains two parts. In the first part, it iterates over all the recorded tuples in the list and check their validities. The values appearing in a valid one have supports on c. In the second part, it uses constraint checks to search for supports for only those values that have not found a support. This part is similar to what GAC3 does. If a new tuple is found as a new support of a value, it will be added into the dynamic list. Obviously, it may execute a great number of validity checks if we use this simple strategy during search. To solve this problem, we propose a method to avoid redundant validity checks. For each constraint, it records the tuples deleted at each level and restores them after a backtracking occurs. This method ensures that the tuples that have been verified to be invalid at level *i* will not be rechecked at level *j* (j > i). If a backtracking occurs, restoring tuples for each constraint can be done in constant time. In the following data structures, the tuples mean only the tuples that have been checked to satisfy constraint c, not all tuples satisfying c.

• *tupleList(c)* is a dynamic array of tuples. It records all the tuples that have been checked to be allowed by *c*. New tuples will be added at the end of the array. The

tuples in tupleList(c), which have not been verified to be invalid, are called active tuples.

- *firstActive*(*c*) is the position of the first active tuple in *tupleList*(*c*). It is initialized to 0. All active tuples in *tupleList*(*c*) are indexed from *tupleList*(*c*).*length*-1 to *firstActive*(*c*). The tuples before *firstActive*(*c*) are deleted.
- *levelLast*(*c*) is an array of size *n* + 1 where *n* is the number of variables. *levelLast*(*c*)[*p*] is the position of the last invalid tuple of *tupleList*(*c*) removed when the search was at level *p*. Level 0 corresponds to the preprocessing step. *levelLast*(*c*)[*p*] = −1 if no tuple was removed at level *p*. It is used to record and restore *firstActive*(*c*) at each level during search.

The GTR algorithm for a constraint is present in algorithm 3 and it is called at line 5 in algorithm 1. When MGTR is propagating at level *i*, all the tuples in *tupleList(c)* before *firstActive(c)* have been verified to be invalid at previous levels, so they will not be checked. The first part of GTR is implemented at lines 3 to 11 and the second part is from lines 13 to 24. In part1, all the values with residue supports are identified. In part2, a GAC3 scheme is employed to find a support for the rest values. If a support is found, it is added to the end of *tupleList(c)* at line 22. If lines 3 to 11 and lines 22 to 24 are removed, algorithm 3 degenerates into a constraint-oriented GAC3 algorithm.

Algorithm 3 GTR(c:constraint, *level*:integer)

```
1: for each variable x \in scp(c) and x is not assigned do
 2:
         gacValues(x) \leftarrow \emptyset;
 3: index \leftarrow tupleList(c).length-1;
 4: while index \ge firstActive(c) do
 5:
          \tau \leftarrow tupleList(c)[index];
         if \tau is valid then
 6:
 7:
              for each x \in scp(c) and x is not assigned do
 8:
                   gacValues(x) \leftarrow gacValues(x) \bigcup \{\tau[x]\};
 9:
              index \leftarrow index -1;
10:
         else
              REMOVETUPLE(index, level, c);
11:
12: changedVars \leftarrow \emptyset;
     for each variable x \in scp(c) and x is not assigned do
13:
          for each value a \in dom(x) \setminus gacValues(x) do
14:
15:
              \tau \leftarrow \text{FINDSUPPORT}(x, a, c);
              if \tau = NULL then
16:
                   remove a from dom(x);
17:
18:
                   if dom(x) = \emptyset then
19:
                       return FAIL;
20:
                   changedVars \leftarrow changedVars \lfloor \ \rfloor \{x\};
21:
              else
                   add \tau to the end of tupleList(c);
22:
                   for each x \in scp(c) and x is not assigned do
23:
                       gacValues(x) \leftarrow gacValues(x) \mid \{\tau[x]\};
24:
25: return changedVars;
```

Algorithm 4 removes the tuple indexed at *i* by switching it with the first active tuple indexed by *firstActive*(*c*) and increasing *firstActive*(*c*) by 1. In this way, all the deleted tuples are moved to the position before *firstActive*(*c*), so they will not be rechecked if no backtracking occurs. If a backtracking occurs at level *i*, the RESTORE procedure in algorithm 5 restores the tuples deleted at level *i* by simply recovering *firstActive*(*c*). After the restoring, the order of the active tuples may be different. This is not a problem, because all tuples after *firstActive*(*c*) will be checked in next invocation. This mechanism also ensures that the new added tuples will not be missed.

Algorithm 4 REMOVETUPLE(*i*, *p*:integer, *c*:constraint)

- 1: **if** levelLast(c)[p] = -1 **then**
- 2: $levelLast(c)[p] \leftarrow firstActive(c);$
- 3: *tempTuple* \leftarrow *tupleList*(*c*)[*i*];
- 4: $tupleList(c)[i] \leftarrow tupleList(c)[firstActive(c)];$
- 5: $tupleList(c)[firstActive(c)] \leftarrow tempTuple;$
- 6: $firstActive(c) \leftarrow firstActive(c) + 1;$

Algorithm 5 RESTORE(D.Integel, C.Constrain	RE(<i>p</i> :integer. <i>c</i> :constraint)	RESTORE(Algorithm 5
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- 1: **if** $levelLast(c)[p] \neq -1$ **then**
- 2: $firstActive(c) \leftarrow levelLast(c)[p];$
- 3: $levelLast(c)[p] \leftarrow -1;$

The example in Fig. 1 illustrates how the data structures of GTR work. A constraint *c* defined by a predicate x + y = z, $dom(x) = dom(y) = \{1, 2, 3\}$ and $dom(z) = \{3, 4, 5\}$. At the beginning, tupleList(c) is empty, firstActive(c) is 0 and the elements in levelLast(c) are -1.

- (a) The first invocation skips part1. It finds the supports for all values and adds the tuples into *tupleList*(c) at part2.
- (b) At level 1, (x, 1) is removed by other constraint. At part1, the algorithm checks tuples indexed from 4 to 0 and (1,2,3),(1,3,4) are removed. At part2, (2,2,4) is found as the support for (y, 2) and it is added to the end of *tupleList*(c).
- (c) At level 2, (y, 3) is removed. At part1, the algorithm checks tuples indexed from 5 to 2 and (2,3,5) is removed. As the new support for (z, 5), (3,2,5) is added to the end.
- (d) Assuming that c is never checked again until the searching backtracks to level 1. After the backtracking, both (x, 1) and (y, 3) are restored. In order to restore the deleted tuples, *firstActive(c)* is set to *levelLast(c)*[1] in Fig. 1b. Then, *levelLast(c)*[1] is set to -1. In the 4th invocation, all the 7 tuples will be checked at part1.

Fig. 1 Data structures of GTR



Proposition 1 The worst-case time complexity of GTR to establish GAC at the preprocessing step is $O(er^3d^{r+1})$ with space complexity $O(er^2d)$.

Proof At the preprocessing step, the tuples in tupleList(c)which are verified to be invalid can be discarded. In the worst case, each tuple in tupleList(c) supports only one value, so there are at most rd tuples in tupleList(c) at the preprocessing step. Therefore, the part1 of algorithm 3 costs r^2d time, because each validity check costs r time. The part2 cost $r^2 d^r$ time, because there are r variables in scp(c), d values in the domain of each variable, FINDSUP-PORT(x, a, c) may iterate over at most d^{r-1} tuples and each constraint check costs r time. So the worst-case time complexity of algorithm 3 is $O(r^2d^r)$. For each constraint c, algorithm 3 will be called at most rd times and there are e constraints; therefore, the worst-case time complexity to establish GAC is $O(er^3d^{r+1})$. As for space, there are at most *rd* tuples in tupleList(c), each tuple needs *r* space and there are *e* constraints, so the worst-case space complexity is $O(er^2d)$.

Proposition 2 The worst-case space complexity of maintaining GTR during search is $O(erd^r)$.

Proposition 2 is straightforward, because the total number of tuples recorded in tupleList(c) is at most d^r when GTR is maintained during search.

Proposition 3 *The GTR algorithm avoids all positive repeats when it is maintained during search.*

Proof For each constraint c, all the tuples that are checked to be allowed are stored in *tupleList(c)*, so a positive repeat occurs iff a tuple in *tupleList(c)* is checked. During each invocation, all the tuples before *firstActive(c)* are invalid, so they will not be checked. The tuples after *firstActive(c)* are valid and all the values appearing in these tuples are identified as having supports. In part2, the algorithm seeks supports for only those values that have not been identified as having supports and each of the tuples after *firstActive(c)* contains at least one value that has been identified as having supports, so these tuples will not be checked. Therefore, no positive repeat occurs when maintaining GTR during search.

Property 1 For each constraint c, there is no duplicate tuple in tupleList(c).

The property is true, because maintaining GTR during search has no positive repeat, so a tuple allowed by c will be checked and added into *tupleList*(c) at most once.

Besides naive GTR, the algorithm can be improved by the methods used in STR2. The improved version, GTR2, is shown in algorithm 6. GTR2 does not improve the worst-case time complexity, but it avoids some unnecessary operations. The additional data structures used in GTR2 is same as those used in STR2. The S^{sup} records the variables that at least one value has not found a support. If all values in dom(x) have already found a support, we remove x from S^{sup} , then efficiency is gained by iterating over only variables in S^{sup} at lines 15, 24, and 34. The S^{val} records the variables whose domain was changed between last invocation and this invocation. The lastSize(c)[x] records the domain size of x after each invocation of constraint c and is used to determine whether the domain of a variable was changed recently. To check the validities of the tuples, we do not check the values of variable x if dom(x) was not changed between last invocation and this invocation. Therefore, at line 14 of algorithm 6, the procedure is Valid(τ , S^{val}) checks only the variables in S^{val} to verify if τ is valid.

Algorithm 6 GTR2(c:constraint, level:integer) 1: $S^{sup} \leftarrow \emptyset$: 2: $S^{val} \leftarrow \emptyset$; 3: if the last assigned variable $x_l \in scp(c)$ then $S^{val} \leftarrow S^{val} \mid J\{x_l\};$ 4: for each variable $x \in scp(c)$ and x is not assigned do 5: 6: $gacValues(x) \leftarrow \emptyset;$ $S^{sup} \leftarrow S^{sup} \mid J \{x\};$ 7: 8: if $|dom(x)| \neq lastSize(c)[x]$ then $S^{val} \leftarrow S^{val} \cup \{x\};$ 9: $lastSize(c)[x] \leftarrow |dom(x)|;$ 10: 11: $index \leftarrow tupleList(c).length-1;$ while $index \geq firstActive(c)$ do 12: $\tau \leftarrow tupleList(c)[index];$ 13: **if** is Valid (τ, S^{val}) **then** 14: for each $x \in S^{sup}$ do 15: if $\tau[x] \notin gacValues(x)$ then 16: 17: $gacValues(x) \leftarrow gacValues(x) \bigcup \{\tau[x]\};$ if |gacValues(x)| = |dom(x)| then 18: $S^{sup} \leftarrow S^{sup} \setminus \{x\};$ 19: index \leftarrow index -1; 20: 21: else REMOVETUPLE(*index*, *level*, *c*); 22: changedVars $\leftarrow \emptyset$; 23: for each variable $x \in S^{sup}$ do 24: for each value $a \in dom(x) \setminus gacValues(x)$ do 25: $\tau \leftarrow \text{FINDSUPPORT}(x, a, c);$ 26: if $\tau = NULL$ then 27: remove *a* from dom(x); 28: if $dom(x) = \emptyset$ then 29: return FAIL; 30: changedVars \leftarrow changedVars $\mid | \{x\};$ 31: 32: else 33. add τ to the end of *tupleList*(*c*); for each $x \in S^{sup}$ do 34: if $\tau[x] \notin gacValues(x)$ then 35: $gacValues(x) \leftarrow gacValues(x)$ 36: $\{\tau[x]\};$ if |gacValues(x)| = |dom(x)| then 37: $S^{sup} \leftarrow S^{sup} \setminus \{x\};$ 38: 39: $lastSize(c)[x] \leftarrow |dom(x)|;$ 40: return changedVars;

4 Discussion and related works

GTR avoids all positive repeats and the corresponding negative repeats executed before each positive repeat. However, the negative repeats for proving a value having no support cannot be avoided by GTR. When GTR is maintained during search, the part1 iterates over $O((1-t)d^r)$ allowed tuples where t is constraint tightness. For each value, the part2 tries $O(td^{r-1})$ disallowed tuples before finding a support. If the constraint is tight, $(1-t)d^r$ is a relatively small number and it is relatively harder to find a support for a value; therefore, GTR should be efficient when being used on tight constraints. On the contrary, if the constraint is loose, it is relatively easier to find a support and $(1-t)d^r$ may be a large number, so GTR may be inefficient for loose constraints.

The part1 of GTR is similar to the simple tabular reduction (STR) algorithm [8, 13, 19, 21] which operates on extensional constraints listing all allowed tuples in a table. Both them identify the values which have supports by iterating over the allowed tuples. One difference is that STR iterates over a static table of allowed tuples of a constraint, whereas GTR iterates over a dynamic list of tuples that are checked to be allowed. When a backtracking occurs, STR has a mechanism to restore tuples with few time cost, which is suitable for only static tables. GTR employs a similar mechanism to restore deleted tuples with the same time cost, which works on dynamic lists. Another difference is that STR determines if a value has supports after iterating the table, because all allowed tuples are already in the table, whereas GTR usually identifies some of the values with supports after iterating the dynamic list, for the rest values, it uses constraint checks to determine if they have supports. We prefer STR to GTR on extensional constraints, because STR is a specialised algorithm for these constraints. However, GTR is a generic GAC algorithm that works on all kinds of constraints.

GTR is also similar to the GIC4 algorithm proposed in [2], but they enforce different consistencies. Another difference between them is that GTR records the results of constraint checks for each constraint, whereas GIC4 maintains only one list of global solutions. GIC4 enforces global consistency, so maintaining GIC4 during search in backtrack-free and it does not restore deleted solutions. The major advancement of GTR over GIC4 is that GTR has a mechanism to cope with backtracking and the deleted tuples can be restored with low cost.

Exploring multiple residue supports, the GAC3rm_k algorithms [11], where k is the maximum number of residues, enforces less positive repeats than GAC3rm. When searching for a support for a value, GAC3rm_k first checks the validities of the k residues. If none of the residues is valid, it starts to search for a new support by constraint checks. If k is set to infinity, no residue support is discarded,

Table 1 Results on non-binary

			2
instances	with	tight	constraints

BIBD (30)cp23.6220.2187.51 (1 out)15.3710.636 (1 out)22.31cc27KM224M-127M127M-203Mvc20M33M-127M127M-20Km11K21K-119K119K-20KBIBD 6-30-15-36cpu14913229853481059117cc2.4B2.1B0.8B0.8B0.8B13.7B1.6Bvc20M25M10840M40M40M23Mm4K8K212K212K212K4K8KBIBD 7-42-18-36cpu20.8198236161156out177cc5.40M429M100K100K100K100K-14KCostas Array (8)cpu25.2925.0337.1223.142.192.9.542.9.47cc5.40M429M300M300M300M300M300M21.1M4.5cc5.601.552.602.61X2.61X2.61X2.61X2.61X2.7cc5.601.553.73.722.142.192.12K2.1cc5.64M3.91M3.91M3.91M3.91M3.91M3.91M3.91Mcc7.1M5.64M3.91M3.91M3.91M3.91M3.91M3.91M3.91Mcc7.1M5.64M3.92M1.5B<	Instance		GAC3rm	GAC3rm2	GAC3rm_inf	GTR	GTR2	GAC3.1	GAC3.2
cccc27M24M-127M127M20Mva25M33M-42M42M-28MBIBD 6-30-15-45cu14913229853481059117cc2.4B2.1B0.8B0.8B0.8B0.3B1.37B1.6Bcc2.4B2.1B0.8B0.8B0.2B1.37B1.6Bcc2.4B2.1B2.0S1.611.560ut1.77mm8.C1.2B1.2B1.2B1.2B1.2Bcc5.4M1.982.3B1.301.611.560.011.78cc5.4M1.5B1.3M1.3M-7.5Mm8.C1.5K100K100K100K100K1.2Bcc7.4M1.5K100K100K100K1.2D2.542.74cc1.7M2.5M2.5M2.712.711.722.742.74cc1.8M2.8M1.5B2.6M3.0	BIBD (30)	cpu	23.62	20.21	87.51 (1 out)	15.03	13.27	106.86 (1 out)	22.31
vc mvc m25M m33M m-42M m42M m-28M mBIBD 6-30-15-36 cc24B c c21B m28B c m21B m0.8B m <td></td> <td>cc</td> <td>278M</td> <td>224M</td> <td>-</td> <td>127M</td> <td>127M</td> <td>-</td> <td>203M</td>		cc	278M	224M	-	127M	127M	-	203M
nnnnlikliklikliklikliklikliklikliklikBIBD 6-30-15-36cp2482.180.880.880.380.3780.380.3780.380.3780.380.3780.380.3780.380.3780.380.3780.380.378<		vc	25M	33M	_	42M	42M	-	28M
BIBD 6-30-15-3-6 cp 149 132 298 53 48 1059 117 vc 2.4B 2.1B 0.8B 0.		rn	11K	21K	-	119K	119K	-	20K
cc vc 	BIBD 6-30-15-3-6	cpu	149	132	298	53	48	1059	117
ve mve m20M25M10B40M40M20M23MBIBD 7-42-18-504K8K212K212K12K4K8KBIBD 7-42-18-50-24B19B19B1.9B-1.7Bce2.4B2.3B1.9B1.9B-1.7Bre6.7M8M1.6B13M13M2.9C7.5Mre5.40M420M300M300M300M301M458Mre9.8M2.9S42.62K2.62K2.62K3.8Q2.7Kre9.8M9.2K2.62K2.62K2.62K3.8Q2.9Kre9.1M5.9LM3.9LM3.1M3.9LM4.5M6.0SHre7.1M5.4M3.9LM3.1M3.9LM4.5M6.0SHre7.1M5.4M3.9LM3.1M3.9LM4.5M6.0SHre7.1M5.4M3.9LM3.1M3.9LM4.5M6.0SHre7.1M5.4M3.9LM3.1M3.1M4.5M6.0SHre7.1M5.4M5.7K7.7K7.7K7.7K7.7Kre7.1M5.4M7.7K7.7K7.7K7.7K7.7Kre7.1M5.7K7.7K7.7K7.7K7.7K7.7Kre7.7K7.7K7.7K7.7K7.7K7.7K7.7Kre7.7K7.7K7.7K7.7K7.7K7.7K7.7Kre <td></td> <td>cc</td> <td>2.4B</td> <td>2.1B</td> <td>0.8B</td> <td>0.8B</td> <td>0.8B</td> <td>13.7B</td> <td>1.6B</td>		cc	2.4B	2.1B	0.8B	0.8B	0.8B	13.7B	1.6B
mm4K8K212K212K4K8K9KBIBD 7-42-18-36cp2081982361617.500rt177cp2.3B2.3B1.9B1.9B1.9B1.9B1.9B1.9B1.7Bvp6.7M8M1.6B13M1.3M-1.7Bvp5.2925.033.7122.142.022.5442.947vp198m28M1.5B200M301M301M341M2.21Mvp198m2.81M3.552.62K2.62K4.7K2.2K3.52vp3.153.2.9243.352.9.122.763.8.023.8.2vp3.153.2.9243.352.9.122.763.8.023.8.2vpvp3.153.2.9243.352.9.122.763.8.023.8.2vpvp1.54M3.91M3.91M3.91M453M2.91M2.91Mvpvp1.54M3.91M3.91M3.91M4.53M2.91M2.91Mvpvp1.521.502.311.041.227.81.22KCostas Array-10vp1.521.502.31M1.601.601.281.38vpvp3.6K1.75K7.8K1.22K7.8K1.32K1.35Kvpvp1.521.55K7.5K1.6K1.6K1.22K1.5Kvpvp2.7K1.6K1.		vc	20M	25M	10B	40M	40M	20M	23M
BIBD 7-42-18-3-6 co		rn	4K	8K	212K	212K	212K	4K	8K
cc2.4B2.3B1.9B1.9B1.9B1.9B1.7Bvc6.7M8M1.6B13M13M-7.5Mvc6.7M8K15K100K100K10K2.742.5MCostas Array(8)c252.9225.0337.122.1421.922.542.947cc540M429M300M300M300M301M41M458Mvc198M2.9M1.5B260M20M199M221Mru313.153.2243.352.122.76538.0238.25cc711M564M391M391M312M453M605Mvc261M392M1.8B312M312M261M291Mru7.3K1.45K50.7K50.7K50.7K7.3K11.2KCostas Array-10ru7.3K1.45K50.7K50.7K50.7K7.3K11.2KCostas Array-17ru3.2B2.6B1.8B1.8B1.8B1.8B1.8B1.8B1.8B1.8B2.1B2.8Bru1.2B1.6M	BIBD 7-42-18-3-6	cpu	208	198	236	161	156	out	177
ve6.7M8M1.6B13M13M-7.5Mrn8K15K100K100K100K-14KCostas Array (8)92.5925.0337.1223.1421.9229.5429.47rc540M428M300M300M300M30M30M20M41M458Mvc198M288M1.5B260K26.2K4.7K7.2KCostas Array-16rp33.1532.9243.3529.1227.6538.0238.25costas Array-17rp161M392M1.8B312M312M453M605Mro7.3K14.5K50.7K50.7K50.7K7.8K11.2KCostas Array-17rp152150231140132.7517811.2KCostas Array-17rp1521.6B1.8B1.8B1.8B2.1B2.8Bro3.2B2.6B1.8B1.8B1.8B2.1B1.35Kradar 8-30-3-0-15rpu0.550.670.600.550.830.71rc2.2M2.0M1.6M1.6M1.6M2.5M2.3M2.3Kradar 8-30-3-0-15rpu0.750.690.670.600.550.830.71rd2.2M2.0M1.6M1.6M1.6M2.5M2.3M2.3Kradar 8-30-3-0-15rpu0.2790.3660.300.2810.4G0.34rd2.2M<		cc	2.4B	2.3B	1.9B	1.9B	1.9B	-	1.7B
rm8K15K100K100K100K100K100K20.414KCostas Array(8)c540M429M300M300M300M31M41M458Mco540M298M1.5B260M260M199M21Mrm4.7K9.2K26.2K26.2K26.2K4.7K7.2KCostas Array-16cp33.1532.9243.3529.1227.6538.0238.25cc711M564M391M391M391M453M605Mvc261M392M1.8B312M312M261M291Mrm7.3K14.5K50.7K50.7K50.7K7.3K11.2KCostas Array-17cu3.2B2.6B1.8B1.8B1.8B1.8B1.8B1.8B1.8B1.8B1.8B1.6B1.6B1.2B1.3FKradar 8-30-3-015cp0.750.690.670.600.570.830.711.2K1.2Kradar 8-30-3-015cp0.750.670.600.541.2K1.3K1.3K1.3K1.2K1.3K <td< td=""><td></td><td>vc</td><td>6.7M</td><td>8M</td><td>1.6B</td><td>13M</td><td>13M</td><td>_</td><td>7.5M</td></td<>		vc	6.7M	8M	1.6B	13M	13M	_	7.5M
Costas Array (8)cpu25.2925.0337.1223.1421.9229.5429.47cc540M429M300M300M300M300M341M458Mwc198M258M1.5B260M260M260M204M221Mm4.7K928M26.2K26.2K26.2K4.7K7.2KCostas Array-16cpu33.1532.9243.3529.1227.6538.0238.25cc711M564M391M391M391M453M605Mvc261M392M1.8B312M312M261M291Mm7.3K14.5K50.7K50.7K50.7K7.7K17.8Costas Array-17cpu1521502311401.2.751781.7KCostas Array-17cpu1521.5D2311401.6Z1.2B1.3Bradar 8-30-3-015cpu1.2B1.8B1.8B1.8B1.8B2.1B2.8Bradar 8-30-3-015cpu0.750.690.670.600.550.830.71radar 8-30-3-015cpu0.750.690.670.600.550.830.71radar 8-30-3-015cpu0.750.690.670.600.550.830.71radar 8-30-3-015cpu0.750.750.750.750.750.750.760.74radar 8-30-3-015cpu0.750.750.750.75 </td <td></td> <td>rn</td> <td>8K</td> <td>15K</td> <td>100K</td> <td>100K</td> <td>100K</td> <td>_</td> <td>14K</td>		rn	8K	15K	100K	100K	100K	_	14K
cc540M429M300M300M300M341M458Mvc198M289M1.5B260M260M199M221Mrm4.7K9.2K26.2K26.2K26.2K36.2S38.25Costas Array-16icu31.1M504M391M31LM453M605Mvc261M392M1.8B312M312M451M291M700701152150231140132.7517811.2KCostas Array-17icu1.2E1502311401.32.751781.3BCostas Array-17icu1.2E1502311401.52.51781.3BCostas Array-17icu1.2E1.5K79K79K79K8.8K1.3BCostas Array-18icu1.2B1.6B1.6B1.6B1.6B1.3B1.3BCostas Array-19icu1.75.K79K79K78K8.8K3.2K2.0Hicu1.2B2.0M1.6M1.6M1.6M1.6M2.5M2.0Hicu2.2M2.0M1.6M1.6M1.6M2.5M2.0H2.5Micu2.2M2.0M1.6M1.6M1.6M2.5M2.0H2.5Micu2.2M2.0M1.6M1.6M1.6M2.5M2.0H3.3Gicu2.2M2.0M1.6M1.6M1.6M2.5M3.0K3.3Gicu2.2M2.2M <td< td=""><td>Costas Array (8)</td><td>cpu</td><td>25.29</td><td>25.03</td><td>37.12</td><td>23.14</td><td>21.92</td><td>29.54</td><td>29.47</td></td<>	Costas Array (8)	cpu	25.29	25.03	37.12	23.14	21.92	29.54	29.47
vcive		cc	540M	429M	300M	300M	300M	341M	458M
mm4.7K9.2K26.2K26.2K26.2K4.7K7.2KCostas Array-16cpu33.1532.9243.3529.1227.6538.0238.25cc711M564M391M391M391M453M605Mvc261M392M1.8B312M312M261M291Mm7.3K15023114050.7K50.7K7.3K112KCostas Array-17cc3.2B2.6B1.8B1.8B1.8B2.1B2.8Bvc1.2B1.6B9.4B1.6B1.6B1.2B1.3Bradar 8-30-3-0-15cpu0.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M326Kradar 8-30-3-0-15cpu0.750.570.570.530.310.71cc2.2M2.0M1.6M1.6M1.6M2.5M326Kradar 8-30-3-0-15cpu0.750.570.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M1.6M1.6M326K326Kradar 8-30-3-0-15cpu0.2F0.2F0.2F0.830.712.5M326Kradar 8-30-3-0-15cpu0.2F0.2M1.6M1.6M1.6M1.6M3.5M326Kradar 8-30-3-0-15cpu0.2F0.2M1.6M1.6M1.6M1.6M		vc	198M	298M	1.5B	260M	260M	199M	221M
Costas Array-16cpu33.1532.9243.3529.1227.6538.0238.25cc711M564M391M391M391M453M605Mvc261M392M1.8B312M312M261M291Mm7.3K14.5K50.7K50.7K50.7K7.3K11.2KCostas Array-17cpu152150231140132.75178177cc3.2B2.6B1.8B1.8B1.8B1.8B1.2B1.3Bm8.8K1.75K79K79K79K8.8K1.35Kradar 8-30-3-0-15cpu0.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mvc2.44K357K10M787K787K243K326Kmknap-1-3cpu0.2190.2790.3860.300.2810.4060.331cc1.11M1.09M1.09M1.09M1.26M1.08M59Kmknap-1-4cpu2.5218453180771372209mknap-1-4cpu2.5218453180171312M209mknap-1-4cpu2.5M13M28B29M9.5M11M11Mmagic-Square-5cpu3.065.982.102.073.02.32magic-Square-6cpu1.5M2.9K112K112K </td <td></td> <td>rn</td> <td>4.7K</td> <td>9.2K</td> <td>26.2K</td> <td>26.2K</td> <td>26.2K</td> <td>4.7K</td> <td>7.2K</td>		rn	4.7K	9.2K	26.2K	26.2K	26.2K	4.7K	7.2K
ccr11M564M391M391M391M453M605Mvc261M392M1.8B312M21M291Mvc7.3K14.5K50.7K50.7K50.7K7.3K11.2KCostas Array-17cpu1521502311401.32.75178177vc1.2B1.8B9.4B1.6B1.6B2.1B1.3B3.81radar 8-30-3-0-15m8.8K17.5K79K79K79K79K79Kvc2.2M0.690.670.600.550.830.71vc2.4K357K10M787K78K243K326Kradar 8-30-3-0-15m2.6K5K10M787K78K243K326Kvc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mvc2.2M2.0M1.0M787K78K243K326Kmanap-1-3m2.6K5K18K18K18K436K63Kmanap-1-3m2.5M53K732K50K50K94K53Kmanap-1-4m21.1M1.09M1.09M1.09M1.26K50K50K50K94K53Kmanap-1-5m23.5K2.5M732K50K50K50K3.6K63K32K32K32K32K32Kmanap-1-4m25.5K2.5K3.6K5.9K5.1K51K51K51K51K<	Costas Array-16	cpu	33.15	32.92	43.35	29.12	27.65	38.02	38.25
vc261M392M1.8B312M312M261M291Mrm7.3K14.5K50.7K50.7K7.3K11.2KCostas Array-17cpu152150231140132.75178177cc3.2B2.6B1.8B1.8B1.8B2.1B2.8Bvc1.2B1.8B9.4B1.6B1.6B1.2B1.3Brm8.8K17.5K79K79K79K8.8K13.5Kradar 8-30-3-0-15cpu0.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mrdar 8-30-3-0-15cpu0.210.200.670.600.550.830.71rdar 8-30-3-0-16rm2.2M35K10M787K78K243K326Kradar 8-30-3-0-16rpu0.210.2010.670.600.550.830.71rdar 8-30-3-0-16rpu2.2M35K10M787K78K243K326Kradar 8-30-3-0-16rpu2.2M35K10M787K78K243K326Kradar 8-30-3-0-16rpu2.6K5K18K18K16M1.6M2.6K326Kradar 8-30-3-0-16rpu0.210.2790.3860.300.2810.4060.343reu1.6K5K18K18K18K18M1.6M1.6M1.6Mreu </td <td></td> <td>cc</td> <td>711M</td> <td>564M</td> <td>391M</td> <td>391M</td> <td>391M</td> <td>453M</td> <td>605M</td>		cc	711M	564M	391M	391M	391M	453M	605M
rnr.3K14.5K50.7K50.7K50.7K7.3K11.2KCostas Array-17rpu152150231140132.75178177cc3.2B2.6B1.8B1.8B1.8B1.8B2.1B2.8Brcu1.2B1.8B9.4B1.6B1.6B1.2B1.3Bradar 8-30-3-015cpu0.70.690.670.600.550.830.71cc2.4W357K10M78K78K2.5M2.0Mrdu2.4K357K10M1.6M1.6M2.5M2.0Mrdu2.4K357K10M787K2.4K2.0Krdu2.6K5K18K18K18K2.6K326Krm2.6K5K18K1.6M1.6M2.5M2.0Mrm2.6K5K18K18K18K2.6K326Krm2.6K5K18K18K2.6K4.8Kmknap-1-3cc1.11M1.09M1.09M1.09M1.26Mrcu49.6K65K732K50K50K49.4K59Krm43.88668K8K438670rm49.6K134M28B29M29M9.5M11Mrm6141218591K51K51K61K49.4Krm6141218591K591K591K51K43M43Mrm6.3M2.3M		vc	261M	392M	1.8B	312M	312M	261M	291M
Costas Array-17 cecpu152150231140132.75178177ce3.2B2.6B1.8B1.8B1.8B1.8B2.1B2.8Bve1.2B1.8B9.4B1.6B1.6B1.2B1.3Bradar 8-30-3-0-15cpu0.750.690.670.600.550.830.71ce2.2M2.0M1.6M1.6M1.6M2.5M2.0M2.0Mradar 8-30-3-0-17ce2.2M2.0M1.6M787K787K243K326Kradar 8-30-3-0-17ce2.2M2.0M1.6M1.6M1.6M2.5M2.0Mradar 8-30-3-0-17ce2.2M2.0M1.6M787K787K243K326Kradar 8-30-3-0-17ce2.4K357K10M787K787K243K326Kradar 8-30-3-0-172.6K5K18K18K18K1.6M1.08Mradar 8-30-3-0-170.2910.2910.2920.3860.300.2810.4060.343radar 8-300.2910.2910.2920.38650K50K49.4K59K59Kradar 8-16ce1.11M1.10M1.09M1.09M1.09M1.26M1.08Mradar 8-1723218453180717372209radar 8-16ce714722092.102.013.02.32radar 8-16ce714714714 <td></td> <td>rn</td> <td>7.3K</td> <td>14.5K</td> <td>50.7K</td> <td>50.7K</td> <td>50.7K</td> <td>7.3K</td> <td>11.2K</td>		rn	7.3K	14.5K	50.7K	50.7K	50.7K	7.3K	11.2K
cc3.2B2.6B1.8B1.8B1.8B2.1B2.8Bvc1.2B1.8B9.4B1.6B1.6B1.2B1.3Bradar 8-30-3-0-15cu0.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mvc244K357K10M787K787K243K326Kmknap-1-3cu0.2910.2790.3860.300.2810.4060.343cc1.11M1.00M1.09M1.09M1.26M1.08Mvc9.66K65K732K50K50K49.4K59Kmknap-1-4cu2352184531807/1372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M29M9.5M11Mmagic-Square-5cu9.4M163M81M81M81M135M2.32ragic-Square-6cu1.79016.7214.9813.4613.3028.3913.76ragic-Square-7cu1.5M2.99K112K112K12K15K2.99Kragic-Square-6cu1.5M2.1M26M5.0M5.0M3.0K5.7Kragic-Square-7cu1.5M2.1M26M5.0M5.0M1.5M1.9Mragic-Square-6cu1.5M2.1M26M5.0M5.0M<	Costas Array-17	cpu	152	150	231	140	132.75	178	177
vc1.2B1.8B9.4B1.6B1.6B1.2B1.3Bradar 8-30-3-0-150.750.8K17.5K79K79K8.8K13.5Kradar 8-30-3-0-150.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mcc2.2M357K10M787K787K243K326Kmmanper 1.30.20.210.2790.3860.300.2810.4060.343mknap-1-30.20.210.2790.3860.300.2810.4060.343mknap-1-40.24.96K65K732K50K49.4K59K1.08Mmknap-1-41.01.09M1.09M1.09M1.26M1.08M59Kmknap-1-41.3325K21885K8K8K8K36670mknap-1-41.01.08M1.09M1.09M1.09M1.09M1.09M1.09M1.08Mmknap-1-41.01.03M1.09M1.09M1.04M59K31M51M51Kmknap-1-41.01.35M1.01M1.09M1.09M1.09M1.02M1.02M51Kmknap-1-41.01.35M1.01M1.02M1.02M51K51K3.01M51Kmknap-1-51.01.35M1.31M1.31M1.31M1.31M1.12K1.12K1.25K2.32Kmknap-1-61.31.31M <td>-</td> <td>сс</td> <td>3.2B</td> <td>2.6B</td> <td>1.8B</td> <td>1.8B</td> <td>1.8B</td> <td>2.1B</td> <td>2.8B</td>	-	сс	3.2B	2.6B	1.8B	1.8B	1.8B	2.1B	2.8B
radar 8-30-3-0-15rm8.8K17.5K79K79K79K8.8K13.5Kradar 8-30-3-0-15Cpu0.750.690.670.600.550.830.71cu2.2M2.0M1.6M1.6M1.6M2.5M2.0Mcu2.4KK357K10M787K787K243K326Kmknap-1-3Cu0.210.2790.3860.300.2810.4060.343cu1.11M1.0M1.09M1.09M1.09M1.26M1.08Mmknap-1-4Qu2.910.2790.3860.300.2810.4060.343mknap-1-4Qu0.2910.2790.3860.300.2810.4060.343mknap-1-4Yu9.46K65K732K50K50K49.4K59Kmknap-1-4Qu235218453180171372209mknap-1-4Qu2.55M13M28B29M9.1M11M11Mmknap-1-49.5M13M28B29M9.5M11M11M12M12Kmknap-1-49.35M3.065.982.102.013.02.323.03.1magic-Square-5Qu3.5K3.0K6.3M29M11M15M4.3M5.7Mmagic-Square-6Qu3.0K6.0K9.7B1.2K1.5K2.79K112K1.2K1.5K2.79K <trr<tr>magic-Square-6Qu3.0K</trr<tr>		vc	1.2B	1.8B	9.4B	1.6B	1.6B	1.2B	1.3B
radar 8-30-3-0-15cpu0.750.690.670.600.550.830.71cc2.2M2.0M1.6M1.6M1.6M2.5M2.0Mvc244K357K10M787K787K243K326Kmmanp-1-3cpu0.2910.2790.3860.300.2810.4060.343cc1.11M1.00M1.09M1.09M1.09M1.26M1.08Mvc49.6K65K732K50K50K49.4K59Kmmanp-1-4cpu235218453180771372209mknap-1-4cpu235218453180771372209mknap-1-4cpu235218453180171372209mknap-1-4cpu235218591K543M543M917M516Mmknap-1-4cpu3.5M13M28B29M95M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32magic-Square-6cpu1.5K2.99K112K112K12K1.5K2.79Kmagic-Square-6cpu1.5M2.1M26M5.0M5.0M5.0M1.3Mmagic-Square-6cpu1.5M2.1M26M5.0M5.0M5.7K1.3Mmagic-Square-7cpu1.5M2.1M26M5.0M5.0M5.0M5.7Kmagic-Square-7cpu<		rn	8.8K	17.5K	79K	79K	79K	8.8K	13.5K
r.r.2.2M2.0M1.6M1.6M1.6M2.5M2.0Mvc244K357K10M787K787K243K326Kmknap-1-3cpu0.2910.2790.3860.300.2810.4060.343cc1.11M1.10M1.09M1.09M1.09M1.26M59Kmknap-1-4cpu23521885C8K8K8K438670mknap-1-4cpu235218453180171372209cc721M708M543M543M543M917M516Mmagic-Square-5cpu3.453.065.982.102.013.02.32magic-Square-6cpu1.5K2.99K112K112K1.5K2.79Kmagic-Square-7cpu1.5M6.3M293M17M4.3M5.7Mmagic-Square-7cpu0.050.77B0.77B0.77B1.5B0.76Bmagic-Square-7cpu0.050.77B0.77B0.77B1.5M1.9Mmagic-Square-7cpu0.010.6C96K96K3.0K5.7K1.9Mmagic-Square-7cpu0.15M2.1M2.0M5.0M5.0M5.0M5.7K1.9Mmagic-Square-7cpu0.15M2.1M2.6M5.0M5.0M5.0M5.7K1.9Mmagic-Square-7cpu0.0L0.0L0.0C0.0L5.0M5.0M5.0M </td <td>radar 8-30-3-0-15</td> <td>cpu</td> <td>0.75</td> <td>0.69</td> <td>0.67</td> <td>0.60</td> <td>0.55</td> <td>0.83</td> <td>0.71</td>	radar 8-30-3-0-15	cpu	0.75	0.69	0.67	0.60	0.55	0.83	0.71
vc244K357K10M787K787K243K326Kmknap-1-32.6K5K18K18K18K2.6K4.8Kmknap-1-30.2910.2790.3860.300.2810.4060.343cc1.11M1.09M1.09M1.09M1.02M1.26M1.08Mvc49.6K65K732K50K50K49.4K59Kmknap-1-4cpu235218453180771372209cc721M708M543M543M543M917M516Mmagic-Square-5cpu3.453.065.982.012.013.02.32magic-Square-6cpu7.15M6.3M293M17M1.5K2.7K2.7Kmagic-Square-6cpu7.15M6.3M293M17M1.5K2.7K2.7Kmagic-Square-6cpu7.5M6.3M293M17M1.5K2.7K2.7Kmagic-Square-6cpu1.5K2.9K112K112K1.5K2.7Kmagic-Square-7cpu1.5M2.1M26M5.0M5.0M1.5M1.3Kmagic-Square-7cpu0.1C0.95B0.7TB0.7TB1.5M1.9Kmagic-Square-7cpu1.5M2.1M26M5.0M5.0M3.0K5.7Kmagic-Square-7cpu1.5M2.1M26M5.0M5.0M3.0K5.7Kmagic-Square-7cpu		сс	2.2M	2.0M	1.6M	1.6M	1.6M	2.5M	2.0M
nn2.6K5K18K18K18K2.6K4.8Kmknap-1-30.2910.2790.3860.300.2810.4060.343cc1.11M1.09M1.09M1.09M1.09M1.26M1.08Mvc49.6K65K732K50K50K49.4K59Kmknap-1-4cpu235218453180171372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M9.5M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32magic-Square-6cpu1.5K2.99K112K112K12K1.5K2.79Kmagic-Square-6cpu1.5M2.99K112K112K12.5K2.79Kmagic-Square-6cpu1.5M2.99K112K112K1.5K2.79Kmagic-Square-6cpu1.5M2.99K112K112K1.5K2.79Kmagic-Square-7cpu1.5M2.1M26M5.0M5.0M1.5M1.5Kmagic-Square-7cpu1.5M2.1M26M5.0M5.0M1.5M1.9Mmagic-Square-7cpu0.1C0.0E5.0M5.0M1.5M1.9M1.9Mmagic-Square-7cpu0.1C2.1M26M5.0M5.0M3.0K5.7Kmagic-Square-7cpu0.1C0.1C106<		vc	244K	357K	10M	787K	787K	243K	326K
mknap-1-3cpu0.2910.2790.3860.300.2810.4060.343cc1.11M1.00M1.09M1.09M1.09M1.26M1.08Mvc49.6K65K732K50K50K49.4K59Kmknap-1-4cpu235218453180171372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M9.5M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32cc194M163M81M81M81M135M112Mmagic-Square-6cpu1.5K2.99K112K112K1.5K2.79Kmagic-Square-6cpu1.5K2.99K112K112K1.5K2.79Kmagic-Square-6cpu1.5M2.99K112K13.461.5M1.376magic-Square-6cpu1.5M2.99K112K112K1.5K2.79Kmagic-Square-6cpu1.5M2.91K5.0M5.0M1.5B0.76Brd1.5M2.1M26M5.0M5.0M1.5M1.9Mrd3.0K6.0K96K96K3.0K5.7K3.7Kmagic-Square-7cpuout106610621026outoutrd3.0K6.0K96K96K3.0K5.7K5.7Kmagic-Square-7cpu <td></td> <td>rn</td> <td>2.6K</td> <td>5K</td> <td>18K</td> <td>18K</td> <td>18K</td> <td>2.6K</td> <td>4.8K</td>		rn	2.6K	5K	18K	18K	18K	2.6K	4.8K
cc1.11M1.10M1.09M1.09M1.09M1.26M1.08Mvc49.6K65K732K50K50K49.4K59Krn4388668K8K8K438670mknap-1-4cp235218453180171372209cc721M708M543M543M543M917M516Mrm6141218591K591K614941magic-Square-5cp3.453.065.982.102.013.02.32rm1.5K2.99K112K112K112K135M112M5.7Kmagic-Square-6cp1.5K2.99K112K112K12.83.065.7Kmagic-Square-6cp1.5M6.3M293M17M17M4.3M5.7Kmagic-Square-6cp1.5M2.9FK112K112K1.5K2.79Kmagic-Square-6cp1.5M2.9GK112K13.461.5M13.76rm1.5M2.9GK14.9813.461.3.028.3913.76rm5.5M2.10K2.10K2.6M5.0M5.0M1.5M1.9Mrm1.5M2.1M2.6M9.6K9.6K3.0K5.7K3.7Krmagic-Square-7cpsith5.7B5.7B5.7B5.7K5.7Krm3.0K6.0K9.6K9.6K9.6K3.0K5.7K5.7K </td <td>mknap-1-3</td> <td>cpu</td> <td>0.291</td> <td>0.279</td> <td>0.386</td> <td>0.30</td> <td>0.281</td> <td>0.406</td> <td>0.343</td>	mknap-1-3	cpu	0.291	0.279	0.386	0.30	0.281	0.406	0.343
vc49.6K65K732K50K50K49.4K59Kmknap-1-474388668K8K8K438670mknap-1-47235218453180171372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M29M9.5M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32cc194M163M81M81M81M135M112Mvc4.3M6.3M293M17M17M4.3M5.7Mmagic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76magic-Square-6cpu1.5M2.1M26M5.0M5.0M1.5M1.9Mmagic-Square-6cpu1.5M2.1M26M5.0M5.0M1.5M1.7Kmagic-Square-6cpu1.5M2.1M26M5.0M5.0M1.5M1.9Mmagic-Square-7cpu0.0L0.0K96K96K3.0K5.7K1.9Mmagic-Square-7cpu0.1L1066106210260ut0utmagic-Square-7cpu0.0L0.0L1.62M2.5M5.7K0.7Tmagic-Square-7cpu0.1L1066106210260ut0utmagic-Square-7mu3.0K6.7K57B57		cc	1.11M	1.10M	1.09M	1.09M	1.09M	1.26M	1.08M
mknap-1-4m4388668K8K8K438670mknap-1-4cpu235218453180171372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M29M9.5M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32cc194M163M81M81M81M135M112Mmagic-Square-6cpu1.5K2.99K112K112K1.5K2.79Kmagic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76magic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76magic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76magic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76magic-Square-7cpu17.9026M5.0M5.0M5.0M1.5M1.9Mmagic-Square-7cpu3.0K6.0K96K96K3.0K5.7Kmagic-Square-7cpuout106610621026outoutcc57B57B57Bmagic-Square-7m162M25M25M2.5Mmagic-Square-757B5		vc	49.6K	65K	732K	50K	50K	49.4K	59K
mknap-1-4cpu235218453180 171 372209cc721M708M543M543M543M917M516Mvc9.5M13M28B29M29M9.5M11Mmagic-Square-5cpu3.453.065.982.102.013.02.32cc194M163M81M81M81M135M112Mvc4.3M6.3M293M17M17M4.3M5.7Mmagic-Square-6cpu17.9016.7214.9813.4613.3028.3913.76cc1.0B0.95B0.77B0.77B0.77B1.5B0.76Bmagic-Square-7cpuout26M5.0M5.0M1.5M1.9Mmagic-Square-7cpu1.5M2.1M26M5.0M5.0M1.5M1.9Mmagic-Square-7cpuoutout106610621026outoutrd3.0K6.0K96K96K3.0K5.7K7.7Kmagic-Square-7cpuout106610621026outoutrd $ -$ rd $ -$		rn	438	866	8K	8K	8K	438	670
cc 721M 708M 543M 543M 543M 917M 516M vc 9.5M 13M 28B 29M 29M 9.5M 11M magic-Square-5 cpu 3.45 3.06 5.98 2.10 2.01 3.0 2.32 cc 194M 163M 81M 81M 81M 135M 112M vc 4.3M 6.3M 293M 17M 17M 4.3M 5.7M magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 1.5M 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 3.0K 5.7K magic	mknap-1-4	cpu	235	218	453	180	171	372	209
vc9.5M13M28B29M29M9.5M11Mmagic-Square-5 614 1218591K591K591K614941magic-Square-5 cpu 3.453.065.982.102.013.02.32cc194M163M81M81M81M135M112Mvc4.3M6.3M293M17M17M4.3M5.7Mmagic-Square-6 cpu 1.5K2.99K112K112K112K1.5K2.79Kmagic-Square-6 cpu 17.9016.7214.9813.4613.3028.3913.76cc1.0B0.95B0.77B0.77B0.77B1.5B0.76Bvc1.5M2.1M26M5.0M5.0M1.5M1.9Mmagic-Square-7 cpu 0ut0ut106610621026outoutrd3.0K6.0K96K95B57B57Brd cu $ -$ 57B57B57B $ -$ rd $ 62M$ $25M$ $25M$ $ -$		cc	721M	708M	543M	543M	543M	917M	516M
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		vc	9.5M	13M	28B	29M	29M	9.5M	11M
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		rn	614	1218	591K	591K	591K	614	941
cc 194M 163M 81M 81M 81M 135M 112M vc 4.3M 6.3M 293M 17M 17M 4.3M 5.7M magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M magic-Square-7 cpu out 0.04 5.0M 5.0M 1.5M 1.9M magic-Square-7 cpu out 0.05B 0.77B 0.77B 1.5M 1.9M magic-Square-7 cpu out 1066 1062 1026 out out cc - - 57B 57B 57B - - magic-Square-7 cpu out 1066 1062 1026 out out rc -	magic-Square-5	cpu	3.45	3.06	5.98	2.10	2.01	3.0	2.32
vc 4.3M 6.3M 293M 17M 17M 4.3M 5.7M magic-Square-6 n 1.5K 2.99K 112K 112K 112K 1.5K 2.79K magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M magic-Square-7 cpu out 0.04 1066 1062 1026 out out magic-Square-7 cpu out out 1066 1062 1026 out out magic-Square-7 cpu out 0ut 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 357K 357K 357K -		cc	194M	163M	81M	81M	81M	135M	112M
magic-Square-6 m 1.5K 2.99K 112K 112K 112K 1.5K 2.79K magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M magic-Square-7 cpu out 0.05K 96K 96K 3.0K 5.7K magic-Square-7 cpu out 0ut 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 162M 25M 25M - - magic-Square-7 - - 357K 357K 357K - -		vc	4.3M	6.3M	293M	17M	17M	4.3M	5.7M
magic-Square-6 cpu 17.90 16.72 14.98 13.46 13.30 28.39 13.76 cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M rn 3.0K 6.0K 96K 96K 96K 3.0K 5.7K magic- Square-7 cpu out 0ut 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 162M 25M 25M - - magic- Square-7 - - 357K 357K 57B - -		rn	1.5K	2.99K	112K	112K	112K	1.5K	2.79K
cc 1.0B 0.95B 0.77B 0.77B 0.77B 1.5B 0.76B vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M m 3.0K 6.0K 96K 96K 96K 3.0K 5.7K magic- Square-7 cpu out 0ut 1066 1062 1026 out out vc - - 57B 57B 57B - - vc - - 162M 25M 25M - - m - 357K 357K 357K - -	magic-Square-6	cpu	17.90	16.72	14.98	13.46	13.30	28.39	13.76
vc 1.5M 2.1M 26M 5.0M 5.0M 1.5M 1.9M magic- Square-7 3.0K 6.0K 96K 96K 96K 3.0K 5.7K magic- Square-7 cpu out 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 162M 25M 25M - - m - - 357K 357K 357K - -	0	сс	1.0B	0.95B	0.77B	0.77B	0.77B	1.5B	0.76B
m 3.0K 6.0K 96K 96K 96K 3.0K 5.7K magic- Square-7 cpu out out 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 162M 25M 25M - - m - 357K 357K 357K - -		vc	1.5M	2.1M	26M	5.0M	5.0M	1.5M	1.9M
magic- Square-7 cpu out out 1066 1062 1026 out out cc - - 57B 57B 57B - - vc - - 162M 25M 25M - - rn - - 357K 357K 357K - -		rn	3.0K	6.0K	96K	96K	96K	3.0K	5.7K
cc 57B 57B 57B vc 162M 25M 25M rn 357K 357K	magic- Square-7	cpu	out	out	1066	1062	1026	out	out
vc – – 162M 25M 25M – – m – – 357K 357K 357K – –	- 1	cc	_	_	57B	57B	57B	_	_
rn – – 357K 357K 357K – –		vc	_	_	162M	25M	25M	_	_
		rn	_	_	357K	357K	357K	_	_

The best cpu time is in italic. rn is the number of residue supports

so GAC3rm_*infinity* also avoids all positive repeats and the worst-case space complexity of GAC3rm_*infinity* is the same as that of GTR. However, compared with GTR, GAC3rm_*infinity* cannot avoid redundant validity checks. It may execute much more validity checks than GTR. The dynamic list of GTR may be adopted in MGAC3rm_*infinity* to avoid redundant validity checks, but each value on each constraint needs a dynamic list, so it maintains *rd* dynamic lists for each constraint, which is much more than that of GTR.

The MAC3cache algorithm [14] caches the results of all possible constraint checks. This method was proposed to work on binary CSPs, because caching the results of all possible constraint checks will cost d^r space for each r-arity constraint. When a constraint check is repeated, MAC3cache can get the result with low cost. GTR also caches the results of some constraint checks, but only the allowed results, not all possible constraint checks. Actually, we would prefer the STR algorithm to a classical GAC algorithm after all allowed tuples are cached.

Table 2 Results on non-binary instances without tight constraints

5 Experiments

The experiments were run on a PC with Intel(R) Core(TM) i5-3210M CPU @2.5GHz, 4GB RAM, JDK 1.7. The performance of maintaining each GAC (or AC for binary instances) algorithm for finding the first solution or proving unsatisfiable is measured by CPU time (cpu) in seconds, number of constraint checks (cc), and number of validity checks (vc). The numbers of cc and vc are present by kilo (K), million (M), and billion (B). Timeout (out) is 1200 s and maximum memory is set to 1000M. In the average results, the cpu time of timeout instances are counted as 1200 s. The results of those instances where all solvers are timeout are eliminated from the average results. The variable ordering heuristic is *dom/wdeg* [6] equipped with a random restart strategy [7, 22]. The GAC3rm_k algorithms are implemented with a static FIFO policy and the later added residues are checked earlier.

Firstly, we compared GTR with GAC3rm, GAC3rm_2, GAC3rm_infinity, GAC3.1, and GAC3.2 on some non-binary

Instance		GAC3rm	GAC3rm2	GAC3rm_inf	GTR	GTR2	GAC3.1	GAC3.2
Chessboard coloration 25-25-2	cpu	15.44	15.41	13.96	18.69	41.32	_	_
	cc	12M	2.0M	0.6M	0.6M	0.6M	out	out
	vc	34.6M	48.7M	63.7M	43.2M	43.2M	of	of
	rn	0.7M	1.2M	0.5M	0.5M	0.5M	memory	memory
Chessboard Coloration 9-9-3	cpu	1.76	2.03	4.26	2.49	3.26	4.46	3.62
	cc	8.1M	4.5M	0.2M	0.2M	0.2M	10M	6.2M
	vc	24.8M	35.3M	149M	54.4M	54.4M	24.8M	31.5M
	rn	15K	31K	85K	85K	85K	15K	28K
tdsp C1-1-20	cpu	4.42	4.40	16.0	14.91	11.40	5.66	4.15
	cc	26M	22M	3M	3M	3M	34M	22M
	vc	30M	44M	1.4B	675M	675M	30M	41M
	rn	10K	18K	409K	409K	409K	10K	18K
tdsp C1-1-21	cpu	5.60	5.30	17.21	13.57	10.22	6.29	5.07
	cc	33M	26M	7M	7M	7M	34M	26M
	vc	37M	54M	1.4B	522M	522M	37M	51M
	rn	10K	19K	392K	392K	392K	10K	19K
Golomb ruler 34-9-a4	cpu	6.58	10.06	190	88	81	14.5	15.8
	cc	50M	44M	8M	8M	8M	67M	47M
	vc	132M	202M	9.1B	1.9B	1.9B	135M	186M
	rn	63K	117K	7M	7M	7M	63K	124K
Golomb Ruler 44-10-a4	cpu	51	95	_	-	-	146	162
	cc	422M	373M	out	out	out	548M	399M
	vc	1.02B	1.58B	of	of	of	1.04B	1.47B
	rn	132K	248K	memory	memory	memory	132K	261K

The best cpu time is in italic

instances containing intensional constraints. GTR and the GAC3rm algorithms are light when being used in search, whereas GTR2 maintains the data structure *last Size*. GAC3.1 and GAC3.2 maintain the data structure *last* during search. As we mentioned at Section 4, the gain in efficiency that GTR brings is related to constraint tightness, so we tested five problems contain some tight constraints (*Multi-Knapsack Instances (mknap), magicSquare, Balanced Incomplete Block Designs (BIBD), CostasArray, Radar Surveillance (radar)*) and three problems contain only loose constraints (*ChessboardColoration, Two-Dimensional Strip Packing Problems (tdsp)* and *Golomb Ruler*). All these benchmark instances are downloaded from http://www.cril.univ-artois.fr~lecoutre/benchmarks.html.

Table 1 presents the results of some problems containing tight constraints. The integers in the brackets under instance names are the number of tested instances in that series. On the *mknap*, *magicSquare*, and *BIBD* instances, we can see that GTR algorithms trade a relatively small number of validity checks (compared to the number of constraint checks) for some constraint checks. Consequently, they save some cpu time. More specifically, the gain on each mknap instance is from the only tight constraint and the gains on magicSquare and BIBD instances are from the weightedSum constraints in these instances. On the CostasArray instances, GTR algorithms save a little time from the trading, it reduces the sum of validity checks and constraint checks. The radar-8-30-3-0-15 instance is the only representative which needs 729 search tree nodes to solve (others are easier). GAC3.2 enforces less constraint checks than GTR on the *mknap* instances, because it maintains the data structure last during search, which avoids some negative repeats.

Table 2 presents the results of some problems containing only loose constraints. The results of *Chessboardcoloration*, *tdsp*, and *Golomb ruler* show that it is not recommended to trade constraint checks for validity checks on loose constraints. The GTR algorithms are not efficient on these instances, but they are still better than GAC3rm_*infinity*, because GAC3rm_*infinity* needs much more validity checks. The GTR2 algorithm maintains additional data structures during search, so it costs more cpu time than the GTR algorithm on the instances containing a large number of constraints, e.g., the *Chessboardcoloration* instances, but it improves GTR in general.

Secondly, we compared GTR with AC3rm on binary instances including real-world, patterned, and academic binary instances. The results in Table 3 show that GTR is not efficient on these binary instances even on those instances containing tight constraints [12]. The main reason is that

Table 3 Results on binary real-world, patterned, and academic problems

Instance		AC3rm	AC3rm2	GTR
RLFAP	cpu	27.1	29.4	172 (1 out)
scens11 (10)	cc	307M	215M	-
RLFAP	cpu	40.1	42.9	350
scens11-f4	сс	486M	334M	141M
RLFAP	cpu	2.97	3.13	23.7
scens11-f6	сс	36M	26M	11M
Job-Shop	cpu	84.56	83.75	126 (4 out)
(41)	cc	2.4B	1.9B	-
Job-Shop	cpu	13.8	14.7	114
e0ddr1-10-by-5-1	сс	400M	371M	328M
Job-Shop	cpu	1069	991	out
e0ddr2-10-by-5-10	сс	33B	23B	-
Queens-Knights	cpu	1.05	1.05	8.83
(12)	сс	10.3M	9.9M	8.7M
Queens-Knights	cpu	3.52	3.54	31.23
25-5-add	сс	35M	34M	30M
Queens-Knights	cpu	4.12	4.21	46.97
25-5-mul	сс	41M	39.8M	34.9M

GTR is built in a GAC scheme which is less efficient than an AC scheme. Both GTR and an AC scheme check the validity of residue supports. The GTR needs two operations to check the validity on binary constraints, whereas an AC scheme needs only one operation. Each valid residue support supports only one value in both schemes. For an r-ary constraint, GTR needs r operations to check the validity of a residue support and the residue support supports *r* values.

In summary, the GTR algorithms have few improvement on binary instances, but they bring some improvements on non-binary tight constraints.

6 Conclusion and future work

In this paper, we propose a new generalized arc consistency algorithms GTR and its improved version GTR2, which avoid all positive repeats. The experimental results show that the GTR algorithms save a number of constraint checks. It is not suggested to use GTR on binary instances, but it works well on those tight constraints with larger arity. If we adopt the dynamic list of supports in some higher level local consistencies where the cost of finding a support is much more expensive, potentially, it may bring more improvement on cpu time. Acknowledgments This work was supported by the Fundamental Research Funds for the Central Universities (NO. 2412016KJ034), the Education Department of Jilin Province (Project NO. JJKH20170911KJ) and Key Laboratory of Symbolic Computation and Knowledge Engineering of Ministry of Education, Jilin University (NO.93K172017K06).

Compliance with ethical standards

Conflict of interests The authors declare that they have no conflicts of interest.

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