METHODOLOGIES AND APPLICATION

Assessment of voltage stability margin by comparing various support vector regression models

M. V. Suganyadevi · C. K. Babulal · S. Kalyani

Published online: 13 December 2014 © Springer-Verlag Berlin Heidelberg 2014

Abstract Voltage stability assessment and prediction of loadability margin are the major concerns in real-time operation of power systems. This paper proposes a support vector machine (SVM) regression network for the voltage stability assessment for normal condition as well as for contingency cases. The loadability margin of any given operating conditions is obtained for pre-contingency and post-contingency based on the computation of a stability index. SVM takes real and reactive power at all buses of the system and gives the loading margin. The validity of the proposed SVM-based index is tested on IEEE 30 and Indian 181 bus systems. The results of the proposed method are compared with neural network, extreme learning machine, online sequential extreme learning machine and extreme support vector machine regression methods. The feasibility of application of the proposed SVM regression network for real-time stability assessment is discussed. Also, FACTS devices are produced to improve the system loadability and their results are discussed.

Keywords Contingency · Extreme learning machine · Loadability margin · Regression · Support vector machine · Voltage stability assessment · FACTS

S. Kalyani

Department of Electrical and Electronics Engineering, Kamaraj College of Engineering and Technology, Virudhunagar, Tamilnadu, India e-mail: kal_yani_79@yahoo.co.in

Abbreviations

1 Introduction

In recent years, the analysis and assessment of volt[age sta](#page-11-4)bility have become a major concern in many power system planning and operation, since it has been the cause for many power system blackouts around the world [\(Taylor 1994](#page-11-0)). Lack of new generation and transmission facilities and over exploitation of the existing facilities geared by increased load demand, especially in the deregulated environment, forces the system to operate closer to their security boundaries leading to system instability. Loading margin analysis has been profoundly identified as one of the fundamental measurement in voltage collapse or voltage stability studies. The voltage collapse condition is predicted to occur when the load is increased exceeding the maximum loading point and subsequently the system starts to lose its equilibriums.

Generally, loading margin determination can be achieved by two techniques, viz., direct method and Homotopy method. The direct method can find the critical point where the Jacobian is singular by solving the enlarged system of power flow equations in one step. The major drawback of this method is requirement of good initial assumptions for a successful convergence. It also doubles the number of equations to be solved. For a more complicated practical nonlinear problem, the direct methods may not work at all [\(Ajjarapu](#page-10-0) [2006\)](#page-10-0).

The lack of knowledge of an initial guess can be tackled by the Homotopy method. This method first defines an easy problem for which a solution is known and then it defines path between the easy problem to the problem that need to be solved. The easy problem, with which the Homotopy method starts, is gradually transformed into the solution of the hard problem. The Homotopy method consists of a continuation equation, whose Jacobian matrix is not singular at bifurcation points, hence they are numerically robust [\(Kundur 1994](#page-11-1)). The continuation power flow (CPF) approach is one of the Homotopy methods and consists of predictor–corrector scheme to forecast the bifurcation point of PV or QV curve [\(Ajjarapu](#page-10-0) [2006;](#page-10-0) [Naoto Yorino 2005;](#page-11-2) [Chiang et al. 1995;](#page-10-1) [Canizares](#page-10-2) [2002\)](#page-10-2). At some critical points (turning or fold points), the singularity of the Jacobian matrix often causes trouble either in the prediction or correction process because the selection of step size in step length control scheme is problem dependent.

In ref [Irisarri et al.](#page-11-3) [\(1997](#page-11-3)), interior-point optimization method is used to determine optimal loading parameters for loading maximization. Multiple load flow solutions are proposed to obtain the minimum loadability margins in Yorino et al. [\(1997\)](#page-11-4). An energy function-based approach is proposed to evaluate the loadability margins on a given loading direction in the references [Klump and Overbye](#page-11-5) [\(1997](#page-11-5)) and Chiang and Ningqiang [\(2013\)](#page-11-6). In [Tare and Bijwe](#page-11-7) [\(1997\)](#page-11-7), the authors maximize the loadability margin in a given loading direction through minimizing the reactive power losses near the critical loading point by linear programming technique.

Thus, various techniques for estimating the loadability margin of power systems have been proposed related to voltage stability limits but all these techniques tend to depend on size and nonlinearity of the problem [\(Zambroni et al. 2011](#page-11-8); [Nagao et al. 1997](#page-11-9); [Van Cutsem 2002](#page-11-10)).

In recent years, research has been devoted to neural network applications to voltage security assessment and monitoring. Artificial neural network (ANN) is a highly efficient computational tool that could be used for online loadability evaluation. Multi-layered feedforward neural network has been widely used for power margin estimation associated with static voltage stability limits by adopting different training c[riteria](#page-10-3) [and](#page-10-3) [algorithms.](#page-10-3) [In](#page-10-3) [references](#page-10-3) Saikat and Benjamin [\(2007\)](#page-10-3), [Bahmanyar and Karami](#page-10-4) [\(2014](#page-10-4)), [Wan and Song](#page-11-11) [\(1998](#page-11-11)), [Srivastava et al.](#page-11-12) [\(2000](#page-11-12)), the active and reactive powers, bus voltage magnitude and angles had been used as the input [attributes](#page-11-13) [to](#page-11-13) [the](#page-11-13) [their](#page-11-13) [proposed](#page-11-13) [ANN](#page-11-13) [model.](#page-11-13) [In](#page-11-13) Torre et al. [\(2007\)](#page-11-13), the authors have proposed a new methodology for loading margin estimation based on subtractive clustering and adaptive neuro-fuzzy inference system, wherein various voltage stability indices has been selected as inputs. This method has proven to give good results to deal with uncertain load behavior and hence, can be implemented in a real-time environment. However, most of the artificial intelligence (AI)-based methods have failed to predict the voltage stability margin correctly, because they cannot find the global minima accurately.

Ant Colony optimization technique has been used in [Kalil](#page-11-14) [\(2006](#page-11-14)) while in reference [Ismail and Titik](#page-11-15) [\(2003](#page-11-15)), utilized Evolutionary Programming. The results obtained using the multi-layer perceptron (MLP) associated with hybrid particle swarm optimization technique have been compared with the results of the conventional CPF technique in [El-Dib](#page-11-16) [\(2006](#page-11-16)). An expert system called fuzzy logic approach has been used to determine the maximum loadability limit in [Babulal et al.](#page-10-5) [\(2008](#page-10-5)), but has not [proved](#page-11-17) [to](#page-11-17) [give](#page-11-17) [global](#page-11-17) [optima.](#page-11-17) [In](#page-11-17) Suganyadevi and Babulal [\(2009](#page-11-17)), the comparative study of various voltage stability indices for the estimation of loadability margin is presented which gives some other useful information such as identification of critical bus/line of a power system.

A new DEPSO algorithm combining the advantages of differential evolution (DE) and particle swarm optimization (PS[O\)](#page-11-18) [algorithm](#page-11-18) [has](#page-11-18) [been](#page-11-18) [proposed](#page-11-18) [in](#page-11-18) Gnanambal and Babulal [\(2012](#page-11-18)) to determine the maximum loading point which has provided more promising and accurate results compared

to other evolutionary algorithms. In general, in any evolutionary technique, the time per iteration is greater and hence, it cannot be directly applicable to solve practical large scale interconnected power system networks.

A vast observation of literature review shows that the problem of loadability prediction still needs further investigation for real-time applications. In the operation of practical power systems, different loading scenarios may result in very different loadability margins for the same operating point, hence loadability margins should be predicted for any loading direction. Therefore, this paper presents a new methodology for the estimation of voltage stability margin of a power system based on support vector regression (SVR).

The proposed method is tested on IEEE 30 bus and Indian 181 bus systems. The simulation results are compared with the results obtained by CPF and ANN, ESVM [\(Liu et al. 2008\)](#page-11-19), ELM [\(Huang et al. 2006](#page-11-20)) and OS-ELM regression methods [\(Liang et al. 2006\)](#page-11-21). The performance of SVR [\(Vapnik 1998\)](#page-11-22) is compared with other algorithms on statistical measures like mean square error and average computation time.

FACTS controllers enhance the voltage profile and also the [loadability](#page-11-23) [margin](#page-11-23) [of](#page-11-23) [power](#page-11-23) [systems.](#page-11-23) Natesan and Radman [\(2004\)](#page-11-23), [Gotham and Heydt](#page-11-24) [\(1998\)](#page-11-24). FACTS devices are used for pow[er](#page-11-25) [flow](#page-11-25) [analysis](#page-11-25) [\(Enrique et al. 2004](#page-10-6)[;](#page-11-25) Eberhart and Kennedy [1995](#page-11-25)). FACTS devices can be connected to a transmission line in various ways, such as in series, shunt, or combination of series and shunt. For example, the static VAR compensator (SVC) and static synchronous compensator (STATCOM) are connected in shunt; static synchronous series compensator (SSSC) and thyristor-controlled series capacitor (TCSC) are connected in series; thyristorcontrolled phase-shifting transformer (TCPST) and unified power flow controller (UPFC) are connected in a series and shunt combination. The behavior of the test system with and without FACTS device under different loading conditions is studied.

The organization of the paper is as follows: In Sect. [2,](#page-2-0) a summarized prediction of loadability margin of a power system during normal as well as contingency cases is presented. Section [3](#page-3-0) presents the SVM methodology and in Sect. [4,](#page-5-0) the algorithm for the prediction of loadability margin and the process of data generation are described in Sect. [5.](#page-5-1) The simulation results of the proposed approach and the comparison of SVR result with and without FACTS devices and compared with other algorithms are in Sect. [6.](#page-7-0) Finally, important conclusions are drawn in Sect. [7.](#page-10-7)

2 Prediction of loadability margin

Loadability margin is the distance with respect to the loading parameter, from the current operating point to voltage

Fig. 1 PV curve and voltage stability limit

collapse point [\(Kundur 1994\)](#page-11-1). The most common method to determine the loadability margin is the continuation power flow (CPF) technique. CPF employs a predictor–corrector scheme to find a solution path of a set of power flow equations reformulated to include a load parameter. It starts from a known solution and uses a tangent predictor to estimate subsequent solution corresponding to a different value of the load parameter. This estimate is then corrected using Newton– Raphson power flow. The local parameterization provides a mean of identifying each point along the solution path and plays an integral part in avoiding singularity in Jacobian. Figure [1](#page-2-1) shows a typical PV curve, which is recognized as an important tool for assessing voltage stability. The continuation method systematically increases the loading level or bifurcation parameter, until bifurcation or point of collapse is determined.

2.1 LM index

In this paper, we define a term called Loadability Margin index (LM_Index), which is given by Eqs. [\(1\)](#page-2-2) and [\(2\)](#page-2-3), shown below, indicating the distance from the current operating point to the voltage collapse point in terms of loading parameter for a given system operating condition with and without specified contingency. The LM_Index for pre-contingency and post-contingency is given by the following equations.

$$
LM_Index_{(Post-Contingency)} = \lambda_{VC(Post)} - \lambda_0
$$
 (2)

2.2 Loadability margin for pre-contingency

The prediction of load direction in real time is not known, hence the present work has considered seven different load

scenarios under normal operating conditions covering the whole spectrum of load direction prediction. The various scenarios considered are listed as follows:

- Scenario 1-real power load alone increased at a single load bus.
- Scenario 2-real power load alone increased at multiple load buses.
- Scenario 3-reactive power load alone increased at a single load bus.
- Scenario 4-reactive power load alone increased at multiple load buses.
- Scenario 5-both real and reactive power loads are increased simultaneously at a single load bus for different loading factor.
- Scenario 6-both real and reactive power loads are increased simultaneously at multiple load buses for different loading factor.
- Scenario 7-both real and reactive power loads at all load buses are increased keeping the power factor as constant at 0.8 lagging.

The conventional CPF algorithm available in PSAT toolbox [\(Federico 2009](#page-11-26)) is used for the above scenarios to generate sufficient data set. The data set includes real and reactive power load at all buses and the corresponding loading parameter λ.

2.3 Loadability margin for post-contingency

Post-contingency is the state of the power system after a contingency has occurred A contingency is the loss or failure of a small part of the power system (e.g., a transmission line), or an individual equipment failure (such as a generator or transformer). This is also called an unplanned "outage".

Many possible outage conditions could happen in a power system. This has necessitated a need to study the system behavior under a large number of contingency cases, so that power system operator can be given a warning signal to initiate an appropriate corrective action so as to prevent serious damage or overload on other equipments. The problem of studying all possible outages becomes very difficult and time-consuming, since it is required to present the results quickly for corrective actions to be taken.

The post-contingency analysis is used as a tool for the offline analysis of contingency events, and as an online tool to show operators what would be the effects of future outages. It allows operators to be better prepared to react to outages using pre-planned recovery scenarios.

The sequence of steps carried out offline in applying regression approach for the prediction of loadability margin is depicted in Fig. [2.](#page-3-1)

Fig. 2 Loadability margin prediction framework

3 SVM methodology

SVM has two special properties that SVMs can achieve [\(1\)](#page-2-2) high generalization by maximizing the margin and [\(2\)](#page-2-3) support an efficient learning of nonlinear functions by kernel trick. For the classification, the SVM tries to find the optimal hyperplane, which is expressed as a linear combination of a subset of training data (called support vectors) by solving a linearly constrained quadratic programming (QP) problem with a maximum margin between the two classes. Additionally, with the introduction of Vapnik's ε -insensitive loss function, the SVM has been extended to solve a nonlinear regression-estimation problem, called the SVM for regression [\(Jin-Tsong 2006\)](#page-11-27).

SVM Regression (SVR) is a method to estimate a function that maps from an input object to a real number based on training data. Similar to SVM Classification (SVC), SVR has the same properties of the margin maximization and kernel trick for nonlinear mapping [\(Hwanjo and Sungchul 2010\)](#page-11-28).

Figure [3](#page-4-0) shows the structure of SVR predictor which realizes the mapping function. The basic idea of SVR is to map the data of input space into high dimensional feature space via a nonlinear mapping and to do linear regression in this space.

Fig. 3 Structure of support vector regression predictor

 $\hat{x}_{t+1} = f(x_t, x_{t-1}, x_{t-2}, \ldots, x_{t-(m-1)})$ in which \hat{x}_{t+1} is the predicted value and \vec{x}_t is the observed value.

A training set for regression is represented as follows.

$$
D = \{(x_1, y_1), (x_2, y_2), \dots, (x_m, y_m)\}\
$$
 (3)

where \mathbf{x}_i is a n-dimensional vector and y is the real number for each \mathbf{x}_i . The SVR function $F(\mathbf{x}_i)$ makes a mapping from an input vector \mathbf{x}_i to the target y_i and takes the form.

$$
F(X) = w * x - b \tag{4}
$$

where w is the weight vector and b is the bias. The goal is to estimate the parameters (**w** and *b*) of the function that give the best fit of the data. An SVR function $F(\mathbf{x})$ approximates all pairs (x_i, y_i) while maintaining the differences between estimated values and real values under precision. That is, for every input vector **x** in *D*,

$$
y_i - w * x_i - b \le \varepsilon
$$

$$
w * x_i + b - y_i \le \varepsilon
$$

The margin is

$$
m \arg in = \frac{1}{\|w\|}
$$

By minimizing $||w||^2$ to maximize the margin, the training in SVR becomes a constrained optimization problem as follows.

Minimize:
$$
L(w) = \frac{1}{2} ||w||^2
$$
 (5)

Subject to: $y_i - w * x_i - b \le \varepsilon$ (6)

$$
w * x_i + b - y_i \le \varepsilon \tag{7}
$$

The solution of this problem does not allow any errors. To allow some errors to deal with noise in the training data, the soft margin SVR uses slack variables ξ and ξ

Then, the optimization problem can be revised as follows.

Minimize:
$$
L(w, \xi) = \frac{1}{2} ||w||^2 + C \sum_{i} (\xi_{2i}, \hat{\xi}_{2i}), C > 0
$$
 (8)

Subject to:

$$
y_i - w * x_i - b \le \varepsilon + \xi_i \quad \forall (x_i, y_i) \in D \tag{9}
$$

$$
w * x_i + b - y_i \le \varepsilon + \hat{\xi}_i \quad \forall (x_i, y_i) \in D \tag{10}
$$

$$
\xi_i, \hat{\xi}_i \ge 0 \tag{11}
$$

The constant $C > 0$ is the trade-off parameter between the margin size and the amount of errors.

The slack variables ξ and $\hat{\xi}$ deal with infeasible constraints of the optimization problem by imposing the penalty to the excess deviations which are larger than ε .

To solve the optimization problem Eq. [\(8\)](#page-4-1), we can construct a Lagrange function from the objective function with Lagrange multipliers as follows:

minimize:
$$
L = \frac{1}{2} ||w||^2 + C \sum_i (\xi_i, \hat{\xi}_i)
$$

$$
- \sum_i (\eta_i \xi_i + \hat{\eta}_i \hat{\xi}_i)
$$
(12)
$$
- \sum_i \alpha_i (\varepsilon + \eta_i - y_i + w \cdot x_i + b)
$$

$$
- \sum_i \hat{\alpha}_i (\varepsilon + \hat{\eta}_i + y_i - w \cdot x_i - b)
$$
subject to: $\eta_i, \hat{\eta}_i > 0$ (13)

$$
\text{subject to: } \eta_l, \eta_l \geq 0 \tag{13}
$$

$$
\alpha_i, \hat{\alpha}_i \ge 0 \tag{14}
$$

where η_i , $\hat{\eta}_i$, α_i , $\hat{\alpha}_i$ are the Lagrange multipliers which satisfy positive constraints.

The following is the process to find the saddle point using the partial derivatives of L with respect to each lagrangian multipliers for minimizing the function L.

$$
\frac{\partial L}{\partial b} = \sum_{i} (\alpha_i - \hat{\alpha}_i) = 0
$$
(15)

$$
\frac{\partial L}{\partial w} = w - \sum_{i} (\alpha_i - \hat{\alpha}_i) x_i = 0, w = \sum_{i} (\alpha_i - \hat{\alpha}_i) x_i
$$
(16)

$$
\frac{\partial L}{\partial \hat{\xi}_i} = C - \hat{\alpha}_i - \hat{\eta}_i = 0, \,\hat{\eta}_i = C - \hat{\alpha}_i \tag{17}
$$

The optimization problem with inequality constraints can be changed into the following dual optimization problem by substituting Eqs. [\(15\)](#page-4-2), [\(16\)](#page-4-3) and [\(17\)](#page-4-4) in [\(12\)](#page-4-5).

maximize:
$$
L(\alpha) = \sum_{i} y_i (\alpha_i - \hat{\alpha}_i) - \varepsilon \sum_{i} (\alpha_i + \hat{\alpha}_i)
$$

$$
-\frac{1}{2} \sum_{i} \sum_{i} (\alpha_i - \hat{\alpha}_i)(\alpha_i - \hat{\alpha}_i) x_i x_j
$$
(18)

subject to:
$$
\sum_{i}^{i} (\alpha_i - \hat{\alpha}_i) = 0
$$
 (19)

$$
0 \le \alpha, \hat{\alpha} \le C \tag{20}
$$

The dual variables η_i , $\hat{\eta}_i$ are eliminated in revising Eq. [\(12\)](#page-4-5) in Eq. [\(18\)](#page-4-6). Equations [\(16\)](#page-4-3) and [\(17\)](#page-4-4) can be rewritten as follows.

$$
w = \sum_{i} (\alpha_i - \hat{\alpha}_i) x_i
$$
 (21)

$$
\pm \supset B\eta_i = C - \alpha_i \tag{22}
$$

$$
\hat{\eta}_i = C - \hat{\alpha}_i \tag{23}
$$

where **w** is represented by a linear combination of the training vectors \mathbf{x}_i . Accordingly, the SVR function $F(\mathbf{x})$ becomes the following function.

$$
F(x) = \sum_{i} (\alpha_i - \hat{\alpha}_i) x_i x_j + b \tag{24}
$$

Equation [\(25\)](#page-5-2) can map the training vectors to target real values with allowing some errors but it cannot handle the nonlinear SVR case. The same kernel trick can be applied by replacing the inner product of two vectors \mathbf{x}_i and \mathbf{x}_j with a kernel function $K(x_i, x_j)$. The transformed feature space is usually high dimensional, and the SVR function in this space becomes nonlinear in the original input space. Using the kernel function K , The inner product in the transformed feature space can be computed as fast as the inner product $\mathbf{x}_{i} \times \mathbf{x}_{j}$ in the original input space. Once replacing the original inner product with a kernel function K , the remaining process for solving the optimization problem is very similar to that for the linear SVR. The linear optimization function can be changed using kernel function as follows.

maximize:
$$
L(\alpha) = \sum_{i} y_i (\alpha_i - \hat{\alpha}_i) - \varepsilon \sum_{i} (\alpha_i + \hat{\alpha}_i)
$$

$$
-\frac{1}{2} \sum_{i} \sum_{j} (\alpha_i - \hat{\alpha}_i)(\alpha_i - \hat{\alpha}_i) K x_i x_j
$$
(25)

subject to:
$$
\sum_{i} (\alpha_i - \hat{\alpha}_i) = 0
$$
 (26)

$$
\hat{\alpha}_i \ge 0, \alpha_i \ge 0 \tag{27}
$$

$$
0 \le \alpha, \hat{\alpha} \le C \tag{28}
$$

Finally, the SVR function $F(\mathbf{x})$ becomes the following using the kernel function.

$$
F(x) = \sum_{i} (\alpha_i - \hat{\alpha}_i) K x_i x_j + b \tag{29}
$$

4 Algorithm for predicting loadability margin

The detailed algorithm for the prediction of loadability margin of a power system is given as follows:

- 1. Run the test systems data in PSAT for all seven loading scenarios.
- 2. From the CPF results, create a database for the input vector in the form of $[P_G, P_L, Q_G, Q_L]$ where P_G, P_L ,

Fig. 4 Structure of SVR model

 Q_G and Q_L are the real and reactive powers in generators and load buses, respectively, and output vector in the form of scalar lambda (loading margin) for the corresponding input vectors as shown in module 1 of Figs. [4](#page-5-3) and [5.](#page-6-0)

- 3. Choose different possibilities, such as kernel type, kernel parameters and SVR parameters (C and γ) to train the SVR network.
- 4. Train the SVR network using the training data set. Test the accuracy of the regression model to unseen test samples and verify the predictor of loadability margin value.
- 5. Compare the predictor loadability margin of SVR with other algorithms and conventional CPF technique in terms of computational time and Mean Squared Error (MSE) as given in following Eq. [\(30\)](#page-5-4).

$$
MSE = \sum_{i=1}^{n} \frac{(X_i - Y_i)^2}{n}
$$
 (30)

where X_i is the predictor loading margin value and Y_i is target value (i.e., loading margin from CPF).

Where $[P_{li,1}, P_{li,2}, \ldots, P_{li,n}]$ is the real power load vector of *i* th bus for 'n' number of patterns and $[Q_{li,1}, Q_{li,2},]$ $Q_{li,n}$ is the reactive load vector of *i*th bus for 'n' patterns.

The structure of the proposed SVR model consists of module 1 and module 2 as shown in the Fig. [3.](#page-4-0) In module 1, the continuation power flow method is used for various loading scenarios of the test systems. The *P* and *Q* values from the module 1 are given as input to the module 2 of different SVRs. These SVR models estimate the loadability margin of the given power system.

5 Test systems and data generation

The proposed algorithm (explained in Sect. [4\)](#page-5-0) is applied to the IEEE 30 bus and Indian 181 bus systems. The numer-

Fig. 5 Nose curve of IEEE 30 bus system

Fig. 6 Nose curve of Indian 181 bus system

ical data for IEEE 30 bus and Indian 181 bus systems are taken from [Power System Test Archive-UWEE](#page-11-29) and Babulal and Kannan [\(2006](#page-10-8)), respectively. In machine learning approaches, the generated data set must adequately represent the entire range of power system operating states.

5.1 Pre-contingency cases

The input and output patterns can be generated either from real time or offline mode of simulation. In this work, a large number of characteristics of operating points are generated through offline simulation using CPF method using PSAT software for IEEE 30 bus system and CPF method using MATPOWER software [\(Zimmerman et al. 2011](#page-11-30)) for Indian 181 bus system. The following Figs. [4,](#page-5-3) [5](#page-6-0) and [6](#page-6-1) shows the nose curves (Loading parameter versus voltage) of IEEE 30 bus and Indian 181 bus systems, respectively. These curves are obtained using CPF for all possible loading scenarios by increasing the real and reactive power load from its base case condition.

In IEEE 30 bus system, 600 patterns were generated by varying the real and reactive power loads randomly from its base case value to 150 %. Out of 600 patterns generated for IEEE 30 bus system, 80 % (480 patterns) are selected

Table 1 Data size of pre-contingency and post-contingency cases

Cases	IEEE 30 bus system	Indian 181 bus system		
Pre-contingency				
Training phase	28,800	217,200		
Testing phase	7,200	54,300		
Post-contingency				
Training phase	21,600	139,008		
Testing phase	5,400	34,752		

arbitrarily for training, while the left 20 % (120 patterns) are used for testing. In Indian 181 bus test system, as many as 750 patterns were generated by changing the loading at each bus randomly in wide range $(\pm 50\%$ of base case). Thus, 271,500 data samples are used for the simulation study. Out of 750 patterns, 80 % (600 patterns) are taken for training and the remaining for testing. The size of the data set adopted for training and testing phases under pre-contingency cases is shown in Table [1.](#page-6-2)

5.2 Post-contingency cases

For loading margin estimation in the contingency cases, a large number of load patterns are generated by all single line outages and selected double line outages in the power system to capture all possible scenarios. In IEEE 30 bus system, 450 patterns were generated for all single line outages and some double line outages. In Indian 181 bus test system, as many as 480 patterns were generated by N-1 contingency scenarios. Out of 450 patterns generated in IEEE 30 bus system, 80 % (360 patterns) are selected arbitrarily for training, while the left 20 % (90 patterns) are used for testing. Similarly for Indian 181 bus system, out of 480 patterns, 80 % (384 patterns) are taken for training and the remaining for testing. The size of the data set adopted for training and testing phases under normal as well as under contingency cases are shown in Table [1.](#page-6-2)

6 Simulation results and discussion

6.1 Selection of kernel and SVR parameters using tenfold cross-validation approach

LIBSVM [\(Babulal and Kannan 2006\)](#page-10-8) and MATLAB routine is used for training the SVMs in both classification and regression. The training performance of the SVR module depends on proper selection of SVR parameter such as cost function *C* and γ and kernel types. The various kernel types considered for SVM regression are the RBF, linear, polynomial and Gaussian. This paper uses RBF kernel type over the others because of its superiority [\(Suganyadevi and Babulal 2013\)](#page-11-31). The best combination of *C* and γ is often selected by a grid search with exponentially growing sequences of *C* and γ , for example, $C \in$ $\{2^{-5}, 2^{-3}, \ldots, 2^{13}, 2^{15}\}; \gamma \in \{2^{-15}, 2^{-13}, \ldots, 2^{1}, 2^{3}\}.$ Typically, every combination of parameter choices is checked using cross-validation, and the parameters with best crossvalidation accuracy are selected. The improper selection of these two parameters can lead to over-fitting or under-fitting

814 M. V. Suganyadevi et al.

problems. Tenfold cross-validation approach in grid-search method is used to determine the optimal value of C and γ so that the regression model can accurately predict the unknown data [\(Kalyani and Swarup 2013\)](#page-11-32). Highest cross-validation accuracy of 93 % is obtained for $C = 100$ and $\gamma = 0.2$.

6.2 Comparison of various regression model's loadability margin without FACTS devices

This section compares the performance of ANN, ELM, OSELM and ESVM with respect to SVM in predicting the loadability margin of IEEE 30 bus and Indian 181 bus systems without FACTS devices. ANN is designed with an input layer of three neurons, a hidden layer of five neurons and an output layer of one neuron. The Levenberg–Marquardt backpropagation network training function is used. The network is trained for up to 300 epochs to an error goal of 0.000001. In ELM and OSELM algorithms, the RBF activation function is used to compute the hidden layer output matrix. Sigmoidal activation function is used for ESVM.

The proposed SVM model is used to determine the loadability margin and the results in terms of training time, testing time, training MSE and testing MSE for both precontingency and post-contingency cases are compared and shown in Tables [2](#page-7-1) and [3.](#page-8-0) Clearly from the tables, the performance of SVM is better than others. Hence the proposed SVM regression model may be suitable for online implementation. As SVM predicts quickly, it may allow the operator to monitor the power system stability from time to time and take appropriate control and preventive actions accordingly.

6.3 Prediction of VSM after placing FACTS devices

The best location for shunt reactive power compensation, as far as the improvement of voltage stability margin is concerned, is the weakest bus of the system. The weakest bus

Table 2 Comparative analysis of various regression models for LM estimation for IEEE 30 bus test system

Table 3 Comparative analysis of various regression models for LM estimation for Indian 181 bus test system

Parameters	ANN	SVM	ESVM	ELM	OSELM	
Pre-contingency cases						
No of neurons	10			25	25	
Training Time (s)	1.360278	0.689144	1.070068	1.104361	1.181827	
Testing Time (s)	6.270195	0.35545	4.1945	5.190138	1.690904	
Training MSE	0.46727	0.09515	0.31332	0.240359	0.309578	
Testing MSE	0.36412	0.04525	0.28603	0.193606	0.28018	
Post-contingency cases						
No of neurons	10			25	25	
Training Time (s)	5.731265	0.267097	4.751874	1.243422	0.92021	
Testing Time (s)	5.717564	0.281543	4.182546	1.121544	0.795975	
Training MSE	1.56422	0.124017	0.464179	0.440359	0.461239	
Testing MSE	0.74573	0.11263	0.446925	0.560426	0.636512	

Fig. 7 Nose curve of IEEE 30 bus system after placing SVC at 26th bus

of the system can be identified using nodal voltage stability indices [\(Suganyadevi and Babulal 2009\)](#page-11-17). Introducing shunt compensation devices at this location will improve the LM. In IEEE 30 bus system, bus number 26 is identified as the weakest bus, the injection of reactive power of 2.5 p.u. in this bus from SVC will increase the loading margin by 33.34 %. Similarly in Indian 181 bus system, SVC (reactive power 3.75 p.u.) is connected at the weakest bus number 109, increases the loading margin by 37.14 %. The enhancement of LM is also studied with the introduction of TCSC in the weakest transmission line (identified by the line voltage stability indices [\(Suganyadevi and Babulal 2009](#page-11-17)). In IEEE 30 bus system, 20 % of line reactance compensation is inserted in the weakest line number 34. An increase in loading of $P = 1.5\%$ and $Q = 1.75\%$ are obtained due to the inclusion of TCSC. Similarly for the Indian 181 bus system, TCSC (30 % of the line reactance of the weakest line between 110 and 141 bus numbers) is connected and the improvement in the loadings of $P = 2.89\%$ and $Q = 3.52\%$ are obtained. The nose curves of IEEE 30 bus and Indian 181 bus systems after placing SVC at its weakest buses are shown in the following Figs. [7](#page-8-1) and [8,](#page-9-0) respectively.

The enhanced LM with SVC and TCSC are determined and the results in terms of training time, testing time and testing MSE are compared and presented for IEEE 30 bus and Indian 181 bus systems in Tables [4](#page-9-1) and [5,](#page-9-2) respectively. The tables show clearly that the proposed SVM model estimate the same loadability margin as obtained by the other techniques with greater accuracy. The training computational time of ELM network is slightly higher than the SVM and ANN. The SVM training and testing time are lesser and accurate when compared to ANN model. The results show that the SVM type predicts the result quickly when compared to other models and the computational time also less in the order of 10^{-4} .

Figure [9](#page-10-9) shows the variation of LM for base case, contingency, TCSC and SVC of IEEE 30 bus system for some testing patterns obtained from SVM models. SVC gives better voltage stability compared to series compensation device TCSC, i.e., the LM of the system with SVC is higher than that of TCSC. Shunt compensation device injects the reactive power at the connected weakest bus but series compensation device inserts the reactive power at the connected line(weakest line). It shows that IEEE 30 bus system needs

Fig. 8 Nose curve of Indian 181 bus system after placing SVC at 109th bus

more reactive power at the load bus than the line. The weakest bus 26 of the system requires more reactive power. Injection of reactive power at bus 26 or in its vicinity can improve the voltage stability margin. Similarly, the variation of LM for base case, contingency, TCSC and SVC of Indian 181 bus system for some testing patterns obtained from SVM models is shown in Fig. [10.](#page-10-10) The weakest bus 109 of the system requires more reactive power. Injection of reactive power at bus 109 or in its vicinity can improve the voltage stability margin.

Table 4

FACTS devices for bus test system

Fig. 9 Variation of LM for IEEE 30 bus system with and without FACTS devices

Fig. 10 LM of Indian 181 bus test system with and without FACTS devices

7 Conclusion

This paper has presented a new SVM model for online prediction of loadability margin for power system leading to fast voltage stability assessment. The comparative results of LM for IEEE 30 bus and Indian 181 bus systems under normal as well as post-contingency cases had proven the efficacy of the SVM model for online estimation. This, in turn, will help the power system operator to take necessary control actions at the appropriate time thereby preventing voltage collapse and system blackout. The proposed SVM model is well trained to predict the voltage stability margin in a short frame of time for the considered power system. Injection of reactive power at the weakest bus using SVC can improve LM than that of TCSC. The estimation of LM using SVM model is achieved with least absolute error, minimum training and testing computational time, compared to other machine learning models. Future work will focus on application of proposed work for the estimation of loadability margin in restructured environment.

Acknowledgments The first author sincerely acknowledge the financial assistance received from Department of Science and Technology, New Delhi, India under Women Scientist Scheme-A vide letter number SR/WOS-A/ET-139/2011, dated on 05-03-2012 and the authors also

thank the Management and Principal of Thiagarajar College of Engineering, Madurai, India to carry out this research work.

References

- Acha E, Fuerte-Esquivel R (2004) Modelling and simulation in power networks. Wiley, England
- Ajjarapu V (2006) Computational techniques for voltage stability assessment and control. Springer, New York
- Babulal CK, Kannan PS (2006) A novel approach for ATC computation in deregulated environment. J Electr Syst 2(3):146–161
- Babulal CK, Kannan PS, Anita JM, Venkatesh B (2008) Determination of voltage stability analysis limit using fuzzy logic. Int J Power Energy Syst 28(1):3651–3657
- Bahmanyar AR, Karami A (2014) Power system voltage stability monitoring using artificial neural networks with a reduced set of inputs. Int J Electr Power Energy Syst 58:246–256
- Canizares C et al (2002) Voltage stability of power systems: concepts, analytical tools and industry experience. IEEE Committee, vol IEEE/PES 93TH0358-2- PWR
- Chakrabarti S, Jeyasurya B (2007) An enhanced Radial Basis Function Network for voltage stability monitoring considering multiple contingencies. Electr Power Syst Res 77(7):780–787
- Chiang HD, Flueck AJ, Shah KS, Balu N et al (1995) CPFLOW: a practical tool for tracing power system steady-state stationary behaviour due to load and generation variations. IEEE Trans Power Syst 10(2):623–634
- Chiang HD, Jiang N (2013) Energy function for power system with detailed DC model: construction and analysis. IEEE Trans Power Syst 28(4):3756–3764
- Eberhart R, Kennedy J (1995) Particle swarm optimization. In: Proceedings of IEEE International Conferernce on Neural Networks, vol 4, pp 1942–1948
- El-Dib AA (2006) Maximum loadability of power system using hybrid particle swarm optimization. Electr Power Syst Res 76(6–7):485– 492
- Gnanambal K, Babulal CK (2012) Maximum loadability limit of power system using hybrid differential evolution with particle swarm optimization. Electr Power Energy Syst 43(1):150–155
- Gotham D, Heydt GT (1998) Power flow control and power flow studies for systems with FACTS devices. IEEE Trans Power Syst 13(1):60–65
- Huang G, Zhu Q, Siew C (2006) Extreme learning machine: theory and applications. Neurocomputing 70(1–3):489–501
- Irisarri GD, Wang X, Tong J, Mokhtari S (1997) Maximum loadability of power systems using interior point non-linear optimization method. IEEE Trans Power Syst 12(1):162–172
- Jeng JT (2006) Hybrid approach of selecting hyper parameters of support vector machine for regression. IEEE Trans Syst Man Cybern Part B Cybern 36(3):699–709
- Kalil MR et al (2006) Ant colony optimization for maximum loadability search in voltage control study. In: IEEE Proceeding on International Power and Energy Conference, Malaysia, pp 240–246
- Kalyani S, Swarup KS (2013) Pattern analysis and classification for security evaluation in power networks. Int J Electr Power Energy Syst 44(1):547–560
- Klump RP, Overbye TJ (1997) Assessment of transmission system loadability. IEEE Trans Power Syst 12(1):416–423
- Kundur P (1994) Power system stability and control. McGraw-Hill Inc., New York
- Liang N, Huang G, Saratchandran P, Sundararajan N (2006) A fast and accurate online sequential learning algorithm for feedforward networks. IEEE Trans Neural Netw 17(6):1411–1423
- Liu Q, He Q, Shi Z (2008) Extreme support vector machine classifier. In: Proceedings of 12th Pacific-Asia Conferrence Advvanced Knowledgw Discovery DataMining. Springer, New York, pp 222–233
- Milano F (2009) A MATLAB toolbox for electric power system analysis and simulation. Available at [http://www.uclm.es/area/gsee/web/](http://www.uclm.es/area/gsee/web/Federico/psat.html) [Federico/psat.html](http://www.uclm.es/area/gsee/web/Federico/psat.html)
- Musirin I, Rahman TKA (2003) Evolutionary Programming based optimization technique for maximum loadability estimation in electric power system. In: IEEE Proceeding of National Power and Energy Conference, Malaysia, pp 205–210
- Nagao T, Tanaka K, Takenaka K (1997) Development of static and simulation programs for voltage stability studies of bulk power system. IEEE Trans Power Syst 12(1):273–281
- Natesan R, Radman R (2004) Effects of STATCOM SSSC and UPFC on voltage stability. In: Proceedings of the Thirty-Sixth Southeastern Symposium on System Theory, pp 546–550
- Power System Test Archive-UWEE (University of Washington) available Online, [http://www.ee.washington.edu/research/pstca.](http://www.ee.washington.edu/research/pstca) Accessed 22 July 2014
- Srivastava L, Singh SN, Sharma J (2000) Estimation of loadability margin using parallel self-organizing hierarchical neural network. Comput Electr Eng 26(2):151–167
- Suganyadevi MV, Babulal CK (2009) Estimating of loadability margin of a power system by comparing voltage stability indices. In: Proceeding of IEEE on International Conference on Control, Automation, Communication And Energy Conservation, 4–6 June 2009
- Suganyadevi MV, Babulal CK (2013) Prediction of Loadability Margin of a Power System using Support Vector Machine. In: Proceeding of IEEE International Conference on Energy Efficient Technologies for Sustainability (ICEETS'13),10th and 12th April 2013
- Tare RS, Bijwe PR (1997) Look-ahead approach to power system loadability enhancement. IEEE Proc Gener Transm Distrib 144(4):357–362
- Taylor CW (1994) Power system voltage stability. McGraw-Hill Education, New York
- Torre SP, Peralta WH, Castro CA (2007) Power system loading margin estimation using a Neuro-Fuzzy approach. IEEE Trans Power Syst 22(4):1955–1964
- Van Cutsem T et al (2002) Voltage stability assessment: concepts, practices and tools. In: IEEE/PES Power System Stability Subcommittee special publication, IEEE product number SP101PSS
- Vapnik V (1998) Statistical learning theory. Wiley, New York
- Wan HB, Song YH (1998) Hybrid supervised and unsupervised neural network approach to voltage stability analysis. Electr Power Syst Res 47(2):115–122
- Yorino N, Harada S, Cheng H (1997) A method to approximate a closet loadability limit using multiple load flow solutions. IEEE Trans Power Syst 12:42
- Yorino N (2005) Predictor/corrector scheme for obtaining Q-Limit points for power flow studies. IEEE Trans Power Syst 20(1):130– 137
- Yu H, Kim S (2010) SVM tutorial: classification, regression, and ranking, handbook of natural computing, vol. 1. Springer, New York
- Zambroni de Souza AC, Mohn FW, Borges IF, Ocariz TR (2011) Using PV and QV curves with the meaning of static contingency screening and planning. Electr Power Syst Res 81(7):1491–1498
- Zimmerman RD, Murillo-Sánchez CE, Thomas RJ (2011) Matpower: Steady state operations. Planning and analysis tools for power systems research and education. IEEE Trans Power Syst 26(1):12–19