

Effect of cation and anion defects on the resistive switching polarity of ZnO_x thin films

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Abstract In this paper, we achieve the resistive switching (RS) polarity from unipolar to bipolar in a simple Al/ ZnO_x /Al structure by moderating the oxygen content in the ZnO sputtering process. In a pure Ar sputtering, Al/ ZnO_x /Al shows unipolar behavior, as oxygen partial pressure increases, the RS polarity changes to bipolar, and the switch current decreases by about five orders of magnitude. The current transport properties of unipolar device show ohmic behavior under both high resistance (HRS) and low resistance states (LRS), but the bipolar device shows Schottky barrier modulated current transport properties. We study the defect types in the unipolar and bipolar devices through photoluminescence (PL) spectra. The PL results show that the interstitial zinc (Zn_i) and interstitial oxygen (O_i) are dominant in unipolar and bipolar devices, respectively. We attribute this phenomenon to Zn_i and O_i playing important role in unipolar (URS) and bipolar resistive switching (BRS), respectively.

Abbreviations

RS	Resistive switching
URS	Unipolar resistive switching
BRS	Bipolar resistive switching
HRS	High resistance state
LRS	Low resistance state
Zn_i	Interstitial zinc
O_i	Interstitial oxygen
V_{O}	Oxygen vacancy
V_{Zn}	Zn vacancy
XRD	X-ray diffraction
SEM	Scanning electron microscopy
PL	Photoluminescence
SCLC	Space charge limited conduction
SCCM	Standard-state cubic centimeter per minute

1 Introduction

The resistive switching phenomenon at simple metal–semiconductor–metal junctions has attracted much attention due to not only fundamental interest in the mechanisms but also the potential uses in next-generation nonvolatile memory devices. Several properties caused the researchers' interest, such as conduction mechanism, cell endurance, switching speed and memory density. Among these crucial parameters, the memristor polarity is an important issue that has attracted interest of many researchers. According to whether it depends on the operating voltage polarity, the RS polarity can be classified into two types: unipolar (URS) and bipolar resistive switching (BRS) [1].

So far, for the same kind of materials even with different preparation methods, the resistance switching polarities are not identical. Such are binary metal oxides: NiO [2, 3],

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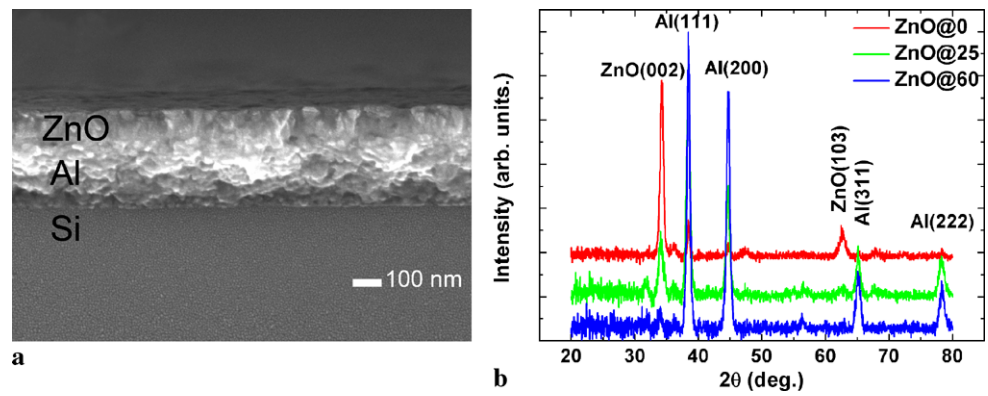
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Fig. 1 (a) The cross section image of ZnO@0/Al/Si structure. (b) The XRD pattern of three types of ZnO films sputterings grown on Al/Si substrate



TiO₂ [4, 5], ZnO [6, 7], TaO_x [8, 9]. We know that different material preparation methods may cause different type defects, metal interstitial-type defect or oxygen vacancy defect, which would affect the device switching polarity.

Previous studies have shown coexistence of BRS and URS in several metal-oxide-based memory cells. Goux et al. showed that the two switching modes can be activated by using an optimized oxidation process (1 min) of a Ni blanket layer. Short oxidation (30 s) resulted in shortcut cell, while long oxidation ($\gg 1$ min) resulted in common URS [10]. The two switching modes also can be changed in pure Ar sputtering ZnO based thin films [11], but the resistive switching mode commonly displays bipolar deposit in mixed Ar and O₂ working gas [12]. Lee et al. [13] and Sun et al. [14] reported resistive switching change between the BRS and URS in SrTiO₃, and the studies indicate the importance of defect control engineering in RS. In addition, there were similar reports about TiO₂ [15] and HfO₂ [16] based RS phenomenon. Recent study has shown the conversion from unipolar to bipolar resistance switching by inserting a stoichiometric Ta₂O₅ layer in Pt/TaO_x/Pt cells [8]. Oka et al.'s recent studies demonstrated that coexistence of anion and cation defects is critical to the transport properties of p-type NiO [17]. So the defects and stoichiometry in the active switching region would have an important influence on electrical transport and RS polarity.

In this study, we showed the effects of cation and anion defects and the device interfacial characteristic on the RS polarity. We studied the RS behavior which can be modulated by changing cation and anion defects concentration during deposition of ZnO thin films. URS mode devices were observed in pure Ar sputtering; interestingly, with increasing oxygen partial pressure, the devices cells show BRS feature. The PL spectra results show that the Zn_i/O_i ratio decreases with increased oxygen content. From these results, a model is discussed to explain these systematic changes.

2 Experimental

The resistive switching structure, having a metal–semiconductor–metal layout, was prepared by depositing Al, ZnO, Al in sequence on P–Si substrate. Initially, 250-nm thick Al films were deposited on P–Si by DC magnetron sputtering. Subsequently, ZnO films were added by reactive RF magnetron using a 10 cm diameter by 0.8 cm thick ZnO target (99.99 %) at room temperature in different oxygen partial pressure. The oxygen partial pressure is defined as $PO_2 (\%) = p(O_2)/p(Ar + O_2)$. The working pressure was maintained at about 0.33 Pa. The resistive switching in ZnO films with oxygen partial pressure of 0, 25 and 60 % obtained with various O₂ flow rates during deposition are hereafter respectively denoted as ZnO@0, ZnO@25 and ZnO@60. For electrical measurement, circuit Al top electrodes (200 nm thick, 200 μ m in diameter) were deposited on ZnO films by sputtering using a metal shadow mask. All fabrication process was carried out at room temperature and no post-processing was used.

The microstructure and crystal structure were examined by scanning electron microscopy (SEM, JSM-7600F) and X-ray diffraction (XRD, PANalytical PW3040/60). The film thickness was observed by SEM and generalized ellipsometer (developed by Huazhong University of Science and Technology). The presence and types of oxygen- and zinc-related defects were investigated by photoluminescence (PL, FP-6500) spectroscopy using the 325-nm line of He–Cd laser as the excitation source. The I – V curves of memory cell were measured by semiconductor characteristic measurement system (Keithley 4200 SCS) at room temperature in ambient condition. The measurements were performed by a DC voltage sweep mode and all the bias voltages were applied on top electrode with bottom electrode grounded.

3 Results and discussion

Figure 1(a) shows the SEM image of ZnO@0 Al/Si cross section of the structure. The result shows that the ZnO films

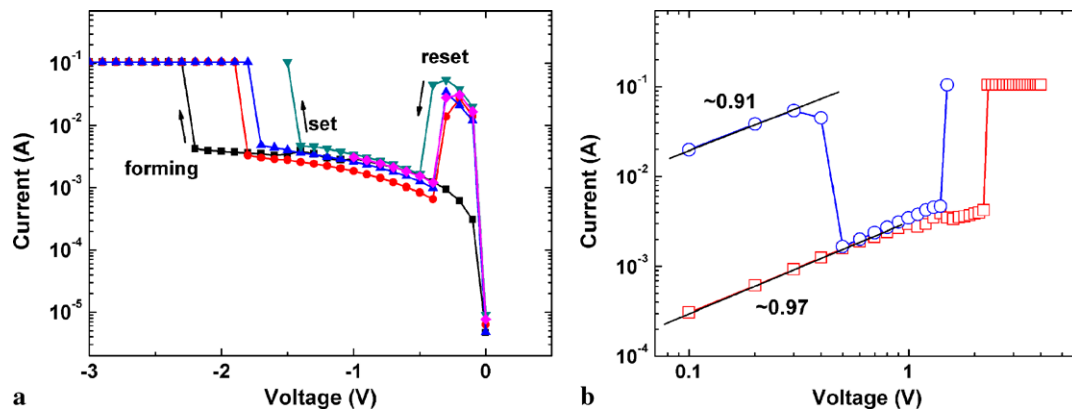


Fig. 2 (a) Typical I - V characteristic of URS in Al/ZnO@0/Al cell. (b) I - V characteristic in a double-logarithmic scale. The different values of slope are listed in the plots

have a columnar grain structure to the substrate. It suggests that the ZnO preferential growth direction is along (002). It can be found that the thickness of ZnO and Al bottom electrode was about 120 and 250 nm, respectively. The film thickness of ZnO@25 and ZnO@60 confirmed by generalized ellipsometer was 60 and 40 nm, respectively. And the deposition rate of ZnO@0, ZnO@25 and ZnO@60 was 8.6, 4 and 3.5 nm/min respectively, according to the deposition time.

The XRD pattern of ZnO films deposited on Al/Si substrate is shown in Fig. 1(b). It shows that the ZnO films exhibit polycrystalline properties, and have a c -axis preferred orientation, in agreement with SEM result. It was also found that with increase in film thickness, the (002) peaks intensity becomes larger. It indicates the improved ZnO crystalline properties.

Figure 2(a) shows the I - V curve of the memory device ZnO@0. The device displays unipolar resistance switching behavior. The I - V curves are systematic with respect to the polarity of the bias voltages. To prevent permanent damage during the forming and setting processes, the current flow was limited by a compliance current [8]. For simplicity, we show the negative voltage side only.

When -3 V voltage was applied to the pristine cell, it was observed that, with increase of the applied voltage from 0 to -3 V, the device was originally in the HRS and a setting process occurred at about -2.2 V (V_{set}).

When again -3 V bias was applied to the device, the LRS returned to a high-resistance state (HRS) at 0.3 V. The URS setting process usually is known to originate from the conduction filament formed by mobile metal ions under electric field. The reset process originated from filamentary rupture controlled by Joule heating, and shows no polarity dependence [2, 7]. Zn_i and oxygen vacancy (V_O) are generally considered to be the main reasons causing unintentional n-type conductivity of ZnO [18]. So the URS characteristic of the Al/ZnO@0/Al cell may come from the Zn_i and V_O related defects.

In order to investigate the origin of the switching characteristic of the device, the I - V curves were re-plotted in double-logarithmic scale, as shown in Fig. 2(b). It can be seen that under HRS and LRS in the voltage sweep region, the I - V curves show linear behavior; this suggests that the I - V characteristic follows the Ohm's law and is regarded as the charge transport of a conductor system. And it also indicates that the interface contact shows ohmic contact.

Figure 3(a) representatively shows voltage sweeps programming the cell of ZnO@25 under different compliance currents. It can be found that the resistive switching polarity shows a typical BRS characteristic. The device is originally in the HRS; with increasing the applied voltage the current of the device increased slowly and the setting voltage became about 3 V. Afterwards, by sweeping the voltage to a negative value, the current decreased and the state of the device switched again to HRS. In order to characterize the effect of the compliance current on the RS polarity, we studied the I - V properties of the memory cell under four different compliance current settings: 90, 400, 600 and 900 μA . It can be seen that the cell is showing a stable BRS behavior.

Both positive and negative sweeps are shown in Fig. 3(b) in double-logarithmic scale. From the positive voltage sweep plot we can see that in the low voltage range ($0 \rightarrow 0.5$ V) the I - V characteristic first exhibits linear properties and then shows quadratic characteristic with the slope ~ 2.08 . With further increasing the applied voltage ($2.5 \text{ V} < V_{\text{applied}} < 3 \text{ V}$), the current rises rapidly and the slope reaches 6–8. The current transport characteristic in the device can be explained by a trap-controlled space charge limited conduction (SCLC) mechanism [19, 20].

Figure 4(a) shows the I - V curve of a sample ZnO@60 in semi-logarithmic scale. Obviously, the resistive switching polarity also shows BRS. The numbered arrows indicate the voltage sweep direction. Clearly, nonlinear and cut-off properties of HRS current characteristic indicate that

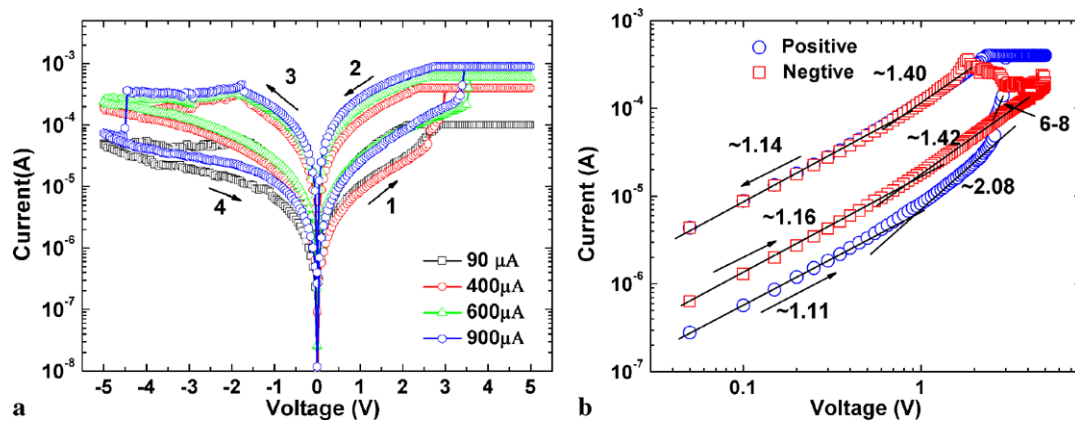


Fig. 3 (a) I - V curves of resistive switching loops at different compliance currents of Al/ZnO@25/Al cell. The numbered arrows indicate the voltage sweep direction. (b) I - V characteristic in a double-

logarithmic scale, the different values of slopes listed in the plots. Arrow shows the voltage sweep direction

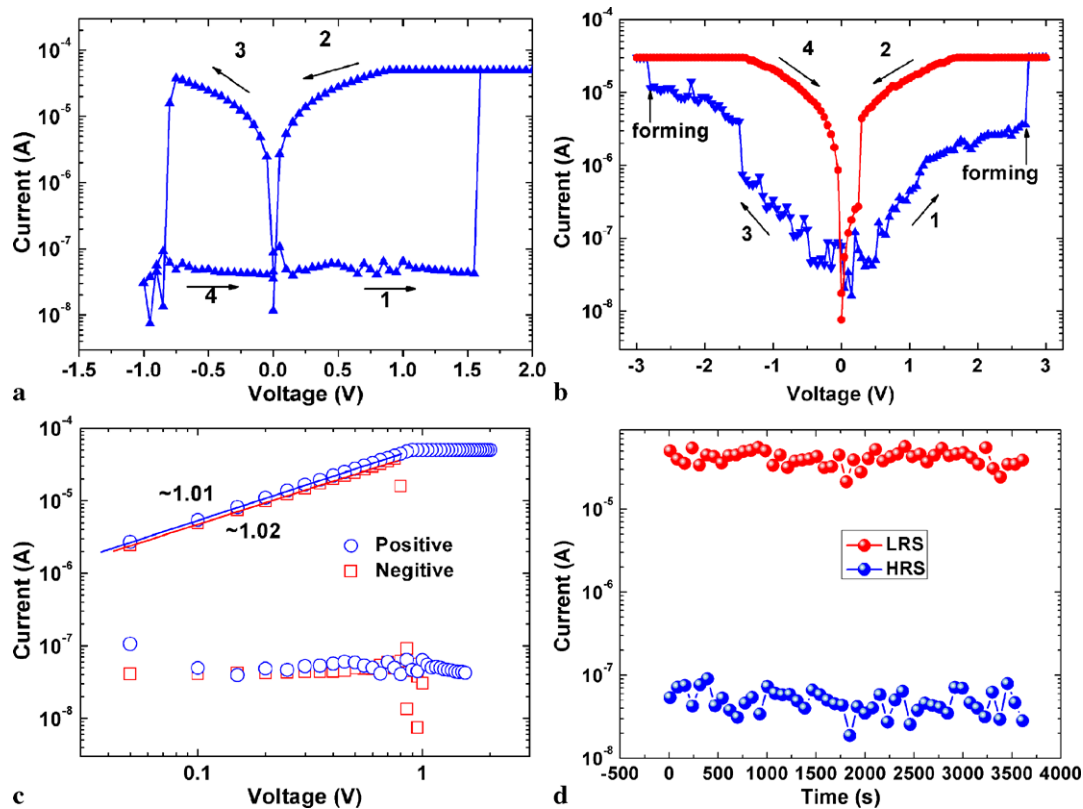


Fig. 4 (a) I - V curve of Al/ZnO@60/Al cell showing BRS characteristic. (b) The forming process of the cell. (c) The voltage sweep I - V curve in a double-logarithmic scale. (d) Retention data of the Al/ZnO@60/Al device at room temperature

the ZnO makes Schottky barrier with bottom and top electrodes. Figure 4(b) shows the cell forming process. The voltage sweep direction is $0 \text{ V} \rightarrow +3 \text{ V} \rightarrow 0 \text{ V}$, and then $0 \text{ V} \rightarrow -3 \text{ V} \rightarrow 0 \text{ V}$. It can be seen that in the initial state both the positive and negative voltage directions show high resistance. Figure 4(c) shows the same I - V curve data in double-logarithmic scale. The I - V characteristic appears

linear under LRS. The slope in positive and negative voltage directions is about 1.01 and 1.02, respectively. This phenomenon indicates that the Schottky barrier disappeared and the interface contact transformed into ohmic contact after a setting process. The device retention (bias at 0.75 V) performance for both resistance states is as displayed in Fig. 4(d). It shows that both states are stable within 3600 s.

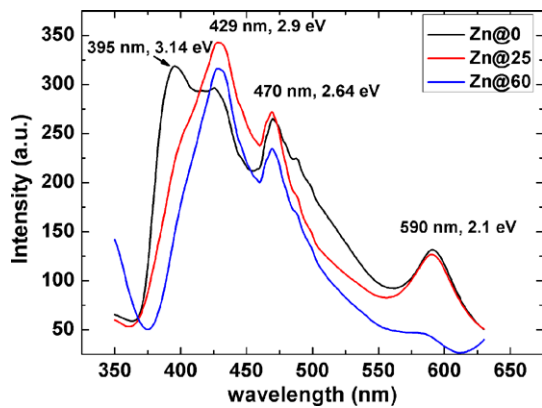


Fig. 5 Room temperature PL spectra of ZnO films prepared at various oxygen contents

According to the metal/semiconductor contact theory, the Schottky barrier should be neglected at the Al/ZnO interface due to the work function value of Al (~ 4.28 eV), ZnO (~ 4.1 eV) [21]. But the interface contact and conduction mechanism for the three devices are obviously different. The ZnO@60 cell shows Schottky contact with electrodes, while the ZnO@0 cell shows ohmic contact, and the ZnO@25 cell shows SCLC mode charged transport. The standard Gibbs free energy of formation of Al oxide is -1582.3 kJ/mol, which is far lower than of the ZnO (-320.5 kJ/mol). Al electrode can get O^{2-} from ZnO and form AlO_x , so the following reaction occurs: $Al + O^{2-} \rightarrow AlO_x$ [22]. And the interfacial oxide layer was confirmed by other studies in the image of TEM [12, 23] or AES [24]. Therefore the AlO_x acts as a barrier layer blocking the current transport, so the HRS of ZnO@60 shows the Schottky contact characteristic, and the original $I-V$ curve at both positive and negative voltages shows cut-off behavior. But to ZnO@0 cell there is not enough thickness of oxide barrier layer at the interface to block the charge transport, the device acts as a conductor in the circuit, so the current shows linear behavior no matter whether before or after the switching. The SCLC type shown in ZnO@25 is intermediate state between Schottky contact and ohmic contact.

To investigate the types of the defect on the resistive switching behavior, PL spectra of ZnO/Al/Si films were carried out at room temperature, and the results are shown in Fig. 5. Four emission peaks at 395 nm (3.14 eV), 429 nm (2.9 eV), 470 nm (2.64 eV), and 590 nm (2.1 eV) are found in the three types of samples. On the basis of previous studies, these peaks related to several types of intrinsic defects in the ZnO growth, such Zn_i , Zn vacancy (V_{Zn}), V_O and O_i [18, 25].

The 395 nm emission is originally from band transition from Zn_i level to valence band according to the full potential linear muffin-tin orbital method [26]. The curve shows that with the oxygen content increase the intensity of 395 nm

emission peak decreases, and the content of Zn_i decreases with the oxygen partial pressure increase.

The 429 nm (2.9 eV) emission peak is due to the O_i related defects. Previous studies show that the O_i lie 2.96 eV from the conduction band in ZnO; they suggest that the 429 nm emission comes from radiation from the conduction band to O_i level [27, 28]. Ahn et al. suggested that ZnO films grown at O_2 flow rates of 50 standard-state cubic centimeters per minute (SCCM) showed blue emission at 2.85 eV by MOCVD at room temperature, but corresponding emission not found at 10 SCCM [29]. They claimed that during ZnO growth process using excess oxygen gas leads to formation of oxygen interstitial sites. So we consider the 429 nm emission peak associated with O_i related defect. It has been shown that the intensity of 470 nm emission peaks enhanced with O_2 partial pressure decreased in Ar/ O_2 atmosphere RF sputtering ZnO films [30], which would account for V_O to the 470 nm emission peak.

Previous studies indicated that the 590 nm (2.1 eV) emission is related to V_OZn_i complex in the films [31], the calculation predicted the energy of complex V_OZn_i lying 2.16 eV below the conduction band minimum [32]. They found that the 590 nm emission peak disappeared in the 60 % oxygen partial pressure. Since with oxygen content increased the concentration of both V_O and Zn_i defects decreased subsequently, the concentration of complex V_OZn_i may have become very low, and resulting 590 nm emission disappeared.

We also observed that the HRS resistance in the three devices was 3.4×10^2 , 1.22×10^5 and $1.57 \times 10^7 \Omega$ at 1 V for respective sample ZnO@0, ZnO@25 and ZnO@60. This means that the switch current of the device decreased with oxygen partial pressure. This reason mainly is that the acceptor O_i can compensate for the electronic carrier formed by V_O and Zn_i , thus reducing the film conductivity. And these non-lattice oxygen ions also play important role in resistance switching process, which would drift to electrode under the applied bias voltage and redox reaction occurring at the interface. We know that the low switching current is important in nanoscale size memory, as the μA -scale switch current is favorable in designing nanoscale RRAM [33].

From the PL results we also observed that the relative intensity of 395 to 429 nm emission peaks decreased with oxygen partial pressure, thereby suggesting that Zn_i defect density decreased with oxygen partial, while O_i density increased with oxygen partial, and the relative quantity of Zn_i to O_i increased at room temperature sputtering.

Based on the above analysis, we assumed the following mechanism for URS and BRS. For the URS, when a negative bias is applied to the top electrode, the Zn^+ drifts under strong electronic field gathering tiny conducting filaments, resulting in low resistance state. Subsequently, when applying to it the same polarity voltage, the cell returned to HRS due to Joule heating enhanced filaments oxidation.

For the BRS, the cell resistance was mainly controlled by naturally formed Schottky barrier junction at the interface. The resistive switching comes from AlO_x barrier layer formation and reduction. After the forming process, the device is in LRS and the I - V curve characteristic shows linear behavior: at this time the Schottky barrier is disappearing. When negative voltage is applied to the top electrode, O^{2-} moves to the bottom electrode resulting in redox action of AlO_x layer as insulated layer. Due to the barrier, I - V curve displays a cut-off characteristic in the HRS. When the positive voltage is applied to the top electrode, the following reduction reaction occurs: $\text{AlO}_x \rightarrow \text{Al} + \text{O}^{2-}$, resulting in LRS.

4 Conclusions

In conclusion, we studied the effect of cation and anion defects on the RS polarity based on $\text{Al}/\text{ZnO}_x/\text{Al}$ memristive device. By modulating the oxygen partial pressure, we can change the resistive switching polarity from URS to BRS. In low oxygen content, the device shows URS, the current transport follows ohmic law, while with increasing oxygen content, the device shows BRS, and the electron transport displays a Schottky barrier modified characteristic. The PL spectra data indicate that the concentration of Zn_i and O_i defects was obviously different in URS and BRS devices. The URS behavior attributes to the Zn_i formed filament and rupture. While the BRS behavior attributes to the reverted AlO_x formed by O^{2-} and the electrode at interface. The presented results suggest that defects of anion and cation play an important role in resistive switching behavior.

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