

Concurrent design process analysis and optimization for aluminum profile extrusion product development

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Abstract Owing to intricate interdependency and information feedback among tasks, the concurrent design process probably cannot converge to the correct solution when there are wrong integration and improper interaction between activities. Therefore research on concurrent design process optimization is necessary. In the paper, a novel methodology is proposed to analyze and optimize the concurrent engineering process scientifically. Based on design structure matrix and graph theory, coupled task recognition and design task level plotting are performed for the concurrent design process of aluminum profile extrusion product development. Three factors are used to describe the coupling property of activity, namely sensitivity, complexity and affection factors, which provide the basis to analyze development process quantitatively. And an optimism algorithm is presented to define the initial iteration order of coupled task set. Finally a rational and efficient concurrent design process model is constructed, which can make aluminum profile product development faster, with lower costs and higher quality. The methodology proposed is also applicable to other concurrent engineering fields.

Keywords Aluminum profile · Product development · Concurrent design · Process optimization

1 Introduction

Concurrent engineering (CE) has become increasingly important for product development in recent years. CE is a philosophy that suggests the need to consider design issues simultaneously where they were considered sequentially in the past. The sequential design process has been considered inefficient, since this type of design process typically leads to greater development time, greater cost, and lower overall design quality, all of which lower the overall profit generated by the design [1]. But concurrent design is not always prior to sequential method. Owing to intricate information feedback, when there are wrong integration and improper interaction between activities, concurrent design process possibly divergent [2]. Therefore research on concurrent design process is necessary. Process analysis and scheduling of concurrent design aims to replace the confused coupling condition by a few of small design loops with information feedback according to the interdependency degree between activities.

In general, it is considered that managing the design process includes four major steps [3]: (1) model the information and dependency structure of the design process; (2) provide a design plan showing the order of execution for the design tasks; (3) reduce the risk and magnitude of iteration between design tasks; (4) explore opportunities for reducing the project cycle time. Several management tools have been developed to model the interface and dependencies among the decomposed tasks of design process. The project evaluation and review technique (PERT) is a diagraph of a project [4]. In the PERT method, three probabilistic time estimates are given to each task. The critical path method (CPM) is a variation of the PERT method. CPM assumes a time-cost tradeoff rather than probabilistic time used in PERT. Both

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methods improve the process flow only by crashing the critical activities, but they do not consider iteration and feedback loops that are characteristics of engineering designs, and they ignore the concurrency and overlapping of the design process. To study information management processes the standardized IDEF0 modeling technique is a useful tool. IDEF0 was driven from structural analysis and design technique [5]. The IDEF0 technique supports the needs of modeling the process in a formalized manner to be able to compare and refine the modeled process. However, the IDEF0 methodology is inefficient to support the modeling of concurrent activities and iterations between activities are difficult to analyze with the help of the IDEF0 technique. Direct graph is a general method to describe procedure relations. It is composed with vertexes representing design activities and vectorial lines representing information connection. The direct graph can not only describe system clear and visually, but also analyze the process quantitatively based on mathematic tools of graph theory (GT) [6]. But when concurrent system is complex and involved activity amount is too large, the direct graph will become very confused. A more compact representation of a design process is the design structure matrix (DSM) [7]. It overcomes the size and visual complexity of all graph-based techniques and matrices are amenable to computer manipulation and storage. DSM has been used in some researches for CE implementation [5, 8–10]. In this paper, we also use DSM representation to describe the product design process. On the basis of DSM and GT, a methodology has been proposed to analyze and optimize concurrent product development process.

This paper has been organized in the following manner. In Sect. 2, the interdependencies among activities during design process are analyzed and summarized into three types. An algorithm to recognize the coupled activities of the product development and figure out the order levels of every design activities is introduced. Section 3 proposes three factors to describe the coupling property of activity and a quantitative optimization algorithm is presented for coupled activities to define iteration order. Section 4 analyzes aluminum profile product development process particularly and presents an optimized process model. Finally a summary and proposed future work are given in Sect. 5.

2 Concurrent design process analysis

2.1 Relations among design activities and DSM

The product design process is a set of design activities. There are various interrelations among these activities

[11, 12]. According to the degree of interdependency, the authors divide the interdependency relationship among activities into three types: sequential relation, parallel relation and coupled relations as shown in Fig. 1.

Sequential relation means that decisions of early design activities affect downstream activities, but no repercussions, generating a sequence of decisions that results in a straightforward process without iteration. Parallel relation means that a design activity is independent from one another or the interdependency degree between them is very low. And there is little information exchange between them. In this situation two or more activities can be executed in parallel. Coupled relationship means that the two activities will affect each other. The interdependency degree between them is very high, and there are information exchanges among coupled activities. Such interdependency needs many iteration loops to set all design information in a consistent way.

By means of DSM (see Fig. 2), an activity set $S = \{a_1, a_2, \dots, a_n\}$ can be represented by matrix B with n rows and n columns and, in which $b_{ij} = 1$ ($i, j = 1, 2, \dots, n$) indicates that activity a_i outputs information to a_j while $b_{ij} = 0$ indicates that activity a_i doesn't output information to a_j . And B is called DSM of S . It is defined that b_{ii} equals to zero. If one interprets the activity ordering in the DSM as the execution sequence, the elements below the diagonal represent the forward information transfer to later (i.e., downstream) activities, and the elements above the diagonal depict information fed back (or iteration) to earlier (i.e., upstream) activities. As for the three relations, there are the following conclusions:

1. If it is sequential relation between a_i and a_j , then $b_{ij} = 1$ and $b_{ji} = 0$.
2. If it is parallel relation between a_i and a_j , then $b_{ij} = b_{ji} = 0$.
3. If it is coupled relations between a_i and a_j , then $b_{ij} = b_{ji} = 1$.

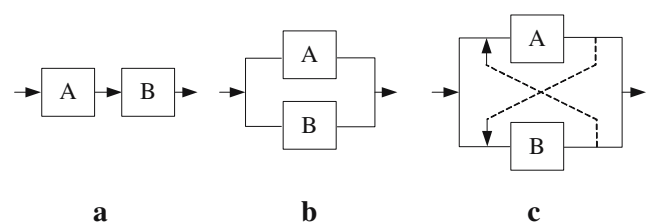


Fig. 1 Relation types between design activities: (a) sequential relation, (b) parallel relation, (c) coupled relation

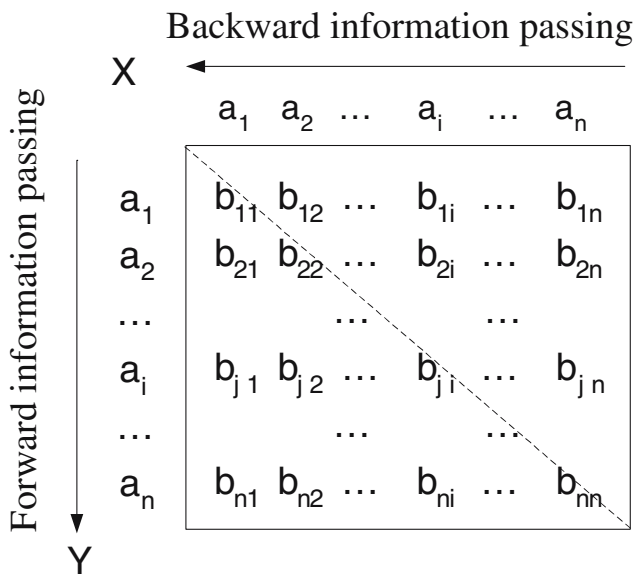


Fig. 2 Design structure matrix

2.2 Coupling activities recognition

In concurrent design, coupled relation is the main relation between activities and the main factor increasing complexity of concurrent design. Frequent information exchanging among activities makes the design and management of process very complicated. Therefore coupled activities should be recognized before re-engineering the design process. Generally designer plans and manages the design tasks by his own domain knowledge and experience qualitatively which is imprecise and unreliable. In this paper, a new algorithm of recognizing coupled activities is introduced as follows.

Defining design activities as nodes of vectorial graph G and activities relation as directed lines, the design process can be described as a directed graph. The directed lines or linkage reflect a dependency or a relationship between the connected activities. And the DSM can be considered as the transpose of the incidence matrix of the corresponding directed graph. The problem of recognizing coupled activities set is translated into the problem of seeking strongly connected component in a directed graph.

Definition 1 Given that $G=(V, E)$ is a directed graph, where $V=(v_1, v_2, \dots, v_n)$, and A is the incidence matrix of G , P is the accessibility matrix of G Then

$$P = A^{(1)} \vee A^{(1)} \vee A^{(2)} \vee A^{(3)} \vee \dots \vee A^{(n)}$$

$$= \bigvee_{j=1}^n A^{(j)} (1 \leq j \leq n, 1 \leq i \leq n) \tag{1}$$

Table 1 Algorithm of “ \vee ”

Algorithm of “ \vee ”		
\vee	0	1
0	0	1
1	1	1

where

$$A^{(i)} = \underbrace{A^{(1)} \wedge A^{(1)} \wedge \dots \wedge A^{(1)}}_i \tag{2}$$

“ \wedge ” is called the Boolean Sum operator, and “ \vee ” is called the Boolean Product operator. And both operations are defined as follows.

- Definition 2** (1) The matrix $R = (r_{ij})_{n \times n}$ is a Boolean matrix, if the values of $r_{ij}=(i, j=1, 2, \dots, n)$ can only be 0 or 1.
- (2) The matrix $W = (w_{ij})_{n \times n}$ is a Boolean Sum of R and S , if $w_{ij} = r_{ij} \vee s_{ij}$ where $R = (r_{ij})_{n \times n}$ and $S = (s_{ij})_{n \times n}$ are Boolean matrices.
- (3) The matrix $U = (u_{ij})_{n \times n}$ is a Boolean Product of R and S , if $u_{ij} = \bigvee_{k=1}^n (r_{ik} \wedge s_{kj})$, where $R = (r_{ij})_{n \times n}$ and $S = (s_{ij})_{n \times n}$ are Boolean matrices.
- (4) The algorithms of “ \vee ” and “ \wedge ” are explained in Tables 1 and 2.

Theorem 1 [6] $P = (p_{ij})_{n \times n}$ is accessibility matrix of graph G . P^T is the transposed matrix of P . Operation $P \cap P^T$ is defined as

$$P \cap P^T = \begin{vmatrix} p_{11} & \dots & p_{1n} \\ \vdots & & \vdots \\ p_{n1} & \dots & p_{nn} \end{vmatrix} \cap \begin{vmatrix} p_{11} & \dots & p_{n1} \\ \vdots & & \vdots \\ p_{1n} & \dots & p_{nn} \end{vmatrix} \tag{3}$$

$$= \begin{vmatrix} p_{11}^2 & p_{12} \cdot p_{21} & \dots & p_{1n} \cdot p_{n1} \\ p_{21} \cdot p_{12} & p_{22}^2 & \dots & p_{2n} \cdot p_{n2} \\ \vdots & \vdots & & \vdots \\ p_{n1} \cdot p_{1n} & p_{n2} \cdot p_{2n} & \dots & p_{nn}^2 \end{vmatrix}$$

Table 2 Algorithm of “ \wedge ”

Algorithm of “ \wedge ”		
\wedge	0	1
0	0	0
1	0	1

If it is accessible from node v_i to node v_j , then $p_{ij}=1$. If it is accessible from node v_j to node v_i , then $p_{ji}=1$. Thus the nodes v_i and v_j are accessible from each other, if and only if $p_{ij} \cdot p_{ji}=1$. As to the matrix $P \cap P^T$, If in i th row of matrix $P \cap P^T$, elements of column j_1, j_2, \dots, j_k is not zero, node $v_i, v_{j_1}, v_{j_2}, \dots, v_{j_k}$ is in a strong connected set. And the activities corresponding to these nodes are in a coupled set.

2.3 Design activities level plotting

If every coupled activity set is merged into one activity, and the rows and columns responding to the coupled activity set have been merged into one row and column, the accessibility matrix P becomes a reduced matrix P' . Design activities levels can be plotted according to Theorem 2.

Theorem 2 [13] If P' is the reduced matrix of accessibility matrix P , $P'E_{m-1} = (p_1, p_2, \dots, p_n)^T$, $m \geq 1$, n -dimension vector $E_0=(1,1,\dots,1)^T$, $E_m=(e_1, e_2, \dots, e_n)^T$, where

$$e_i = \begin{cases} 0 & (p_i \in \{0, 1\}) \\ 1 & (p_i \notin \{0, 1\}) \end{cases} \quad (4)$$

Then the necessary and sufficient condition of $L_r = \{v_i\}$ is $p_i = 1$, where L_r means that a_i is an element of m th level in the Graph G .

3 Concurrent design process optimization

3.1 Design activities reengineering

As the result of design tasks analysis, some deficiencies such as too large coupled set and insufficient concurrent operation can be found. To make whole design process rational, some methods are applied to reengineer tasks. There are mainly two operations proposed, which is shown in Fig. 3.

1. Decoupling operation. If the connection between two interdependent tasks is very weak, the relation can be deleted to make coupling linkage break. In this operation, owing to wrong assumption, it is likely to

cause iteration. The weaker the dependency is, the less risk of iteration there will be.

2. Detailing operation. By dividing a task into several smaller activities, upstream activity can provide design information to downstream earlier while downstream activity can also feedback forward timely [14]. Then overall iterative work and cycle time are reduced.

After all activities and relation have been analyzed, coupled activities recognizing and level plotting will be made for the second time. Then iteration order of coupled activities is defined. The complete flowchart is shown in Fig. 4.

3.2 Iteration order optimization for coupled activities

The objective of iteration order optimization is to reduce the iteration work and time caused by traditional empirically based order definition method and make the process converge as soon as possible [15, 16]. In this paper, an optimization algorithm is presented to get scientific initial iteration order. The coupled activities are plotted by the following principles:

1. Activity affected by other activities greatly or sensitive to other tasks should be executed later to make it acquire sufficient information and to reduce loop times;
2. The more complex an activity is, the higher the iteration cost will be. Therefore complex activity should be executed later;
3. Activity affecting others greatly should be placed forward.

In the design process, every activity can be regarded as an “information machining centre”, in which the input information is transformed into output information. In this paper, the input and output information of an activity is divided into two groups: local information relating to activities in the same coupled set while global information relating to activities out of the set. As shown in Fig. 5, $\{I_1, I_2, \dots, I_n\}$ are the local input information, while $\{I_{n+1}, \dots, I_m\}$ global input information, $\{O_1, O_2, \dots, O_n\}$ local output information and $\{O_{n+1}, \dots, O_m\}$ global output information.

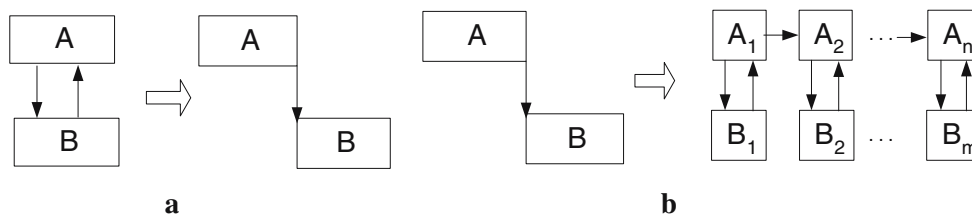


Fig. 3 Design activities reengineering operations: (a) decoupling, (b) detailing

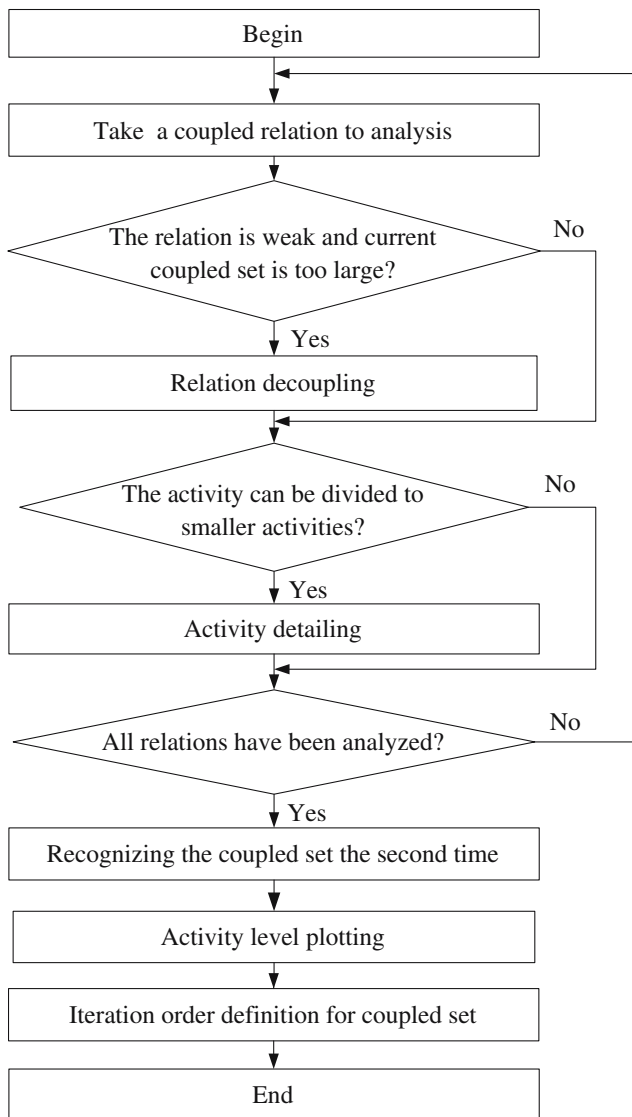


Fig. 4 Design activities reengineering flowchart

To analyze design activity quantitatively, the coupling property of an activity can be represented by three factors:

1. Sensitivity factor f_s Activity sensitivity to other activities in the same set is represented by factor f_s . We define f_s as $f_s = m/n$, in which n refers to total information amount needed by the activity and m refers to local input information.
2. Complexity factor f_c Activity complexity is represented as factor f_c . Factor f_c is mainly decided by required time and cost of this activity. More time and cost a design activity need, more complex it is. We define $f_c \in (0,1)$ and the specific values are decided by domain expert.

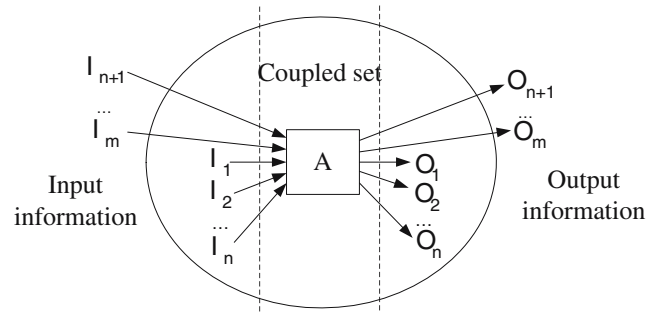


Fig. 5 Information flow of design activity

3. Affection factor f_a Affection power of an activity to others in the same set is represented as f_a . We define $f_a = k$, in which k refers to local output information amount. The iteration order is defined as follows. Firstly, input and output information for every activity will be analyzed in detail. The local and global information are recognized separately. Initial iteration order is mainly decided by sum of f_s and f_c . Considering complexity is more important, we define weight values of f_s and f_c as 0.5 and 1 respectively. When computed results of $f_s/2 + f_c$ are same, iteration order is decided by affection factor f_a .

4 Case study

Aluminum profile extrusion product development involves product design, manufacturing process design, equipment selection and extrusion die design, etc. Owing to large quantities of geometry parameters and intricate relation among procedures of whole product life cycle, it is very complicated to develop a new aluminum profile product. Up to now to a large extent the development process is empirically based. In this paper a scientific process model based on concurrent engineering philosophy and method proposed above is to provided to improve the development efficiency.

4.1 Design tasks analysis of aluminum profile product development

Design activities and procedures involved in the process of aluminum profile extrusion product development can be classified as five groups and each group can be divided further into several specific activities, as shown in Table 3. There are a total of 19

Table 3 Design activities analysis for aluminum profile extrusion product development

Design activities	Input information	Output information
G1	Product design and analysis	<ul style="list-style-type: none"> a₁ Product design
G2	Extrusion process design	<ul style="list-style-type: none"> a₂ Product feasible analysis a₃ Process flow scheduling a₄ Extrusion method selection a₅ Extrusion temperature definition a₆ Extrusion speed definition a₇ Press selection
G3	Extrusion equipment and accessory tools selection or design	<ul style="list-style-type: none"> a₈ Tool type selection a₉ Tool dimension definition a₁₀ Structure and strength analysis for accessory tools a₁₁ Die type selection
G4	Extrusion die design and analysis	<ul style="list-style-type: none"> a₁₂ Profile positioning (for solid die) or porthole design (for porthole die) a₁₃ Die hole working dimension a₁₄ Pre-chamber design a₁₅ Bearing design a₁₆ Adjustment structure design a₁₇ Outer shape design a₁₈ Material selection a₁₉ CAE analysis

activities. To investigate interdependent relationship between activities, the required input information and final output information are analyzed for every activity in detail.

From Table 1, DSM for aluminum profile product development can be gotten as shown in Table 4.

According to Theorem 1, accessibility matrix P and $P \cap P^T$ can be figured out on the basis of DSM as follows:

$$P = \begin{pmatrix} 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\ 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 & 1 \\ & & 1 & & & & & & & & & & & & & & & & & & \\ & & & 1 & & & & & & & & & & & & & & & & & \\ & & & & 1 & & & & & & & & & & & & & & & & \\ & & & & & 1 & & & & & & & & & & & & & & & \\ & & & & & & 1 & & & & & & & & & & & & & & \\ & & & & & & & 1 & & & & & & & & & & & & & \\ & & & & & & & & 1 & & & & & & & & & & & & \\ & & & & & & & & & 1 & & & & & & & & & & & \\ & & & & & & & & & & 1 & & & & & & & & & & \\ & & & & & & & & & & & 1 & & & & & & & & & \\ & & & & & & & & & & & & 1 & & & & & & & & \\ & & & & & & & & & & & & & 1 & & & & & & & \\ & & & & & & & & & & & & & & 1 & & & & & & \\ & & & & & & & & & & & & & & & 1 & & & & & \\ & & & & & & & & & & & & & & & & 1 & & & & \\ & & & & & & & & & & & & & & & & & 1 & & & \\ & & & & & & & & & & & & & & & & & & 1 & & \\ & & & & & & & & & & & & & & & & & & & 1 & \\ & 1 \end{pmatrix};$$

Table 4 Design Structure Matrix (DSM) of aluminum profile concurrent product development

	a ₁	a ₂	a ₃	a ₄	a ₅	a ₆	a ₇	a ₈	a ₉	a ₁₀	a ₁₁	a ₁₂	a ₁₃	a ₁₄	a ₁₅	a ₁₆	a ₁₇	a ₁₈	a ₁₉
a ₁	0	1	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	0
a ₂	1	0	1	1	1	1	1	1	0	0	0	0	0	1	0	0	0	0	0
a ₃	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
a ₄	0	0	0	0	0	0	1	0	0	1	1	0	0	0	0	0	0	1	1
a ₅	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	1	1
a ₆	0	0	0	0	1	0	1	0	0	1	0	0	0	0	0	0	0	1	1
a ₇	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0
a ₈	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
a ₉	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0
a ₁₀	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
a ₁₁	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1
a ₁₂	0	0	0	0	0	0	1	0	1	0	0	0	0	0	1	1	0	0	1
a ₁₃	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₄	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₅	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₆	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₈	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
a ₁₉	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0

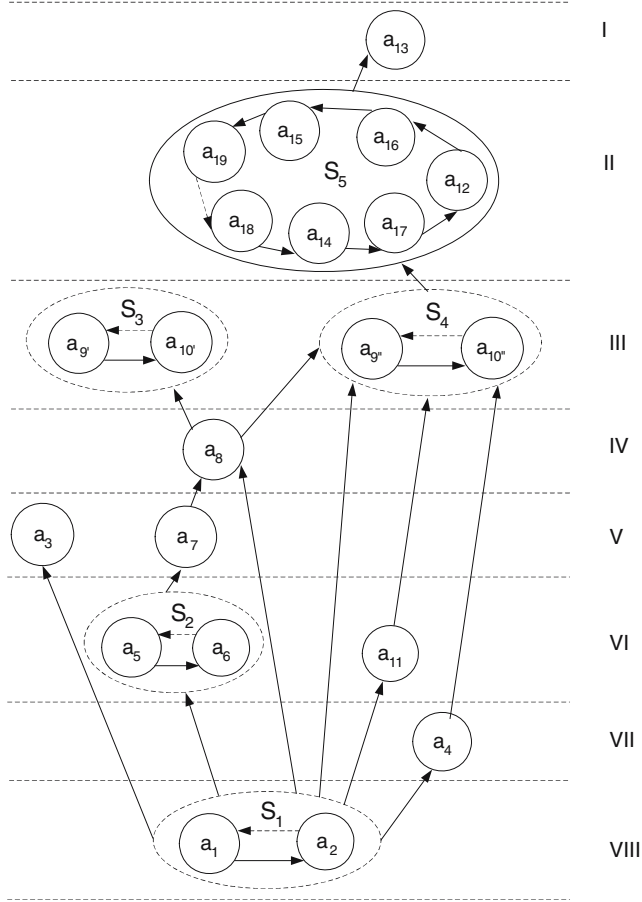


Fig. 7 Optimized process model for aluminum profile extrusion product development

$$E_3 = (1, 0, 0, 0, 0, 0)^T$$

$$PE_3 = (1, 0, 0, 0, 0, 0)^T$$

$$L_4 = \{a_1\}$$

Table 5 Iteration order definition for coupled activities

Coupled set	S ₁		S ₂		S ₃		S ₄										
	a ₁	a ₂	a ₅	a ₆	a' ₉	a' ₁₀	a ₇	a ₈	a'' ₉	a'' ₁₀	a ₁₂	a ₁₄	a ₁₅	a ₁₆	a ₁₇	a ₁₈	a ₁₉
Sensitivity factor f_s	0.50	1.00	0.50	0.50	0.50	1.00	0.33	0.50	1.00	1.00	0.50	0.66	1.00	1.00	1.00	0.33	1.00
Complexity factor f_c	0.20	0.40	0.10	0.15	0.25	0.20	0.10	0.30	0.20	0.50	0.50	0.30	0.40	0.30	0.15	0.15	0.70
Affection factor f_a	3	2	1	1	3	3	1	4	1	2	2	1	1	3	1	1	2
$f_s/2+f_c$	0.45	0.90	0.35	0.40	0.50	0.70	0.27	0.55	0.70	1.00	0.75	0.63	0.90	0.80	0.65	0.32	1.20
Initial iteration order	[1]	[2]	[1]	[2]	[1]	[2]	[1]	[3]	[6]	[10]	[7]	[4]	[9]	[8]	[5]	[2]	[11]

I Plotted design process is shown in Fig. 6. It is noted that in set C₃ only one pair of interdependent relations is labeled and the others are omitted.

II 4.2 Design process optimization

III To avoid coupled set too large, which may cause design divergent and cycle time increasing, the third coupled activity set is reengineered particularly. Considering that cavity geometry of extrusion die is decided mainly by profile shape and their positioning and detailed working dimensions affect die strength very little, relation between activity a₁₃ and activity a₁₉ can be removed. On the other hand, extrusion ram, container and padding relate to inner geometry of die only while die frame relates to the outer shape of die, therefore a₉ (tools dimension definition) can be divided into activity a'₉ (extrusion ram, container and padding dimension definition) and a''₉ (die frame dimension definition) while activity a₁₀ (structure and strength analysis for accessorial tools) is divided into a'₁₀ (extrusion ram, container and padding analysis) and a''₁₀ (die frame analysis). What is more, the interdependent relationship between equipment and tools selection (Group 4) and die design (Group 5) is weak. In practice, equipment and tool selection is usually done before die design. Therefore, activities 7, 8, 9 and 10 are separated from the third set and form a new coupled set. After decoupling and detailing operations, coupled activities and their levels are recognized once more, and the process model is shown in Fig. 7.

IV 4.3 Iteration order definition for coupled activities

V There are total four coupled sets in the optimized process model. Coupling properties of all activities have been analyzed to get the rational initial iteration order. The

calculation results of their sensitivity, complexity, affection factors and obtained iteration order are shown in Table 5.

The iteration order is also illustrated in Fig. 7. Based on sufficient analysis of interdependency and information flow, it is believed that the development process provided by Fig. 7 will be more rational and efficient.

5 Conclusions

The objective of concurrent design process modeling and optimization is to improve parallel degree of operations and reduce whole execution time by eliminating iteration and loop times caused by improper operation order. In the paper, a novel quantitative methodology is proposed to analyze and optimize the concurrent engineering process scientifically which can provide rational concurrent operation order and can make the product development more effective and efficient. The design activities in aluminum profile product development are studied particularly and an optimized process model is presented. The model has been applied in a computer-aided aluminum profile product development system prototype successfully. Further research will be focus on the implement of collaboration mechanism among task teams in a distributed development environment.

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