

Boundary Dynamics of Memcapacitor in Voltage-Excited Circuits and Relaxation Oscillators

Mohammed E. Fouda · Ahmed G. Radwan

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Abstract This paper discusses the boundary dynamics of the charge-controlled memcapacitor for Joglekar's window function that describes the nonlinearities of the memcapacitor's boundaries. A closed form solution for the memcapacitance is introduced for general doping factor *p*. The derived formulas are used to predict the behavior of the memcapacitor under different voltage excitation sources showing a great matching with the circuit simulations. The effect of the doping factor *p* on the time domain response of the memcapacitor has been studied as compared to the linear model using the proposed formulas. Moreover, the generalized fundamentals such as the saturation time of the memcapacitor are introduced, which play an important role in many control applications. Then the boundary dynamics under sinusoidal excitation are used as a basis to analyze any periodic signal by Fourier series, and the results have been verified using PSPICE simulations showing a great matching. As an application, two configuration of resistive-less memcapacitor-based relaxation oscillators are proposed and closed form expressions for oscillation frequency and conditions for oscillation are derived in presence of nonlinear model. The proposed oscillator is verified using PSPICE simulation showing a perfect matching.

Keywords Memcapacitor · Mem-elements · Nonlinear model · Memcapacitor dynamics · Circuit modeling · Relaxation oscillator

M. E. Fouda (\boxtimes) · A. G. Radwan

A. G. Radwan

Nanoelectronics Integrated Systems Center (NISC), Nile University, Cairo, Egypt e-mail: agradwan@ieee.org

Engineering Mathematics and Physics Department, Cairo University, Giza 12613, Egypt e-mail: m_elneanaei@ieee.org

1 Introduction

Mem-elements have become fundamental aspects of the circuit theory due to their promising potential in many needed applications. The first postulated element was by Chua in 1971, and it was the memristor "memory resistor" [\[5\]](#page-17-0) that was not experimentally fabricated as a solid-state device until HP announced that the missing element was physically implemented [\[29](#page-18-0)]. Then later, two elements were added to the mem-element family, which are the memcapacitor and meminductor [\[8](#page-17-1),[9\]](#page-17-2). Moreover, Chua generalized his theory about mem-elements to include infinite higher-order mem-elements and presented them in [\[6](#page-17-3)]. The memcapacitor is one of the new infinite higher-order elements where the constitutive relation between the charge $q(t)$ and the voltage $v(t)$ has pinched hysteresis [\[6\]](#page-17-3).

A lot of prospective research on the solid-state memcapacitor has been started to implement memcapacitors [\[4](#page-17-4)[,20](#page-18-1),[22,](#page-18-2)[23](#page-18-3)[,28](#page-18-4)]. One of these implementation depends on using the concept and analysis of a multilayer structure embedded in the dielectric of a conventional capacitor was introduced as a solid-state memcapacitor in [\[22\]](#page-18-2) where the multi-layer structure is formed by metallic layers separated by an insulator. This memcapacitor shows hysteretic charge–voltage and capacitance–voltage curves. However, it is not commercially available for experimental research, emulators have been introduced to emulate the behavior of the memcapacitor [\[3](#page-17-5),[11,](#page-17-6)[25,](#page-18-5)[31\]](#page-18-6). In addition, the memcapacitor has been used in different applications such as synapses [\[16](#page-17-7)[,21](#page-18-7)], non-volatile memory arrays [\[23\]](#page-18-3), and oscillators[\[17](#page-17-8)].

Various papers have been published to model and analyze mem-elements such as the memristor $[7,26,27]$ $[7,26,27]$ $[7,26,27]$, the meminductor $[14]$ and the memcapacitor $[15]$ $[15]$. However, there is no significant work in the area of the nonlinear mathematical modeling of the memcapacitor, which is very important since it describes the real behavior of the memcapacitor nearby the boundary which affects the response of the memcapacitor. The first nonlinear model of the memristor was initially proposed by Joglekar and Wolf [\[18\]](#page-17-12) showing a symmetric window function and gives single-valued characteristics under any excitation source [\[7](#page-17-9)]. The Joglekar's window function decreases as the boundary approaches until it reaches the boundary where it tends to zero as shown in Fig. [1.](#page-2-0) Then, this nonlinear window function was suggested to be used for modeling the memcapacitor [\[2](#page-17-13)]. The second common nonlinear model was proposed by Biolek et al. [\[2\]](#page-17-13) where the window function depends on a discontinuous window decreasing toward zero as the boundary layer approaches any of the two ends and having sharp discontinuity transitions when the excitation source reverses its polarity. Biolek's window function may allow for multi-valued characteristics under the sign-variation of the excitation source $[7]$. In this paper, the proposed analysis is based on Joglekar's window function that is more suitable for most mem-elements due to single-valued characteristics.

The charge-controlled model of the memcapacitor is introduced in [\[1\]](#page-17-14) based on the general model of the memcapacitor [\[8](#page-17-1)].The reciprocal of the capacitance *D* is given by:

$$
D(t) = D_{\text{max}} + x(t)(D_{\text{min}} - D_{\text{max}}), D \in (D_{\text{max}}, D_{\text{min}}),
$$
 (1)

where $x(t)$ represents the state variable of the memcapacitor, D_{max} and D_{min} are the reciprocal of the boundaries of the memcapacitance C_{max} and C_{min} , respectively. This

Fig. 1 Joglekar's window function $f(x)$ for different *p*

definition is an analogy to the definition of HP's memristor that was presented in 2008 [\[18](#page-17-12)]. The state equation of the memcapacitance was defined as:

$$
\frac{dx}{dt} = \eta k f(x) q(t) = \eta k f(x) \frac{d\sigma}{dt},
$$
\n(2)

where η is the memcapacitor polarity and $\sigma(t)$ is the time integral of the charge $q(t)$. The rate of change in the state variable is directly proportional to the mobility factor *k* and the window function, which is modeled by Joglekar's window function [\[18\]](#page-17-12) and is given by $f(x) = 1 - (2x - 1)^{2p}$, where *p* represents the dopant factor of the memcapacitor, which affects the rate of change in memcapacitor state variable as shown in Fig. [1.](#page-2-0) In this work, we are interested to show the effect of the boundary, so lower values of *p* have the dominant effect as shown in Fig. [1](#page-2-0) for $p = 1, 2, 5$ and 100.

The pinched hysteresis relation of the charge-controlled memcapacitor [\[8\]](#page-17-1) is given by:

$$
q(t) = C(x, q, t)v(t) \text{ or } v(t) = D(x, q, t)q(t). \tag{3}
$$

This paper is organized as follows: Section [2](#page-2-1) discusses the analytical solution of the memcapacitor's nonlinear model, which is described by Joglekar's window function and compares it with the linear model solution. Then, Sect. [3](#page-6-0) discusses the responses of nonlinear and linear models of the memcapacitor under unit step voltage excitation where the maximum time required for memcapacitance to reach its boundary is derived for both cases. Section [4](#page-8-0) discusses the memcapacitance's behavior that is subjected to a sinusoidal voltage source to be used for analyzing any periodic signal using Fourier series expansion in Sect. [5.](#page-10-0) Finally, a resistive-less memcapacitor-based relaxation oscillator is discussed with the necessary conditions for oscillation and verified using PSPICE simulation.

2 Analytical Solution of Memcapacitor's Modeling Equations

In this section, solution of linear and nonlinear models is derived using the memcapacitance, which is given by [\(1\)](#page-1-0). The modelling equations of the memcapacitor can be represented as a state space representation that describes the memcapacitor behavior and are given from (2) and (3) as follows:

$$
\begin{pmatrix}\n\frac{d\sigma}{dt} \\
\frac{dx}{dt}\n\end{pmatrix} = \frac{1}{D(t)} \begin{pmatrix}\n1 & 0 \\
\eta k f(x) - D(t)\n\end{pmatrix} \begin{pmatrix}\nv(t) \\
0\n\end{pmatrix}.
$$
\n(4)

The rate of change of the memcapacitance state variable $x(t)$ is given by:

$$
\frac{dx}{dt} = \frac{\eta k f(x)}{D(t)} v(t).
$$
 (5)

By integrating both sides relative to time where the state variable changes from *x*in to $x(t)$,

$$
\int_{x\text{in}}^{x} \frac{D_{\text{max}} + x D_d}{f(x)} dx = \int_{0}^{t} \eta k v(t) dt,
$$
\n(6)

where $x(t) = (D(t) - D_{\text{max}})/D_d$, $x_{\text{in}} = (D_{\text{in}} - D_{\text{max}})/D_d$ and $D_d = D_{\text{min}} - D_{\text{max}}$.

2.1 Linear Model Analysis

The linear model can be considered a good approximation for the highly doped nonlinear model for $p > 10$ so $f(x) \approx 1$. Then, the reciprocal of the memcapacitance can be obtained as:

$$
D^{2}(t) = D_{o}^{2} + 2\eta k' \big(\varphi(t) - \varphi(0)\big), D = [D_{\max}, D_{\min}],
$$
 (7)

where $k' = k D_d$, $\varphi(t)$ is the time integral of the voltage, $\varphi(0)$ is the flux at $t = 0$ and D_o represents the reciprocal of the memcapacitance at $t = 0$. The initial flux $\varphi(0)$ can be added to the initial memcapacitance and its expression is simplified to $D^2(t) = D_{\text{in}}^2 + 2\eta k' \varphi(t)$ so it can be assumed that $\varphi(0) = 0$. It is noted that the square of the inverse memcapacitance is directly proportional to the flux $\varphi(t)$. In order to keep the memcapacitance out of boundary, the absolute value of the flux $|\varphi(t)|$ should be enclosed between $\varphi_1 = (D_{\text{in}}^2 - D_{\text{max}}^2)/2k'$ and $\varphi_2 = (D_{\text{min}}^2 - D_{\text{in}}^2)/2k'$.

2.2 Nonlinear Model Analysis

The nonlinear dopant model of the memcapacitor is $f(x) = 1 - (2x - 1)^{2p}$ and by substituting by $f(x)$ into [\(6\)](#page-3-0) and by letting $y = 1 - 2x$ so $dx = -dy/2$, the integral Eq. (6) can be written as follows:

$$
\int_{y_{\text{in}}}^{y} \frac{D_s - y D_d}{1 - y^{2p}} \, \mathrm{d}y = \int_{0}^{t} -4\eta k v(t) \, \mathrm{d}t,\tag{8}
$$

where *y* and y_{in} are given by $(D_s - 2D(t))/D_d$ and $(D_s - 2D_{in})/D_d$, respectively, and $D_s = D_{\text{min}} + D_{\text{max}}$. The time integral of the right-hand side equals $-4k\varphi(t)$ but the left-hand-side part is divided into two integrations. Using binomial expansion and the integral of the left-hand-side parts,

$$
I_1 = \int \frac{1}{1 - y^{2p}} dy = \sum_{r=0}^{\infty} \frac{y^{2pr+1}}{2pr + 1} + c,
$$
 (9a)

$$
I_2 = \int \frac{y}{1 - y^{2p}} dy = \sum_{r=0}^{\infty} \frac{y^{2pr+2}}{2pr + 2} + c,
$$
 (9b)

Then, the left-hand side L.H.S is $D_s I_1|_{y_{\text{in}}}^y - D_d I_2|_{y_{\text{in}}}^y$ so let $h(D(t), p)$ $= D_s I_1 - D_d I_2$ so L.H.S = $h(D(t), p) - h(D_{in}, p)$. Therefore, the function $h(D(t), p)$ is given by:

$$
h(D(t), p) = \sum_{r=0}^{\infty} y^{2pr+1} \left(\frac{(2pr+2)D_s - (2pr+1)D_d y}{(2pr+1)(2pr+2)} \right)
$$

=
$$
\sum_{r=0}^{\infty} \left(\frac{D_s - 2D(t)}{D_d} \right)^{2pr+1} \left(\frac{D_s + 2(2pr+1)D(t)}{(2pr+1)(2pr+2)} \right).
$$
 (10)

The implicit relation of the memcapacitance for any dopant drift window function having any arbitrary *p* is given as follows:

$$
\sum_{r=0}^{\infty} \left(\frac{2D(t) - D_s}{D_d} \right)^{2pr+1} \left(\frac{D_s + 2(2pr + 1) D(t)}{(2pr + 1)(2pr + 2)} \right)
$$

=
$$
\sum_{r=0}^{\infty} \left(\frac{2D_{\text{in}} - D_s}{D_d} \right)^{2pr+1} \left(\frac{D_s + 2(2pr + 1) D_{\text{in}}}{(2pr + 1)(2pr + 2)} \right) + 4k\varphi(t).
$$
 (11)

This general relation can be used for any value of *p* even when *p* tends to infinity where (11) tends to (7) (The detailed analysis is shown in "Appendix"). Although it is not easy to get a closed form expression for each *p* but in the case of the minimum window function $p = 1$, the memcapacitance can be given as:

$$
\left(\frac{C(t)}{C_{\text{in}}}\right)^{D_d} \left(\frac{C_{\text{max}} - C(t)}{C_{\text{max}} - C_{\text{in}}}\right)^{D_{\text{max}}} \left(\frac{C_{\text{in}} - C_{\text{min}}}{C(t) - C_{\text{min}}}\right)^{D_{\text{min}}} = e^{4\eta k\varphi(t)}.
$$
 (12)

Although [\(11\)](#page-4-0) gives a closed form solution for instantaneous memcapacitance for any voltage excitations, but it is better to evaluate the memcapacitance using inverse problem technique. So, there is a need to prove that this function is a bijective function (one to one function) or a monotonic function across the working region $(D_{\text{max}}, D_{\text{min}})$ as discussed in [\[7\]](#page-17-9). Then, the flux $\varphi(t)$ can take any value that belongs to R unlike the linear case ($p = 1$) where the flux is enclosed between two values { φ_1, φ_2 } as shown in Fig. [2.](#page-5-0)

Fig. 2 Effect of change flux on the linear ($p = \infty$, *red line*) and nonlinear($p = 1$, *blue line*) dopant model (Color figure online)

Fig. 3 a Memcapacitance-flux characteristics, **b** relative error in capacitance-flux for different *p* of Joglekars window

Figure [3a](#page-5-1) shows the change in memcapacitance due to change in flux for different doping factors $p = 1, 2, 5$ and 100 where η , C_{in} , C_{min} , C_{max} and k equal 1, 1 μ F, 10 nF, 10µF and 10 MC−¹ *S*−1, respectively. Moreover, Fig. [3b](#page-5-1) shows the relative error in memcapacitance in reference to the memcapacitance of the linear model, which is defined as $(C_p - C_\infty) / C_\infty$. As obvious for $p > 10$; the behavior of higher value *p* is similar to the linear model ($p = \infty$).

In the following sections, the memcapacitor response with its closed form expressions will be derived for different input voltage signals: DC, sinusoidal and periodic signals which will be analyzed for linear and nonlinear dopant drift models.

Fig. 4 Transient memcapacitance for different positive applied voltage at doping factor $p = 1$ (*dotted line*) and $p = \infty$ (*solid line*) for η , *k*, C_{min} , C_{max} , $C_{\text{in}} = 1$, $10\text{MC}^{-1}\text{S}^{-1}$, 10nF , 10\mu F , 100nF

3 Step Response

Step response is a standard measure to characterize any system where the step voltage $V(t) = V_{\text{DC}}u(t)$; where $u(t)$ is a unit step function, and V_{DC} is the amplitude. In order to obtain an expression for the memcapacitance, the flux $\varphi(t)$ should be calculated, which is the time integration of the excitation voltage. In this case, the flux of step input is equal to $V_{\text{D}C}t$, consequently the memcapacitance for the linear case is given by:

$$
C(t) = \frac{C_{\text{in}}}{\sqrt{1 + 2\eta C_{\text{in}}^2 k' V_{\text{DC}} t}}, \ C(t) \in [C_{\text{min}}, C_{\text{max}}].
$$
 (13)

But for the nonlinear case $(p = 1)$, the memcapacitance is given as follows:

$$
\left(\frac{C(t)}{C_{\text{in}}}\right)^{D_d} \left(\frac{C_{\text{max}} - C(t)}{C_{\text{max}} - C_{\text{in}}}\right)^{D_{\text{max}}} \left(\frac{C_{\text{in}} - C_{\text{min}}}{C(t) - C_{\text{min}}}\right)^{D_{\text{min}}} = e^{4\eta k V_{\text{DC}}t}.\tag{14}
$$

The more the step voltage increases, the memcapacitance decreases for positive V_{DC} shown in Fig. [4](#page-6-1) where the initial memcapacitance is 100 nF and the rate of decrease in the memcapacitance depends on the amplitude of the applied voltage until the memcapacitance reaches its minimum C_{min} . Also, the rate of change in the nonlinear model is slower than the linear case so the time to reach its boundary is longer. Moreover in case of negative applied voltage, the memcapacitance increases as the absolute of the applied voltage increases until the memcapacitance reaches its maximum *C*max.

From the previous discussion, there is a certain time duration in which the memcapacitance reaches its boundary either maximum or minimum depending on the sign of the input voltage, which is called the saturation time *t*sat and given by:

$$
t_{\rm sat} = \frac{C_{\rm in}^2 - C_{\rm bd}^2}{2\eta C_{\rm in}^2 C_{\rm bd}^2 k' V_{\rm DC}},\tag{15}
$$

where C_{bd} represents the boundary memcapacitance that is either C_{max} or C_{min} . Moreover, the saturation time for nonlinear case is given by:

$$
t_{\rm sat} = \frac{1}{4kV_{\rm DC}} ln \left(\left(\frac{C_{\rm bd}}{C_{\rm in}} \right)^{D_d} \left(\frac{C_{\rm max} - C_{\rm bd}}{C_{\rm max} - C_{\rm in}} \right)^{D_{\rm max}} \left(\frac{C_{\rm in} - C_{\rm min}}{C_{\rm bd} - C_{\rm min}} \right)^{D_{\rm min}} \right). \tag{16}
$$

As obvious from the saturation time t_{sat} tends to infinity for $C_{bd} = C_{min}$ and for $C_{\text{bd}} = C_{\text{max}}$ as shown in Fig. [2,](#page-5-0) so it would not cling to its boundary. However, The maximum saturation time for the linear dopant model is reached when the memcapacitor changes its state from the minimum to maximum values or vice versa. Therefore, the maximum saturation time is given by:

$$
t_{\text{sat}}|_{\max_{p=\infty}} = \frac{C_{\max} + C_{\min}}{2C_{\max}C_{\min}k|V_{\text{DC}}|},\tag{17}
$$

which is a function of the applied voltage and the mobility factor *k*. But for the nonlinear dopant model $p = 1$, the maximum saturation time is infinite, so a new definition for maximum saturation time is defined. It can be assumed that the memcapacitor is saturated when the state variable reaches x_{on} or x_{off} corresponding to D_{satmin} and D_{satmax} , respectively.

$$
D_{\text{sat}_{\text{min}}} = D_{\text{max}} \left(1 - x_{\text{on}} \right) + x_{\text{on}} D_{\text{min}}, \tag{18a}
$$

$$
D_{\text{sat}_{\text{max}}} = D_{\text{max}} \left(1 - x_{\text{off}} \right) + x_{\text{off}} D_{\text{min}}.
$$
 (18b)

So the maximum saturation to reach from $D_{\text{sat}_{\text{min}}}$ to $D_{\text{sat}_{\text{max}}}$ or vice versa is defined by substituting in (16) as follows:

$$
t_{\text{sat}}|_{\max_{p=1}} = \frac{1}{4k|V_{\text{DC}}|}\ln\left(\left(\frac{x_{\text{off}}}{x_{\text{on}}}\right)^{D_{\max}}\left(\frac{1-x_{\text{on}}}{1-x_{\text{off}}}\right)^{D_{\min}}\right).
$$
(19)

But Joglekar's window function $f(x) = 1 - (2x - 1)^{2p}$ is symmetric with respect to $x = 0.5$ so $x_{\text{off}} = 1 - x_{\text{on}}$, then

$$
t_{\text{sat}}|_{\max p=1} = \frac{C_{\max} + C_{\min}}{4C_{\max} C_{\min} k |V_{\text{DC}}|} \ln\left(\frac{x_{\text{off}}}{x_{\text{on}}}\right). \tag{20}
$$

which is similar to the linear dopant model relation but is greater by a scaling factor $\alpha = \frac{1}{2} \ln \left(\frac{x_{\text{off}}}{x_{\text{on}}} \right)$. The maximum saturation time depends on the capacitance boundaries and the applied voltage where it is linearly inverse proportional to the applied voltage. Figure [5a](#page-8-1) shows the maximum saturation time for linear and nonlinear models where case I shows the saturation time when x_{on} and x_{off} equal 0.1 and 0.9, respectively,

Fig. 5 Maximum saturation time versus voltage amplitude for η , k , C_{min} , $C_{\text{max}} = 1$, 10 MC⁻¹ *S*⁻¹, 10 nF, $10 \,\mu F$

where $\alpha = 1.0986$ which is approximately equal to the linear case, moreover, case II shows the saturation time when x_{on} and x_{off} equal 0.01 and 0.99, respectively, where $\alpha = 2.2976$. Figure [5b](#page-8-1) shows the scaling factor α versus x_{on} where α reaches zero when $x_{on} = x_{off} = 0.5$ and tends to ∞ when x_{on} tends to zero which matches Eq. [\(16\)](#page-7-0).

4 Sinusoidal Response

One of the main important responses which should be obtained is the sinusoidal response where a single tone signal is applied to the memcapacitor. For the capacitor, the *i*−v curve is a circle, but in case of the memcapacitor, the *i*−v curve is deformed, as shown in Fig. [6a](#page-9-0) for different frequencies, due to the nonlinear behavior and pinching the $q - v$ hysteresis. Moreover, when the frequency increases, this curve becomes more circular and tends to act more like the capacitor at very high frequencies. The pinched *q*−v hysteresis of the simulated memcapacitor is shown in Fig. [6b](#page-9-0) for different input frequencies using the SPICE model, proposed in [\[2\]](#page-17-13). Also, when the frequency increases, the area inside the $q - v$ hysteresis decreases and the curve becomes more linear. If the $q - v$ characteristic is pinched, this element represents a memcapacitor which is similar to the $i - v$ characteristic in the memristor [\[8\]](#page-17-1).

Assuming a single tone voltage is applied to the memcapacitor given by $v(t)$ = V_0 sin($\omega_0 t$), then by substituting into [\(7\)](#page-3-1), the memcapacitance of linear dopant model can be given by:

$$
D^{2}(t) = D_{\text{in}}^{2} + \frac{4k'V_{o}}{\omega_{o}} \sin^{2}\left(\frac{\omega_{o}t}{2}\right). \tag{21}
$$

but in case of the nonlinear dopant, the memcapacitance is given by:

$$
\left(\left(\frac{C(t)}{C_{\text{in}}} \right)^{D_d} \left(\frac{C_{\text{max}} - C(t)}{C_{\text{max}} - C_{\text{in}}} \right)^{D_{\text{max}}} \left(\frac{C_{\text{in}} - C_{\text{min}}}{C(t) - C_{\text{min}}} \right)^{D_{\text{min}}} \right) = \exp \left(\frac{8\eta k V_o}{\omega_o} \sin^2 \left(\frac{\omega_o t}{2} \right) \right). \tag{22}
$$

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Fig. 6 PSPICE simulation of linear model: **a** I–V characteristics of the memcapacitor, **b** *q* − *V* hysteresis for η , k , C_{min} , $C_{\text{max}} = 1$, $10 \text{MC}^{-1} \text{S}^{-1}$, 10nF , 10μ F

Fig. 7 PSPICE and numerical transient simulation of the memcapacitance: **a** $p = \infty$, **b** $p = 1$ for *Vo*, η, *^k*,*C*min,*C*max,*C*in, *fo* ⁼ 1 V, 1, 10 MC−¹ *^S*[−]1, 10 nF, 10µF, 100 nF, 1 Hz

It is clear from [\(21\)](#page-8-2) and [\(22\)](#page-8-3) that the memcapacitance relation is a function of the input amplitude and the frequency where the more the frequency increases, the more the memcapacitance decreases and the area inside the hysteresis curve decreases as shown in Fig. [6b](#page-9-0). The verification of the memcapacitance Eqs. (21) and (22) using MATLAB compared to the SPICE simulation of the memcapacitor is as shown in Fig. [7a](#page-9-1), b for the linear ($p = \infty$) and the nonlinear ($p = 1$) dopant model, respectively. It is worth noting that in the linear model, the memcapacitance changes widely rather than the nonlinear dopant model due to the effect of the window function which decreases near the boundary. As a result, the rate of change in memcapacitance decreases and reaches zero at the boundary (see Fig. [2\)](#page-5-0).

The more the frequency increases, the memcapacitance tends to be the initial memcapacitance C_{in} . Figure [8a](#page-10-1) shows the effect of changing the frequency on the transient simulation of the memcapacitance where the range of the memcapacitance decreases by increasing the frequency. Moreover, Fig. [8b](#page-10-1) shows the 3D surface $q = q(v, f)$ over $v - f$, i.e. the frequency dependence of the pinched hysteresis $q - v$ loop, where the

Fig. 8 Transient simulation for linear case $(p = 1)$; **a** memcapacitance for sinusoidal input voltage, and (**b**) memcapacitor 3D *q* − v pinched hysteresis of memcapacitance for sinusoidal input voltage

curve rotates when increasing the frequency until the effect of the memory vanishes which means that there is a linear relation between the charge and the voltage.

5 General Periodic Excitation Response

Any periodic signal could be expanded using Fourier series expansion as a composite of the summation of the DC signal and sinusoidal signals as:

$$
v(t) = a_o + \sum_{n=1}^{\infty} a_n \cos(n\omega_o t) + b_n \sin(n\omega_o t),
$$
\n(23)

where a_0 represents the DC component in the applied signal and a_n and b_n represent the amplitudes of cosine and sine terms of the nth harmonic component of the signal, respectively. By substituting by [\(23\)](#page-10-2) into [\(7\)](#page-3-1), the instantaneous memcapacitance for linear dopant model is given by:

$$
D^{2}(t) = D_{\text{in}}^{2} + 2\eta k' \bigg(a_{0}t + \sum_{n=1}^{\infty} \frac{1}{n\omega_{o}} \bigg(a_{n}\sin(n\omega_{o}t) - b_{n}(\cos(n\omega_{o}t) - 1)\bigg)\bigg). \tag{24}
$$

A similar expression can be obtained for nonlinear dopant model, by substituting [\(23\)](#page-10-2) into [\(12\)](#page-4-1) which describes the nonlinear behavior of the applied periodic signals.

Figure [9](#page-11-0) is plotted for applying a square wave signal with amplitudes 1*V* and −1*V*, and the memcapacitor parameters are $k = 10 \text{ M C}^{-1} S^{-1}$, $C_{\text{in}} = 100 \text{ nF}$, $C_{\text{min}} = 10 \text{ nF}$ and $C_{\text{max}} = 10 \,\mu\text{F}$. Any periodic signals having a DC component ($a_0 \neq 0$) leads the memcapacitor to saturate as shown in Fig. 9 since the average of (23) increases or decreases with time depending on the sign of a_o , so a condition on the periodic signal for zero net DC component should be obtained which comes from $a_o = 0$. But in case of nonzero *ao*, the memcapacitor reaches one of its boundaries after the time given in (17) where $V_{DC} = a_o$.

Fig. 9 Numerical simulation of memcapacitance for square wave signal input at *p* = 1 (*dotted line*) and $p = \infty$ (*solid line*) **a** without DC component, and **b** with DC component = 0.2 V at $f = 1$ Hz

As an application to voltage-excited memcapacitor, the memcapacitor-based relaxation oscillator is analyzed and simulated using the linear and nonlinear dopant models. In this oscillator, the memcapacitor is excited using the output voltage of oscillator (feedback voltage in case of oscillator).

6 Application: Resistive-Less Memcapacitor-Based Relaxation Oscillator

One of the novel applications is the mem-element-based relaxation oscillator that is discussed for the first time in [\[24](#page-18-10)[,32](#page-18-11)] where the reactive element, capacitor, is replaced by the memristor. Then later on, a generalized analysis of symmetric and asymmetric relaxation oscillators is introduced in [\[10](#page-17-15)[,19](#page-17-16)[,30](#page-18-12),[33\]](#page-18-13) and also a memristor-based voltage-controlled relaxation oscillator is introduced in [\[12](#page-17-17)[,13](#page-17-18)]. In these oscillators, the current-/charge-controlled models are used due to the availability of SPICE and solid-state models, but a similar analysis can be done for the voltage-controlled models. The main problem that faces the memristor-based oscillator is its power consumption, as discussed in [\[24\]](#page-18-10). In order to overcome this problem, the memcapacitor and capacitor are used instead of resistive elements such as the memristor and resistor [\[17\]](#page-17-8).

6.1 C-MC Oscillator Configuration

The oscillation concept of the resistive-less memcapacitor-based relaxation oscillator, shown in Fig. [10a](#page-12-0), was discussed in [\[17](#page-17-8)] where the oscillation concept can be traced using the transfer function shown in Fig. [10b](#page-12-0) and for $\eta = 1$ as follows:

Starting at $V_0 = V_{\text{oh}}$ (any point between points (a) and (b)), dD/dt is positive which means that D_m increases and V_i increases until it reaches V_p (point (b)), the upper comparator would change its output to be V_{ol} and the output of the lower comparator is still V_{oh} . Therefore, the output of the AND gate is V_{ol} and V_i is inverted to V_p (point (c)). So the output of the upper comparator changes to *V*oh, lower comparator changes to V_{o} and V_o is still V_{o} . As a result of that, D_m decreases and $|V_i|$ decreases until it

Fig. 10 C-MC relaxation oscillator configuration. **a** circuit, and **b** transfer function

reaches *Vn* (point (d)), which is negative, such that the lower comparator output is *V*oh and the upper comparator is still V_{oh} , so the output of the AND gate will be V_{oh} and *V_i* will change to $-V_n$ (point (a)) and so on.

In [\[17](#page-17-8)], the oscillation frequency, and necessary and sufficient conditions of oscillation were derived. However, these expressions were derived for the linear dopant model of the memcapacitor, so in this section, the effect of the boundary is discussed on the oscillation frequency and conditions for oscillation for nonlinear model ($p = 1$) where $f(x) = 4x(1 - x)$. The input voltage to the comparators V_i is given by:

$$
V_i = V_o \left(\frac{D_m}{D_a + D_m} \right),\tag{25}
$$

where $D_a = C_a^{-1}$ and $D_m = C_m^{-1}$. The threshold voltages where the oscillator changes its outputs are when $V_i = V_p$ and $V_i = V_n$, the corresponding inverse memcapacitances of D_m are D_{mn} and D_{mn} , respectively, and are given by:

$$
D_{\rm mp} = D_a \left(\frac{V_p}{V_{\rm oh} - V_p} \right),\tag{26a}
$$

$$
D_{\rm mn} = D_a \left(\frac{V_n}{V_{\rm ol} - V_n} \right). \tag{26b}
$$

From the previous equation and to ensure positive values of C_a and C_m , it follows that $V_{\text{oh}} > V_p$ and that $-V_{\text{ol}} > -V_n$ Moreover, the necessary and sufficient conditions for oscillation are to ensure that the memcapacitance should be within its boundary; $D_{\text{min}} > D_{\text{mp}} > D_m > D_{\text{mn}} > D_{\text{max}}$. So the conditions for oscillation are given by:

$$
D_{\min} \frac{V_{\text{oh}} - V_p}{V_p} > D_a > D_{\max} \frac{V_{\text{ol}} - V_n}{V_n},\tag{27a}
$$

$$
V_p > V_n \frac{V_{\text{oh}}}{V_{\text{ol}}}.\tag{27b}
$$

The charge passing through the memcapacitor $q(t) = \frac{V_o(t)}{D_a+D_m}$ and by substituting into (2) . Thus, the rate of change in the state variable x is

$$
\frac{dx}{dt} = k \frac{V_o}{D_m + D_a} f(x) = 4kx(1 - x) \frac{V_o}{D_m + D_a}.
$$
 (28)

Substituting by *Dm* from [\(1\)](#page-1-0),

$$
\left(\frac{D_{\max} + D_a}{x} + \frac{D_{\min} + D_a}{1 - x}\right) \frac{dx}{dt} = 4kV_o.
$$
 (29)

By integrating the left-han-side term from x_{mn} (corresponding to C_{mn}) to x_{mp} (corresponding to C_{mp}) and the right-hand-side term from 0 to T_H and performing some simplifications, the time of positive half cycle T_H is given by:

$$
T_H = \frac{(D_{\text{max}} + D_a)ln\left(\frac{x_{\text{mp}}}{x_{\text{mn}}}\right) - (D_{\text{min}} + D_a)ln\left(\frac{1 - x_{\text{mp}}}{1 - x_{\text{mn}}}\right)}{4kV_{\text{oh}}}.\tag{30}
$$

Substituting by x_{mn} and x_{mn} , the oscillation frequency is given as follows:

$$
f_o = \frac{2kV_{\text{oh}}}{(D_{\text{max}} + D_a)ln\left(\frac{D_{\text{mp}} - D_{\text{max}}}{D_{\text{mn}} - D_{\text{max}}}\right) + (D_{\text{min}} + D_a)ln\left(\frac{D_{\text{min}} - D_{\text{mn}}}{D_{\text{min}} - D_{\text{mp}}}\right)}.
$$
(31)

In the linear dopant case, the conditions for oscillation are the same as the nonlinear case; however, the oscillation condition is given by:

$$
f_o = \frac{k'V_{\text{oh}}}{\left(D_{\text{mp}} - D_{\text{mn}}\right)\left(D_{\text{mn}} + D_{\text{mp}} + 2D_a\right)}.
$$
\n(32)

Substituting by the value of D_{mn} and D_{mp} , the oscillation frequency is given by:

$$
f_o = \frac{k'V_{\text{oh}}C_a^2(V_{\text{oh}} - V_p)^2(V_{\text{ol}} - V_n)^2}{(V_pV_{\text{ol}} - V_nV_{\text{oh}})(2V_{\text{oh}}V_{\text{ol}} - V_{\text{oh}}V_n - V_{\text{ol}}V_p)}.
$$
\n(33)

6.2 MC-C Oscillator Configuration

Another configuration can be obtained by swapping the memcapacitor with a capacitor as shown in Fig. [11a](#page-14-0). The oscillation concept is traced using the transfer function shown in Fig. [10b](#page-12-0) where $\eta = -1$ as follows:

Starting at $V_0 = V_{\text{oh}}$, dD/dt is negative, which means that D_m decreases and V_i increases until it reaches V_p (point (b)). Then, the output of the AND gate is V_{ol} and V_i is switched to V_p (point (c)). So the output of the upper comparator changes to V_{oh} , lower comparator changes to V_{o} and V_o is still V_{o} . Consequently, D_m increases and

Fig. 11 MC-C relaxation oscillator configuration

 $|V_i|$ decreases until it reaches V_n (point (d)), which is negative, where the output of the AND gate changes to V_{oh} and V_i is switched (point (a)) and so on.

The maximum and minimum inverse memcapacitances D_{m_n} and D_{m_n} , corresponding to V_n and V_p , respectively, are given by:

$$
D_{\rm mp} = D_b \left(\frac{V_{\rm oh} - V_p}{V_p} \right),\tag{34a}
$$

$$
D_{mn} = D_b \left(\frac{V_{\text{ol}} - V_n}{V_n} \right). \tag{34b}
$$

where $D_b = C_b^{-1}$. The necessary and sufficient conditions for oscillation are derived as done for C-MC oscillator such that $D_{\text{min}} > D_{\text{min}} > D_m > D_{\text{mp}} > D_{\text{max}}$ and are given as follows:

$$
D_{\min} \frac{V_n}{V_{\text{ol}} - V_n} > D_b > D_{\max} \frac{V_p}{V_{\text{oh}} - V_p},\tag{35a}
$$

$$
V_p > V_n \frac{V_{\text{oh}}}{V_o l}.\tag{35b}
$$

Then, the oscillation frequency in case of nonlinear dopant model ($p = 1$) is given by

$$
f_o = \frac{2kV_{\text{oh}}}{(D_{\text{max}} + D_b)ln\left(\frac{D_{\text{mn}} - D_{\text{max}}}{D_{\text{mp}} - D_{\text{max}}}\right) + (D_{\text{min}} + D_b)ln\left(\frac{D_{\text{min}} - D_{\text{mp}}}{D_{\text{min}} - D_{\text{mn}}}\right)}.
$$
(36)

But, in linear dopant case, the oscillation condition is given by:

$$
f_o = \frac{k' V_{\text{oh}} C_b^2 V_p^2 V_n^2}{V_p^2 V_{\text{ol}}^2 - V_n^2 V_{\text{oh}}^2}.
$$
\n(37)

6.3 Results and Discussion

In the aforementioned expressions, the oscillation frequency is always linearly proportional to the doping factor k of the memcapacitor. Also, as clear from (33) and [\(37\)](#page-14-1), the oscillation frequency in the linear case is proportional to the square of series capacitance C_a or C_b that are bounded by $C_{a_{min}}$ and $C_{a_{max}}$ or $C_{b_{min}}$ and $C_{b_{max}}$ obtained from [\(27a\)](#page-12-1) and [\(35a\)](#page-14-2), respectively. However, in the case of the nonlinear model, the

Fig. 12 Obtained oscillation frequency versus series capacitance for both models and both configurations

Fig. 13 PSPICE simulation of C-MC oscillator with **a** linear model ($p = \infty$), **b** nonlinear model ($p = 1$) for $C_a = 0.1 \,\mu\text{F}$

oscillation frequency tends to zero at minimum and maximum series capacitance as shown in Fig. [12.](#page-15-0) Figure [12](#page-15-0) is plotted for C_{min} , C_{max} , k , V_{ol} , V_{oh} , V_n and V_p equal to 10 nF, 10μ F, 10μ C⁻¹ S^{−1}, −1 V, 1 V, 0.5 V and 0.75 V, respectively. Moreover, MC-C configuration gives higher frequency than C-MC configuration for linear and nonlinear models in the common region $[C_{a_{min}}, C_{b_{max}}]$ except for a narrow region in the nonlinear case as shown in Fig. [12.](#page-15-0)

In case of using 100 nF series capacitance, the oscillation frequency in case of MC-C configuration is higher than in case of C-MC configuration as clear in Fig. [12.](#page-15-0) Figure [13a](#page-15-1) shows the transient output of the C-MC oscillator configuration where the obtained oscillation frequency $= 0.8325 \text{ Hz}$ which matches the calculated frequency from [\(32\)](#page-13-1). And, Fig. [13b](#page-15-1) shows the transient response for nonlinear dopant drift model $(p = 1)$ where the oscillation frequency equals 0.512 Hz matching that calculated from [\(36\)](#page-14-3). In both models, the memcapacitance C_m changes from 0.0333 to 0.1 μ F. Moreover, in MC-C oscillator configuration, the obtained oscillation frequency for

Fig. 14 PSPICE simulation of MC-C oscillator with **a** linear model ($p = \infty$), **b**nonlinear model ($p = 1$)

linear and nonlinear model is 4.4955 and 1.0437 Hz matching that calculated from [\(37\)](#page-14-1) and [\(36\)](#page-14-3), respectively, as shown in Fig. [14.](#page-16-0) But here, the memcapacitance *Cm* changes from 0.1 to 0.3μ F.

Note that the memcapacitor behavior differs from the memristor behavior where the memristance decreases with positive applied voltage but memcapacitance increases which reverses the oscillation conditions. Also the expression for the oscillation frequency is the same, by replacing *R* with $1/C$. Moreover, there is no static power consumption in memcapacitor, which makes memcapacitor-based oscillators suitable for low power applications.

7 Conclusion

This paper discussed the dynamics of the linear and nonlinear dopant drift model of the memcapacitor where the instantaneous memcapacitance was defined and characterized. The closed form mathematical expressions of the memcapacitance were derived for linear and nonlinear Joglekar's models under DC step and periodic voltage excitations. In addition, the saturation time formula is presented, which can be used for a transient response of the memcapacitor. The derived time domain expressions can be used to analyze circuits having memcapacitors as it was used to derive an expression for the oscillation frequency in two configurations of the memcapacitor-based relaxation oscillator. The derived formulas of the memcapacitance were verified using PSPICE simulations showing great matching.

Appendix

To prove that (11) will be reduced to its conventional linear dopant formula, (10) can be rewriten as

$$
h(D(t), p) = y\left(D_s - \frac{D_d}{2}y\right) + \sum_{r=1}^{\infty} y^{2pr+1} \left(\frac{(2pr+2)D_s - (2pr+1)D_d y}{(2pr+1)(2pr+2)}\right).
$$
\n(38)

Taking the limit $p \to \infty$,

$$
h(D(t), \infty) = D_s y - \frac{D_d y^2}{2} + \sum_{r=1}^{\infty} \lim_{p \to \infty} \left(y^{2pr+1} \left(\frac{(2pr+2)D_s - (2pr+1)D_d y}{(2pr+1)(2pr+2)} \right) \right).
$$
\n(39)

Since *y* ∈ [−1, 1], then y^{2p} → 0, the previous formula will be reduced to:

$$
h(D(t), \infty) = \frac{D_s^2}{2D_d} - \frac{2D^2(t)}{D_d}.
$$
\n(40)

Consequently,

$$
D^{2}(t) = D_{\text{in}}^{2} + 2\eta k\varphi(t). \tag{41}
$$

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