

# Fully Digitally Programmable Generalized Mixed Mode Universal Filter Configuration

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Abstract This paper presents *five* feature-enriched hardware-efficient mixed mode universal filter (UF) biquads using digitally programmable (DP) current conveyors (CC). Common features of proposed UFs include use of only two grounded capacitors, operation in all the four modes, realization of all the filter functions, independently programmable filter parameters and cascadability (last three features, however, are partially present in last UF). Besides all these features, the first proposed UF, designated as Generalized DPUF provides additional features such as reconfigurability, use of minimum input terminals, no component matching constraint and lesser parasitic effects. Although Generalized DPUF encompasses almost all the desirable features of any filter, it uses eight CCs for obtaining these. The need of eight CCs is justified by introducing remaining four programmable/non-programmable UFs. These additional UFs are designated as *Derived* UFs as they are obtained by the deletion of CCs of the Generalized DPUF. Proportionate reduction in features of Derived UFs with number of CCs proves the reasonability of eight CCs in Generalized DPUF. To further strengthen this fact, all the proposed UFs are compared with reported filters. Better/comparable performance of Generalized and reduced-performance Derived filters again justifies the need of eight CCs in Generalized UF.

**Keywords** Digitally programmable  $\cdot$  Reconfigurable  $\cdot$  Generalized  $\cdot$  Mixed mode  $\cdot$  Universal filter

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## **1** Introduction

The second-generation current conveyor (CC) has been most promising candidate for analog signal processing/generation since its inception in 1970 [25]. This is attributed to its higher signal bandwidth, simpler circuitry and lower power consumption [11]. Introduction of digital control/programming in the second-generation CC (CCII) has further enhanced its functional flexibilities and versatility [7, 10, 12, 17, 26, 29].

Analog programming techniques are widely used in a number of applications [15,21,22,31]. However, the limitation on the allowable range of analog tuning voltage makes it inconvenient for the low-voltage applications. Hence, in these applications, the digital control is more attractive [7,10,12,17,26,29]. Digital programming techniques not only yields better accuracy in avoiding parameter variations but also offers additional advantages such as better noise performance [3,24], power saving option [24] (for brevity reasons, these points are not discussed) and most importantly the direct compatibility to modern mixed mode (analog/digital) systems.

Although the concept of digital programmability is not new, the field lacks in providing efficient DPUF realizations. The efficient DPUF structures compatible to integrated circuit (IC) realization should fulfil following conditions: (1) It must be reconfigurable to realize different filter functions (types), namely lowpass (LP), high-pass (HP), bandpass (BP), band reject (BR) and allpass (AP), without any change in configuration. (2) All its parameters, namely pole frequency ( $\omega_0$ ), quality factor (Q) and gain (G), should be independently programmable to set the desired frequency response [5]. (3) It must employ minimum possible number of the active and passive (preferably grounded capacitor [9]) components from area viewpoint. (4) For enhanced versatility, it should operate in all the four modes, i.e. voltage mode (VM), current mode (CM), transimpedance mode (TIM) and transadmittance mode (TAM).

A detailed survey of available literature not only shows limited availability of CC-based DPUFs [7,10,12,16,17,26,29] but also shows that these DPUFs possess limited number of features. All these DPUFs except that of [26] realize three or lesser filter functions, provide limited programmability features (none of these DPUFs provide programming of filter *types*, i.e. reconfigurability) and also lack the mixed mode operation. Recently reported DPUF of [26] realizes all the filter functions in all the modes, but it lacks the programming of gain parameter and filter *types*. UFs reported in [1,2,19–21,31] operate in all the four modes, but none of them have the feature of digital programming. Thus, the major objective of this paper is to present a *Generalized* UF configuration, which could provide *all* the aforementioned features using *minimum* possible number of active and passive components.

The proposed *Generalized DPUF* provides almost all the desirable features such as (1) reconfigurability, (2) realization of all the filter functions in all the modes, (3) independently programmable parameter ( $\omega_0$ , Q, G) set, (4) full cascadability by virtue of high (low) impedance input (output) port for voltage signal and vice versa for current signal, (5) no component matching constraint, (6) minimum number of current and voltage input terminals, i.e. one, and (7) less parasitic effects due to the use of grounded components. However, for providing all these features, it uses two multi-output CCIIs (MOCCIIs), six DP-CCIIs and six grounded passive elements including two capacitors.

Need of eight CCs in Generalized DPUF is justified by introducing *four* additional (new) mixed mode programmable/ non-programmable UFs. These UFs using '2–7' CCs are designated as *Derived UFs* as they are obtained by the deletion of CCs of Generalized DPUF. Proportionate reduction in features of Derived UF with the number of CCs proves the reasonability of eight CCs used in Generalized DPUF. Although the features of the proposed UFs decrease with the number of CCs, all the configurations retain few important features such as cascadability and use of grounded capacitor. Finally, better/comparable performance of Generalized and the reduced-performance Derived filters with that of the reported filters (in comparison section) again justifies the need of eight CCs in the proposed Generalized UF.

This paper is organized as follows: Starting from the Introduction, Sect. 2 briefly illustrates DPCC, Sect. 3 presents the realization of the proposed Generalized mixed mode DPUF, Sect. 4 describes the Derived mixed mode configurations, Sect. 5 discusses the parasitic effects, Sect. 6 presents the systematic comparison of the proposed filters, Sect. 7 deals with simulation result, and finally, the paper is concluded in Sect. 8.

# 2 Overview of DPCCII

Literature reports number of DPCC structures [7, 10, 12, 17, 29]. The concept of digital control in all these structures is similar and is based on employing an *n*-bit current division/summing network (CDN/CSN), which scales up (amplification) or scales down (attenuation) the current gain of the conventional (non-programmable) CCII. In order to describe the functioning of the proposed UFs more clearly, the DPCCII of [12] is used in this paper. However, overall performance of proposed UFs will highly depend on the choice of particular DPCC. The DPCCII of [12] is derived from the CCII (consisting of transistor set M1–M14) structure depicted in Fig. 1. Figure 1 also



Fig. 1 CMOS structure of CCII [12] with additional Z-ports to form MOCCII





Fig. 2 CMOS structure of DPCC [12] with CSN at port Z

shows additional positive and negative Z-ports of CCII to depict the formation of MOCCII.

The DPCCII is derived by replacing the port X (or Z) transistors M11–M12 (or M13-M14) of CCII by a CSN. Figure 2 shows the DPCCII schematic with CSN at port Z. The CSN consists of *n*-transistor arrays (j = 0, 1, ..., n - 1) and codeword ( $a_{n-1}, a_{n-2}, ..., a_0$ )-controlled MOS switches. In Fig. 2, transistors of CSN are designated as M<sub>Pjs</sub> (or M<sub>Njs</sub>), where subscripts P, N and S (if present) simply denote the PMOS, NMOS and switching transistors, respectively. Transistors of each array have aspect ratio set in binary weighted order according to

$$\left(\frac{W}{L}\right)_{\mathrm{M}_{\mathrm{Pj}}, \mathrm{M}_{\mathrm{Pjs}} (\mathrm{or}\,\mathrm{M}_{\mathrm{Nj}}, \mathrm{M}_{\mathrm{Njs}})} = 2^{j} \left(\frac{W}{L}\right)_{\mathrm{M}11 (\mathrm{or}\,\mathrm{M}12)} \qquad j = 0, 1, \dots, n-1$$
(1)

Port Z current, if flowing out of the DPCC is given by

$$I_{Z} = a_{0} \left( I_{M_{P0}} - I_{M_{N0}} \right) + a_{1} \left( I_{M_{P1}} - I_{M_{N1}} \right) + \dots + a_{n-1} \left( I_{M_{Pn-1}} - I_{M_{Nn-1}} \right)$$
$$= \sum_{i=0}^{n-1} a_{j} \left( I_{M_{Pj}} - I_{M_{Nj}} \right)$$
(2)

where  $a_j$  denotes applied code-bit controlling the switching transistors of *j*th array.

Port X current, if flowing out of the DPCC is given by

$$I_{\rm X} = (I_{\rm M11} - I_{\rm M12}) \tag{3}$$

Fig. 3 a Symbol of DPCCII, b modified symbol

Since the overdrive voltage ( $V_{ov}$ ) of  $M_{Pj}$  and  $M_{Nj}$  transistors are equal to that of M11 and M12, respectively, difference of their drain currents using the standard drain current formula  $\left(I_{D} = \frac{K'}{2} \frac{W}{L} v_{ov}^{2}\right)$  is given as

$$I_{\rm MPj} - I_{\rm MNj} = \frac{K'_p}{2} \left(\frac{W}{L}\right)_{\rm MPj} \left|v_{\rm ov}^2\right|_{\rm M11} - \frac{K'_n}{2} \left(\frac{W}{L}\right)_{\rm MNj} \left|v_{\rm ov}^2\right|_{\rm M12}$$
(4)

where  $K'_p$  and  $K'_n$  denote the process transconductance parameters of PMOS and NMOS transistors, respectively.

Difference current of transistors  $M_{Pj}$  and  $M_{Nj}$  using (1) and (4) is given as

$$I_{\rm M_{\rm Pj}} - I_{\rm M_{\rm Nj}} = 2^{j} I_{\rm M11} - 2^{j} I_{\rm M12} = 2^{j} \left( I_{\rm M11} - I_{\rm M12} \right)$$
(5)

Thus, the port Z current using (2, 3) and (5) is given as

$$I_{\rm Z} = \sum_{i=0}^{n-1} a_j 2^j (I_{\rm M_{11}} - I_{\rm M_{12}}) = \left[\sum_{i=0}^{n-1} a_j 2^j\right] I_{\rm X} = K I_{\rm X} = I_{\rm Z}$$
(6a)

Similarly, the placement of CSN to port X modifies (6a) as [12]

$$\frac{I_Z}{I_X} = \frac{1}{K} \tag{6b}$$

It can be seen from (6) that the current gain of CCII is programmable by *n*-bit digital codeword  $(a_{n-1}, a_{n-2}, ..., a_0)$ . Parameter 'K' (Eq. 6) denotes the decimal equivalent of *n*-bit codeword  $(=a_{n-1}, a_{n-2}, ..., a_0)$ . It is given as

$$K = \sum_{j=0}^{n-1} a_j 2^j$$
(7)

Transfer matrix of DPCCII can be expressed as

$$\begin{bmatrix} I_{Y} \\ V_{X} \\ I_{Z} \end{bmatrix} = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & K^{m} & 0 \end{bmatrix} \begin{bmatrix} V_{Y} \\ I_{X} \\ V_{Z} \end{bmatrix}$$
(8)





Power integer,  $m = \pm 1$  corresponds to the two variants of DPCCII. The value m = -1 denotes the DPCCIIK<sup>-</sup> block with CSN at port X and current attenuation  $(K^{-1} = K^- = 1/K)$  in range '1  $\rightarrow 1/(2^n - 1)$ '. Similarly, m = +1 denotes the DPCCIIK<sup>+</sup> block with CSN at port Z and current amplification  $(K^{+1} = K^+ = K)$  in range '0  $\rightarrow (2^n - 1)$ '. The concept of *zero* gain (for K = 0; i.e. all bits zero) is used for programming the generation of various filter function (types).

DPCCII in the symbolic form may be represented as four-port block as shown in Fig. 3a. But for simplified presentation, the modified block shown in Fig. 3b is used in this paper. It shows *i*th DPCCIIK<sup>±</sup> block (sometimes also referred as  $K_i$  block) with current gain  $K_i^{\pm}$  and applied codeword  $K_i$ . Non-programmable CCIIs/MOCCIIs, used in the paper, are designated by encircled numbers. Also, the inverted Z-ports of MOCCII are shown by '-Z'.

## **3 Proposed Generalized Mixed Mode DPUF**

The Generalized DPUF is depicted in Fig. 4a. It uses six DPCCIIs (5DPCCIIK<sup>+</sup> and 1DPCCIIK<sup>-</sup>), two MOCCIIs and six grounded passive elements including two capacitors. Circuit analysis yields the following output equations

$$V_0 = I_0 R_L \tag{9}$$

$$I_{0} = \frac{-\left[s^{2} \{K_{1}\} - s \frac{1}{C_{2}R_{2}} \frac{K_{6}}{K_{4}} \{K_{2}\} + \frac{K_{5}K_{6}}{C_{1}C_{2}R_{1}R_{2}} \{K_{3}\}\right] \left(I_{i} + \frac{v_{i}}{R_{3}}\right)}{s^{2} + s \frac{1}{C_{2}R_{2}} \frac{K_{6}}{K_{4}} + \frac{K_{5}K_{6}}{C_{1}C_{2}R_{1}R_{2}}}$$
(10)

Setting codeword condition  $K_5 = K_6$  modifies (10) as

$$I_{0} = \frac{-\left[s^{2} \{K_{1}\} - s \frac{1}{C_{2}R_{2}} \frac{K_{5}}{K_{4}} \{K_{2}\} + \frac{K_{5}^{2}}{C_{1}C_{2}R_{1}R_{2}} \{K_{3}\}\right] \left(I_{i} + \frac{V_{i}}{R_{3}}\right)}{s^{2} + s \frac{1}{C_{2}R_{2}} \frac{K_{5}}{K_{4}} + \frac{K_{5}^{2}}{C_{1}C_{2}R_{1}R_{2}}}$$
(11)

It is evident from (11) that all the coefficients of its numerator and denominator functions are independently programmable by various codewords. Codewords  $K_1$ ,  $K_2$  and  $K_3$  programming the numerator coefficients not only govern the generation of various filter functions in all the four modes but also provide them the independently programmable gain factors too. Table 1 summarizes the codeword conditions for generating different filter functions and the respective gain parameters in all the four modes. It is to be noted that none of the responses in any of the mode requires any component matching constraint.



Fig. 4 a Generalized mixed mode DPUF. b First Derived DPUF. c Second Derived DPUF d Third Derived DPUF. e Fourth Derived UF

Filter parameters  $\omega_0$  and Q from (11) are given as

$$\omega_0 = K_5 \sqrt{\frac{1}{C_1 C_2 R_1 R_2}} = [K_5] \,\omega_c \tag{12a}$$

$$Q = K_4 \sqrt{\frac{C_2 R_2}{C_1 R_1}} = [K_4] Q_c$$
(12b)

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Fig. 4 continued

Table 1 Filter functions and respective gains in different mode for various codeword combinations

Filter function	Codeword combination	Gain paran	neter in diffe	erent modes	
		TAM	СМ	TIM	VM
HP	$K_2 = K_3 = 0, K_1 \neq 0$	$K_{1}/R_{3}$	$K_1$	$K_1 R_L$	$K_1 R_L / R_3$
BP	$K_1 = K_3 = 0, K_2 \neq 0$	$K_2/R_3$	$K_2$	$K_2 R_L$	$K_2 R_L / R_3$
LP	$K_1 = K_2 = 0, K_3 \neq 0$	$K_{3}/R_{3}$	<i>K</i> <sub>3</sub>	$K_3 R_L$	$K_3 R_L / R_3$
BR	$K_2 = 0, K_1 = K_3 \neq 0$	$K_{1}/R_{3}$	$K_1$	$K_1 R_L$	$K_1 R_L / R_3$
AP	$K_1 = K_2 = K_3 \neq 0$	$K_1/R_3$	$K_1$	$K_1 R_L$	$K_1 R_L / R_3$

where  $\omega_c$  and  $Q_c$  are defined as component-dependent factors of  $\omega_0$  and Q, respectively. It is given as

$$\omega_c = \sqrt{\frac{1}{C_1 C_2 R_1 R_2}} \tag{13a}$$

$$Q_c = \sqrt{\frac{C_2 R_2}{C_1 R_1}} \tag{13b}$$

It can be seen from (12) that  $\omega_0$  and Q of all the responses are independently programmable through codeword  $K_5(=K_6)$  and  $K_4$ , respectively.

Aforementioned discussion shows the *need of six* programmable blocks (DPCC-1–6, i.e.  $K_1-K_6$  blocks) for obtaining all the *programmability features*. Obviously, the deletion of DPCCs will lead to proportionate reduction in the programmability features as well. This fact is justified by the Derived UFs discussed in the next section.

One additional advantage offered by the proposed Generalized and Derived (to be described in the next section) DPUFs is the *downscale* programming of pole frequency  $\omega_0$  (given by Eq. 12a). This is achieved by reversing the gain parameter ' $K^+$ ' of DPCC-5, 6 by ' $K^-$ ', i.e. by changing DPCCIIK<sup>+</sup> by DPCCIIK<sup>-</sup> block. In this case, pole frequency decreases with increasing codeword ( $=a_{n-1}, a_{n-2}, \ldots, a_0$ ). This approach is useful in achieving the low-frequency operation without the requirement of *large* component values, i.e. it helps in reducing the *chip* area [24].

Component value  $C_1 = 2C_2$  (or  $R_1 = 2R_2$ ) may be set at design level for having  $Q = Q_c = 1/\sqrt{2}$  for  $K_4 = 1$ , as given by (12b and 13b). It is required for maximal flat LP and HP responses. On the other hand, high Q-values (without large component spread), required for BP and BR, may be realized by programming  $K_4$  (Eq. 12b), which can be further increased by (1) cascading an identical DPCC with DPCC-4. This results in  $Q_{\text{max}} = (2^n - 1)^2$ . (2) Adding additional transistor arrays in CDN of DPCCII-4, i.e. by increasing codeword size or number of bit (*n*) of DPCC-4. This results in  $Q_{\text{max}} = (2^n - 1)$ .

#### 4 Additional (Derived) Mixed Mode UF Configurations

Four mixed mode programmable/non-programmable UFs, Derived from the Generalized DPUF, are discussed in this section. All these configurations use '2–7' CCs/DPCCs, '3–6' resistors and two grounded capacitors only. For concise presentation, all the equations in this section are given for codeword condition  $K_5 = K_6$ .

## 4.1 First Derived Mixed Mode DPUF

The First mixed mode DPUF as depicted in Fig. 4b is Derived by *deleting the CC-8* of Generalized configuration. The proposed modification needs *three current inputs rather than one*, but still it *retains all the programmability* features of the Generalized DPUF. This is because of the availability of all the programmable blocks (DPCC '1–6'). Output function of this DPUF is given by

$$V_{0} = R_{L}I_{0}$$
(14a)  

$$V_{i} \left[ \frac{s^{2} \{K_{1}\}}{R_{3}} - s \frac{K_{5} \{K_{2}\}}{K_{4}C_{2}R_{2}R_{4}} + \frac{K_{5}^{2} \{K_{3}\}}{C_{1}C_{2}R_{1}R_{2}R_{5}} \right] +$$

$$I_{0} = \frac{\left[ s^{2} \{K_{1}\} I_{1} - s \frac{K_{5}I_{2} \{K_{2}\}}{K_{4}C_{2}R_{2}} + \frac{I_{3}K_{5}^{2} \{K_{3}\}}{C_{1}C_{2}R_{1}R_{2}} \right]}{s^{2} + s \frac{1}{C_{2}R_{2}} \frac{K_{5}}{K_{4}} + \frac{K_{5}^{2}}{C_{1}C_{2}R_{1}R_{2}}}$$
(14b)

It can be seen from (14b) that the generation of all the filter functions (types) follows the same programmability rule as given in Table 1 for Generalized DPUF. Also, the equality of denominator of (14b) and (11) leads to same parameter ( $\omega_0$  and Q) expressions as given in (12). Thus, this configuration is also *fully programmable*. Besides the need of three current inputs, the proposed modification also requires component matching constraint  $R_3 = R_5$  and  $R_3 = R_4 = R_5$  for the realization of BR and AP responses, respectively, for voltage output.

#### 4.2 Second Derived Mixed Mode DPUF

Further *deletion of DPCCs '1–3'* results in Second Derived DPUF (Fig. 4c). This modification not only requires three input signals for both current and voltage variables but also *misses the programmability feature of filter functions* (type). The configuration provides both inverting and non-inverting forms of voltage outputs simultaneously, at the low impedance port. Mixed mode operation and filter parameters of this configuration are characterized by the following set of equations.

$$\begin{cases} V_{0} = R_{L}I_{o}, \quad V_{0}' = -R_{L}'I_{o}, \\ \left[\frac{s^{2}}{R_{2}}V_{1} - \frac{sK_{5}}{K_{4}C_{2}R_{2}R_{3}}V_{2} + \frac{K_{5}^{2}}{C_{1}C_{2}R_{1}R_{2}R_{4}}V_{3}\right] + \\ I_{0} = \frac{\left[s^{2}I_{1} - \frac{sK_{5}}{K_{4}C_{2}R_{2}}I_{2} + \frac{K_{5}^{2}}{C_{1}C_{2}R_{1}R_{2}}I_{3}\right]}{s^{2} + s\frac{R_{L}'}{C_{2}R_{2}R_{3}}\frac{K_{5}}{K_{4}} + \frac{K_{5}^{2}R_{L}}{C_{1}C_{2}R_{1}R_{2}R_{4}}} \\ \omega_{0} = K_{5}\sqrt{\frac{R_{L}}{C_{1}C_{2}R_{1}R_{2}R_{4}}}, \quad Q = K_{4}\frac{R_{3}}{R_{L}'}\sqrt{\frac{C_{2}R_{2}R_{L}}{C_{1}R_{1}R_{4}}}$$
(16)

It is evident from (15, 16) that this configuration only retains *the programming feature* of  $\omega_0$  and Q, since the configuration lacks the programming feature of filter type; its filter function generation depends on the proper combination of input variables as shown in Table 2. This configuration needs slightly more matching constraints as  $(R_4 = R_L)$  for BR and  $(R_4 = R_L, R_3 = R_L)$  for AP in case of current output and

additional constraint ( $R_1 = R_2$ ) over BR and ( $R_1 = R_2 = R_3$ ) over AP in case of voltage output.

## 4.3 Third Derived Mixed Mode DPUF

Further *deletion of DPCC-4* results in Third Derived mixed mode DPUF as shown in Fig. 4d. It uses only three CCs and four resistors. However, deletion of  $K_4$  block *vanishes the programmability of Q-factor*. Output functions and parameters of this DPUF are given as

$$\left[\frac{s^2}{R_2}V_1 + \frac{sK_5}{C_2R_2R_1}V_2 + \frac{K_5^2}{C_1C_2R_1R_2R_3}V_3\right] + \left[s^2I_1 + \frac{sK_5}{C_2R_2}I_2 + \frac{K_5^2}{C_1C_2R_1R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_1C_2R_1R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_1C_2R_1R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_1C_2R_1R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_2R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_2R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_2 + \frac{K_5}{C_2R_2}I_3\right] + \left[s^2I_1 + \frac{K_5}{C_2R_2}I_3 +$$

$$w_{0} = K_{L} I_{0}, \quad I_{0} = \frac{1}{s^{2} + s \frac{K_{5}}{C_{2}R_{2}} + \frac{K_{5}^{2}R_{L}}{C_{1}C_{2}R_{1}R_{2}R_{3}}}$$
(17)

$$\omega_0 = K_5 \sqrt{\frac{R_L}{C_1 C_2 R_1 R_2 R_3}}, \qquad Q = \sqrt{\frac{C_2 R_2 R_L}{C_1 R_1 R_3}}$$
(18)

It is evident from (17, 18) that the proposed modification provides  $\omega_0$  programming only and also it requires an additional inverter for AP realization. This case too, requires proper combination of inputs for generation of various filter functions as depicted in Table 2. Current output requires common matching constraint ( $R_L = R_3$ ) for both BR and AP, whereas voltage output requires ( $R_2 = R_3 = R_L$ ) and ( $R_1 = R_2 = R_3 = R_L$ ) for BR and AP, respectively.

Need of an extra inverter for AP realization can be *removed* by changing the polarity of CC-5, 6. In this case, Eq. (17) remains same except the sign inversion, which appears in the coefficient of 's' of its numerator.

#### 4.4 Fourth Derived Mixed Mode UF

Further *deletion of DPCC-5* from the Third configuration (Fig. 4d) results in *complete omission of the programmability* features and derives the Fourth non-programmable mixed mode UF as depicted in Fig. 4e. Mixed mode operation of UF is governed by following equations

$$V_{0} = R_{L}I_{o}, \quad I_{0} = \frac{\left[\frac{s^{2}}{R_{2}}V_{1} - \frac{s}{C_{2}R_{1}R_{2}}V_{2}\right] + \left[s^{2}I_{1} - \frac{s}{C_{2}R_{2}}I_{2} + \frac{1}{C_{1}C_{2}R_{1}R_{2}}I_{3}\right]}{s^{2} + s\frac{1}{C_{2}R_{2}} + \frac{1}{C_{1}C_{2}R_{1}R_{2}}}$$
(19)

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Filter functions	Input combination	
	Voltage	Current
HP	$V_2 = V_3 = 0, V_1 = V_i^{b}$	$I_2 = I_3 = 0, I_1 = I_i^{b}$
LP	$V_1 = V_2 = 0, V_3 = V_i$	$I_1 = I_2 = 0, I_3 = I_i$
BP	$V_1 = V_3 = 0, V_2 = V_i$	$I_1 = I_3 = 0, I_2 = I_i$
BR	$V_2 = 0, V_1 = V_3 = V_i$	$I_2 = 0, I_1 = I_3 = I_i$
AP	$V_1 = V_2^a = V_3 = V_i$	$I_1 = I_2^a = I_3 = I_i$

 Table 2
 Derived filter functions for various input combinations

<sup>a</sup>  $V_2$ ,  $I_2$  are *negative* for Third Derived UF

<sup>b</sup>  $V_i$  and  $I_i$  show actual applicable input

$$\omega_0 = \sqrt{\frac{1}{C_1 C_2 R_1 R_2}}, \qquad Q = \sqrt{\frac{C_2 R_2}{C_1 R_1}}$$
(20)

It is evident from (19) that the proposed configuration realizes all the filter functions for current output but only HP and BP functions for voltage output. Filter function generation follows the same rules as given in Table 2. Although this configuration lacks the programmability feature of  $\omega_0$  and Q from (20), it can be controlled independently by first setting moderate component ratios ( $C_2/C_1$  or  $R_2/R_1$ ) for desired Q and then varying  $\omega_0$  keeping these ratios unchanged.

#### **5** Analysis of Parasitic Effects

CCII has high valued parasitic resistance  $R_Y$  (or  $R_Z$ ) in parallel with low-value parasitic capacitance  $C_Y$  (or  $C_Z$ ) at port Y (or Z). The effect of parasitic resistances at port Y and Z is not considered as these are much greater than the external resistances of the circuit. Modified output function of Generalized DPUF considering these parasitics is given as

$$V_{\rm op} = \frac{R_L I_{\rm op}}{1 + s R_L (C_{Z73} + C_{Y7})}$$

$$- \left[ \frac{s^2 \{K_1\}}{(1 + s R_{X1} C_{Z81})} - \frac{s K_6 \{K_2\}}{K_4 C_{2P} R_{2P} (1 + s R_{X2} C_{Z82})} + \frac{K_5 K_6 \{K_3\}}{C_{1P} C_{2P} R_{1P} R_{2P} (1 + s R_{X3} C_{Z83})} \right] \left( \frac{V_i}{R_{3p}} + \frac{R_3 I_i}{R_{3p}} \right)$$

$$I_{\rm OP} = \frac{s^2 \{1 + s R_{X7} (C_{Z1} + C_{Z6})\} + \frac{s K_6}{K_4 C_{2P} R_{2P} \{1 + s R_{X4} (C_{Z2} + C_{Z71})\}}}{s^2 \{1 - s R_{X7} (C_{Z1} + C_{Z6})\} + \frac{s K_6}{K_4 C_{2P} R_{2P} \{1 + s R_{X4} (C_{Z2} + C_{Z71})\}}}$$

$$(22)$$

where  $C_{1P} = C_1 + C_{Z3} + C_{Y5} + C_{Z72}$ ,  $C_{2P} = C_2 + C_{Z4} + C_{Z5} + C_{Y6}$ ,  $R_{1P} = R_1 + R_{X5}$ ,  $R_{2P} = R_2 + R_{X6}$ ,  $R_{3P} = R_3 + R_{X8}$ . Subscript 'P' denotes the modified output functions and component values under the influence of parasitic. Second and

third (if any) subscripts associated with parasitic elements denote the *i*th CC (i = 1, 2, ...8) and port number in case of multiple similar ports, respectively. Advantage of grounded components is obvious from these equations. They absorb large number of parasitics and thus do not create any unwanted pole. On the other hand, parasitic products  $R_{XI}C_{Z81}$ ,  $R_{X2}C_{Z82}$ ,  $R_{X3}C_{Z83}$ ,  $R_{X4}(C_{Z2} + C_{Z71})$  and  $R_{X7}(C_{Z1} + C_{Z6})$  are too small and can be easily neglected without having any significant impact over the circuit operation. Under these conditions, current output (Eq. 22) approximates to that of ideal response, and thus, it may be considered *insensitive* to parasitics. On the other hand, the interaction of  $R_L$  and  $(C_{Z73} + C_{Y7})$  creates an unwanted pole and affects the voltage output (Eq. 21) at frequencies above

$$\omega_{\rm P} = \frac{1}{R_L \left( C_{\rm Z73} + C_{\rm Y7} \right)} \tag{23}$$

Although this frequency is quite high, it can be further increased by decreasing  $R_L$  without having any constraint over lower limit as required in [22].

#### 6 Comparison

Large number of mixed mode UFs based on various active elements are reported in the literature. However, for fair comparison, only CCII-based configurations are taken into account. The proposed UF configurations are compared (Table 3) in the following important categories of various features (For reader's convenience, desired characteristics of all these features are indicated against each categories in 'Yes', 'No', 'Minimum' or in 'numbers').

- Independently programmable filter parameters—'Yes'
   (1a) ω<sub>0</sub>, (1b) *Q*-factor and (1c) G
- (2) Reconfigurability—programmable filter functions (type)
- (3) Number of input terminals required—'Minimum', i.e. '1'(3a) Voltage inputs and (3b) Current inputs
- (4) Appropriate port impedance—'Yes'
  - (4a) Input ports (a1) High for all voltage inputs, (a2) Low for all current inputs
  - (4b) Output ports (b1) Low for all voltage outputs (b2) High for all current outputs
- (5) Number of grounded/floating (g/f) passive components—'Minimum', i.e. '2/0'
   (5a) Resistors—g/f and (5b) Capacitors—g/f
- (6) Component matching required—'No'
- (7) Number of operational modes—'All 4'
- (8) Number of filter functions generated each modes—'All 5'
- (9) Type of programming analog/digital (A/D) 'Digital'
- (10) Number of active elements'Minimum'.

As already mentioned and also indicated in Table 3, only the proposed UF configurations (excluding Fourth Derived configuration) provide both the features of mixed mode operation and digital programming simultaneously, and thus they stand better in one or the other aspect. Even if the programmability features are ignored, the proposed

References/ features	(1) Ind pro filte Ye	ependent grammat 2r parame .s'	ly ole sters	(2) Reconfig- urability 'Yes'	(3) No. o termii	of input nals '1'	(4) Appro port in 'Yes'	priate npedanc	9		(5) Numbe of grou floating passive compoi	r inded/ 3 (g/f) nents	(6) Component matching required 'No'	(7) Number of operational modes '4'	(8) Number of filter functions in each mode '5'	(9) Tuning method analog/ digital 'Digital'	(10) Number of active elements 'Minimum'
							4a—ir	Iput	4b—o	utput							
	(1a) $\omega_0$	(1b) Q	(1c) G		(3a) V	(3b) I	(a1) V	(a2) I	(b1) V	(b2) I	(5a) $R_{g/f}$	(5b) $C_{g/f}$					
[22] (Fig. 6) <sup>f</sup>	Υ	Y	Y	z	NA	1	NA	Y	NA	Y	8/0	2/0	Z	CM	5d	A	7VG-CCII, 3CCII
[18]	Υ	Υ	Y	z	NA	-	NA	Y	NA	Y	8/0	2/0	Z	CM	5d	А	5VG-CCII, 3CCII
[E]	$N^{a}$	$N^{a}$	z	z	7	3	z	z	z	Y	2/6	2/0	Y	4	5	NA	6CCII+, 1D0CCII+
[2]	$N^{a}$	Na	z	z	4	б	z	z	z	Y	4/3	2/0	Y	4	5	NA	3CCII-, 1CCII,1D0CCII
[31]	Υ	Υ	z	z	-	3	z	z	z	Y	0/0	2/0	Z	4	3	A	2CCCII,3DOCCCII
[21]	Υ	Υ	Y	z	3	7	Y	Y	Y	Y	0/0	2/0	Y	4	5	A	4MO-CCCII
[28]	$N^{a}$	$N^{a}$	z	z	0	-	NA	Y	z	NA	3/1	2/0	N	TIM	3	NA	4CCII+
[13]	$N^{a}$	$N^{a}$	z	z	NA	5	NA	z	NA	Y	2/1	2/0	z	2	5	NA	2CCII, 1MOCCII
[20]	$N^{a}$	N <sup>a</sup>	z	Z	3	3	z	z	z	Υ	1/3	3/0	Y	4	5	NA	3CCII+
[19]	Υ	Υ	z	z	4	4	z	z	z	Y	1/0	0/2	Y	4	5	A	1DOCCCII,1MOCCCII
[27]	Υ	Υ	Y	z	4	NA	Y	NA	Y	NA	3/4	2/0	Y	VM	5	D	4(DPCCII+VF) <sup>e</sup> , 2DPCCII
[26]	Υ	Υ	z	z	б	б	Y	Y	z	Y	4/2	2/0	Y	4	5	D	3DP-CCII, 2DP-DOCCII
[29]	Υ	Υ	z	z	1	NA	Y	NA	z	NA	12/0	4/0	z	VM	3	D	5DP-FDCC
[12]	$\boldsymbol{\gamma}^{\mathrm{b}}$	$\gamma^{\mathrm{b}}$	$\gamma^{\mathrm{b}}$	z	-	NA	Y	NA	z	NA	6/0	2/0	z	VM	3	D	5DP-CCII
[10]	Y	$\gamma^{\mathrm{b}}$	z	Z	1	0	Y	NA	z	NA	2/1	2/0	z	VM	2	D	4DP-CCII
[7] (Fig. 10) <sup>f</sup>	Υ	Υ	z	z	-	0	Y	NA	z	NA	4/0	2/0	N	VM	2	D	3DP-CCII, 2CDN
[16]	Υ	Y	z	z	NA	1	NA	z	NA	z	3/0	1/1	Z	CM	3	D	3DP-CCII

 Table 3
 Performance comparison chart of Generalized and Derived configurations

Table 3 conti	nued																
References/ features	(1) IndeF progr filter 'Yes'	pendently ammabl paramet	y le ers	(2) Reconfig- urability 'Yes'	(3) No. of termin	input ials '1'	(4) Approj port irr 'Yes'	priate	8		(5) Number of groun floating passive compon '2/0'	r nded/ (g/f) tents	(6) Component matching required 'No'	(7) Number of operational modes '4'	(8) Number of filter functions in each mode '5'	(9) Tuning method analog/ digital 'Digital'	(10) Number of active elements 'Minimum'
							4a—in	iput	4b—ot	utput							
	(1a) <i>w</i> <sub>0</sub>	(1b) Q	(1c) G		(3a) V	(3b) I	(a1) V	(a2) I	(b1) V	(b2) <i>I</i>	(5a) $R_{g/f}$	$C_{g/f}$					
Generalized DPUF (Fig. 4a)	Y	Y	¥	Y	-	1	Y	¥	¥	Y	4/0	2/0	N	4	5	D	6DP-CCII, 2MOCCII
First Derived DPUF (Fig. 4b)	Y	¥	¥	Y	-	б	¥	¥	¥	Y	6/0	2/0	Y	4	5	D	6DP-CCII, IMOCCII
Second Derived DPUF Fig. 4c	Y	Y	z	z	ŝ	ŝ	z	Y	Y	Y	3/3	2/0	Y	4	5	D	3DP-CCII,1MOCCII
Third Derived DPUF Fig. 4d	Y	Z	z	z	б	ŝ	z	Y	Y	Y	1/3	2/0	Y	4	5	D	2DP-CCII,1MOCCII
Fourth Derived UF Fig. 4e	Na	N <sup>a</sup>	z	z	7	ŝ	z	z	z	Y	1/2	2/0	z	4	5/2 <sup>c</sup>	NA	ICCII, IMOCCII
Y yes, N no, N <sup>a</sup> Independent! <sup>b</sup> Conditionally <sup>c</sup> Only two filte <sup>d</sup> General biqu. <sup>e</sup> [27] Actually	A not y cont y cont ir func adratic uses f	applica rolled t rammat stion fou c filter four dig	ble, V but not ble r voltag țitally <sub>F</sub>	voltage, I c programma șe output yrogrammal	urrent, ible ble curr	A analo	og, D c dback	ligital amplifi	ier, exp	Jressed	as (DP0	CC + V	F) and two I	DPCCs			
Figures of res	specuv	/e rerer(	ences														

configurations show better/comparable performance to that of the reported UFs using equal or more number of active elements.

Comparison in Table 3 shows that the CM general biquadratic UFs of [18,22] which in limited sense may be considered as analog counterpart of the proposed Generalized DPUF, not only use quite large number of active and/or passive components but also lack number of features. Another application [27], realizing VM DPUF, uses six programmable blocks as used in Generalized DPUF, but it provides programmability of filter parameters only, not the reconfigurability. This feature (reconfigurability) is unique to the Generalized DPUF and is not available in any CC-based reported DPUFs. This feature not only makes the proposed configuration fully programmable but also makes it suitable for integration. To further prove the potentiality of Generalized configuration, its CM variant is compared with few voltage/current followers (VF/CF)-based DPUFs [4–6]. For comparison purpose, blocks  $K_1 - K_3$ , CC-7 and CC-4, 8 can be replaced by MOS switches, CF and digitally programmable CFs, respectively. Thus, effectively, it uses total seven followers, three switches and minimum number of passive components ( $R_3$  and  $R_L$  are redundant in CM operation) only. On the other hand, for providing same programmability features, CM DPUF of [4] needs one additional follower and one resistor, while that of [6] needs two additional followers and that of (Fig. 6 of Ref. [5]) needs two additional resistors.

The First Derived configuration (Fig. 4b) possessing almost all the features of Generalized configuration is compared with the non-programmable UF of [1], using equal number of active elements (seven). The proposed configuration stands better in the terms of least input voltage terminals requirement, use of only grounded resistors and appropriate input/output port impedances. Only the feature in which the proposed configuration lacks is unavailability of inverted response as provided by [1], but this can be easily achieved by taking the output from opposite Z-port. VM DPUF of [27] uses six active elements and provides almost similar features of First Derived configuration. However, it is not reconfigurable and also it uses quite large number of floating elements.

Comparison of Second Derived configuration (Fig. 4c) with structures [10,21,28] using equal number of active elements (four) shows that whereas [21] is constrained by the proper (weighted) current ratio for AP realization, which needs additional circuitry, [10] and [28] operates in one mode for three or less than three [10] filter functions only, while no such constraint applies over the proposed Second Derived configuration. Additional constraint over [10] lies in programming of Q, which also varies the gain of BP response. Programmable and non-programmable UFs of [26] and [2], respectively, possess almost all the features of Second Derived UF. However, they need an additional CC, i.e. total five. Other five CC-based UFs [12,29,31] use lesser number of inputs, but they provide three filter functions only. Moreover, only [31] operates in all the four modes, while [12,29] operate in VM only. Besides this, [12] uses two different filter configurations for programming all the three parameters  $(G, \omega_0, Q)$ , but still its programming is independent in limited sense. On the other hand, the missing feature of gain programming as provided by [12] can be easily incorporated in the proposed Second Derived DPUF by simply cascading one additional DPCC at its output without imposing any programming constraint over remaining two filter parameters. Apart from this, the proposed Second Derived configuration is capable of providing inverting

and non-inverting responses simultaneously. This feature is available in UF of [26] only. Complimented voltage outputs of [1] are not available simultaneously, and also it uses large number of voltage input terminals for providing these outputs.

The Third Derived configuration (Fig. 4d) is partially programmable as it provides only  $\omega_0$  programming. Furthermore, it needs an additional inverter or change in polarity of CC-5, 6 for AP realization. Its comparison with three CC-based UFs [7,13,16,20] shows that either these configurations have more or less similar constraints [7,20] or they involve CM processing [13,16], which results in simplified circuit. UF of [7] needs two additional CDNs, while that of [16] needs two additional switches and three capacitors. Moreover, all these configurations [7,13,16,20] lack in providing the output at appropriate impedance port. Another application realizing the CM UF [15] provides same feature of  $\omega_0$  programming using analog technique, but it employs quite large number of elements; total seven CCs of both polarities and eight passive elements.

Fourth and the last Derived configuration (Fig. 4e) completely missing the programmability feature is compared with that of [19], using equal number of active elements. UF of [19] implements all the filter functions in all the four modes by using two floating capacitors; the situation is counterbalanced as the proposed configuration does not provide all the filter functions for voltage output but uses grounded capacitors only. The potential structure of this configuration can be better explained by considering its CM variant. The CM variant of this configuration uses only *two* CCs and *minimum* number of grounded passive components (resistance  $R_L$  is redundant in this case). To the best knowledge of author, this component count is *minimum* for any CC-based CM UF using grounded elements only. Apart from this, the proposed CM UF is suitable for *very* high frequency application as all the parasitic capacitances of CCs are absorbed in the external capacitances of UF. UFs of [14] use exactly equal number of active (both proposed and reported UFs use one MOCC and one negative CCII) and grounded passive components. However, it is not insensitive to parasitics.

The systematic comparison presented herein shows that the features of Derived UFs (realized from the Generalized configuration) decrease as the number of active elements are decreased. But still these reduced-performance Derived UFs show bet-ter/comparable performance to reported UFs using equal or more number of active elements. Thus, this fact in turns justifies the need of eight CCs in Generalized DPUF for obtaining all the aforementioned features.

## 7 Simulation Result

Performance of the proposed Generalized DPUF is verified by SPICE simulations using 0.18 µm TSMC parameters, transistor dimensions  $(W/L)_{M1,M2,M5,M6}$ =(14.4µ/ 0.72µ),  $(W/L)_{M3,M4,M7,M8}$  = (1.44 µ/1.44 µ),  $(W/L)_{M9,M10}$  = (1.44 µ/0.72 µ),  $(W/L)_{M11,M12,M13,M14,MP0,MP0s,MN0,MN0s}$ =(72 µ/0.72 µ),  $(W/L)_{MP1,MP1s,MN1,MN1s}$  = (144 µ/0.72 µ),  $(W/L)_{MP2,MP2s,MN2,MN2s}$  = (288 µ/0.72 µ) and bias voltages  $V_{B1}$  = -255 mV,  $V_{B2}$  = 235 mV and supply voltage ±0.75 V. DPUF is simulated for VM case using DPCC layout (area = 0.0094 mm<sup>2</sup>) shown in Fig. 5 and passive component values  $R_1 = R_2 = 9 K\Omega$ ,  $R_3 = R_L = 1 K\Omega$  and



Fig. 5 DPCC layout

 $C_1 = 20 \text{pf}, C_2 = 40 \text{pf}$ . It results in component-dependent filter parameters as  $f_c = 1/2\sqrt{2} \pi \text{RC} = 725.2 \text{ KHz}, Q_c = 1/\sqrt{2}$  and  $G_c = 1$ . The parameter  $G_c$  denotes the component-dependent factor of G. It is to be noted that all the passive components of Generalized DPUF are grounded, and thus they are suitable for integration. However, for tracking the wide variations of filter parameters, they are used (and also suggested to be used) externally. In this case, passive components are used to decide the parameter values, while the available tuning range is used to compensate the parameter variations. The approach is particularly helpful for small n DPCCs (Note: the DPCC used in present paper is suitable for small n because the transistor size of its CDN/CSN increases in binary weighted order with n. Also, its linearity becomes poorer [7]). An important feature of the proposed Generalized DPUF is that it is not constrained by any component matching requirement. Thus, any component values may be chosen. The optional matching constraint of  $C_1 = 2C_2$  (for  $Q_c = 1/\sqrt{2}$ ), however, has less impact over Q-factor because the error in Q is subtractive and is given by

$$\frac{\Delta Q}{Q} = \pm \frac{1}{2} \left( \frac{\Delta C_1}{C_1} - \frac{\Delta C_2}{C_2} \right) \tag{24}$$

where  $\Delta C_1$  and  $\Delta C_2$  show the unwanted variations in  $C_1$  and  $C_2$ , respectively.

Programming matrix of various filter parameters is listed in Table 4. Firstly, the desired filter function (type) and its G is set by codewords  $K_1$ ,  $K_2$  and  $K_3$ ; then the parameter values  $f_0$  and Q are programmed independently by codewords  $K_5 = K_6$ 

Filter functions	Codeword programming of filter parameters		
	Pole frequency $(f_0 = K_5 f_c)$	$Q$ -factor ( $Q = K_4 Q_c$ )	$\operatorname{Gain} \left( G = K_{i}^{\mathrm{b}} G_{c} \right)$
HP $K_2 = K_3 = 0{000} \neq K_1$	$K_5 = K_6 = 1 - 7\{001 - 111\}, K_4 = 1, K_1 = K_G$	NA	$K_1 = 1-7\{001-111\}, K_4 = K_O(=1), K_5 = K_6 = K_f$
LP $K_1 = K_2 = 0{000} \neq K_3$	$K_5 = K_6 = 1 - 7\{001 - 111\}, K_4 = 1, K_3 = K_G$	NA	$K_3 = 1-7\{001-111\}, K_4 = K_Q(=1), K_5 = K_6 = K_f$
BP $K_1 = K_3 = 0{000} \neq K_2$	$K_5 = K_6 = 1 - 7\{001 - 111\}, K_4 = K_Q, K_2 = K_G$	$K_4 = 1-7\{001-111\}, K_5 = K_6 = K_f, K_2 = K_G$	$K_2 = 1-7\{001-111\}, K_4 = K_Q, K_5 = K_6 = K_f$
BR $K_2 = 0{000} \neq K_1 = K_3^a$	$K_5 = K_6 = 1 - 7\{001 - 111\}, K_4 = K_Q,$ $K_1 = K_3^a = K_G$	$K_4 = 1-7\{001-111\}, K_5 = K_6 = K_f, K_1 = K_3^a = K_G$	$K_1 = K_3 = 1 - 7\{001 - 111\}, K_4 = K_Q, K_5 = K_6 = K_f$
AP $K_1 = K_2 = K_3 \neq 0{000}$	NA	NA	$K_1 = K_2 = K_3 = 1 - 7\{001 - 111\}, K_4 = K_Q, K_5 = K_6 = K_f$
${}^{a}K_{1} < K_{3}$ (or $K_{1} > K_{3}$ ) can be : b $K_{1}$ denotes nonzero codeword at	set for realization of LP (or HP) notch nong $K_1, K_2$ or $K_3$		

Table 4 Programming matrix of proposed DPUFs

I				
Parameters	This work	Ref. [8]	Ref. [4]	Ref. [6]
Technology (nm)	180	250	350	180
Supply	土0.75	±1.25	主1.5	主0.9
Order/functions	2/all functions	2/LP, HP, BP	2/LP, HP, BP	4/LP, HP, BP
Power (mW)	3.26 @ 4 MHz	6 @ 500 KHz	6.9 <sup>a</sup>	1.55 @ 1MHz
THD	$-40  dB @ 62 mVp - 100 KHz^{c}$	-40 dB @ 10 mV-9 MHz	-60 dB @ 1 Vpp-10 KHz	NR
Area (mm <sup>2</sup> )	$0.0094^{a}$	NR	$0.0493^{a}$	NR
Integrated input noise	159.7 $\mu V_{rms}$ over 1 KHz–4 MHz	NR	$(658 \text{ nV}/\sqrt{\text{Hz}})^{\text{b}}$	$89 \muV_{rms}$ over $BW = 1 MHz$
Dynamic range	65.7 dB @ -40 dB THD	NR	NR	SFDR = 64  dB
Tuning range (Hz)	725 K-4 M	500 K-14 M	300 K-3 M	250 K-4 M
Brief description	DPCCII-based fully programmable DPUF using 3-bit CSN	DP-DVCC-based fully differential filter using 8-bit CDN <sup>d</sup>	DP-CF-based DPUF using 6-bit CDN <sup>d</sup>	DPCCII and DCCA-based DPUF using 8-bit CDN <sup>d</sup>
<i>DVCC</i> differential volta <sup>a</sup> Shows the parameters <sup>b</sup> Input referred noise sp <sup>c</sup> At gain setting of 'sew <sup>d</sup> Employ CDN of [3]	ge current conveyor, <i>SFDR</i> spurious of employed active element ectral density	free dynamic range		

Table 5Comparison of quantitative performance of Generalized DPUF

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Fig. 6 a Various filter functions of Generalized DPUF. b Gain and pole frequency programming of LP

and  $K_4$ , respectively. Figure 6a depicts the realization of various filter functions for parameter values  $G = 7G_c$  (by setting  $K_G = 7\{111\}$ ),  $f_0 = 4f_c$  (by setting  $K_5 = K_6 = K_f = 4\{100\}$ ) and  $Q = Q_c$  (for maximal flat LP and HP response by setting  $K_4 = K_Q = 4\{001\}$ ) or  $7Q_c$  (for BP and BR by setting  $K_4 = K_Q = 7\{111\}$ ; required to be high). In Table 4,  $K_G$ ,  $K_Q$  and  $K_f$  denote any nonzero codeword combination for obtaining desired values of parameter of G, Q and  $f_0$ , respectively. Pole frequency programming is demonstrated using LP response in Fig. 6b. For  $G = 7G_c$  (using  $K_1 = K_2 = 0$ ,  $K_3 = 7$ ) and  $Q = Q_c$  (using  $K_4 = 1$ ),  $f_0$  is programmed by varying  $K_5 = K_6$  to codeword values 1{001}, 4{100} and 7{111}. Similarly, G-programming of LP response for parameter values  $f_0 = 4f_c$  and  $Q = Q_c$  is shown for  $K_3 = 1, 4, 7$ in Fig. 6b. Practically, the codewords values are set by an *n*-bit shift register/counter, which is controlled by a tuning circuitry/algorithm. Literature reports number of tuning techniques [23, 30] for programmable filters.



Fig. 7 a Quality factor programming of BP—by cascading. b Quality factor programming of BR—by increasing CDN size

Quality factor programming for BP and BR responses is depicted in Fig. 7a, b, respectively, for  $f_0 = 4 f_c$  and  $G = 7G_c$ . High-*Q* realization approaches discussed in Sect. 4 are demonstrated in this section. For high-*Q* BP realization, *cascade* approach is used. A DPCC (let, say  $K_{4C}$ ) identical to  $K_4$  block is cascaded with it. Codeword  $K_4$  is varied from 1{001} to 7{111} by keeping  $K_{4C} = 7{111}$ . It varies the *Q*-values from  $7 \times 1 = 7Q_c$  to  $7 \times 7 = 49Q_c$  in steps of seven. On the other hand, high-Q realization of BR response is obtained by *increasing* the CDN size of DPCC-4 to n = 5. It results in *Q* variation from  $Q_c$  ( $K_4 = 1$ ) to  $31Q_c(K_4 = 31)$ . *Q*-factor is programmed for codeword values  $K_4 = 1{00001}, 4{00100}, 8{01000}, 16{10000}$  and  $31{1111}$ .

As already shown in comparison section that the proposed configurations use almost equal or lesser number of active and/or passive components than that of reported UFs for a given set of features; thus they (proposed UFs) are expected to be more area and power efficient if same DPCC (for programmable configurations) or CC (for non-programmable configurations) is used in both the proposed and the reported UFs.

Total harmonic distortion (THD) of the proposed Generalized DPUF for LP response with gain of 16.9 dB ( $K_3 = 7$ ) and highest bandwidth setting (for  $K_5 =$  $K_6 = 7$ ) is found to be better than -40 dB for 62 mVp sinusoidal signal with frequency 100 KHz. Total input integrated noise over passband (1 KHz–4 MHz) is  $159.7 \,\mu V_{rms}$ . This results in dynamic range (DR) of 65.7 dB. Power dissipation of LP response for maximum gain/bandwidth ( $K_3 = K_5 = K_6 = 7$ ) setting is obtained as 3.26 mW. It is to be noted that all these parameters are highly affected by the choice of particular DPCC, which in turns depends over its core element-CDN. For example, DPCCs of [7, 10, 12, 29] are suitable for power efficient design because their CDNs consume no standby power. While for area efficient design, better linearity and extended tuning range, DPCC of [7,29] is preferred because of the excellent features of its CDN element discussed in [3]. Quantitative performance of the Generalized DPUF is compared in Table 5 with few reported DPUFs (as it is available for few DPUFs only). Better/comparable performance of the proposed Generalized DPUF is obvious from the Table 5 itself. It uses lowest supply voltage and consumes less power. Only the parameter, in which the proposed DPUF lacks, is the extended range [8]/fine tuning [4,6] of pole frequency. DPUFs of [4,6] attains this feature due to the use of their CDN element [3], which could be operated for large n. Thus, this feature can be easily incorporated in the proposed DPUFs by employing DPCC of [7,29] as they use same CDN.

## 8 Conclusion

This paper presents a novel fully programmable Generalized mixed mode DPUF. It provides attractive features such as full cascadability, realization of all filter functions in all the modes, no component matching constraint, use of only grounded passive elements, least input signal requirement and almost insensitive performance to DPCC parasitics. All these features are obtained by using quite large number of active elements. Reduction in number of active elements of Generalized DPUF also reduces its features accordingly. This is verified by introducing four mixed mode Derived UFs obtained by the modification of Generalized configuration. The systematic comparison of all the proposed UFs not only shows better/ comparable performance over that of reported UFs but also justifies the need of eight CCs in Generalized DPUF.

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# References

- M.T. Abuelma'atti, A. Bentrcia, S.M. Al-Shahrani, A novel mixed-mode current-conveyor based filter. Int. J. Electron. 91(3), 191–197 (2004)
- M.T. Abuelma'atti, A. Bentrcia, A novel mixed-mode CCII-based filter. Act. Passive Electron. Compon. 27(4), 197–205 (2004)
- H.A. Alzaher, H.O. Elwan, M. Ismail, A CMOS highly linear channel-select filter for 3G multistandard integrated wireless receivers. IEEE J. Solid State Circuits 37(1), 27–37 (2002)

- H.A. Alzaher, A CMOS digitally programmable universal current mode filter. IEEE Trans. Circuits Syst. II 55(8), 758–762 (2008)
- 5. H.A. Alzaher, N.A. Tasadduq, O. Al-Ees, Digitally programmable high-order current-mode universal filters. Analog Integr. Circuits Signal Process. **67**(2), 179–187 (2011)
- H.A. Alzaher, N.A. Tasadduq, O. Al-Ees, Implementation of reconfigurable nth-order filter based on CCII. Analog Integr. Circuits Signal Process. 75(3), 539–545 (2013)
- H. Alzaher, N. Tasadduq, O. Al-Ees, F. Al-Ammari, A complementary metal-oxide semiconductor digitally programmable current conveyor. Int. J. Circuit Theory Appl. 41(1), 69–81 (2013)
- 8. P. Beg, S. Maheshwari, M.A. Siddiqi, Digitally controlled fully differential voltage and transadmittance-mode biquadratic filter. IET Circuits Devices Syst. **7**(4), 193–203 (2013)
- 9. M. Bhushan, R. Newcomb, Grounding of capacitor in integrated circuits. Electron. Lett. **3**(4), 148–149 (1967)
- A.A. El-Adawy, A.M. Soliman, H.O. Elwan, Low voltage digitally controlled CMOS current conveyor. Int. J. Electron. Commun. 56(3), 137–144 (2002)
- 11. G. Ferri, N.C. Guerrini, Low-voltage low-power CMOS current conveyors (Kluwer, Boston, 2003)
- T.M. Hassan, S.A. Mahmoud, Fully programmable universal filter with independent gain-ω<sub>0</sub>-Q control based on new digitally programmable CMOS CCII. J. Circuits Syst. Comput. 18(5), 875–897 (2009)
- J.W. Horng, High-order current-mode and transimpedance-mode universal filters with multiple-inputs and two-outputs using MOCCIIs. Radioengineering 18(4), 537–543 (2009)
- J. Jiang, Y. He, Tunable frequency versatile filters implementation using minimum number of passive elements. Analog Integr. Circuits Signal Process. 59(1), 53–64 (2009)
- C.A. Karybakas, C.A. Papazoglou, Low-sensitive CCII-based biquadratic filters offering electronic frequency shifting. IEEE Trans. Circuits Syst. II 46(5), 527–539 (1999)
- I.A. Khan, M.R. Khan, N. Afzal, Digitally programmable multifunctional current mode filter using CCIIs. J. Act. Passive Electron. Devices 1(3), 213–220 (2006)
- S.A. Mahmoud, M.A. Hashiesh, A.M. Soliman, Low voltage digitally controlled fully differential current conveyor. IEEE Trans. Circuits Syst. I 52(10), 2055–2064 (2005)
- S. Minaeil, O.K. Sayin, H. Kuntman, Nth-order current transfer function synthesis using a high performance electronically tunable current conveyor. In IEEE Mediterranean Electrotechnical Conference (MELECON), 2006 May 16–19, Benalmádena (Malaga), Spain (2006)
- N. Pandey, S.K. Paul, S.B. Jain, A. Bhattacharyya, Generalized mixed mode universal filter realization using current controlled Conveyors (online). HAIT J. Sci. Eng., (2007) http://people.clarkson.edu/ ~nanosci/jse/B/inpress/paul.pdf
- N. Pandey, S.K. Paul, A. Bhattacharyya, S.B. Jain, A new mixed mode biquad using reduced number of active and passive elements. IEICE Electron. Express 3, 115–121 (2006)
- N. Pandey, S.K. Paul, Mixed mode universal filter. J. Circuits Syst. Comput. 22(1), 1250064-1– 1250064-10 (2013)
- C.A. Papazoglou, C.A. Karybakas, Noninteracting electronically tunable CCII-based current mode biquadratic filters. IEE Proc. Circuits Devices Syst. 144(3), 178–184 (1997)
- S. Pavan, Y. Tsividis, Tuning in continuous-time filters, in high frequency continuous time filters in digital CMOS processes (Springer, Berlin, 2000), pp. 165–190
- K.P. Pun, C.S. Choy, C.F. Chan, J.E. Da-Franca, Current division based digital frequency tuning for active RC filters. Analog Integr. Circuits Signal Process. 45(1), 61–69 (2005)
- A. Sedra, K.C. Smith, A second generation current conveyor and its applications. IEEE Trans. Circuit Theory 17(1), 132–134 (1970)
- D. Singh, N. Afzal, Digitally programmable current conveyor based mixed mode universal filter. Int. J. Electron. Lett. (2014). doi:10.1080/21681724.2014.917714
- D. Singh, N. Afzal, Digitally programmable high-Q voltage mode universal filter. Radioengineering 22(4), 995–1006 (2013)
- 28. A.M. Soliman, Mixed mode biquad circuits. Microelectron. J. 27(6), 591–594 (1996)
- A.M. Soliman, A.S. Eman, Digitally programmable second generation current conveyor based FPAA. Int. J. Circuit Theory Appl. 41(10), 1074–1084 (2013)
- G. Zatorre, S. Celma, C. Aldea, N. Medrano, E. Peralias, Digital auto-tuning system for analog filters. In Proceedings of the 6th International Caribbean Conference on Devices, Circuits and Systems, pp. 37 – 41, April 26–28, Playa del Carmen (2006)
- L. Zhijun, Mixed-mode universal filter using MCCCII. Int. J. Electron. Commun. 63(12), 1072–1075 (2009)