

## LASER FORMATION OF Pt-Si SCHOTTKY BARRIERS ON SILICON

C. J. Doherty, C. A. Crider, and H. J. Leamy  
Bell Telephone Laboratories  
Murray Hill, New Jersey 07974

G. K. Celler  
Western Electric Company  
Engineering Research Center  
Princeton, New Jersey 08540

(Received September 14, 1979)

135 nsec pulses of  $\lambda = 1.06\mu\text{m}$  light from a Nd: YAG laser have been used to form Schottky barriers by irradiation of a  $500\text{\AA}$  thick metal film on n-type silicon. Large area barriers were fabricated by over-lapping individual  $30\mu\text{m}$  diameter laser pulses of from 4 to  $12\text{ J/cm}^2$ . The barrier height was  $0.73 \pm 0.03\text{ V}$ , independent of the laser power. The barrier quality, as assessed by measurement of the forward current characteristic, decreased with laser power to a value of  $n=1.5$  at  $12\text{ J/cm}^2$ .

Keywords: Laser annealing Schottky barriers Pt-Si

### *Introduction*

The formation of surface layers of metal-silicon intermetallic compounds via solid state reaction is a common method for the production of Schottky barriers or ohmic contacts on silicon.<sup>(1)</sup> The equiatomic PtSi intermetallic produced by reaction of thin Pt films on silicon forms an ohmic contact to  $n^+$  and p-type silicon and a Schottky barrier of 0.82 to 0.85 eV height on lightly doped ( $\sim 10^{17}\text{cm}^{-3}$ ) n-type material.<sup>(2)</sup> Our aim here is to report that Pt-Si surface alloy layers formed by laser processing also form Schottky barriers on n-type silicon. It has also been established<sup>(3)</sup> that laser processed Pt-Si alloy layers form ohmic contacts to p-type silicon, and that p-n/PtSi junction diodes may be fabri-

cated by laser processing a Pt coated, p-type crystal that has been implanted with As ions. Laser processing thus offers an alternative to standard furnace processing for the production of these common contact structures.

Following the revelation<sup>(4)</sup> that pulsed laser light could be employed to anneal ion implantation damage in silicon surface layers, we discovered that the Pt-Si reaction could also be stimulated by laser irradiation.<sup>(5)</sup> The path of the reaction, however, was found to be quite different from that followed during furnace annealing. Pulsed laser irradiation at intensity levels below that required to melt silicon produces a thin film of molten Pt-Si alloy whose thickness and composition depend linearly upon the laser pulse energy density.<sup>(5)</sup> Sulfidation of this molten layer at the high rates common to laser processing<sup>(6)</sup> yields a polyphase solid layer of extremely fine ( $\sim 100\text{\AA}$ ) grain size that is uniform in composition and thickness. The composition of the film depends upon the quantity of silicon that is consumed by the molten layer. Alloy compositions in the range from  $Pt_2Si$  to  $PtSi_{10}$  may be reproducibly formed, and the phase mixture produced by laser processing depends upon this composition. Because these surface films, which may be formed by local, patterned laser processing, exhibit smooth surface morphology and good dimensional stability, their electrical properties are of interest.

### Experimental

Arsenic doped silicon wafers of  $0.5\ \Omega\ \text{cm}$  resistivity were selected as substrates, and were processed to produce an  $n^+$  layer on the back surface. A  $3000\text{\AA}$  thick thermal oxide was grown on the polished front surface. An array of circular windows of  $0.05\ \text{cm}$  diameter was then photolithographically patterned and etched into the  $SiO_2$  layer, and  $500\text{\AA}$  of Pt was deposited in an oil free vacuum system onto the wafer surface. The Pt coated crystals were then irradiated with  $135\ \text{nsec}$  pulses of  $\lambda = 1.06\ \mu\text{m}$  light from a Nd:YAG laser that was operated in the  $TEM_{00}$  mode at a pulse repetition rate of  $11.5\ \text{kHz}$ . Each pulse was focused to a  $1/e$  diameter of  $30\ \mu\text{m}$ , and large areas were processed by translating the laser beam  $8\ \mu\text{m}$  between successive pulses to produce an array of overlapping pulse spots.

The Pt film covering the oxidized silicon was melted and agglomerated into discrete,  $1\text{-}5\ \mu\text{m}$  spheres at all power levels sufficient to induce the Pt-Si reaction. The individual, circular unoxidized windows within which the reaction occurred were consequently isolated from one another and electrical measurements were performed without further processing.

*Electrical Properties*

The occurrence of laser induced reaction of Pt and Si is accompanied by a change in surface luster and can easily be established visually. For all laser power levels at which the reaction occurred, Schottky diode behavior was observed. Representative I-V characteristics of diodes produced at several laser power levels are plotted in Fig. 1.

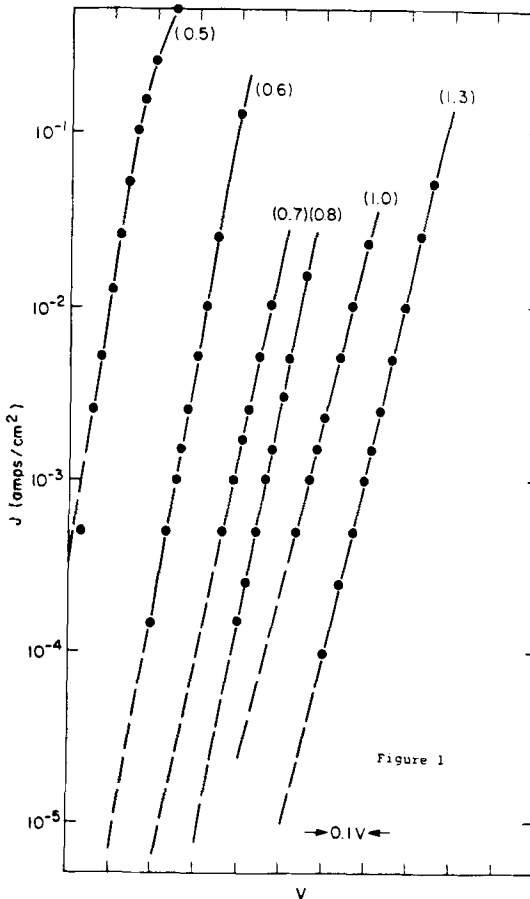


Fig. 1 The forward bias characteristics of laser processed Pt-Si Schottky barriers. Each curve has been offset 0.1V on the horizontal axis for clarity. The number in parenthesis is the average laser power used in diode formation.

The saturation current,  $J_s$ , determined by linear extrapolation of the data in Fig. 1 to zero volts is listed in Table I, together with the barrier height that may be inferred<sup>(7)</sup> from it. This table illustrates that the barrier heights are independent of laser power over the range examined, which spans the power levels of practical interest. At 4.7 J/cm<sup>2</sup> (0.5 W avg. power) for example, no visible evidence for reaction was observed. The barrier behavior shown in Fig. 1 for this pulse energy consequently differs greatly from those produced at higher power, and probably reflects the properties of the Pt/Si contact. At 12.3 J/cm<sup>2</sup>, the surface of the reacted material was visibly roughened, the individual laser spots being easily discerned. Over the range from 5.6 to 12.3 J/cm<sup>2</sup>, the mean alloy layer composition produced in similar films varied linearly with pulse energy from  $\sim$  PtSi to PtSi<sub>10</sub>.

TABLE I

## PROPERTIES OF LASER PROCESSED SCHOTTKY DIODES

<i>Laser Power</i>		<i>Laser Energy</i>	$J_s$	$\phi_B$	$n$
avg.	pulse		(amp/cm <sup>2</sup> )	(V)	
0.5W	35MW/cm <sup>2</sup>	4.7J/cm <sup>2</sup>	$2.85 \times 10^{-4}$	0.63	1.0
0.6W	42MW/cm <sup>2</sup>	5.6J/cm <sup>2</sup>	$6.1 \times 10^{-6}$	0.73	1.149
0.7W	49MW/cm <sup>2</sup>	6.6J/cm <sup>2</sup>	$6.6 \times 10^{-6}$	0.735	1.42
0.8W	56MW/cm <sup>2</sup>	7.6J/cm <sup>2</sup>	$6.9 \times 10^{-6}$	0.73	1.24
1.0W	70MW/cm <sup>2</sup>	9.45J/cm <sup>2</sup>	$2.3 \times 10^{-5}$	0.7	1.66
1.3W	91MW/cm <sup>2</sup>	12.3J/cm <sup>2</sup>	$8.9 \times 10^{-6}$	0.725	1.57

The diode quality as assessed by the slope,  $n = qV/kT \ln(J)$ , of the data shown in Fig. 1 decreases with increasing laser power as shown in Table I. Clearly, optimum diodes are produced at laser power levels just sufficient to induce the reaction. This conclusion is also reflected in the data of Fig. 2, in which the reverse bias characteristics of the diodes have been plotted.

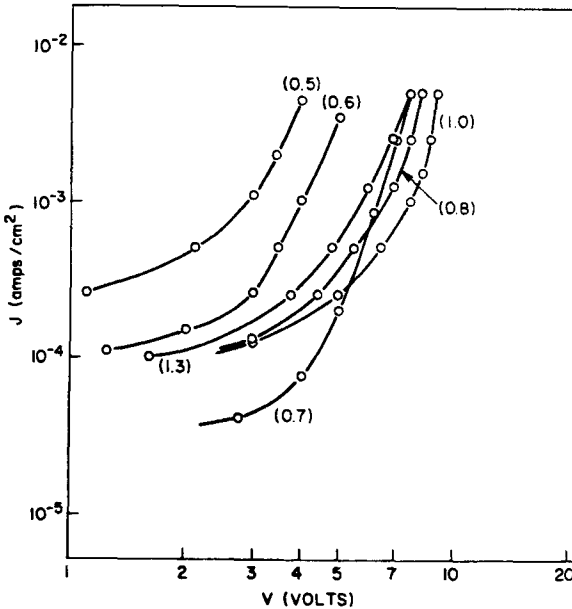


Fig. 2 The reverse bias characteristics of the diodes shown in Fig. 1.

Although we assign no specific significance to the values of breakdown voltage and leakage current displayed in Fig. 2, the curves are representative of diodes produced by irradiation of Pt films over oxide windows, and do indicate a minimum in leakage current for the diode produced at  $6.6 \text{ J/cm}^2$  ( $0.7 \text{ W}$  avg. power).

### Conclusions

These preliminary results indicate that Schottky diodes of reproducible barrier height may be fabricated by laser irradiation of thin Pt films on n-type silicon. The barrier height is insensitive to laser power while diode quality is degraded by processing at pulse energies above those required to form the surface alloy.

In a separate development,<sup>(3)</sup> we have recently learned that As implants beneath Pt surface layers are driven before the molten surface alloy during laser processing. In p-type material, this results in As activation and the formation of a p-n junction below the surface alloy, which forms an ohmic contact to the  $n^+$  region created by the As dopant. This result suggests that the barrier height of laser processed Pt-Si surface alloys may be modified by implantation of suitable n-type dopants prior to processing as is current practice<sup>(8)</sup> for furnace annealed barriers. Laser processing of Pt coated silicon thus appears to offer great flexibility for the formation of ohmic contacts and Schottky barriers on silicon, and offers the advantage that processing need not be performed over the whole of a given device.

#### *Acknowledgment*

We are indebted to T. E. Seidel and J. M. Poate for useful discussions.

#### *References*

- (1) M. P. Lepselter and J. M. Andrews in: *Ohmic Contacts to Semiconductors* ed. B. Schwartz (Electrochem. Soc., Princeton, NJ 1969) p. 159.
- (2) J. M. Andrews and M. P. Lepselter, *Solid State Elect.* *13*, 1011 (1970).
- (3) C. J. Doherty, H. J. Leamy, T. E. Seidel and G. K. Celler, unpublished.
- (4) See: *Physics Today* *31*, 17 (1978) and references therein.
- (5) J. M. Poate, H. J. Leamy, T. T. Sheng and G. K. Celler, *Appl. Phys. Lett.* *33*, 918 (1978).
- (6) See for example, D. H. Auston, J. A. Golavchenko, A. L. Simons, R. E. Slusher, P. R. Smith, C. M. Surko, and T. N. C. Venkatesan in: *Laser Solid Interactions and Laser Processing - 1978*, S. D. Ferris, H. J. Leamy, and J. M. Poate eds. (Amer. Inst. Phys., New York 1979) pp. 11-26.
- (7) S. M. Sze, *Physics of Semiconductor Devices* (Wiley-Interscience, New York 1969) pp. 393-409.
- (8) M. Wittmer and T. E. Seidel, *J. Appl. Phys.* *49*, 5827 (1978) and J. B. Bindell, W. M. Moller, and E. F. Labuda, private communication.