

# On the Time Required for Reference Count Management in Retention Block-Structured Languages. Part 2<sup>1</sup>

D. M. Berry,<sup>2</sup> L. M. Chirica,<sup>2</sup> J. B. Johnston,<sup>3</sup> D. F. Martin,<sup>2</sup> and A. Sorkin<sup>2</sup>

*Received February 1975; revised June 1976*

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In this paper, two implementations of generalized block-structured languages are presented and compared for time requirements. One implementation, the Lifetime Stack Model, implements the deletion strategy with lifetime checks; the other, the Partial Reference Count Contour Machine, implements the retention strategy. For a large subset of the lifetime well-stacking programs, those that run correctly on the first model, the two models are shown to require nearly the same order of magnitude of time. The use of full label values is shown to have a detrimental effect on the time efficiency of the latter model. Part 1, in Volume 7, Number 1, of this journal, gives a general description of the machines, some of their definitions, and proof of the results. Part 2, in this issue, serves as an appendix to Part 1 and contains most of the formal definitions of the machines.

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**KEY WORDS:** Block-structured languages; retention vs. deletion; contour model; stack model; reference counts; lifetime checks; time estimates.

This appendix defines the machine instructions for the LSM and the PRCCM, alluded to in Sects. 3.6 and 4.6 in Part 1 of this paper. The definitions are given in parallel with the definition of the LSM in the left column and the definition of the PRCCM in the right column. This was done both to take advantage of similarities and to point out the differences. When the two machines agree completely, the description runs across both columns. When they differ, the description is split into two columns.

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<sup>1</sup> Supported (in part) by the National Science Foundation, Grant No. DCR 75-08659.

<sup>2</sup> Computer Science Department, UCLA, Los Angeles, California 90024.

<sup>3</sup> Computer Science Department, New Mexico State University, Las Cruces, New Mexico 88003.

### 3.6 MACHINE INSTRUCTIONS

#### 4.6 MACHINE INSTRUCTIONS

Every GBSL program is translated into Polish code consisting of the instructions listed in Sections 3.6.2 and 4.6.2. There are twenty-two basic instructions. Some of them, i.e., VAL, POP, IND, STO, EXIT, and RETURN have more than one option, each indicated by a different suffix (no suffix is considered an empty suffix).

For each basic instruction, an English description of the basic function of the instruction is given. If the instruction has several options indicated by different suffixes, each of the variations is listed with an English description of the change from the basic function. Following the English description is commented pseudo Algol microcode to implement the instruction. This is followed by an estimate as to the time required to execute the instruction (e.g., constant, proportional to "X", potentially unbounded, T (to execute something), or a sum of any of these). If the two models have identical times<sup>†</sup> for a given instruction, then the time indication is asterisked in both columns. There are some conventions for giving the microcode:

- 1) Statements are in free format and are separated by semi-colons.
- 2) Comments begin with a "c" on any line and end at the end of the same line.
- 3) Keywords are words with all uppercase letters.
- 4) Identifiers are strings of consecutive lowercase letters, digits, and underscores.
- 5) The conditional is IF...THEN...ELSE...FI. The for loop is FOR...FROM...BY...TO...DO...OD.
- 6) Arithmetic can be done on pointers (by "pointer" is not meant a pointer value but the contents of a lo or hi field).
- 7) Indirection of a pointer is indicated by a "\*" following the pointer, e.g.,  
$$\text{ep}^* \quad \text{ptr}^* \quad [\text{i}] = (\text{ptr} + \text{i})^*$$
- 8) Subscripting of a contiguous block of words pointed to by a pointer is defined by  
$$\text{ptr}^* \quad [\text{i}:\text{j}] \quad \text{ptr}^* \quad [\text{i}:\text{j}]$$
- 9) A trimmer can be used to select a consecutive list of words, e.g.,  
$$\text{ptr}^* \cdot \text{hi}$$
- 10) A field of a word can be selected by use of a field name following a ".", e.g.,  
$$\text{ptr}^* \cdot \text{hi}$$
- 11) In expressions, the following precedence is used:  
highest:      unary +, -  
                  x, /  
binary +, -  
, ≤, =, ≠, ≥, >

- Within a precedence level, the association is left-to-right.
- 12) The following synonyms are available for use as selectors:
- as substitutes for hi:  
ep, sp, life, sl
  - as substitutes for lo:  
ip, dl, ptr, ts
  - as a substitute for mid:  
nh

- 13) The components of a processor's soa may be selected directly by applying a component name to  $\Pi$ , e.g.,

$\Pi.ip, \Pi.ep, \Pi.nh, \Pi.sp, \Pi.ts$

14) The display is selected by a special abbreviation, DISP.

15) Some abbreviations for use in accessing words of the stack are given:

1st is an abbreviation for

$(\Pi.ts + \Pi.sp - 1)^*$

and selects the top word of the stack

2nd is an abbreviation for

$(\Pi.ts + \Pi.sp - 2)^*$

and selects the second word of the stack

$n^*$  is an abbreviation for

$(\Pi.ts + \Pi.sp - n)^*$

and selects the nth word of the stack

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† The two models are said to require identical time for an instruction if the microcode implementing the instruction is identical in both models.

§ T(x) is the Time for x.

There are some basic primitives called by the microcode. Time estimates are given with the primitives. The time for any primitive whose description stretches over both columns is the same in both models.

- 1) makeint (int): forms a properly tagged integer value with int as the val.  
Time: constant\*
- 2) makebool (bool): forms a properly tagged boolean value with bool as the val.  
Time: constant\*
- 3) makeptr (ep, ptr): forms a properly tagged pointer value with ep as the ep and ptr as the pointer proper.  
Time: constant\*
- 4) makelproc (ep, nh, ip): forms a properly tagged procedure value with ep as the ep, nh as the nh, and ip as the ip.  
Time: constant\*
- 5) makelbl (ep,nh,ip): forms a properly tagged label value with ep as the ep, nh as the nh, and ip as the ip.  
Time: constant\*
- 6) makestk (sp,ts): forms a properly tagged STK word with sp as the sp and ts as the ts.  
Time: constant\*

makeund(): form a properly tagged undefined value

Time: constant

8) `makertl (nh, ip)`: forms a properly tagged return label with nh as the nh and ip as the ip.

Time: constant\*

9) `makeARcont (sl, nh, dl)`: forms a properly tagged AR control word with sl as the sl, nh as the nh, and dl as the dl.

Time: constant\*

10)

`alloc_contour (n)`: search the free list of the contour segment from the beginning to find a free block of length greater than or equal to n. If one is not found then initiate garbage collection. Otherwise carve out the first n words of the free block to make a contour. The remainder of the free block is left in the free list in the same place as the original free block. The tag of the first two words of the contour are set to LNK and CT respectively. Search the allocated contour list from the end for the proper place to link the new contour so that the allocated list is ordered by address; link the contour into the list. Set the reference count of the contour to 1. Return a pointer pointing to the 2nd word of the contour.

Time: potentially unbounded

**free\_contour (ptr):** search the free list of the contour segment from the beginning to find the proper place in the address ordered free list for inserting the contour pointed to by ptr. The contour is added to the free list, and if it is physically adjacent to either of its neighbors on the list, then it must be merged with its adjacent neighbor(s). If the LNK word of the freed contour is no longer used as a link then it is reset to an undefined value.

Time: potentially unbounded

**alloc\_stack (n):** same as alloc\_contour except using stack segment. It returns a pointer pointing to a fully tagged ces of length n.

Time: potentially unbounded

**free\_stack (ptr):** same as free\_contour, except using stack segment and the ces pointed to by ptr.

Time: potentially unbounded

There are some "hardware" procedures called by the microcode: as with basic primitives, time estimates are given.

- 1) **push (v) =**  
 $\Pi.ts + \Pi.ts + 1; \&\text{Increment processor's ts by 1.}$   
 $1st \leftarrow v; \&\text{Assign value to new stack top.}$

Time: constant\*

- 2) **pop =**  
 $\Pi.ts \leftarrow \Pi.ts - 1; \&\text{Decrement processor's ts by 1.}$

Time: constant\*

- 3)  $\text{pop}(n) = \text{II.ts} \leftarrow \text{II.ts} - n;$   $\text{@Decrement processor's ts by } n.$   
 Time: proportional to  $n:$

4)

```
incre(ptr) = 
  IF ptr is not NIL,
    IF ptr ≠ NIL       $\diamond$ increment ref count of
    THEN            $\diamond$ cell pointed to by ptr
      ptr*.rc ← ptr*.rc+1;    $\diamond$ by 1 and
      RETURN ptr*.rc;        $\diamond$ return new ref
    ELSE error
  FI
```

Time: constant

5)

```
decr(ptr) = 
  IF ptr is not NIL,  $\diamond$ decrement ref count of
  THEN            $\diamond$ cell pointed to by ptr
    ptr*.rc ← ptr*.rc-1;    $\diamond$ by 1 and
    RETURN ptr*.rc;        $\diamond$ return new ref
  ELSE error
  FI
```

Time: constant\*

6) reconstruct\_display =

```
ptr ← II.ep;  $\diamond$ Set ptr to copy of II's ep.
FOR i FROM II.nh - 1 BY -1 WHILE ptr ≠ NIL  $\diamond$ For as long as there is
DO            $\diamond$ a static chain, each successively lower DISP element is
  DISP[i] ← ptr;  $\diamond$ loaded with each successive element of the
  ptr ← ptr*.sl  $\diamond$ static chain.
OD;
```

Time: proportional to  $II.nh - 1:$

7)

is_scoped (tag)=	¢Used to determine if tag is that
RETURN( tag=PTR v tag=PRC	¢of a reference
v tag=LBL v tag=RIL);	¢countable word.

Time: constant

One pair of suffixes appears quite frequently and is explained once here. For VAL, POP, IND, and RETURN there is a choice of two suffixes: none and P. All of these instructions end up leaving or popping one value in or from the (expression) stack. For exactly what value is left or popped, see the individual instruction descriptions. The suffix gives the type of value being left or popped. If the suffix is P, then the value is of a pointer, label or procedure mode. If the suffix is empty, the value is of an integer or boolean mode.

In LSM, the presence of a P suffix indicates the possibility of a lifetime check being performed as part of the instruction execution.

In PRCCM, the presence of a P suffix indicates the possibility of some reference count updating being done as part of the instruction execution.

### 3.6.1 Instruction cycle

Both machines follow the normal fetch increment and execute cycle as follows

```
WHILE is-instruction (II.ip*) ¢Is there a next instruction?
DO inst ← II.ip*; ¢Fetch instruction.
    II.ip ← II.ip + length (inst); ¢increment ip.
    execute (inst); ¢Execute the instruction.
OD
```

where 1)is-instruction (inst) returns TRUE if and only if inst is an instruction.

2)length (inst) is the number of words that inst takes up in the instruction stream.

3)execute (inst) is the execution of the instruction inst as described by the microcode in the following section.

## 3.6.2 Execution of Instructions §

- |                 |                                                     |                                     |
|-----------------|-----------------------------------------------------|-------------------------------------|
| 1) CONST, i     | Push i, where i is an integer, TRUE, FALSE, or NIL. | Execution of Instructions §         |
| push (i);       | Push the constant itself; note that push (i);       | Push the constant itself; note that |
| Time: constant* | Time: constant*                                     | Time: constant*                     |
- § the constant must contain its own tag.
- 2) VAL, i,j Push the value starting at the jth word of the AR or contour pointed to by DISP [i].

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§ For the sake of brevity in the instruction definitions, both models assume that all variables accessed have been initialized, and therefore, neither model checks whether a used value has the correct tag. Likewise neither model checks whether a pointer value to be indirection is non-NIL. These checks could easily be added to both models at a similar cost.

a) VAL, i,j	Push an integer or boolean value.	push ((DISP[i] + j)*);	Time: constant*	
b) VALP, i, j	Push a pointer, label or procedure value. No change	push ((DISP[i] + j)*); Time: constant	Increment the reference count of the cell referred to by the pushed value. push ((DISP[i] + j)*); incrc (1st.ep); Time: constant	Time: constant
3) ADR i,j	Push a pointer pointing to the jth word of the AR or contour pointed to by DISP[i].	push (makeptr (DISP[i], DISP[i] + j)); Time: constant	The reference count of the cell referred to by the pushed pointer is incremented. push (makeptr (DISP[i], DISP[i] + j)); incrc (1st.ep); Time: constant	Time: constant
4) ADD	Replace the second stack word by the sum of the top two words and pop once.	2nd ← makeint (1st.val + 2nd.val); pop; Time: constant*	2nd ← makeint (1st.val + 2nd.val); pop; Time: constant*	Time: constant
5) EQ	Replace the second stack word by the truth value of whether the top two words are equal, and pop once.	2nd ← makebool (1st.val = 2nd. val); pop; Time: constant*	2nd ← makebool (1st.val = 2nd.val); pop; Time: constant*	Time: constant

6) NEG Replace the first word of the stack by its arithmetic negation.

1st + makeint(-1st.val);

Time: constant\*

7) POP Pop one value from the top of the stack.

a) POP Pop an integer or boolean value.

pop;

Time: constant\*

b) POPP Pop a pointer, label, or procedure value.

No change

pop;

Time: constant

Decrement the reference count of the cell referred to by the popped value.

decr (1st,ep);

pop;

Time: constant

Decrement ref count.

Time: constant\*

<p>8) IND</p> <p>a) IND      Replace the pointer on top of the stack by the value it points to. The pointed-to value is an integer or boolean value.</p>	<p>The reference count of the cell referred to by the pointer is decremented.</p>
<p>1st + 1st.ptr*;</p>	<p>      <math>\dagger</math>Pick up pointed-to value.</p>
<p>Time: constant</p>	
<p>b) INDP</p> <p>The pointed-to value is a pointer, <b>label</b>, or <b>procedure value</b>.</p> <p>No change</p>	<p>In addition to the reference count decrementation of a), the reference count of the cell referred to by the pointed-to value is incremented.</p>
<p>1st + 1st.ptr*;</p>	<p>      <math>\dagger</math>Decrement ref count.       1st + 1st.ptr*;           <math>\dagger</math>Pick up pointed-to value.</p>
<p>Time: constant</p>	<p>      Time: constant</p>
<p>9) STO</p>	<p>STO has two independent sets of suffixes.</p>
<p>a) STOV, i,j</p>	<p>1) The first suffix is either V or I. The V suffix is used when the left hand side of the implemented assignment is a variable. The I suffix is used when the left hand side is an indirection.</p> <p>2) The second suffix is either none or P. This suffix indicates the type of the value being assigned as with VAL.</p>
<p>(DISP[i] + j)* ← 1st;</p>	<p>In the case of the STOV's, the instruction has the (i,j) pair of the variable being assigned to as its two arguments. In the case of the STOI's, a pointer value pointing to the cell to be assigned to is assumed to be in the second stack position. In either case, the value to be assigned is on top of the stack. The assignment is performed, the pointer value, if any, is cleared from the stack, and the assigned value is left in the stack as the value of the assignment.</p>
<p>b) Assign an integer or boolean value to a variable.</p>	<p>(DISP[i] + j)* ← 1st;   <math>\dagger</math>Assign top stack value to                                   <math>\dagger</math>word accessed by (i,j).</p>

Time: constant\*

- b) STOVP, i,j Assign a pointer, label, or procedure value to a variable. If the to-be-erased value in the to-be-assigned word is a pointer, label or procedure values, then the reference count of the cell referred to by this value must be decremented, unless the value resides in the cell. After the assignment is done, the reference count of the cell referred to by the just-assigned value must be incremented, unless the assigned-to word is in the referred-to cell.

§ Upon allocation of a contour, all programmer accessible words are guaranteed to have either an INT, BOQ, STK, or UND tag. Therefore, because of the compile time type checking on assignments, if a word that is to be assigned a pointer, label, or procedure value has any reference countable value at all, it must be a pointer, label, or procedure value validly initialized since allocation of the contour. Thus, it is necessary for STOVR, with  $x = V$  or  $I$ , to check only whether the to-be-erased word has no PTR, LBL, or PRC tag, to be sure that the to-be-erased word has no reference countable value.

```

IF DISP[i] < 1st.ep      ;Lifetime check.
THEN lifetime_error;    ;If not OK then error;
ELSE (DISP[i]+j)* + 1st   ;else do
FI;                      ;assignment.
                                ;Get address of cell to be
                                ;assigned to.
                                ;If to-be-erased value is a
                                ;pointer, label, or procedure
                                ;value, and
THEN
    IF DISP[i] = ptr*.ep ;if value not reside in
    THEN                 ;REFERRED-to cell then,
        decrc(ptr*.ep) FI FI; ;decr ref count.
        ptr* + 1st;          ;Assign value.
        IF DISP[i] # ptr*.ep ;if assigned value not reside
        THEN                 ;in REFERRED-to cell, then
            incrc(ptr*.ep) FI; ;increment ref count.

```

Time: constant

- c) STOI Assign an integer or boolean value to an indirection. The value to be assigned is on top of the stack, and the pointer value pointing to the cell to be assigned to is below that. After the assignment, the assigned value is slid down the stack one position and the stack is popped by one. The reference count of the cell referred to by the pointer value must be decremented.

```

2nd.ptr* + 1st;          ;Assign value to ptd-to cell.
2nd + 1st;              ;Move value down stack.
pop;                   ;Pop stack once.
                                ;Assign value to ptd-to cell.
                                ;Decrement ref count.
                                ;Move value down stack.
                                ;Pop stack once.

```

Time: constant

- d) STOIP Assign a pointer, label, or procedure value to an indirection. In addition to that which is done in STOI, it is necessary to decrement the reference count of the cell referred to by the to-be-erased value and to increment the reference count of the cell referred to by the assigned value.

```

IF 2nd.ptr*.tag = PTR V 2nd.ptr*tag = LBL V
  2nd.ptr*.tag = PRC
  THEN
    IF 2nd.ep # 2nd.ptr*.ep
      THEN decrc(2nd.ptr*.ep) FI FI;
    2nd.ptr* + 1st;
    decrc(2nd.ep);
    IF 2nd.ep # 2nd.ptr*.ep
      THEN incrc(2nd.ptr*.ep) FI;
    2nd + 1st;
    pop;
  END;
END;

```

Time: constant

- 10) MAKE-LAB-STACK   Manufacture and STK word for the 1th word of the AR or contour pointed by the processor's ep.
- The STK word consists of a copy of the processor's sp and ts.

Form a ces for the use of all label constants declared in the block just entered. The ces is initialized to a copy of the currently active portion

of the ES, and the reference counts of all cells referred to by the reference countable values in the active portion of the ES must be incremented. A pointer to the ces and the processors current ts are used to make the STK word.

<pre> m ← II.ts;    <i>†Height of active portion of ES.</i> ptr ← alloc_stack(m+1); <i>†Allocate ces of size or</i>                       <i>†m+1, and</i> ptr*[0:m-1]←II.sp*[0:m-1]; <i>†Set ptr to point to it.</i>                            <i>†ces.</i> FOR i FROM 0 TO m-1 DO <i>†Incr ref count of</i>                       <i>†each cell.</i>   IF is_scoped(ptr*[i]. tag) <i>†ptd to by a ptr, la-</i>     THEN <i>†bel, or procedure</i>           <i>†in ces.</i>     increc(ptr*[i].ep)   FI OD;   II.ep*[i] ← makestk(ptr,m); <i>†Set STK word with</i>                                 <i>†sp=ptr and ts=m.</i> </pre>	<p>Time: Potentially unbounded</p>	<p>11) MAKELABEL, ip Form and push a label value using the argument ip as the ip, and the processor's nh and ep as the nh and ep.</p> <p>It is necessary to increment the reference count of the contour pointed to by the ep.</p> <pre> push(makelbl(II.ep,II.nh,ip)); <i>†Push value.</i> increc(1st.ep); <i>†Increment ref count.</i> </pre>	<p>Time: constant</p>
<p>12) GOTO</p> <p>Pick up the label value on top of the stack, and reset the processor's ip, ep, and nh to copies of those of the label value. Reset the processor's sp and ts to copies of those of the STK word of the AR now pointed to by the processor's ep. Then, reconstruct the display.</p>	<p>Time: constant</p>	<p>push(label); <i>†Push value.</i> label ← stack[0]; <i>†Get label value.</i> ip ← label.ip; <i>†Reset ip.</i> ep ← label.ep; <i>†Reset ep.</i> nh ← label.nh; <i>†Reset nh.</i> sp ← label.sp; <i>†Reset sp.</i> ts ← label.ts; <i>†Reset ts.</i> </p>	<p>Time: constant</p>

It is necessary to decrement the reference counts of the cells referred to by the values in the ES before the goto, and to increment the reference counts of the cells referred to by the values in the ES after the goto. It is also necessary to ripple reference count decrementation down the chain of contours in the normal return sequence from the processor's ep before the goto. The normal return chain is via the static link in block contours and via the return label ep in procedure contours. This rippling is required to force deallocation of contours when a non-local goto is done.

```

    II.ep + 1st.ep;      ;Save processor's old ep.
    II.ep + 1st.nh;     ;Reset II's ep, nh, and ip from
    II.nh + 1st.nh;     ;those of the label value.
    II.ip + 1st.ip;
    II.sp + (II.ep + 1)*.sp; ;Reset II's sp and ts from
    II.ts + (II.ep + 1)*.ts; ;STK word of new top AR.
    reconstruct_display;

    IF is_scoped(II.sp*[i].tag)
    THEN
        ;by values in ES
        ;except for label
        decrc(II.sp*[i].ep) FI OD; ;on top of ES.
        sptr + II.ep*[i].sp;   ;Pick up sp of STK word.
        m + II.ep*[i].cs;     ;Pick up ts of STK word.
        II.sp [0:m-1] + sptr*[0:m-1]; ;Copy ces to ES.
        II.ts + m;           ;Set II's ts.
        FOR i FROM 0 TO m-1
            ;Incr ref counts
            ;of cells referred
            ;by values in new
            ;ES.
        IF is_scoped(II.sp*[i].tag)
        THEN incrc(II.sp*[i].ep)
        FI OD;

```

```

decr(епtr);           Decr ref count of contour pointed
                      to by processor's old ep.
fin ← FALSE;          Set loop finish flag.
WHILE епtr ≠ NIL ∧   As long as eptr points to a
    ~fin              contour and not finished,
    do:
        IF епtr*[0].rc = 0   If ref count of contour is
        THEN                zero then,
            IF епtr*[1].tag = RTL  if contour has return
            THEN next ← епtr*[1].ep  label, then next
                                         is ret label ep else next
            ELSE next ← епtr*[0].sl  is static link.
        FI;
        FOR i FROM 0 BY 1 TO епtr*[-1].len -2  Decr
        DO                  ref count of cells referred
            IF is_scoped(епtr*[i].tag)  to by values in
            THEN _decr(епtr*[i].ep);  contour.
            епtr*[i] ← makeund();
        FI OD;
        IF епtr*[1].tag = STK  If inaccessible con-
        THEN                tour has a ces, then:
            ptr ← епtr*[1].sp  For each value in the
            FOR i FROM 0 BY 1 TO епtr*[1].ts - 1
            DO
                IF is_scoped(ptr*[i].tag)  ces, if it is ref coun-
                THEN _decr(ptr*[i].ep);  table, decr
                ptr*[i] ← makeund();  ref count of
                                         preferred to
            FI OD;
            free_ces(ptr)  Now free ces.
        FI;
        free_contour(епtr);  Finally free contour.
    епtr ← next  Prepare for next loop cycle.
    ELSE  Else contour's ref count > 0 and
        fin ← TRUE  halt loop
    FI OD;
reconstruct_display;

```

Time: constant

- 13) MAKEPROC, ip, n  
 Form and push a procedure value with the argument ip as the ip, n as the nh, and  
 if n > 0 then DISP[n-1] otherwise NIL as the ep.  
 It is necessary to increment the reference count of  
 the contour pointed to by the procedure value's ep.

```
IF n > 0
  THEN push(makeproc(DISP[n-1],n,ip));
  ELSE push(makeproc(NIL,0,ip));
  FI;
  ;Push value.
  FI;
```

Time: constant

- 14) RESERVE  
 Reserve one word that will be filled in later with  
 the return label.

```
push(makeint(0)); ;Push a zero to reserve a word.
```

Time: constant

Time: Potentially unbounded

With respect to the PRCCM, this instruction is a  
 NO-OP.

; ;This is an empty statement

Time: constant

- 15) ALLOC-ADJ, n  
With respect to the LSM, this instruction is a NO-OP.

Allocate a contour with  $n+2$  words (i.e., with  $n$  words for the return label and the parameters). Set its static link and nh to a copy of the ep and one less than the nh of the procedure value in the nth word of the stack (underneath all the parameter values). Leave a pointer value pointing to the contour on top of the stack. It is necessary to increment the reference count of the contour pointed to by the procedure's ep.

```
ptr ← alloc_contour (n+2);      ;tAllocate contour; set
;                                ;ptr to point to it.
ptr*.sl ← nth.ep;              ;Set static link of contour.
ptr*.nh ← nth.nh-1;            ;Set nh of contour.
push (makeptr (ptr,ptr));       ;Push pointer to contour.
incr (nth.ep);                 ;Incr ref count.
```

Time: constant

Time: constant + T(alloc\_contour)

- 16) PASS, n  
With respect to the LSM, this instruction is a NO-OP. There is no need to physically pass the parameters because the mes used to evaluate the actual parameters becomes the formal parameter AR for the called procedure.

Pass the  $n$  words below the top of the stack to the  $n$  consecutive words of the contour pointed to by the pointer value on top of the stack, starting at the word of the contour indexed by 2. Move the pointer value in the top of the stack  $n$  words down and pop the stack by  $n$ . Since the  $n$  parameter values are moved to the contour and are then popped from the stack, there is no net change in reference counts.

```
1st•ep*: [2:n+1] ←      ; Move the n words from the stack  
    II.sp*: [II.ts-n-1:II.ts-2]; ; to the contour.  
    (n+1)th ← 1st;           ; Slide the pointer value down.  
    pop(n);                 ; Pop by n.
```

Time: proportional to n

Time: constant

- 17) CALL, n  
 Save the processor's current ip (which has already been incremented) and nh in the return label subcell of the formal parameter AR being constructed. Reset the processor's ip and nh from the called procedure value. Save the processor's ep in the dynamic link of the AR and reset the processor's ep to point to the AR. Finally, reconstruct the display. The argument n of the instruction gives the size of the return label and parameter part of the AR being constructed out of the top n+1 words of the topmost mes. Thus, the return label cell is in the nth word of the stack and the procedure value is in the (n+1)th word of the stack. The (n+1)th word will become the static link/dynamic link cell of the AR.

```

nh ← makeRTL(II.nh, II.ip);    /*Save processor's ep, nh
                                *ip and nh in return label.
II.ip←(n+1)th.ip;   /*Reset the processor's ip from
                                *procedure value.
II.nh←(n+1)th.nh;  /*The processor's new nh is
                                *that of the procedure.
(n+1)th ← makeARcont(
                                *(n+1)th.s1, (n+1).th.nh-1,
                                *into an AR control
II.ep );               /*word.

II.ep-II.sp+II.ts-(n+1) /*Reset II's ep to point to
                                *AR control word.
reconstruct_display;
```

Time: constant + T(reconstruct\_display)

- Save the processor's current ip (which has already been incremented), nh, and ep in the return label cell of the formal parameter contour. Reset the processor's ip and nh from those of the called procedure value. The processor's ep is reset to point to the new contour. The contour is pointed to by the top of the stack and the procedure value is in the second stack word. These two values are popped and the display is reconstructed. In the course of the instruction execution, there is no net change to the reference count of the contour, but the reference count of the cell pointed to by the popped procedure value's ep must be decremented. (Note that the argument n is never used in the PRCCM.)

```

1st.ep*[1] ← makertl(   /*Save processor's ep,
                                *II.ep, II.nh, II.ip);
II.ip ← 2nd.ip;      /*Reset II's ip from procedure
                                *II's nh
II.nh ← 2nd.nh;     /*nh of procedure.
II.ep ← 1st.ep;      /*Reset II's ep to new
                                *contour.
decr(2nd.ep);        /*Decrement ref count of cell
                                *REFERRED to by procedure value.
pop(2);              /*Pop pointer-to-contour and procedure.
reconstruct_display;
```

Time: constant + T(reconstruct\_display)

- 18) RETURN, n, j<sub>1</sub>, ..., j<sub>n</sub>  
 Return from procedure call.  
 Pick up the value to be returned and decrement the processor's ts so that the top of the stack is just beneath the topmost AR (i.e., II.sp + II.ts = II.ep). Reset the processor's ip and nh

Reset the processor's ip, nh, and ep from the return label of the contour pointed to by the processor's ep. It is not necessary to move the returned value because it is already in its proper

from the return label of the AR. Then reconstruct the display. Finally push the returned value into the stack. Rather than popping and pushing, the stack will be popped to where it would be after the push and the returned value is merely assigned to the top of the stack. (The arguments of the instruction are ignored in LSM.)

place in the contiguous ES. The exited contour's reference count is decremented and if the reference count goes to zero, the contour must be freed. Before freeing it, it is necessary to decrement the reference count of all cells referred to by any pointers, labels, or procedures in the contour (in this case, it is not necessary to check that the referred-to cell is not the same contour since it is already being returned to the free list). Since the contour is that of a procedure, it has no STK word, so there is no ces to free. It is also necessary to reset to undefined values all words containing pointer, label, or procedure values, the return label, or the static link. The purpose of the argument list is to indicate that there are  $n$  words containing reference countable data and that their displacements into the contour are  $j_1, \dots, j_n$ . Finally the display is reconstructed.

a) RETURN, n, j<sub>1</sub>, ..., j<sub>n</sub>      The returned value is an integer or boolean value.  
 val + 1st;      ;Save returned value.

```

ptr + II.ep;    ;Save processor's current ep pointing
                ;to exited contour.
II.ts + II.ep-II.sp + 1; ;Drop all but one word of AR.
II.ip + (II.ep + 1)*.ip; ;Reset processor's ip & nh
II.nh + (II.ep + 1)*.nh; ;from return label
II.ep + II.ep*.dl; ;Reset processor's ep from dl.
reconstruct_display;
1st + val; ;Put returned value in top of stack.

ptr* [1].ip; ;Set ret label to undefined
ptr* [1].nh; ;Set ref count to zero
ptr* [1].ep; ;Set word to undefined.
ptr* [1] + makeund(); ;Decr contour's ref count and
IF decrc(ptr) = 0 ;if ref count goes to zero then For
THEN ;FOR i FROM 1 BY 1 TO n ;each ref countable
      DO ;decr(ptr*[j].ep); ;word in the contour: Decr
          ptr*[j] + makeund(); ;ref count of ptd-to cell.
          OD; ;Set word to undefined.

decr((ptr*[0].sl)); ;Decr ref count of contour
ptr*[0] + makeund(); ;ptd to by static link, and
free_contour(prr); ;set sl word to undefined,
ELSE ;else exited contour has ref count > 0...
      incrc(II.ep) ;There is an additional pointer
      RL; ;pointing to contour that processor's ep now
            ;points to.

reconstruct_display;
```

Time: constant + T(reconstruct\_display)

Time: constant + proportional to n + T(reconstruct\_
 display) + T(free\_contour)

b) RETURNP, n, j<sub>1</sub>, ..., j<sub>n</sub>      The returned value is a pointer, label, or procedure.  
 A lifetime check must be performed.

```

IF 1st.ep ≥ II.ep*.dl ;Lifetime check; if not
THEN lifetime_error FI; ;OK then error.
      val + 1st;
      II.ts + II.ep-II.sp + 1 ;above
```

;Exactly as for a) above

```

 $\Pi.ip \leftarrow (\Pi.ep + 1)^{*}.ip;$ 
 $\Pi.nh \leftarrow (\Pi.ep + 1)^{*}.nh;$ 
 $\Pi.ep \leftarrow \Pi.ep^{*}.dl;$ 
reconstruct_display;
1st  $\leftarrow$  val;

```

Time: constant + T(reconstruct\_display)

## 19) ALLOC-ENTER, n

Block entry.

Push an AR with n words for the identifiers.  
Set its static link and dynamic link to copies of the processor's current ep, and reset the processor's ep to point to the new AR. Increment the processor's nh processor's new ep into the new top display element.

```

push (makeARcont(
     $\Pi.ep, \Pi.nh, \Pi.ep)$ );      /*Push an AR control word
                                *with  $\Pi.ep$  as sl and dl.
 $\Pi.ep \leftarrow \Pi.sp + \Pi.ts - 1;$    /*Set processor's ep to point
                                *to AR control word.
 $\Pi.ts \leftarrow \Pi.ts + n;$            /*Push in n blank words.
DISP[nh]  $\leftarrow \Pi.ep;$           /*Copy processor's ep into new
                                *display element.
 $\Pi.nh \leftarrow \Pi.nh + 1;$           /*Increment processor's nh.

```

Time: constant

Time: as in a)

19) ALLOC-ENTER, n

Allocate a contour with n words for identifiers.  
Set its static link to a copy of the processor's current ep and reset the processor's ep to point to the new contour. Increment the processor's nh by 1 and copy its new ep into the new top display element. There is no net change in reference count to either the old or the new contour.

```

ptr  $\leftarrow$  alloc_contour (n+2);    /*Allocate contour of
                                *size n+2;      set ptr to point to it.
ptr^.sl  $\leftarrow \Pi.ep;$           /*Set sl of contour.
ptr^.nh  $\leftarrow \Pi.nh;$           /*Set nh of contour.
 $\Pi.ep \leftarrow \Pi.ep +$         /*Reset processor's ep.
DISP[nh]  $\leftarrow$  ptr;            /*Set new DISP element to point
                                *to new contour.
 $\Pi.nh \leftarrow \Pi.nh + 1;$           /*Increment processor's nh.

```

Time: constant + T(alloc\_contour)

20) EXIT, n, j<sub>1</sub>, ..., j<sub>n</sub> Exit from a block.

EXIT has two independent sets of suffixes:  
 1) The first, either L or none, is used to indicate whether or not the block declares at least one label constant.

2) The second, either P or none, is used to indicate the type of the value returned by the block, as with VAL.

In LSM, only the second suffix is significant, as it indicates whether or not a lifetime check will be needed on the value being returned.

Pick up the value to be returned and decrement the processor's ts so that the top of the stack is where it would be if the top AR were popped and then the returned value were pushed. Reset the processor's ep from the dynamic link of the popped AR. Decrement the processor's nh, thus popping the display by 1. Finally, assign the returned value to the top of the stack (The arguments of the instruction are ignored in LSM.).

In PRCCM, only the first suffix is significant, as it is used to indicate whether or not the contour being exited has an STK word and thus a ces. In PRCCM, the first suffix is not significant because the value being returned is properly in position on top of the ES, and no reference count updating need be performed as a result of this value.

Reset the processor's ep from the static link of the exited contour. The exited contour's reference count is decremented. If the reference count goes to zero, using the arguments as in RETURN, the reference counts of cells referred to by pointer, label, and procedure values in the exited contour are decremented. Also the words containing reference countable values and the static link are reset to undefined. If the contour has a ces, then the reference counts of the cells referred to by its reference countable values are decremented and the ces is freed. Finally the exited contour is freed.

If, however, the exited contour's reference count has not gone to zero, then the reference count of the contour the processor's ep now points to is incremented. In either case, the processor's nh is decremented by 1 to pop the display by 1.

a) EXIT, n, j<sub>1</sub>, ..., j<sub>n</sub> The returned value is an integer or boolean value, and the block declares no labels.

```
val + 1st;    ;Save returned value.
Pi.ts + Pi.ep - Pi.sp + 1; ;Pop all but one word of AR.
Pi.ep + Pi.ep*.dl; ;Reset processor's ep from dl.
Pi_nh + Pi_nh-1; ;Decrement processor's nh.
```

ptr + Pi.ep; ;Save processor's ep pointing to
;Exited contour.

Pi.ep + ptr.sl; ;Reset processor's ep from sl.
If decr(ptr) = 0 ;Decr contour's ref count and

---

1st  $\leftarrow$  val;       $\diamond$ Move returned value to top of stack.

```

THEN    $\diamond$ if ref count goes to zero then for
FOR i FROM 1 BY 1 TO n  $\diamond$ each ref countable
DO    $\diamond$ word in exited contour: Decr ref
    decrec(ptr*[j_i].ep);  $\diamond$ count of ptd-to cell.
    ptr*[j_i]  $\leftarrow$  makeund();  $\diamond$ Set word to undefined.
    OD;
    ptr*[0]  $\leftarrow$  makeund();  $\diamond$ Set s1 word to undefined.
    free_contour(ptr);  $\diamond$ Free contour.
ELSE_  $\diamond$ Else exited contour has ref count >0...
    incrc(II.ep)  $\diamond$ There is an additional pointer
    incrc(II.ep)  $\diamond$ pointing to contour that II's ep
    FI;  $\diamond$ now points to.
    II.nh  $\leftarrow$  II.nh-1;  $\diamond$ Decrement processor's nh.

```

Time: constant + proportional to  $n + T(\text{free\_contour})$

---

b) EXITP, n, j<sub>1</sub>, ..., j<sub>n</sub>      The returned value is a pointer, label, or procedure value, and the block declares no labels.

A lifetime check is required.

```

IF 1st.ep  $\geq$  II.ep $\&$  dl  $\diamond$ Lifetime check; if not
THEN lifetime_error FI;  $\diamond$ OK then error else...
val  $\leftarrow$  1st;
II.ts  $\leftarrow$  II.ep-II.sp + 1;
II.ep  $\leftarrow$  II.ep $\&$  dl;
II.nh  $\leftarrow$  II.nh-1;
1st  $\leftarrow$  val;

```

Time: as in a)

c) EXTL, n, j , ..., j

The returned value is an integer or boolean value, and the block declares at least one label constant.

If the exited contour is to be freed, its ces must be scanned for reference countable values. After decrementing reference counts and resetting the reference countable values to undefined, the ces is freed.

¶Exactly as in a) above.

```

ptr ← II.ep;    ¶As in a) above except as noted.
II.ep ← ptr.s1;
IF decrc(ptr) = 0
THEN
  FOR i FROM 1 BY 1 TO n
  DO
    decrc(ptr*[i].ep);
    ptr*[i] ← makeund();
  OD;
  ptr*[0] ← makeund();
  sptr ← ptr*[1].sp;    ¶Get pointer to ces.
  FOR i FROM 0 BY 1 TO ptr*[1].ts - 1    ¶For each
  DO
    cref countable value in ces,
    IF is_scoped(sptr*[i].tag)    ¶decrement ref
    THEN
      decrecm(sptr*[i].tag)    ¶count and set to
      undefined.
      sptr*[i] ← makeund();
    FI OD;
    free_ces(sptr);    ¶Free ces.
    free_contour(ptr);
  ELSE
    increcm(II.ep)
  FI;
  II.nh ← II.nh - 1;
Time: constant + proportional to n + T(free-contour)
      + potentially unbounded to scan ces + T(free_ces)

```

Time: constant

- d) EXITLP, n, j<sub>1</sub>, ..., j<sub>n</sub>      The returned value is a pointer, label, or procedure value, and the block declares at least one label constant.

      ¢Exactly as in b) above.

Time: constant

      | ¢Exactly as in c) above.

Time: as in c)

- 21) HOPTO, ip      Reset the processor's ip to the ip argument.

      II.ip ← ip

Time: constant\*

Time: constant\*

- 22) HOPTOF, ip      Pick up and pop the top word of the stack. If it is FALSE, reset the processor's ip to the ip argument.

val ← 1st;      ¢Pick up top word.

pop;      ¢Pop one word.

IF val = FALSE      ¢If word is FALSE, do

THEN II.ip ← ip FI;      ¢HOPTO.

Time: constant\*

      | val ← 1st;      ¢Pick up top word.

      | pop;      ¢Pop one word.

      | IF val = FALSE      ¢If word is FALSE, do

      | THEN II.ip ← ip FI;      ¢HOPTO.

Time: constant\*