

Lncosh-Based Adaptive Control Algorithm for Single-Stage SPV Grid-Interfaced System



Mohammad Nair Aalam, Majid Jamil, and Ikhtlaq Hussain

Abstract This work proposes the use of a logarithmic hyperbolic cosine (Lncosh)-based adaptive algorithm for a single-stage solar photovoltaic (SPV) grid-interfaced system. The design incorporates a VSC coupled SPV array to provide active power along with reactive power compensation. It also serves to offer load balancing, reduction of harmonics, and power factor correction. A maximum power point (MPP) extraction method based on the incremental conductance (InC) technique, integrated with the VSC control algorithm, is employed to ensure that the SPV array operates at the MPP under varying levels of irradiation. The Lncosh-based algorithm simultaneously offers benefits of both the least mean squares (LMS) and sign-error LMS algorithms, providing faster convergence without sacrificing the mean-square-error (MSE) performance. The proposed configuration is implemented for simulation in MATLAB with the Simulink environment; the steady-state and the dynamic behavior is observed and verified to be well within recommended limits.

Keywords SPV · Lncosh · LMS · Power quality · Power electronics · MPPT

1 Introduction

The last few decades have seen the decay of fossil-fuel-based energy sources as the demand for energy consumption continued to grow. Along with an increasing concern for the global environment, this has motivated the search and development of renewable alternatives. Among these alternatives, there has been widescale adoption of technologies such as SPV systems in the form of distributed energy resources (DERs) [1].

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K. Namrata et al. (eds.), *Smart Energy and Advancement in Power Technologies*,
Lecture Notes in Electrical Engineering 927,
https://doi.org/10.1007/978-981-19-4975-3_50

There is a host of difficulties to be tackled when interfacing SPV systems with the utility grid. The majority of these include poor reliability, voltage instability, and degraded power quality [2]. SPV systems may be integrated with the utility grid in either single-stage or double-stage topologies. The double-stage topology performs MPP extraction in the first stage and output power control in the second stage, while the single-stage topology achieves both functions in one stage. Several MPPT techniques have been discussed in [3].

The integration of SPV arrays with the utility grid is done with the help of power electronic converters, mainly VSCs [4]. The converter can be controlled to offer load balancing, reduction of harmonics, power factor correction, and/or zero voltage regulation (ZVR). Numerous algorithms have been reported for the control of VSC coupled SPV systems [5–8].

The use of algorithms based on adaptive filtering theory, for unfamiliar systems, has confirmed the capability of the theory to track variations in the properties of such systems [2]. The algorithm parameters are self-adjusted in response to changes in the surrounding environment, ensuring that the behavior of the system remains appropriately in order. The least mean squares (LMS) algorithm [9] and sign-error LMS (SELMS) algorithm are two such algorithms that have been used for the control of VSC coupled SPV [10]. The LMS algorithm offers faster convergence compared to the SELMS algorithm [11]; however, the SELMS algorithm offers better mean-square-error (MSE) performance.

Recently a Lncosh-based algorithm, consisting of a cost function employing the natural logarithm of the hyperbolic cosine function, has been proposed in [12]. The Lncosh-based algorithm provides convergence characteristics akin to the LMS algorithm and robustness analogous to the SELMS algorithm [13]. It offers advantages of both the algorithms, performing similar to the LMS algorithm for errors of small magnitude and like the SELMS algorithm for errors of large magnitude [14].

In this work, the proposed Lncosh-based algorithm is implemented for the grid integration of a VSC coupled SPV array. The proposed configuration is implemented for simulation in MATLAB with the Simulink environment for Unity Power Factor (UPF) correction mode of operation as well as load balancing and reduction of harmonics.

1.1 Configuration of Proposed System

A composite circuit/block diagram representing the proposed configuration is shown in Fig. 1. The procedure for the calculation of the system parameters is given in [15]. The parameters for the proposed configuration are given in the Appendix.

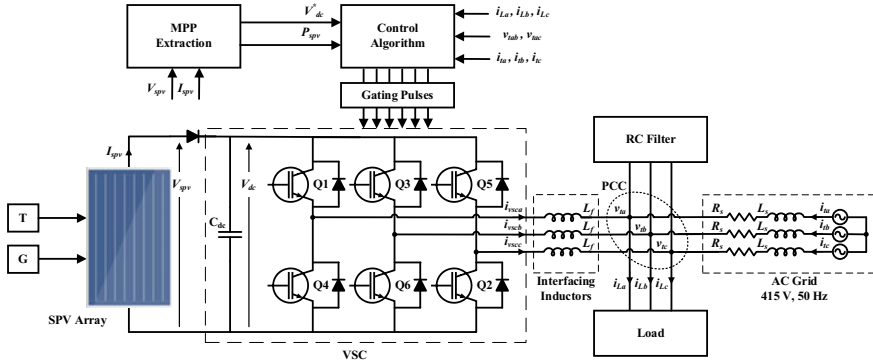


Fig. 1 Configuration of proposed system

2 Control Algorithm

The proposed control algorithm is developed for a single-stage three-phase configuration and consists of two parts: MPP Extraction Control and VSC Control.

The following signals are sensed: SPV array current and voltage (I_{spv} , V_{spv}), grid line-voltages at the PCC (v_{tab} , v_{tbc}), VSC DC-link voltage (V_{dc}), three-phase load currents (i_{La} , i_{Lb} , i_{Lc}), and three-phase grid currents (i_{ta} , i_{tb} , i_{tc}).

2.1 MPP Extraction Control

The goal of the MPP Extraction control is to ensure that the SPV array operates at the MPP under varying levels of irradiation, and provide a VSC DC-link reference-voltage (V_{dc}^*) for the sensed VSC DC-link voltage (V_{dc}) to track.

The output power of the SPV array is proportional to the amount of incident radiation. As a result, peak power extraction is needed to increase performance. An InC-based MPP extraction method is used to acquire the desired power maxima. The method uses incremental changes in V_{spv} and I_{spv} to estimate the result of a voltage shift.

In case of the proposed SPV system, the V_{dc} equals the V_{spv} . At steady-state, the V_{dc} equals the SPV array voltage at maximum power (V_{MPP}). The InC-based MPP extraction method, besides being high-speed and accurate under a variety of environmental conditions, is easy to implement.

2.2 VSC Control

The VSC control algorithm focuses on the generation of a series of trigger signals for the appropriate switching of the converter switches. The algorithm may also offer ancillary services such as load balancing, reduction of harmonics, improved PCC voltage regulation, and power factor correction.

A Lncosh-based converter control algorithm, shown in Fig. 2, is proposed [12].

The PCC phase voltages (v_{ta} , v_{tb} , v_{tc}) are evaluated from the grid line-voltages (v_{tab} , v_{tbc}) sensed at the PCC, using (1).

$$v_{ta} = \frac{2v_{tab} + v_{tbc}}{3}, v_{tb} = \frac{-v_{tab} + v_{tbc}}{3}, v_{tc} = \frac{-v_{tab} - 2v_{tbc}}{3} \tag{1}$$

From the phase voltage measurements, the unit templates in-phase (x_{pa} , x_{pb} , x_{pc}) with the grid voltage are estimated using (2)

$$x_{pa} = \frac{v_{ta}}{V_{PCC}}, x_{pb} = \frac{v_{tb}}{V_{PCC}}, x_{pc} = \frac{v_{tc}}{V_{PCC}} \tag{2}$$

where V_{PCC} is the magnitude of the PCC terminal voltage, given using (3).

$$V_{PCC} = 0.816\sqrt{(v_{ta}^2 + v_{tb}^2 + v_{tc}^2)} \tag{3}$$

The unit templates in-quadrature (x_{qa} , x_{qb} , x_{qc}) with the grid voltage are derived from x_{pa} , x_{pb} , x_{pc} using (4).

$$x_{qa} = \frac{-x_{pb} + x_{pc}}{\sqrt{3}}, x_{qb} = \frac{3x_{pa} + x_{pb} - x_{pc}}{2\sqrt{3}}, x_{qc} = \frac{-3x_{pa} + x_{pb} - x_{pc}}{2\sqrt{3}} \tag{4}$$

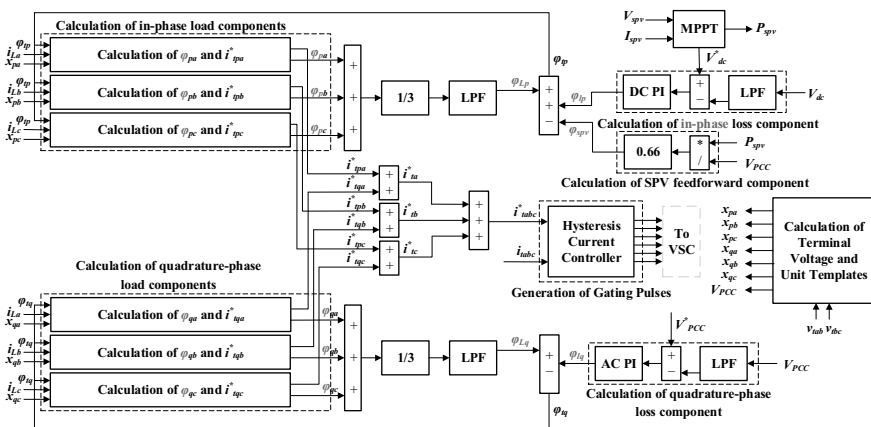


Fig. 2 Lncosh-based control algorithm for voltage source converter control

The VSC DC-link voltage (V_{dc}) is controlled by a P.I. controller in such a way that it tracks the VSC DC-link reference-voltage (V_{dc}^r). The VSC DC-link voltage error (V_{dce}) is determined for the i th sampling instant using (5)

$$V_{dce}(i) = V_{dc}^r(i) - V_{dc}(i) \quad (5)$$

and supplied to the DC-P.I. controller. The DC-P.I. controller provides the in-phase loss component (φ_{ip}) required to maintain V_{dc} at V_{dc}^r . φ_{ip} is estimated for the i th sampling instant using (6).

$$\varphi_{ip}(i+1) = \varphi_{ip}(i) + \lambda_{DCp}(V_{dce}(i+1) - V_{dce}(i)) + \lambda_{DCi}(i+1)V_{dce}(i+1) \quad (6)$$

where λ_{DCp} and λ_{DCi} are respectively the values of proportional gain constant and integral gain constant for the DC-P.I. controller.

Similarly, another P.I. controller, termed the AC-P.I. controller, regulates the terminal voltage (V_{PCC}) so that it tracks the reference terminal voltage (V_{PCC}^r) (set to $0.816 * 415 = 340$ V). The terminal voltage error (V_{PCCe}) is given using (7)

$$V_{PCCe}(i) = V_{PCC}^r(i) - V_{PCC}(i) \quad (7)$$

and acts as input to the AC-P.I. controller. The AC-P.I. controller outputs the quadrature-phase loss component (φ_{iq}) which is used to regulate V_{PCC} at V_{PCC}^r . φ_{iq} is estimated for the i th sampling instant using (8).

$$\varphi_{iq}(i+1) = \varphi_{iq}(i) + \lambda_{ACp}(V_{PCCe}(i+1) - V_{PCCe}(i)) + \lambda_{ACi}(i+1)V_{PCCe}(i+1) \quad (8)$$

where λ_{ACp} and λ_{ACi} are respectively the values of proportional gain constant and integral gain constant for the AC-P.I. controller.

To improve the performance of the SPV system under dynamic conditions, the control algorithm includes an SPV feed-forward component. The SPV feed-forward component (φ_{spv}) for the i th sampling instant is given using (9)

$$\varphi_{spv}(i) = 0.66 * \frac{P_{spv}(i)}{V_{PCC}} \quad (9)$$

where $P_{spv}(i)$ is the SPV array power output for the i th sampling instant.

The in-phase load component of phase- a (φ_{pa}), for the i th sampling instant, is evaluated from the proposed Lncosh-based algorithm using (10)

$$\varphi_{pa}(i+1) = \varphi_{pa}(i) + \tau_s \tanh[\eta_{pa}(i)]x_{pa}(i) - \rho \frac{\text{sgn}[\varphi_{pa}(i)]}{1 + \varepsilon|\varphi_{pa}(i)|} \quad (10)$$

where τ_s is the step-size, ρ is the zero-attractor coefficient, and ε^{-l} is the shrinkage coefficient [10, 12]. $\eta_{pa}(i)$ is the error of the in-phase current component of phase- a , given for the i th sampling instant using (11)

$$\eta_{pa}(i) = i_{La}(i) - x_{pa}(i)\varphi_{pa}(i) \quad (11)$$

where $i_{La}(i)$ is the sensed load current in phase- a for the i th sampling instant.

Correspondingly, the in-phase load components of phase- b (φ_{pb}) and phase- c (φ_{pc}), for the i th sampling instant, are estimated using (10) for the phases b and c .

The quadrature-phase load component of phase- a (φ_{qa}), for the i th sampling instant, is evaluated using (12)

$$\varphi_{qa}(i + 1) = \varphi_{qa}(i) + \tau_s \tanh[\eta_{qa}(i)]x_{qa}(i) - \rho \frac{\text{sgn}[\varphi_{qa}(i)]}{1 + \varepsilon|\varphi_{qa}(i)|} \quad (12)$$

where τ_s is the step-size, ρ is the zero-attractor coefficient, and ε^{-l} is the shrinkage coefficient [10, 12]. $\eta_{qa}(i)$ is the error of the quadrature-phase current component of phase- a , given for the i th sampling instant using (13).

$$\eta_{qa}(i) = i_{La}(i) - x_{qa}(i)\varphi_{qa}(i) \quad (13)$$

Correspondingly, the quadrature-phase load components of phase- b (φ_{qb}) and phase- c (φ_{qc}), for the i th sampling instant, are estimated using (12) for the phases b and c .

The total in-phase grid reference-current component (φ_{tp}) is given using (14)

$$\varphi_{tp} = \varphi_{Lp} + \varphi_{lp} - \varphi_{spv} \quad (14)$$

where the average in-phase load component (φ_{Lp}) is given using (15).

$$\varphi_{Lp} = \frac{(\varphi_{pa} + \varphi_{pb} + \varphi_{pc})}{3} \quad (15)$$

Similarly, the total quadrature-phase grid reference-current component (φ_{tq}) is given using (16)

$$\varphi_{tq} = \varphi_{lq} - \varphi_{Lq} \quad (16)$$

where the average quadrature-phase load component (φ_{Lq}) is given using (17).

$$\varphi_{Lq} = \frac{(\varphi_{qa} + \varphi_{qb} + \varphi_{qc})}{3} \quad (17)$$

The in-phase reference-currents (i_{tpa}^* , i_{tpb}^* , i_{tpc}^*) and the quadrature-phase reference-currents (i_{tqa}^* , i_{tqb}^* , i_{tqc}^*) are estimated using (18) and (19), respectively.

$$i_{tpa}^* = \varphi_{tp} x_{pa}, i_{tpb}^* = \varphi_{tp} x_{pb}, i_{tpc}^* = \varphi_{tp} x_{pc} \quad (18)$$

$$i_{tqa}^* = \varphi_{sq} x_{qa}, i_{tqb}^* = \varphi_{sq} x_{qb}, i_{tqc}^* = \varphi_{sq} x_{qc} \quad (19)$$

The summation of i_{tpa}^* , i_{tpb}^* , i_{tpc}^* and i_{tqa}^* , i_{tqb}^* , i_{tqc}^* for each phase respectively gives the three-phase total reference-currents (i_{ta}^* , i_{tb}^* , i_{tc}^*).

$$i_{ta}^* = i_{tpa}^* + i_{tqa}^*, i_{tb}^* = i_{tpb}^* + i_{tqb}^*, i_{tc}^* = i_{tpc}^* + i_{tqc}^* \quad (20)$$

For the generation of VSC trigger signals, a hysteresis current controller (HCC) is brought into use. The HCC takes as input a 3×1 vector of error signals generated from calculating the difference between i_{ta}^* , i_{tb}^* , i_{tc}^* and i_{ta} , i_{tb} , i_{tc} for each individual phase, respectively and generates the appropriate trigger signals.

3 Simulation Results and Discussion

The proposed configuration is implemented for simulation in MATLAB with the Simulink environment for UPF correction mode of operation, and its steady-state and dynamic behavior is verified under various conditions.

3.1 Steady-State Behavior Under Non-linear Load

The behavior of the proposed configuration, under steady-state, with a non-linear load connected at the PCC is simulated, and the results are shown in Fig. 3. V_{dc} is sustained at the V_{MPP} of the SPV array. The active power output (P_t) of the grid is negative as the VSC is supplying some of the active power generated by the SPV array (P_{spv}) back to the grid. Since the VSC meets the load's reactive power requirements, the grid's reactive power output (Q_t) is zero. The power factor at the grid side is corrected to unity and the grid side total harmonic distortion (THD) values are in agreement with the IEEE-519 standard [16].

3.2 Dynamic Behavior Under Variable Irradiance

Figure 4 illustrates the dynamic behavior of the configuration for a simulated change in irradiance G from 1000–600 W/m² at 0.6 s. As G is reduced, the power output

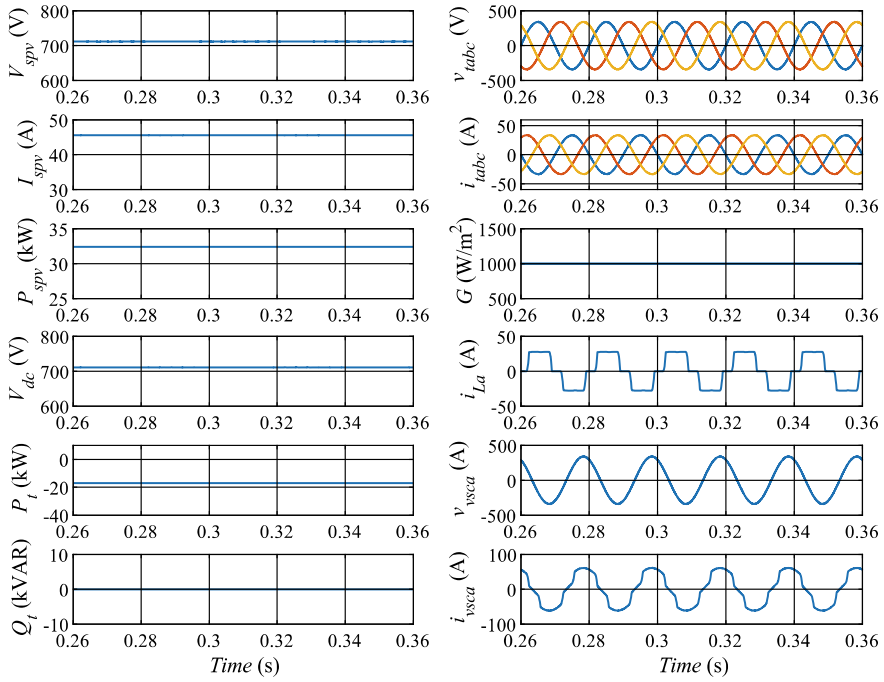


Fig. 3 Steady-state behavior under non-linear load

P_{spv} of the SPV array drops; as such P_t rises to a value, less negative than earlier. The output current I_{spv} of the SPV array and the grid currents i_{tabc} also decrease. The power factor at the grid side is maintained at unity and the grid side THD values comply with [16].

3.3 Dynamic Behavior Under Imbalanced Load

At 0.35 s, the load connected at phase *a* is disconnected to simulate an imbalance in load, and the behavior of the configuration is observed. The findings are depicted in Fig. 5. V_{dc} is maintained as per the MPP extraction control. The further decrease in the value of P_t can be accounted for by the fact that more of the SPV array power P_{spv} is available to be transferred to the grid side after the loss of load. The voltage and the current waveforms on the grid side are sinusoidal with unity power factor, showing the proper operation of the control algorithm for load balancing. The THD values on the grid side are again observed to be in compliance with the IEEE-519 standard.

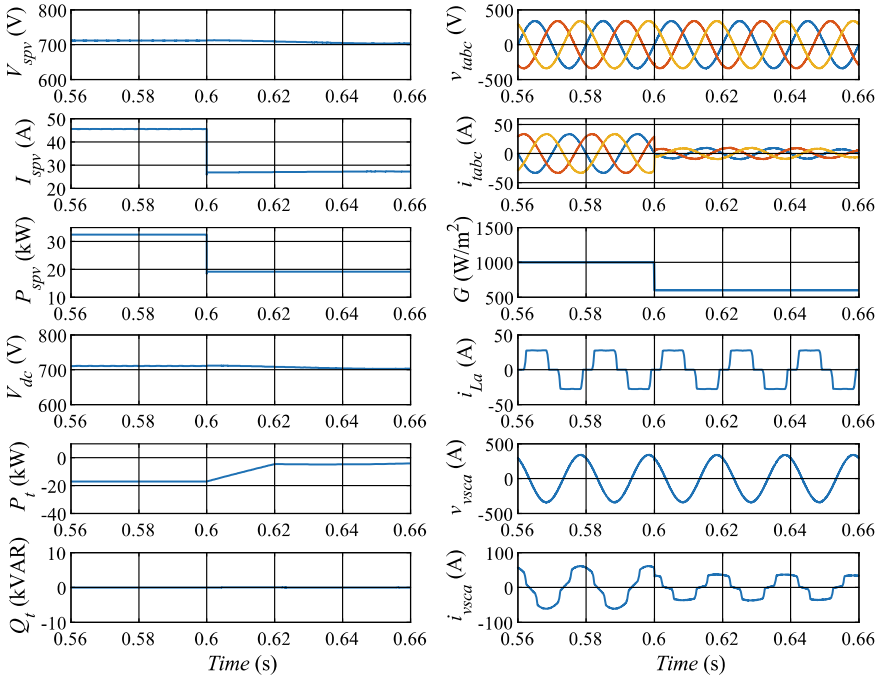


Fig. 4 Dynamic behavior of the configuration for change in solar irradiance

3.4 Comparison with LMS and SELMS Algorithm

In Fig. 6, a comparison of the average in-phase load component (φ_{Lp}) under a simulated imbalance in load, from 0.25–0.45 s, is shown for the proposed Lncosh-based algorithm, the LMS algorithm, and the SELMS algorithm. The convergence characteristics of the Lncosh-based algorithm are similar to the LMS algorithm and much faster than the SELMS algorithm. The oscillations in φ_{Lp} for the proposed algorithm are observed to be less than that in the case of LMS and comparable to that for the SELMS algorithms. The THD values for the grid current are 1.60%, 2.87% and 1.53%, respectively, for the proposed Lncosh-based algorithm, the LMS algorithm, and the SELMS algorithm.

4 Conclusion

The Lncosh-based adaptive algorithm has been described for the grid interfacing of a single-stage three-phase SPV system. The proposed configuration is implemented for simulation in MATLAB with the Simulink environment. The steady-state behavior and the dynamic behavior of the configuration under conditions of variable irradiance

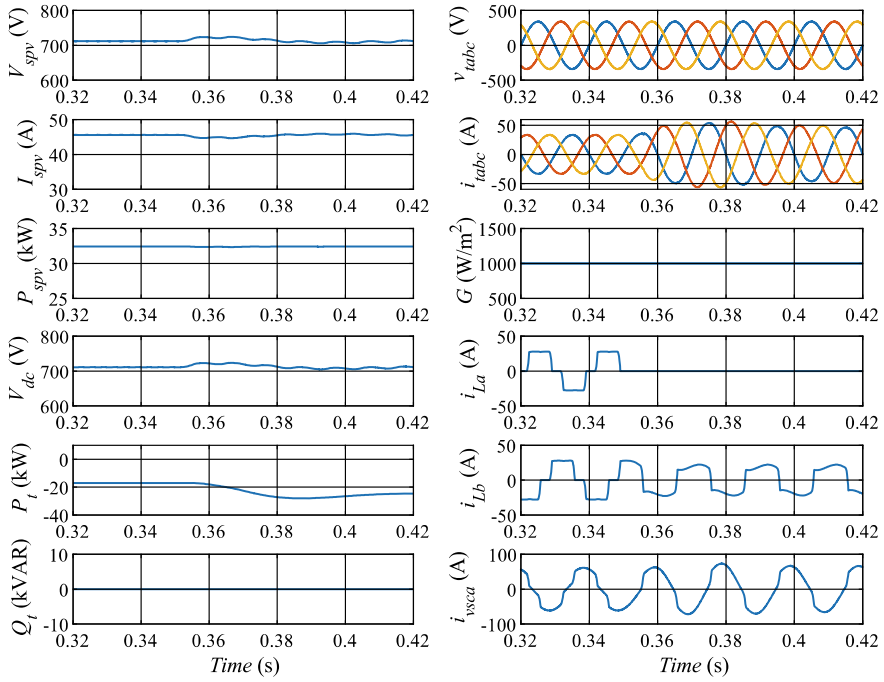


Fig. 5 Dynamic behavior of the configuration for imbalance in load

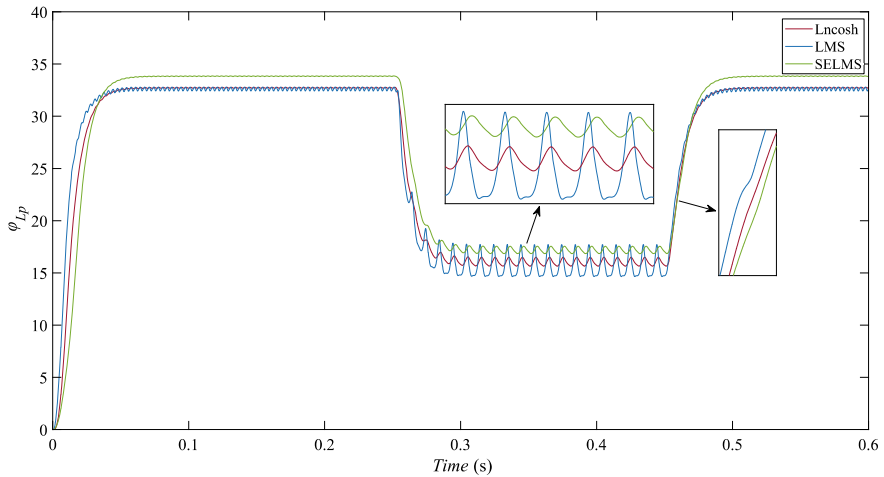


Fig. 6 Comparison of the Lncosh-based algorithm against LMS and SELMS algorithms

and load imbalance are shown to be reliable. The tracking of the MPP is proper under varying levels of irradiance. Moreover, the response of the proposed control algorithm for a simulated imbalance in load is observed to be better than conventional LMS and SELMS algorithms. The results show correspondence with IEEE-519 standards.

Appendix: System Parameters

Grid voltage, $V_{LL} = 415$ V (rms); SPV array parameters: $V_{MPP} = 710$ V, $I_{MPP} = 46$ A, $P_{MPP} = 32$ kW (all parameters at $G = 1000$ W/m²); VSC DC-link voltage, $V_{dc} = 700$ V; interfacing inductance, $L_f = 2 * 10^{-3}$ H; DC-link capacitance, $C_{dc} = 8 * 10^{-3}$ F; sampling time, $T_s = 1 * 10^{-6}$ s; ripple filter parameters, $R_{rf} = 5\Omega$, $C_{rf} = 1 * 10^{-5}$ F; AC-P.I. controller parameters, $\lambda_{ACp} = 0.7$ and $\lambda_{ACi} = 0.5$; DC-P.I. controller parameters, $\lambda_{DCp} = 0.7$ and $\lambda_{DCi} = 0.5$; load parameters, 3-phase diode-bridge rectifier feeding a series RL load of $20\ \Omega$ and $1 * 10^{-1}$ H; Hysteresis bandwidth = 0.01.

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