Improved Window Decoding of Spatially Coupled LDPC Codes



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Abstract In this paper, we analyze the performance of window decoding (WD) scheme for spatially coupled low-density parity-check (SC-LDPC) codes, and an improved WD scheme is proposed. Compared with the belief propagation (BP) decoding algorithm, window decoding has the advantages of low decoding latency, low decoding complexity, and small memory. But, the performance of WD is not as good as that of BP. It is found that the error bit number **err** of the target symbol does not decrease monotonously with the iteration progresses. This means that the target symbol likelihoods generated when decoding is terminated may not be the best choice to make a decision. To optimize the performance of WD, an improved WD is proposed. In this scheme, the achievable minimum of **err** is monitored, and the related likelihoods is stored to estimate the target symbol at the end of decoding. Simulation results show that the improved WD outperforms the conventional WD.

1 Introduction

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Spatially coupled low-density parity-check (SC-LDPC) codes are first proposed by Felstrom and Zigangirov [1]. It is a kind of convolutional LDPC code [2]. Kudekar et al. have proved that SC-LDPC code has threshold saturation characteristic [3], that is, the belief propagation (BP) thresholds of SC-LDPC code can reach the maximum a posteriori (MAP) thresholds of corresponding regular LDPC code.

Using BP decoding long codes may cause high latency and need large memory. In [4], a window decoding (WD) method is proposed to solve this problem. The parity-check matrix H_{sc} of SC-LDPC code shows the structure of a nonzero diagonal band.

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Hence, the BP decoding can be operated in a window. The window slides along the diagonal band of the parity matrix and the target symbols are estimated one by one with low decoding latency until the whole codeword is decoded. Limiting BP decoding to a window has the advantage of reducing decoding latency, complexity, and memory, but at the cost of decoding performance. Therefore, Shi Yuan Mo, Inayat, and Peng Kang propose to introduce supervision [5], effective decoding termination [6], and retain local reliable message [7], respectively, to improve the performance of WD. Then, the P-EXIT analysis is applied for SC-LDPC code with window decoding in [8] to reduce decoding complexity. The research of Shi Yuan Mo et al. shows that the average error probability of the target symbols they define does not monotonically decrease with the iteration.

Inspired by this phenomenon, we find that the number of error bits **err** of the target symbols does not monotonically decrease with the iteration progresses. This means that the log-likelihood ratio (LLR) generated when the window decoding terminates is not the best choice for making decisions. Therefore, we propose an improved window decoding (iWD) that monitors the minimum of **err** and store the related LLR. Then, the target symbols can be estimated according to the stored LLR at the end of the window decoding. Then, we analyze and simulate the performance of SC-LDPC code WD and iWD in the additive white Gaussian noise (AWGN) channel. The simulation results show that the performance of the iWD outperforms the conventional WD.

2 SC-LDPC Codes

An SC-LDPC code can be constructed by spatial coupling of protographs [9, 10]. Given a regular LDPC protograph, an SC-LDPC protograph can be obtained by coupling L replicas of a regular LDPC protograph through edge spreading, where L represents the coupling length. For example, a (J, K)-regular LDPC protograph is given in Fig. 1a, where J represents column weight and K represents row weight. Figure 1b is to make L copies of regular LDPC protograph. And they are indexed by t. Figure 1c shows the process of edge spreading. Defining w as the coupling width,



Fig. 1 a A (3, 6)-regular LDPC protograph, **b** *L* replicas of (3, 6)-regular LDPC protographs, **c** process of edge spreading with w = 2, and **d** protograph of the (3, 6) SC-LDPC codes with w = 2

then the edge of the variable nodes (VNs) at *t* is connected to the checking nodes (CNs) at t, t + 1, ..., t + w. The protograph of the (3, 6) SC-LDPC codes is shown in Fig. 1d.

A base matrix $B = [B(i, j)]_{a \times b}$ can represent a regular LDPC protograph (a = J/(gcd(J, K)), b = K/(gcd(J, K)), gcd(J, K)) denote the maximum common divisor of J and K). Figure 1a shows that the base matrix corresponding to the (3, 6)-regular LDPC protograph is $B = [3 \ 3]$. The edge spreading divides the base matrix into component base matrices, and the component base matrices corresponding to Fig. 1c are $B_i = [1 \ 1], i = \{0, 1, 2\}$.

Therefore, the base matrix of SC-LDPC code is

$$\boldsymbol{B}_{sc} = \begin{bmatrix} \boldsymbol{B}_{0} & & \\ \boldsymbol{B}_{1} & \boldsymbol{B}_{0} & \\ \vdots & \boldsymbol{B}_{1} & \ddots & \\ \boldsymbol{B}_{w} & \vdots & \ddots & \boldsymbol{B}_{0} \\ & \boldsymbol{B}_{w} & \ddots & \boldsymbol{B}_{1} \\ & & \ddots & \vdots \\ & & & \boldsymbol{B}_{w} \end{bmatrix}_{((L+w)a \times Lb)}$$
(1)

 H_{sc} can be obtained by further lifting B_{sc} by the lifting factor M. Hence, the code rate of SC-LDPC code is

$$R_{\rm sc} = 1 - \frac{(L+w)Ma}{LMb} \tag{2}$$

3 Window Decoding and Improved WD

The parity matrix of SC-LDPC code presents a nonzero diagonal band structure. As shown in Fig. 2, the gray area is its nonzero diagonal band. Therefore, BP decoding can be constrained in a window, and the window size W needs to be satisfied with the condition $(w + 1) \le W \le L$.

The general principle of WD is given below. After receiving partial codeword, the decoding window starts BP decoding through a partial Tanner graph of the codeword and then slides along the diagonal band to estimate the target symbols one by one and generate a lower decoding latency. The symbol set contained in the leftmost protograph in the decoding window is called the target symbol. Once the BER of the target symbol is 0 or the number of iterations reaches the maximum, the decoding window terminates and outputs the decoding result of the current target symbol. Then, the decoding window slides to the next position to decode the next target symbol,



and the sliding process is shown in Fig. 2. Then, the above process is repeated until the entire codeword is decoded.

Assume that, the codeword is binary phase-shift keying (BPSK) modulated and sent to the AWGN channel. Let $s = (s_1, s_2, ..., s_N)$ and $r = (r_1, r_2, ..., r_N)$ represent the transmitted and accepted codeword, respectively, where N = LMb. The decoding process of WD is as follows.

Initialization: After receiving r_m from the channel in the *m*th window, the LLR of s_m can be initialized to

$$L_m = \ln\left(\frac{P(s_m = 0|r_m)}{P(s_m = 1|r_m)}\right)$$
(3)

Iterative Process: Only exchange and update the information of VNs and CNs in the current window. At *l*th iteration, the information transmitted by check node (CN) n to the connected variable node (VN) m is

$$R_{nm}^{l} = \ln \frac{1 + \prod_{k \in M_{1}(n) \setminus m} \tanh(Q_{nk}^{l-1}/2)}{1 - \prod_{k \in M_{1}(n) \setminus m} \tanh(Q_{nk}^{l-1}/2)}$$
(4)

Information passed by VN m to the connected CN n is

$$Q_{nm}^{l} = \begin{cases} L_{m} & l = 0\\ L_{m} + \sum_{k \in M_{2}(m) \setminus n} R_{km}^{l} & l > 0 \end{cases}$$
(5)

 $M_1(n)\setminus m$ represents the set of remaining VNs connected to CN *n* except the *m*th VN. Similarly, $M_2(m)\setminus n$ represents the set of remaining CNs connected to VN *m* except for the *n*th CN.

Decision And Window Sliding: After the current iteration, the LLR of VNs is calculated, and then, the hard decision is made based on the Q_{nm}^l . If $Q_{nm}^l \ge 0$, s' = 0, or s' = 1, otherwise. Let **err** denote the number of error bits of the target symbol. If the corresponding bit is decoded incorrectly, **err** will be increased by

$$\mathbf{err} = \mathbf{err} + 1 \tag{6}$$

If the BER of the target symbol is zero or the number of iterations reaches the maximum, the current window decoding will terminate and output the decoding result and then keep and propagate the information of the overlapped part (the shadow area in Fig. 2) from the current window to the next window and slide to the next position for decoding; otherwise, return to step 2 to continue the iteration.

Note that, **err** does not monotonically decrease with the iteration progresses. Therefore, improved WD is proposed. This scheme monitors the minimum of **err** and stores the related LLR. Let err^{*} denote the minimum of error bits of the target symbol. err^{*} will be initialized as $\text{err}^* = 10^{10}$. As the iteration progresses, if **err** < err^{*}, err^{*} will be updated by

$$\operatorname{err}^* = \operatorname{err}$$
 (7)

And the LLR of the target symbol will be stored. Once the BER of the target symbol is zero or the number of iterations reaches the maximum, the current decoding window will estimate the target symbol based on the stored LLR. Then, the current decoding window slides to the next position to decode the next target symbol.

4 Simulation Results and Analysis

This section simulates the (3, 6) SC-LDPC code, its corresponding base matrix $B = \begin{bmatrix} 3 & 3 \end{bmatrix}$, the coupling width w = 2, and the coupling length L = 50. The codeword is transmitted in the AWGN channel after BPSK modulation.

Figures 3 and 4 show the BER performance of the SC-LDPC code with the lifting factor M = 10 and M = 50, respectively. Hence, the codeword length is 1000 and 5000, respectively, and both code rates are 0.48. E_b/N_0 represents the signal-to-noise ratio. It is shown in each figure that iWD performance is better than WD performance at different window sizes. When the window size is 12 and the BER is 10^{-4} , the iWD of SC-LDPC with code length of 1000 has a gain of about 0.22 dB compared with that of WD. When the window size is 12 and the BER is 10^{-4} , the iWD of SC-LDPC with code length of about 0.12 dB compared with that of WD. Theoretically, compared with WD, iWD only monitors the minimum error bit of the target symbol, and its decoding termination conditions have not changed. Therefore, the decoding complexity of iWD and WD is approximately the same.







5 Conclusions

This paper introduces the window decoding of SC-LDPC codes and then proposes an improved scheme. The improved scheme monitors the minimum number of error bits of the target symbols and stores the related LLR. Then, the target symbols are estimated according to the stored LLR to make the more reliable decoding choices. The simulation results show that the improved scheme outperforms conventional WD.

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