

Power Quality Improvement of a Fuel Cell-Based Distributed Generation System Using Unified Power Quality Conditioner



Sarita Samal, Tarakanta Jena, and Prasanta Kumar Barik

Abstract This paper deals with power quality profile improvement of fuel cell (FC)-based distributed generation (DG) system using unified power quality conditioner (UPQC). Despite of the several benefits of DG like excellent energy supply, reducing expansion of power distribution system, environmental friendly, and so on, there are several challenges existing due to the integration of DG with the grid or operating it in stand-alone mode. Power quality (PQ) issue is one of the main technical challenges in DG power system. In order to provide improved PQ of energy supply, it is necessary to analyze the harmonics distortion of the system as well as the voltage sag and swell. The UPQC has been extensively useful and it is verified to be the best solution to diminish this PQ issue. This paper explores the detail of PQ impacts in FC-based DG system operating in stand-alone mode. The voltage sag compensation with current and voltage harmonics are estimated at varying load conditions by using control modified synchronous reference frame (MSRF) technique. The proposed model is developed in MATLAB/SIMULINK[®] and the result obtained validates the superiority of proposed technique over others in terms of harmonics elimination and sag compensation.

Keywords Distributed generation · Power quality · Harmonics · Sag · MSRF

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1 Introduction

Distributed generation (DG) can be represented as a small-scale power system that contains loads, energy sources, energy storage units, and control and protection systems [1]. Using DG is more attractive as it improves the system quality, decreases the carbon emission, and reduces the losses in transmission and distribution systems [2]. The control of active and reactive power is easy, when DG connected to grid rather than standalone system. However, under autonomous operation, the DG is disconnected from the utility grid and operates in islanded condition. Usually, a standalone DG system is used to supply power to isolated areas or places interconnected to a weak grid. The application of above DG, on other hand, reduces the probability of energy supply scarcity. The proposed DG consists of a fuel cell (FC) along with controllable loads [3, 4]. The FC is the mostly developed energy source used now [5]. However, electric power system is mostly affected by nonlinear loads, mostly arc furnaces, power electronics converters, and house hold electronic equipment plays a key role in polluting the supply voltages and currents. The increase of power electronics-based equipment in household appliances and industries are the main cause of pollution of power system [6]. The research in the area of power electronics makes sure that unified power quality conditioner (UPQC) plays a vital role for achieving superior power quality levels.

In the present scenario, the series active power filters (APFs) and shunt APF, normally termed as SAPF, alone do not meet the requirement for compensating the PQ distortions. A UPQC consists of two inverter integrated with the DC-link capacitor where the series APF is integrated through a series transformer and the shunt is through interfacing inductor. The series inverter acts as a voltage source where as the shunt one acts as a current source. Simultaneous compensation of voltage and current-related PQ distortions using UPQC is achieved by proper controlling of series APF and shunt APF. The shunt APF is employed for providing compensating currents to PCC for generation/absorption of reactive power and harmonics suppression. Moreover, the operation of SAPF depends on three main parts which are momentous in its design; these consist of the control method used for generation of reference current, technique used for switching pulses generation for the inverter, and the controller used for DC-link capacitor voltage regulation. Different control strategy explained in literature as follows. The use of SAPFs for current harmonic compensation typically in domestic, commercial, and industrial applications has explained in Montero et al. [7]. The experimental study and simulation design of a SAPF for harmonics and reactive power compensation are explained by Jain et al. [8]. The power balance theory for active and reactive power compensation has been developed by Singh et al. [9]. The instantaneous reactive power techniques of three phase shunt active filter for compensation of source current harmonics have been explained by Akagi et al. [13]. Sag is the most significant PQ problem faced by lots of industrial consumers. The control for such a case can be analyzed by protecting sensitive loads in order to preserve a load voltage without sudden phase shift [10]. Different control strategies for series APF are analyzed by Benachaiba

et al. [11] with importance on the reimbursement of voltage sags with phase jump. Different control techniques to reimburse voltage sags with phase jump are also projected and compared by Jowder et al. [12]. To ensure stable operation and improve the system performance of DG in island mode, a comparative study of two different control techniques used in UPQC like reference current generation, i.e., synchronous reference frame (SRF) method and modified synchronous reference frame (MSRF) method in conjunction with pulse width modulation-based hysteresis band controller is proposed in this paper by using MATLAB simulation software. The PQ issues like voltage sag compensation, current, and voltage harmonics were analyzed both at linear and nonlinear loads.

2 Proposed System

The projected DG system (comprising of solar and fuel cell-based energy sources) is shown in Fig. 1 where DG system generates DC power to the DC bus and by using a power inverter, this DC power is converted to AC. The AC bus delivers the power to the load which may be a linear or nonlinear. The UPQC is located in between the DG and nonlinear load manages the power quality of the system by using different control techniques.

2.1 Modeling of Fuel Cell System

Proton exchange membrane (PEM) fuel cell is considered as another energy source of the DG. The fuel cell consists of two electrodes, i.e., positive cathode, negative

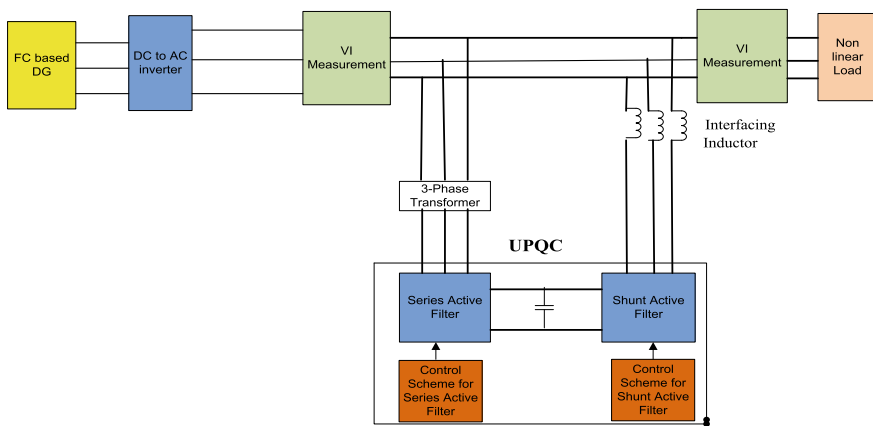


Fig. 1 Basic block diagram of DG with UPQC

Fig. 2 Fuel cell model

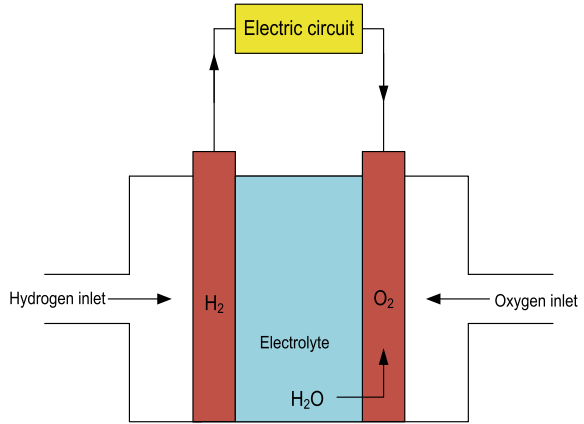
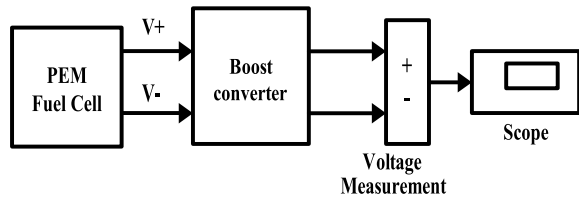
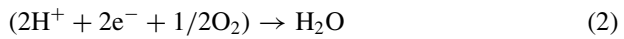


Fig. 3 Fuel cell with boost converter



anode, and an electrolyte. The pressurized hydrogen gas enters the anode of the fuel cell and oxygen enters the cathode [14, 15]. The basic model of PEM FC is shown in Fig. 2 and its chemical reactions are given in Eqs. (1–3) [16].



The simulation fuel cell with boost converter is shown in Fig. 3 and the output voltage which is match with the output voltage of other DGs is shown in Fig. 4. Table 1 represents different parameters of fuel cell.

3 Modeling of UPQC

This chapter begins with system configuration and detailed description on UPQC. The basic structure of UPQC is shown in Fig. 5 which consists of two inverter connected to a common dc-link capacitor. The series inverter is connected though a

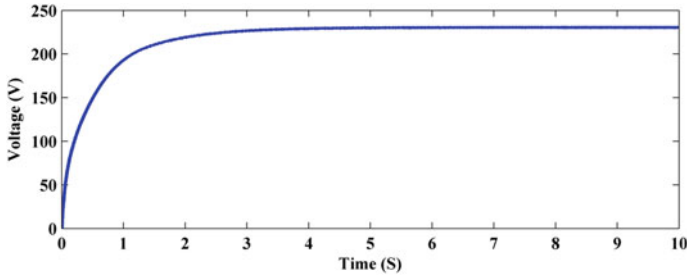
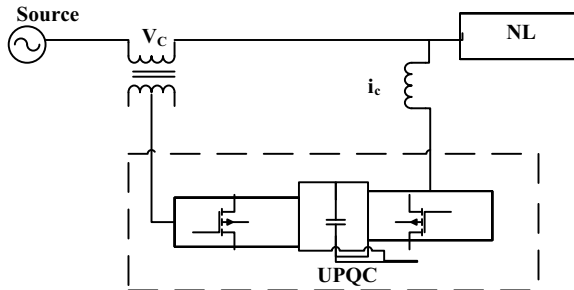


Fig. 4 Output voltage of boost converter

Table 1 Parameters of the fuel cell

Different parameters	Values
Load resistance (R_l)	5Ω
Oxygen percentage in air (O_2)	59.3%
Each cell voltage (v_s)	1.128 V
Cell resistance (R_f)	0.70833Ω
Number of cell (k)	65
Hydrogen percentage in fuel (H_2)	99.56%
Fuel cell voltage (V_{fc})	230 V

Fig. 5 Basic UPQC system block diagram



series transformer and the shunt inverter is connected in parallel with the point of common coupling. The series inverter acts as a voltage source where as the shunt one acts as a current source. The main function of UPQC is to control the power flow and reduce the harmonics distortion both in voltage and current waveforms.

The series APF topology is shown in Fig. 6. The series APF protects load from the utility side disturbances. In case of series APF, Park’s transformation method is used for generation of unit vector signal. A PWM generator, generating synchronized switching pulses, is given to the six switches of the series converter.

Figure 7 shows the basic structure of shunt active filter. The shunt active power filter injects compensating current to the PCC such that the load current becomes harmonics free. The SAPF generates compensating current which is in opposition to

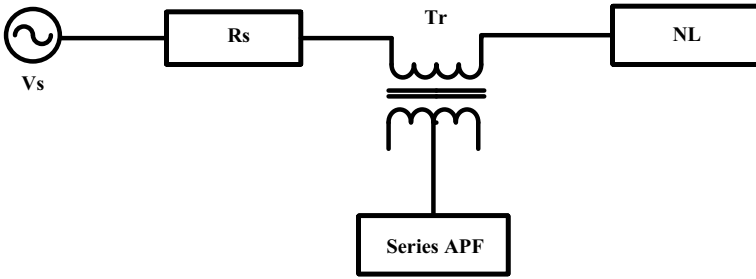


Fig. 6 Block diagram of series APF

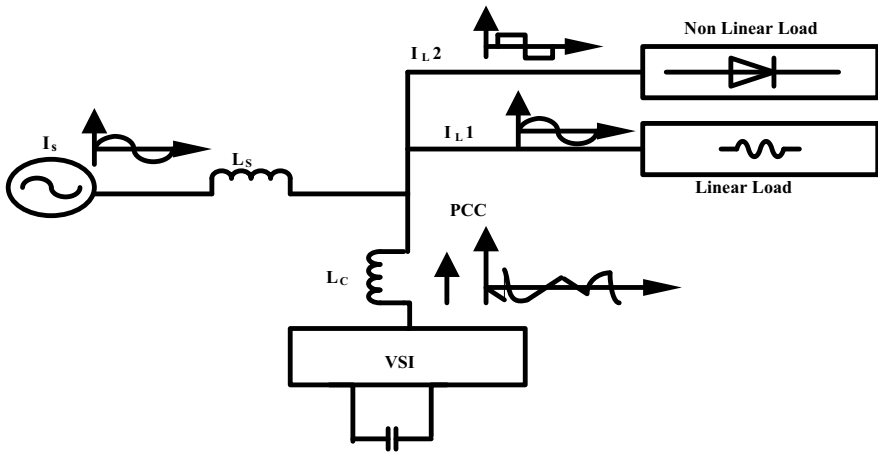


Fig. 7 Block diagram of shunt active filter

the harmonic current generated by nonlinear load. This compensating current cancel out the current harmonics caused and makes the load current sinusoidal. So, the SAPF is used to eradicate current harmonics and reimburse reactive power at the source side so as to make load current harmonics free.

Equations 4 and 5 show instantaneous current and the source voltage.

$$I_s(t) = I_L(t) - I_C(t) \tag{4}$$

$$V_s(t) = V_m \sin \omega t \tag{5}$$

Fourier series method is used for expressing the nonlinear load current as shown in Eq. 6.

$$I_s(t) = I_1 \sin(\omega t + \Phi_1) + \sum_{n=2}^{\epsilon} I_n \sin(n\omega t + \Phi_n) \tag{6}$$

The i_c is expressed by

$$I_c(t) = I_L(t) - I_s(t) \tag{7}$$

Hence, for the exact compensation of reactive power and harmonics, it is essential to determine $I_s(t)$. The instantaneous value of source, load, and compensation current can be expressed by, $I_s(t)$, $I_L(t)$, and $I_C(t)$ where $V_s(t)$ and V_m correspond to instantaneous value and peak value of source voltage.

3.1 UPQC Design

The MSRF controller scheme works in steady state as well as in dynamic condition exquisitely to manage the active, reactive power, and reduce the harmonics in load current. The literature in review reveals that MSRF technique has much more advantages as compare to SRF scheme, so the authors have selected this control scheme for UPQC operation. The control scheme not uses the PLL circuit as used by SRF scheme, which makes the system more compatible and may be operated in load changing condition. The MSRF scheme with its control algorithm is given below.

3.1.1 MSRF Scheme

Figure 8 shows the block diagram of modified SRF method for unit vector generation. The unit vector is generated by vector orientation method and not by PLL. Figure 9 shows the block diagram to generate unit vector by sensing the supply voltage.

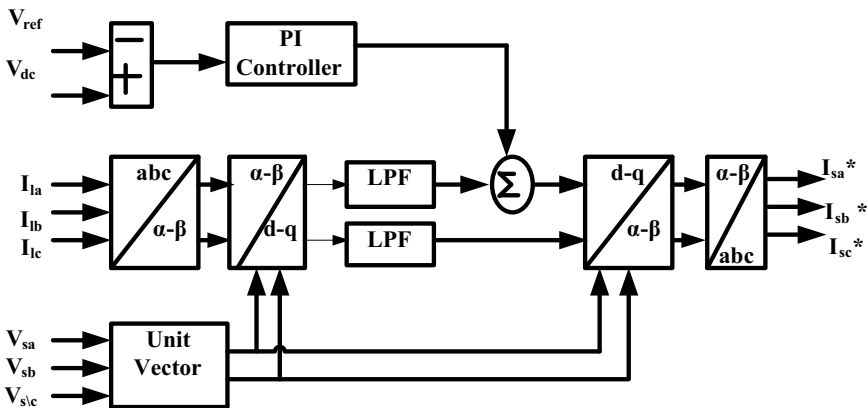


Fig. 8 Block diagram of modified SRF method

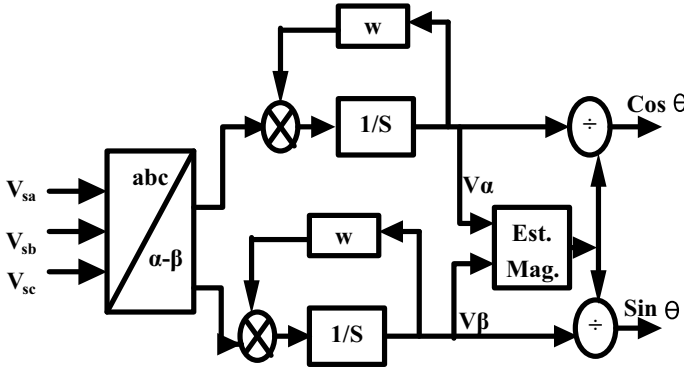


Fig. 9 Unit vector generation block diagram

$$\cos \theta = \frac{V_{\alpha}}{\sqrt{(V_{s\alpha}^2) + (V_{s\beta}^2)}} \tag{8}$$

$$\sin \theta = \frac{V_{\beta}}{\sqrt{(V_{s\alpha}^2) + (V_{s\beta}^2)}} \tag{9}$$

3.1.2 HCC Technique

Figure 10 shows the block diagram of hysteresis current regulator which generates the required pulses for inverter. In the current regulator, the error signal is generated by comparing the reference current I_{sa}^* and actual current I_{sa} .

The switching pulses required for the inverter is designed in such a way that when the error signal go beyond the upper band of hysteresis loop, the lower switches of inverter are ON and upper switches are OFF and similarly, the upper switches are

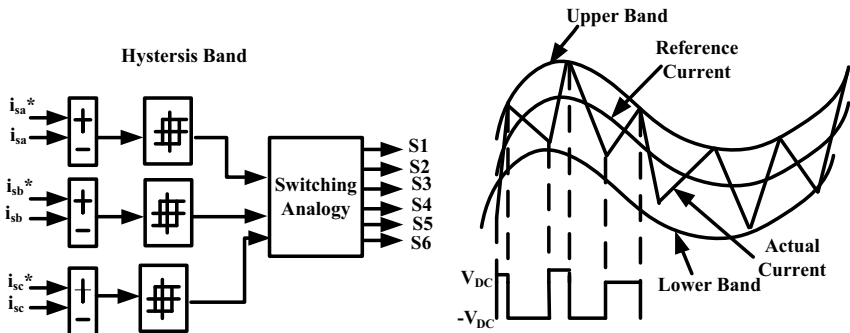


Fig. 10 Hysteresis current controller scheme

ON and lower switches OFF when the error signal exceeds the lower band [17, 18]. So, the actual current is always tracked with respect to reference current inside the hysteresis band.

4 Results and Discussion

Performance analysis of DG connected to nonlinear load with MSRF-based UPQC

In this case, the system performance is analyzed by connecting nonlinear load with the DG system first without UPQC and then with MSRF-based UPQC. The performance of series APF can be evaluated by introducing voltage sag into the system. The profile of load voltage shown in Fig. 11a conforms that voltage sag is introduced from 0.1 to 0.3 s of the load voltage waveform. For sag condition, the series APF detects the voltage drop and injects the required voltage through the series coupling transformer. It maintains the rated voltage across the load terminal. In order to compensate the load voltage sag, UPQC (employing MSRF scheme) is turned on, which injects compensating voltage at the PCC as displayed in Fig. 11b as a result, the load voltage is same as that of source voltage. The load voltage after compensation is shown in Fig. 11c. In general, the operation of the series part of the UPQC can be described as rapid detection of voltage variations at source and it injects the compensation voltage which maintains rated voltage across the load terminal.

The shunt VSI in the UPQC is realized as shunt APF and is applied to solve the current-related PQ distortions, current harmonic distortion, reactive power demand, etc. In order to investigate the performance of shunt APF, a rectifier-based nonlinear load is introduced into the system and the level of harmonics is checked. It is observed from Fig. 12a that the source current waveform has a total harmonic distortion (THD) of 16.60% as per the FFT analysis of the source current shown in Fig. 12b. In order to make source current to be sinusoidal, the shunt APF of the UPQC with conventional MSRF technique is turned on at $t = 0.1$ s which injects compensating current as displayed in Fig. 12c. Hence, the THD level comes down to 2.54% as shown in Fig. 12d.

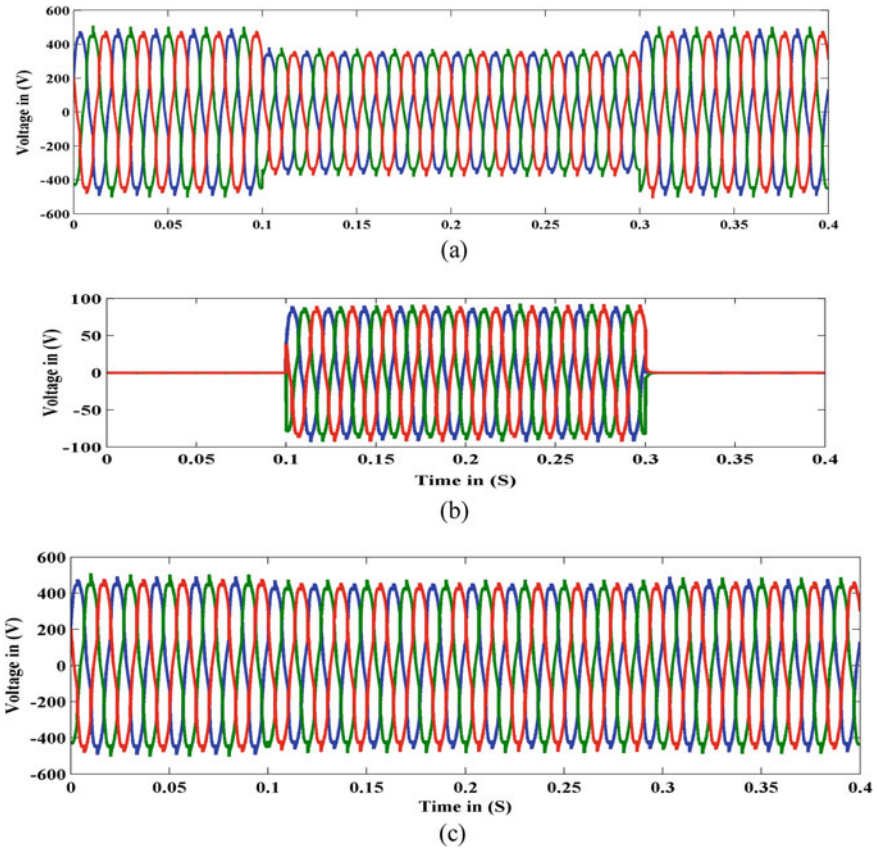


Fig. 11 Profile obtained under (sag compensation). **a** Load voltage before compensation. **b** Compensating voltage injected by UPQC. **c** Load voltage after compensation

5 Conclusion

The research reveals that MSRF technique of UPQC makes possible for improving the power quality of a DG system connected with nonlinear load. The advantage of MSRF technique is that the production of sine and cosine angles for synchronization purpose instead of using PLL circuit, it uses a basic unit vector generation scheme. The suggested method delivers superior output than the existing method in terms of harmonic mitigation and compensation of active and reactive powers.

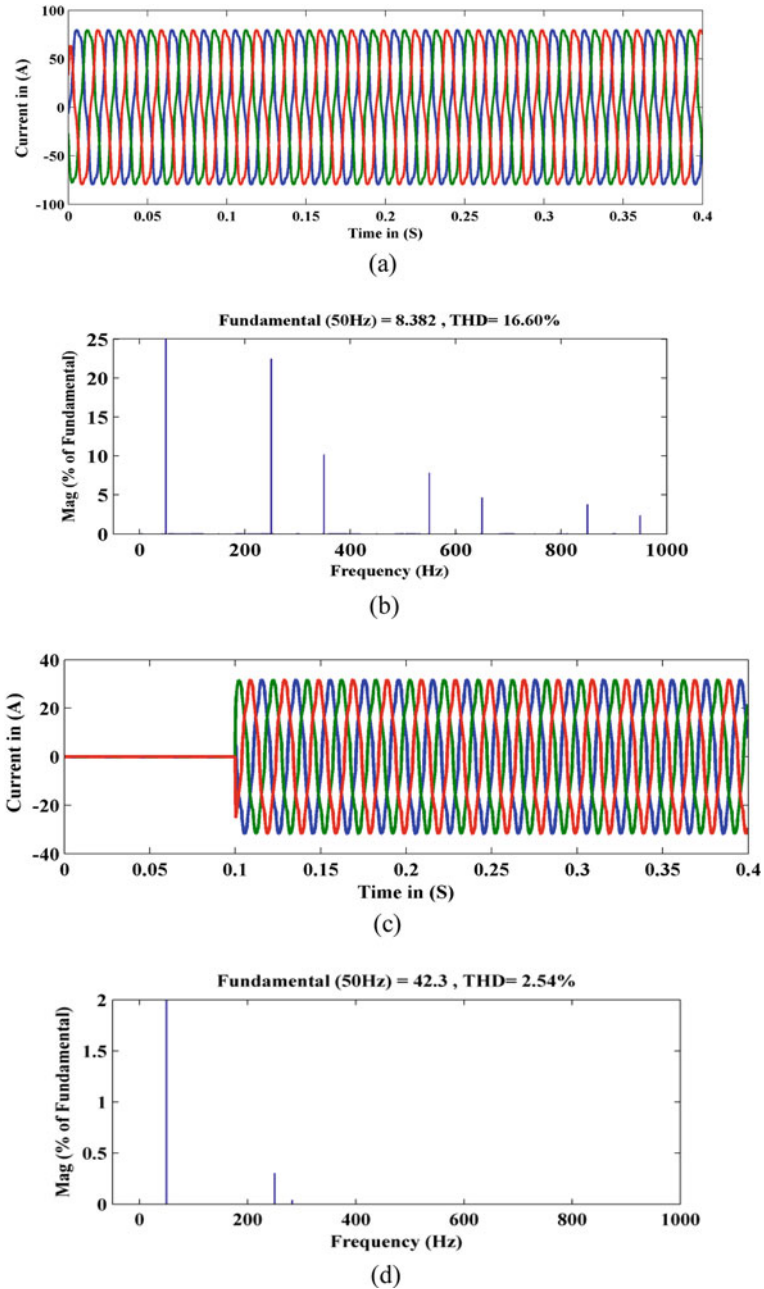


Fig. 12 Profile obtained under (harmonics mitigation). **a** Source current before compensation. **b** Harmonics content before compensation. **c** Compensating current injected by UPQC. **d** Harmonics content after compensation

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