

# Proving Termination Through Conditional Termination

Cristina Borralleras<sup>1</sup>, Marc Brockschmidt<sup>2</sup>, Daniel Larraz<sup>3</sup>, Albert Oliveras<sup>3</sup>,  
Enric Rodríguez-Carbonell<sup>3</sup>(✉), and Albert Rubio<sup>3</sup>

<sup>1</sup> Universitat de Vic - Universitat Central de Catalunya, Vic, Spain

<sup>2</sup> Microsoft Research, Cambridge, UK

<sup>3</sup> Universitat Politècnica de Catalunya, Barcelona, Spain  
erodri@cs.upc.edu

**Abstract.** We present a constraint-based method for proving conditional termination of integer programs. Building on this, we construct a framework to prove (unconditional) program termination using a powerful mechanism to combine conditional termination proofs. Our key insight is that a conditional termination proof shows termination for a subset of program execution states which do not need to be considered in the remaining analysis. This facilitates more effective termination as well as non-termination analyses, and allows handling loops with different execution phases naturally. Moreover, our method can deal with sequences of loops compositionally. In an empirical evaluation, we show that our implementation VeryMax outperforms state-of-the-art tools on a range of standard benchmarks.

## 1 Introduction

Proving program termination requires not only synthesizing termination arguments, but also reasoning about reachability of program states, as most non-trivial programs contain subprocedures or loops that only terminate for the executions that actually reach them. Thus, a termination prover has to segment the program state space according to its termination behavior, ignoring non-terminating but unreachable states. Recent advances in termination proving try to tackle this problem by abducing conditions for non-termination, and focusing the termination proof search on the remaining state space [25, 32]. However, these techniques rely on relatively weak non-termination proving techniques. Furthermore, different termination arguments may be required depending on how a loop or subprocedure is reached, and thus, even though no non-termination argument can be found, the state space needs to be segmented.

---

This work was partially supported by the project TIN2015-69175-C4-3-R (MINECO/FEDER) and by the European Research Council (ERC) under the European Union's Horizon 2020 research and innovation programme (grant agreement ERC-2014-CoG 648276 AUTAR).

© Springer-Verlag GmbH Germany 2017

A. Legay and T. Margaria (Eds.): TACAS 2017, Part I, LNCS 10205, pp. 99–117, 2017.

DOI: 10.1007/978-3-662-54577-5\_6

In this work, we propose to use preconditions for *termination* to drive the unconditional termination proof. The key insight is that a condition  $\phi$  implying termination allows a termination prover to focus on those program states in which  $\neg\phi$  holds. To obtain preconditions for termination, we introduce a new constraint-based method that analyzes program components (i.e., loops or sub-procedures) independently and synthesizes termination arguments together with a conditional supporting invariant [12]. To prove full program termination, we use a novel program transformation we call *unfolding* which syntactically splits terminating from potentially non-terminating states using the generated termination conditions. This allows us to combine several conditional termination arguments, each obtained for a small component of the program independently, into a proof for the input program. In summary, we present the following contributions:

- A new method based on Max-SMT for finding preconditions for termination (cf. Sect. 3 and Algorithm 1).
- A framework to prove termination or non-termination by repeatedly simplifying the program analysis task by combining conditional termination arguments using the *unfolding* transformation (cf. Sect. 4 and Algorithm 2).
- An implementation of the technique in our tool VeryMax for C++ input programs and an extensive experimental evaluation showing that it is not only more powerful than existing tools, but also more efficient (cf. Sect. 5).

## 2 Preliminaries

*SAT, Max-SAT, and Max-SMT.* Let  $\mathcal{P}$  be a fixed set of *propositional variables*. For  $p \in \mathcal{P}$ ,  $p$  and  $\neg p$  are *literals*. A *clause* is a disjunction of literals  $l_1 \vee \dots \vee l_n$ . A (CNF) *propositional formula* is a conjunction of clauses  $C_1 \wedge \dots \wedge C_m$ . The problem of *propositional satisfiability (SAT)* is to determine whether a propositional formula  $F$  has a *model*, i.e., an assignment  $M$  that satisfies  $F$ , denoted by  $M \models F$ . An extension of SAT is *Satisfiability Modulo Theories (SMT)* [6], where one checks the satisfiability of a formula with literals from a given background theory. Another extension is (*weighted partial*) *Max-SAT* [6], where some clauses in the input formula are *soft clauses* with an assigned weight, and the others are *hard clauses*. Here, we look for a model of the hard clauses that maximizes the sum of the weights of the satisfied soft clauses. Finally, *Max-SMT* combines Max-SAT and SMT. In a *Max-SMT* problem a formula is of the form  $H_1 \wedge \dots \wedge H_n \wedge [S_1, \omega_1] \wedge \dots \wedge [S_m, \omega_m]$ , where the hard clauses  $H_i$  and the soft clauses  $S_j$  (with weight  $\omega_j$ ) are disjunctions of literals over a background theory, and the aim is to find a model of the hard clauses that maximizes the sum of the weights of the satisfied soft clauses.

*Programs and States.* We fix a set of integer program *variables*  $\mathcal{V} = \{v_1, \dots, v_n\}$  and denote by  $F(\mathcal{V})$  the conjunctions of linear inequalities over the variables  $\mathcal{V}$ .

Let  $\mathcal{L}$  be the set of program *locations*, which contains a *canonical initial location*  $\ell_{\text{init}}$ . Program *transitions* are tuples  $(\ell_s, \rho, \ell_t)$ , where  $\ell_s$  and  $\ell_t \in \mathcal{L}$  are

the source and target locations respectively, and  $\rho \in F(\mathcal{V} \cup \mathcal{V}')$  describes the transition relation. Here  $\mathcal{V}' = \{v'_1, \dots, v'_n\}$  represent the values of the program variables after the transition.<sup>1</sup>

A *program*  $\mathcal{P}$  is a set of transitions.<sup>2</sup> The set of locations in these transitions is denoted by  $\mathcal{L}(\mathcal{P})$ . We identify a program with its *control-flow graph* (CFG), a directed graph in which nodes are the locations and edges are the transitions.<sup>3</sup> A *program component*  $\mathcal{C}$  of a program  $\mathcal{P}$  is the set of transitions of a *strongly connected component* (SCC) of the CFG of  $\mathcal{P}$ . Its *entry transitions*  $\mathcal{E}_{\mathcal{C}}$  are those transitions  $\tau = (\ell_s, \rho, \ell_t)$  such that  $\tau \notin \mathcal{C}$  but  $\ell_t \in \mathcal{L}(\mathcal{C})$  (and in this case  $\ell_t$  is called an *entry location*), while its *exit transitions*  $\mathcal{X}_{\mathcal{C}}$  are such that  $\tau \notin \mathcal{C}$  but  $\ell_s \in \mathcal{L}(\mathcal{C})$  (and then  $\ell_s$  is an *exit location*).

A *state*  $s = (\ell, v)$  consists of a location  $\ell \in \mathcal{L}$  and a *valuation*  $v : \mathcal{V} \rightarrow \mathbb{Z}$ . *Initial states* are of the form  $(\ell_{\text{init}}, v)$ . We denote a *computation step* with transition  $\tau = (\ell_s, \rho, \ell_t)$  by  $(\ell_s, v) \rightarrow_{\tau} (\ell_t, w)$ , where  $(v, w) \models \rho$ . We use  $\rightarrow_{\mathcal{P}}$  if we do not care about the executed transition of  $\mathcal{P}$ , and  $\rightarrow_{\mathcal{P}}^*$  to denote the transitive-reflexive closure of  $\rightarrow_{\mathcal{P}}$ . Sequences of computation steps are called *computations*.

*Safety and Termination.* An *assertion*  $(\ell, \varphi)$  is a pair of a location  $\ell$  and a formula  $\varphi \in F(\mathcal{V})$ . A program  $\mathcal{P}$  is *safe* for the assertion  $(\ell, \varphi)$  if for every computation starting at an initial state  $s_0$  of the form  $s_0 \rightarrow_{\mathcal{P}}^* (\ell, v)$ , we have that  $v \models \varphi$  holds. Safety can be proved using *conditional invariants* [12], which like ordinary invariants are inductive, but not necessarily initiated in all computations.

**Definition 1 (Conditional Inductive Invariant).** *Let  $\mathcal{P}$  be a program. We say a map  $\mathcal{Q} : \mathcal{L}(\mathcal{P}) \rightarrow F(\mathcal{V})$  is a conditional (inductive) invariant for  $\mathcal{P}$  if for all  $(\ell_s, v) \rightarrow_{\mathcal{P}} (\ell_t, w)$ , we have  $v \models \mathcal{Q}(\ell_s)$  implies  $w \models \mathcal{Q}(\ell_t)$ .*

A program  $\mathcal{P}$  is *terminating* if any computation starting at an initial state is finite. An important tool for proving termination are *ranking functions*:

**Definition 2 (Ranking Function).** *Let  $\mathcal{C}$  be a component of a program  $\mathcal{P}$ , and  $\tau = (\ell_s, \rho, \ell_t) \in \mathcal{C}$ . A function  $\mathcal{R} : \mathbb{Z}^n \rightarrow \mathbb{Z}$  is a ranking function for  $\tau$  if:*

$$\begin{aligned} - \rho \models \mathcal{R} \geq 0 & \qquad \qquad \qquad [\text{Boundedness}] \\ - \rho \models \mathcal{R} > \mathcal{R}' & \qquad \qquad \qquad [\text{Decrease}] \end{aligned}$$

and for every  $(\hat{\ell}_s, \hat{\rho}, \hat{\ell}_t) \in \mathcal{C}$ ,

$$- \hat{\rho} \models \mathcal{R} \geq \mathcal{R}' \qquad \qquad \qquad [\text{Non-increase}]$$

<sup>1</sup> For  $\varphi \in F(\mathcal{V})$ , we denote by  $\varphi' \in F(\mathcal{V}')$  the version of  $\varphi$  using primed variables.

<sup>2</sup> Hence in our programming model procedure calls are not allowed. Note however that programs with non-recursive calls can also be handled by inlining the calls.

<sup>3</sup> Since we label transitions only with conjunctions of linear inequalities, disjunctive conditions are represented using several transitions with the same source and target location. Thus,  $\mathcal{P}$  is actually a multigraph.

The key property of ranking functions is that if a transition admits one, then it cannot be executed infinitely.

A core concept in our approach is *conditional termination*, i.e., the notion that once a condition holds, a program is definitely terminating. As we make heavy use of the program’s control flow graph structure, we introduce this concept as location-dependent.

**Definition 3 (Conditional Termination).** *We say that a program  $\mathcal{P}$  is  $(\ell, \varphi)$ -conditionally terminating if every computation that contains a state  $(\ell, v)$  with  $v \models \varphi$  uses transitions from  $\mathcal{P}$  only a finite number of times. In that case the assertion  $(\ell, \varphi)$  is called a precondition for termination.*

### 3 Synthesizing Conditional Termination Arguments

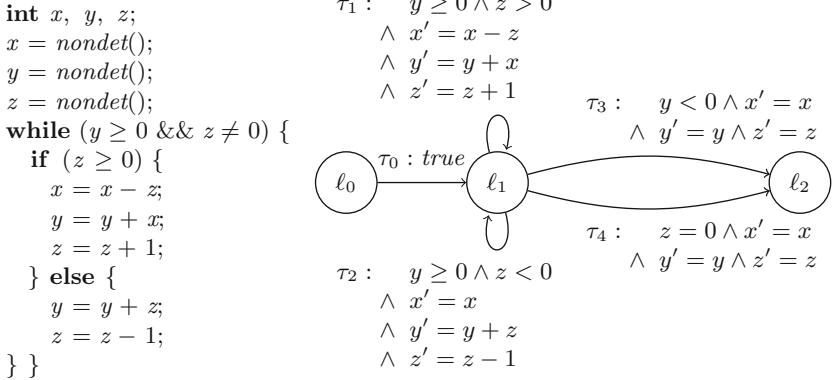
Our approach for synthesizing conditional termination arguments works on one program component at a time. As proving that a program terminates is equivalent to showing that there is no program component where a computation can stay indefinitely, this turns out to be a convenient way to decompose termination proofs.

For a fixed program component  $\mathcal{C}$ , a conditional lexicographic termination argument is constructed iteratively by transition elimination as follows. In each iteration, we synthesize a linear ranking function together with supporting conditional invariants, requiring that they show that at least one transition of  $\mathcal{C}$  is *finitely executable*, i.e., can only occur a finite number of times in any execution. The intuition here is that once we have proven that a transition  $\tau$  can only be used finitely often, we only need to consider (possibly infinite) suffixes of program executions in which  $\tau$  cannot appear anymore. If after some iterations no transition of  $\mathcal{C}$  can be executed infinitely anymore, then the conjunction of all conditional invariants obtained at an entry location of  $\mathcal{C}$  yields a precondition for termination. Indeed, once the conditional invariants hold at that entry location, then by inductiveness they hold from then on at all locations of  $\mathcal{C}$ , and hence the termination argument applies.

*Example 1.* Consider the program in Fig. 1 and its CFG, with initial location  $\ell_{\text{init}} = \ell_0$ . We want to find a precondition for termination of the component  $\mathcal{C} = \{\tau_1, \tau_2\}$ , corresponding to the **while** loop.

In a first iteration, we generate the ranking function  $y$  for  $\tau_2$ , together with the supporting conditional invariant  $z < 0$ . Note that  $z < 0$  is indeed a conditional invariant: it is preserved by  $\tau_2$  as  $z$  decreases its value, and is also trivially preserved by  $\tau_1$  since this transition is in fact disabled if  $z < 0$ . Under the condition  $z < 0$ ,  $y$  is bounded and decreases in  $\tau_2$ , and  $\tau_1$  is disabled and so finitely executable. Hence,  $(\ell_1, z < 0)$  is a precondition for termination. ■

As observed in [9,30], synthesizing lexicographic termination arguments together with supporting invariants requires to keep several copies of the program under analysis. Thus, in the analysis of a component  $\mathcal{C}$ , we keep a set  $\mathcal{M}$  of



**Fig. 1.** Program and its CFG.

possibly infinitely executable transitions (i.e., those for which we have not proved conditional termination yet), called the *termination component*. Nonetheless, to compute sound invariants (i.e., soundly reason about reachable states), we need to take all transitions into account. However, these transitions can be strengthened with the supporting invariants that we synthesized in earlier proof steps. Hence, we keep another copy  $\mathcal{I}$ , called the *conditional invariant component*, which is like the original component  $\mathcal{C}$ , except for the addition of the conditional invariants found in previous iterations. Initially both the termination and the conditional invariant components are identical copies of the component  $\mathcal{C}$ .

The proposed method for generating preconditions for termination is an extension of the constraint-based approach for proving (unconditional) termination presented in [30]. The individual constraints used in our method are displayed in Fig. 2, corresponding to the standard constraints employed in constraint-based techniques [8]. For all locations  $\ell$  in  $\mathcal{C}$ , we introduce templates  $I_\ell$  corresponding to fixed-length conjunctions of linear inequalities on the program variables; i.e.,  $I_\ell$  is of the form  $\bigwedge_{1 \leq i \leq k} (a_i + \sum_{v \in \mathcal{V}} a_{i,v} v \leq 0)$  for some  $k$  and where the  $a_*$  are integer template variables that do not appear in  $\mathcal{V}$ .

For $\tau = (\ell_s, \rho, \ell_t)$ :			
<b>Initiation:</b>	$\mathbb{I}_\tau$	$\stackrel{def}{=} \rho \Rightarrow I'_{\ell_t}$	
<b>Consecution:</b>	$\mathbb{C}_\tau$	$\stackrel{def}{=} I_{\ell_s} \wedge \rho \Rightarrow I'_{\ell_t}$	
<b>Boundedness:</b>	$\mathbb{B}_\tau$	$\stackrel{def}{=} I_{\ell_s} \wedge \rho \Rightarrow R \geq 0$	
<b>Decrease:</b>	$\mathbb{D}_\tau$	$\stackrel{def}{=} I_{\ell_s} \wedge \rho \Rightarrow R > R'$	
<b>Non-increase:</b>	$\mathbb{N}_\tau$	$\stackrel{def}{=} I_{\ell_s} \wedge \rho \Rightarrow R \geq R'$	

**Fig. 2.** Constraints used for generating preconditions for termination.

Furthermore, we also define a template  $R$  for a linear ranking function<sup>4</sup> with integer coefficients, i.e.,  $R$  is of the form  $a + \sum_{v \in \mathcal{V}} a_v v$ . For a given component  $\mathcal{C}$  with entries  $\mathcal{E}_{\mathcal{C}}$ , we combine these constraints in the (non-linear) formula  $\mathbb{F}$  as follows:

$$\mathbb{F} \stackrel{\text{def}}{=} \bigwedge_{\tau \in \mathcal{E}_{\mathcal{C}}} \mathbb{I}_{\tau} \wedge \bigwedge_{\tau \in \mathcal{I}} \mathbb{C}_{\tau} \wedge \bigwedge_{\tau \in \mathcal{M}} \mathbb{N}_{\tau} \wedge \bigvee_{\tau \in \mathcal{M}} (\mathbb{B}_{\tau} \wedge \mathbb{D}_{\tau}).$$

However, not all of these constraints are treated as hard constraints. Most notably, we turn  $\bigwedge_{\tau \in \mathcal{E}_{\mathcal{C}}} \mathbb{I}_{\tau}$  into soft constraints. Intuitively this means that, if possible, we want to synthesize a true (unconditional) supporting invariant, but will also allow invariants that do not always hold. However, we keep  $\bigwedge_{\tau \in \mathcal{I}} \mathbb{C}_{\tau}$  as a hard constraint, ensuring that our conditional invariants are indeed inductive, i.e., keep on holding after they have been satisfied once. Similarly,  $\bigwedge_{\tau \in \mathcal{M}} \mathbb{N}_{\tau} \wedge \bigvee_{\tau \in \mathcal{M}} (\mathbb{B}_{\tau} \wedge \mathbb{D}_{\tau})$  are kept as hard constraints, enforcing that a true ranking function is found, though it may only hold in those cases where the supporting invariant is initiated. The conditions for the supporting invariants will eventually become our preconditions for termination.

Algorithm 1 shows our procedure `CondTerm` for generating preconditions for termination. It takes as inputs the component  $\mathcal{C}$  under consideration and its entry transitions  $\mathcal{E}_{\mathcal{C}}$ , and returns a conditional invariant  $\mathcal{Q}$  that ensures that no infinite computation can remain within  $\mathcal{C}$ .

---

**Algorithm 1.** Procedure `CondTerm` for computing preconditions for termination

---

**Input:** component  $\mathcal{C}$ , entry transitions  $\mathcal{E}_{\mathcal{C}}$

**Output:** `None` |  $\mathcal{Q}$ , where  $\mathcal{Q}$  maps locations in  $\mathcal{L}(\mathcal{C})$  to conjunctions of inequalities

- 1:  $(\mathcal{I}, \mathcal{M}) \leftarrow (\mathcal{C}, \mathcal{C})$
  - 2:  $\mathcal{Q} \leftarrow \{ \ell \mapsto \text{true} \mid \ell \in \mathcal{L}(\mathcal{C}) \}$
  - 3: **while**  $\mathcal{M} \neq \emptyset$  **do**
  - 4:     construct formula  $\mathbb{F}$  from  $\mathcal{I}, \mathcal{M}, \mathcal{E}_{\mathcal{C}}$
  - 5:      $\sigma \leftarrow \text{Max-SMT-solver}(\mathbb{F})$
  - 6:     **if**  $\sigma$  is a solution **then**
  - 7:          $\mathcal{I} \leftarrow \{ (\ell_s, \rho \wedge \sigma(I_{\ell_s}), \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{I} \}$
  - 8:          $\mathcal{M} \leftarrow \{ (\ell_s, \rho \wedge \sigma(I_{\ell_s}), \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{M} \}$
  - 9:          $\mathcal{M} \leftarrow \mathcal{M} - \{ \tau \in \mathcal{M} \mid \sigma(R)$  is a ranking function for  $\tau \}$
  - 10:          $\mathcal{Q} \leftarrow \{ \ell \mapsto \mathcal{Q}(\ell) \wedge \sigma(I_{\ell}) \mid \ell \in \mathcal{L}(\mathcal{C}) \}$
  - 11:     **else return** `None`
  - 12: **return**  $\mathcal{Q}$
- 

In Algorithm 1, we continue to extend the termination argument as long as there are still potentially infinitely executable transitions (line 3). For this, we build a Max-SMT problem  $\mathbb{F}$  to generate a ranking function and its supporting

---

<sup>4</sup> While using a different ranking function for each program location is possible, we have found that the added power does not justify the increased complexity of the ensuing SMT problem.

conditional invariants. If no solution can be found, then the procedure gives up (line 11). Otherwise, a solution  $\sigma$  to  $\mathbb{F}$  yields a linear function  $\sigma(R)$  (the instantiation of the template ranking function  $R$  determined by  $\sigma$ ) together with conditional invariants  $\sigma(I_{\ell_s})$ . Since the  $\sigma(I_{\ell_s})$  are conditional invariants, they can be used to strengthen transitions  $\tau = (\ell_s, \rho, \ell_t)$  by conjoining  $\sigma(I_{\ell_s})$  to  $\rho$ , both in the conditional invariant component and in the termination component (lines 7–8). Most importantly, we identify the subset of the transitions  $\tau$  from  $\mathcal{M}$  for which  $\sigma(R)$  is a ranking function, and hence can be removed from  $\mathcal{M}$  (line 9). Finally, conditional invariants from previous iterations are accumulated so that, in the end, a global conjunction can be returned (lines 10 and 12).

In essence, this process corresponds to the step-wise construction of a lexicographic termination argument. For a location  $\ell$  at which the component  $\mathcal{C}$  is entered, the conjunction of all obtained  $\sigma(I_\ell)$  is then a precondition for termination. The following theorem states the correctness of procedure `CondTerm`:

**Theorem 1 (CondTerm soundness).** *Let  $\mathcal{P}$  be a program,  $\mathcal{C}$  a component, and  $\mathcal{E}_\mathcal{C}$  its entry transitions. If the procedure call `CondTerm`( $\mathcal{C}, \mathcal{E}_\mathcal{C}$ ) returns  $\mathcal{Q} \neq \text{None}$ , then  $\mathcal{C}$  is  $(\ell, \mathcal{Q}(\ell))$ -conditionally terminating for any  $\ell \in \mathcal{L}(\mathcal{C})$ .*

Of course, Algorithm 1 is an idealized, high-level description of our procedure. In an implementation of the procedure `CondTerm`, a number of small changes help to improve the overall number of solved instances.

*Constraint Strengthening.* Additional constraints can be added to formula  $\mathbb{F}$  to favor conditional invariants that are more likely to be useful. In particular, a constraint requiring that the conditional invariants are compatible with the entry transitions and with the previously generated conditional invariants has proven useful, i.e.

$$\bigvee_{(\ell_s, \rho, \ell_t) \in \mathcal{E}_\mathcal{C}} \exists \mathcal{V}, \mathcal{V}' (I'_{\ell_t} \wedge \rho \wedge \mathcal{Q}(\ell_t)').$$

*Constraint Softening.* Similarly, we can increase the allowed range of models by turning more of the clauses into soft clauses. For example, this can be used to allow *quasi-ranking functions* [30] in addition to ranking functions. Quasi-ranking functions are functions that satisfy the non-increase condition, but may fail to decrease or be bounded, or both. By using them to partition transitions and perform case analysis, programs can also be shown to be terminating.

*Pseudo-Invariants of Termination Component.* In some circumstances, inductive properties of the termination component  $\mathcal{M}$  (i.e., satisfying **Consecution** only for transitions in  $\mathcal{M}$ ) can be sound and useful; namely, when the complement of the property disables a transition.

Formally, let  $\mathcal{Q}$  be a map from  $\mathcal{L}(\mathcal{P})$  to  $F(\mathcal{V})$  such that  $\mathcal{Q}(\tilde{\ell}_s) \wedge \tilde{\rho} \Rightarrow \mathcal{Q}(\tilde{\ell}_t)'$  for all  $(\tilde{\ell}_s, \tilde{\rho}, \tilde{\ell}_t) \in \mathcal{M}$ , and  $\neg \mathcal{Q}(\ell_s) \wedge \rho \models \text{false}$  for some  $\tau = (\ell_s, \rho, \ell_t) \in \mathcal{M}$ . Moreover, assume that  $\mathcal{Q}$  supports a ranking function  $\mathcal{R}$  for  $\tau$ . Then  $\tau$  can only be used finitely often. To see this, assume that there is an infinite computation

in which  $\tau$  occurs infinitely often. Then there is a state at location  $\ell_s$  in the computation from which only transitions in  $\mathcal{M}$  are taken. Since  $\mathcal{Q}$  is inductive over transitions in  $\mathcal{M}$ , if  $\mathcal{Q}(\ell_s)$  holds at that state then it holds from then on, and therefore  $\mathcal{R}$  proves that  $\tau$  cannot be executed an infinite number of times. Otherwise, if  $\mathcal{Q}(\ell_s)$  does not hold, then  $\tau$  cannot be executed at all. This weaker requirement on  $\mathcal{Q}$  allows removing transitions from  $\mathcal{M}$  and is easier to satisfy. Still, it is insufficient to do a case analysis as a full conditional invariant allows.

## 4 Proving Termination Using Conditional Termination

Our key contribution is to leverage conditional termination arguments to perform a natural case analysis of program executions. In this way, as our analysis progresses, more and more program runs are excluded from the program analysis, allowing the method to focus on those parts of the program for which termination has not been guaranteed yet. The core component of this is a syntactic program transformation we call *unfolding* that implements the semantic intuition of distinguishing program states for which termination has not been proven yet.

### 4.1 Program Unfoldings

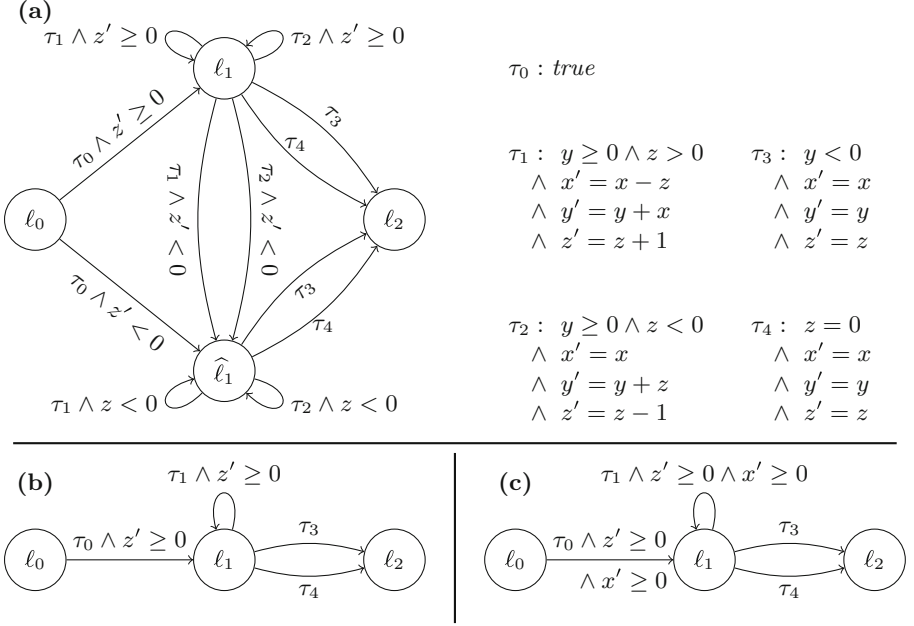
We begin this subsection with an example that illustrates how conditional invariants can be used to unfold the component under consideration.

*Example 2.* Consider the program from Fig. 1 again. In Example 1 it was shown that all computations for which  $z < 0$  holds at location  $\ell_1$  are finite. In fact, a byproduct of the proof was that  $z < 0$  is a conditional invariant at location  $\ell_1$ . We show how to exploit this to prove *unconditional* termination next.

Following the intuition of a case analysis, we unfold the program component by introducing a copy of it in which we assume that the conditional invariant holds. In our example, we duplicate the location  $\ell_1$ , introducing a copy denoted by  $\widehat{\ell}_1$ . We also duplicate all transitions in, from and to the component, using the newly introduced location. However, all copied transitions should also reflect our case analysis, and are thus strengthened by the conditional invariant  $z < 0$ . In our case analysis, the original component now corresponds to the case that the conditional invariant does *not* hold, and thus, all of the original transitions are strengthened to assume the negation of the conditional invariant. Finally, to allow for computations where the invariant eventually becomes true, we add copies of the transitions from the original component to the copied location, again strengthened by the invariant. The resulting program is shown in Fig. 3(a).

The original program and its unfolding behave equivalently, in particular regarding termination. However, we already know from Example 1 that under the assumption  $z > 0$ , the new component has no infinite executions. Hence, we can *narrow* the set of potentially infinite computations and focus on the program shown in Fig. 3(b), obtained by removing all known-terminating locations





**Fig. 3.** Unfolding of the program from Fig. 1 for conditional invariant  $z < 0$  at  $l_1$  (a), ensuing narrowing/simplification (b) and narrowing after unfolding for  $x < 0$  at  $l_1$  (c).

(i.e.,  $\hat{l}_1$ ) from the unfolding and simplifying. If this narrowed program terminates, we can conclude that the original program terminates too.

Synthesizing another conditional termination argument for the program from Fig. 3(b) now yields the ranking function  $y$ , supported by the conditional invariant  $x < 0$  at  $l_1$ . Then we can unfold with  $x < 0$  again and narrow, obtaining the program in Fig. 3(c). Finally this program can be proven terminating with ranking function  $x$  without the need of any conditional invariant and, hence, without precondition. This concludes the proof of termination of the original program.

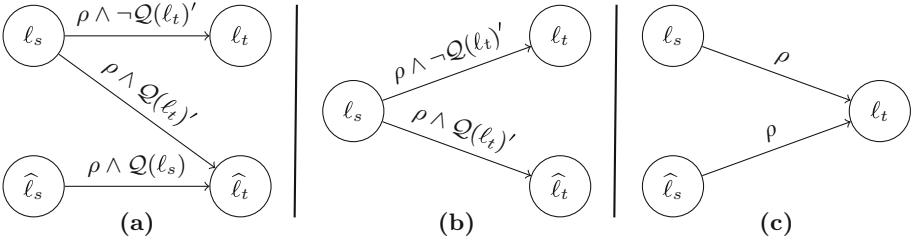
Note that the unfolding/narrowing mechanism provides not only a termination proof but also a characterization of the program execution phases. In particular, our example can be viewed to have three phases, corresponding to the unfoldings we have applied. One phase corresponds to the case where  $z < 0$  (where the **else**-block is repeatedly used), one to the case  $z > 0 \wedge x < 0$ , and finally, one corresponds to the case  $z > 0 \wedge x \geq 0$ . ■

To formalize this execution phase-structured proof technique, we first define the *unfolding* program transformation:

**Definition 4.** Let  $\mathcal{P}$  be a program,  $\mathcal{C}$  a component of  $\mathcal{P}$ ,  $\mathcal{E}_{\mathcal{C}}$  its entry transitions,  $\mathcal{X}_{\mathcal{C}}$  its exit transitions, and  $\mathcal{Q} : \mathcal{L}(\mathcal{C}) \rightarrow F(\mathcal{V})$  a conditional invariant for  $\mathcal{C}$ . The unfolding of  $\mathcal{P}$  is

$$\begin{aligned}
\widehat{\mathcal{P}} = & \{ (\ell_s, \rho \wedge \neg \mathcal{Q}(\ell_t)', \ell_t), \\
& (\ell_s, \rho \wedge \mathcal{Q}(\ell_t)', \widehat{\ell}_t), \\
& (\widehat{\ell}_s, \rho \wedge \mathcal{Q}(\ell_s), \widehat{\ell}_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{C} \} \\
\cup & \{ (\ell_s, \rho \wedge \neg \mathcal{Q}(\ell_t)', \ell_t), \\
& (\ell_s, \rho \wedge \mathcal{Q}(\ell_t)', \widehat{\ell}_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{E}_{\mathcal{C}} \} \\
\cup & \{ (\ell_s, \rho, \ell_t), \\
& (\widehat{\ell}_s, \rho, \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{X}_{\mathcal{C}} \} \\
\cup & \{ \tau \mid \tau \in \mathcal{P} \setminus (\mathcal{C} \cup \mathcal{E}_{\mathcal{C}} \cup \mathcal{X}_{\mathcal{C}}) \}
\end{aligned}$$

where for each  $\ell \in \mathcal{L}(\mathcal{C})$  there is a fresh location  $\widehat{\ell}$  such that  $\widehat{\ell} \notin \mathcal{L}(\mathcal{P})$ .



**Fig. 4.** Transitions in unfolding  $\widehat{\mathcal{P}}$  for conditional invariant  $\mathcal{Q}$  corresponding to a transition  $\tau = (\ell_s, \rho, \ell_t) \in \mathcal{P}$ , depending on whether (a)  $\tau \in \mathcal{C}$ , (b)  $\tau \in \mathcal{E}_{\mathcal{C}}$ , (c)  $\tau \in \mathcal{X}_{\mathcal{C}}$ .

Figure 4 represents graphically how a transition of the original program is transformed, depending on whether it is a transition of the component, an entry transition, or an exit transition. The following result states that a program and its unfolding are semantically equivalent, i.e., that the encoded case analysis is complete.

**Theorem 2.** *Given states  $(\ell_0, v_0)$  and  $(\ell_k, v_k)$  such that  $\ell_0, \ell_k \in \mathcal{L}(\mathcal{P})$ , there is a computation in  $\mathcal{P}$  of length  $k$  of the form  $(\ell_0, v_0) \xrightarrow{*}_{\mathcal{P}} (\ell_k, v_k)$  if and only if there is a computation in  $\widehat{\mathcal{P}}$  of length  $k$  of the form  $(\ell_0, v_0) \xrightarrow{*}_{\widehat{\mathcal{P}}} (\ell_k, v_k)$  or of the form  $(\ell_0, v_0) \xrightarrow{*}_{\widehat{\mathcal{P}}} (\widehat{\ell}_k, v_k)$ .*

Now we are ready to formally define the narrowing of a program:

**Definition 5.** *Let  $\mathcal{P}$  be a program,  $\mathcal{C}$  a component of  $\mathcal{P}$ ,  $\mathcal{E}_{\mathcal{C}}$  its entry transitions, and  $\mathcal{Q} : \mathcal{L}(\mathcal{C}) \rightarrow F(\mathcal{V})$  a conditional invariant for  $\mathcal{C}$ . The narrowing of  $\mathcal{P}$  is:*

$$\begin{aligned}
\text{narrow}(\mathcal{P}) = & \{ (\ell_s, \rho \wedge \neg \mathcal{Q}(\ell_t)', \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{C} \cup \mathcal{E}_{\mathcal{C}} \} \cup \\
& \{ \tau \mid \tau \in \mathcal{P} - (\mathcal{C} \cup \mathcal{E}_{\mathcal{C}}) \}
\end{aligned}$$

The narrowing of a program can be viewed as the result of eliminating the copies  $\widehat{\ell}$  of the locations  $\ell \in \mathcal{L}(\mathcal{C})$  in the unfolding  $\widehat{\mathcal{P}}$  and their corresponding transitions. Alternatively, one may take the original program  $\mathcal{P}$  and consider that for any transition  $\tau = (\ell_s, \rho, \ell_t) \in \mathcal{C} \cup \mathcal{E}_{\mathcal{C}}$ , the relation is replaced by  $\rho \wedge \neg \mathcal{Q}(\ell_t)'$ .

The intuition is that, if a call to  $\text{CondTerm}(\mathcal{C}, \mathcal{E}_{\mathcal{C}})$  has been successful (i.e.,  $\mathcal{Q} \stackrel{\text{def}}{=} \text{CondTerm}(\mathcal{C}, \mathcal{E}_{\mathcal{C}}) \neq \text{None}$ ), by the inductiveness of  $\mathcal{Q}$  a computation that satisfies  $\mathcal{Q}(\ell)$  for a certain  $\ell \in \mathcal{C}$  cannot remain within  $\mathcal{C}$  indefinitely. Hence we only need to consider computations such that whenever a location  $\ell \in \mathcal{C}$  is reached, we have that  $\mathcal{Q}(\ell)$  does not hold.

**Corollary 1.** *Let  $\mathcal{P}$  be a program,  $\mathcal{C}$  a component of  $\mathcal{P}$ ,  $\mathcal{E}_{\mathcal{C}}$  its entry transitions, and  $\mathcal{Q} : \mathcal{L}(\mathcal{C}) \rightarrow F(\mathcal{V})$  a conditional invariant for  $\mathcal{C}$  obtained from a call to  $\text{CondTerm}(\mathcal{C}, \mathcal{E}_{\mathcal{C}})$ . There is an infinite computation in  $\mathcal{P}$  that eventually only uses transitions from  $\mathcal{C}$  if and only if there is such a computation in  $\text{narrow}(\mathcal{P})$  using transitions from  $\text{narrow}(\mathcal{C})$ .*

*Proof.* The right to left implication holds by Theorem 2 as  $\text{narrow}(\mathcal{P}) \subseteq \widehat{\mathcal{P}}$ . For the left to right implication, by Theorem 2 an infinite computation of  $\mathcal{P}$  staying in  $\mathcal{C}$  yields an infinite computation of  $\widehat{\mathcal{P}}$  staying in  $\widehat{\mathcal{C}}$ . By induction, for any location  $\ell \in \mathcal{L}(\mathcal{C})$  we have that  $\mathcal{Q}(\ell)$  is an (unconditional) invariant at location  $\widehat{\ell}$  of  $\widehat{\mathcal{P}}$ : now the initiation condition also holds by definition because all transitions arriving at  $\widehat{\ell}$  require  $\mathcal{Q}(\ell)$  to hold. By Theorem 1, no infinite computation in  $\widehat{\mathcal{P}}$  staying in  $\widehat{\mathcal{C}}$  can reach a location of the form  $\widehat{\ell}$ , where  $\ell \in \mathcal{L}(\mathcal{C})$ . So such an infinite computation is a computation of  $\text{narrow}(\mathcal{P})$  that eventually only uses transitions from  $\text{narrow}(\mathcal{C})$ .  $\square$

Our termination proofs are sequences of relatively simple program transformations and termination proving techniques. This formal simplicity allows one to easily implement, extend and certify the technique. As discussed in Example 2, the unfolding/narrowing mechanism provides not only a termination proof, but also a characterization of the execution phases. In contrast to other works [37], these phases are obtained semantically from the generated conditional invariants, and do not require syntactic heuristics.

## 4.2 Proving Program Termination

So far we have discussed the handling of a single component at a time. By combining our method with an off-the-shelf safety checker, full program termination can be proven too. The next example illustrates this while comparing with previous Max-SMT-based techniques for proving termination [30].

*Example 3.* The method from [30] considers components following a topological ordering. Each component is dealt with locally: only its transitions and entries are taken into account, independently of the rest of the program. The analysis of a component concludes when the

```

int x = nondet();
int y = nondet();
int z = nondet();
assume(x > z && z ≥ 0);
while (z > 0) {
    x = x - 1;
    z = z - 1; }
ℓ : while (y < 0)
    y = y + x;
    
```

**Fig. 5.** Program that cannot be proven terminating with the approach in [30].

**Algorithm 2.** Procedure Term for proving or disproving program termination**Input:** program  $\mathcal{P}$ **Output:** Yes (resp., No) if  $\mathcal{P}$  terminates (resp., does not terminate), or  $\perp$  if unknown

---

```

1:  $\mathcal{S} \leftarrow$  stack of components of  $\mathcal{P}$ 
2: while  $\mathcal{S} \neq \emptyset \wedge \neg \text{timed\_out}()$  do
3:    $\mathcal{C} \leftarrow \text{Pop}(\mathcal{S})$ 
4:    $\mathcal{E} \leftarrow \mathcal{E}_{\mathcal{C}}$ 
5:    $\mathcal{Q} \leftarrow \text{CondTerm}(\mathcal{C}, \mathcal{E})$ 
6:   while  $\mathcal{Q} \neq \text{None}$  do
7:     if  $\forall \ell \in \mathcal{L}(\mathcal{C}) \cap \mathcal{L}(\mathcal{E})$ :  $\mathcal{P}$  is safe for assertion( $\ell, \mathcal{Q}(\ell)$ ) then      {Call safety
      check}
8:       break
9:        $\mathcal{C} \leftarrow \{ (\ell_s, \rho \wedge \neg \mathcal{Q}(\ell_t)', \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{C} \}$       {Narrow component}
10:       $\mathcal{E} \leftarrow \{ (\ell_s, \rho \wedge \neg \mathcal{Q}(\ell_t)', \ell_t) \mid (\ell_s, \rho, \ell_t) \in \mathcal{E} \}$       {Narrow entries}
11:       $\mathcal{Q} \leftarrow \text{CondTerm}(\mathcal{C}, \mathcal{E})$ 
12:   if  $\mathcal{Q} = \text{None}$  then return (ProvedNonTermination( $\mathcal{C}, \mathcal{E}, \mathcal{P}$ ) ? No :  $\perp$ )
13: return ( $\mathcal{S} = \emptyset$  ? Yes :  $\perp$ )

```

---

component is proven (unconditionally) terminating. Hence, for the program in Fig. 5, the first loop is proven terminating using the ranking function  $z$ . However, if no additional information is inferred, then the proof of termination of the second loop cannot succeed: the necessary invariant that  $x \geq 1$  between the two loops, at program location  $\ell$ , is missing.

On the other hand, the approach proposed here is able to handle this program successfully. Indeed, the first loop can be proven terminating with  $z$  as a ranking function as observed above. Regarding the second loop, the conditional invariant  $x \geq 1$  together with the ranking function  $-y$  are generated. To prove  $x \geq 1$  holds at  $\ell$  a safety checker may be used, which then makes a global analysis to verify the truth of the assertion. Finally full termination can be established. Note that components may be considered in any order, not necessarily a topological one.  $\blacksquare$

Example 3 illustrates that combining our conditional termination proving technique with a safety checker is necessary to efficiently handle long and complex programs.

It is also important to note that, as the proof of Corollary 1 indicates, the narrowed program is termination-equivalent to the original program. In particular, this means that a non-termination proof for the narrowed program is a non-termination proof for the original program, as only terminating computations have been discarded by the transformation. Further, our program transformation does not only add information to the entry transitions (as in [32]) but also to all transitions occurring in the component under analysis. This significantly improves the precision of our otherwise unchanged non-termination analysis (cf. Sect. 5).

Altogether, our procedure for proving or disproving program termination is described in Algorithm 2. It takes as input a program  $\mathcal{P}$  and returns **Yes** if the program can be proved to terminate, **No** if it can be proved not to terminate, or  $\perp$  otherwise. Components are handled in sequence one at a time provided the time limit has not been exceeded (line 2). For each component, preconditions for termination are computed (lines 5 and 11), which are then checked to hold by calling an external safety checker (line 7). If this test is passed, the component is guaranteed to terminate and the next one can be considered. Otherwise narrowing is applied (lines 9–10) and the process is repeated. If at some point the generation of preconditions for termination fails, then non-termination is attempted by calling an out-of-the-box non-termination prover (line 12). Note that the outer loop can easily be parallelized (i.e., all components can be considered at the same time), and that similarly, the generation of more preconditions can be attempted in parallel to the safety checks for already generated termination conditions. The correctness of Algorithm 2 follows directly from Corollary 1.

## 5 Related Work and Experimental Results

We build on a rich tradition of methods to prove termination [1, 10, 16, 18, 21, 22, 25, 26, 28, 32, 36, 39, 42–44] and non-termination [4, 13, 14, 24, 29, 46] of imperative programs. Most notably, our constraint-based approach to conditional termination is an extension of existing work on ranking function synthesis using constraint solvers [2, 3, 5, 8, 23, 30, 33, 35], and is most closely related to our earlier work on using Max-SMT solving to infer quasi-ranking functions [30]. There, an independent invariant generation procedure was used before unconditional termination arguments were synthesized for program components. Thus, invariants were not generated “on demand” and the method fails on examples such as Example 3.

The key contribution of our method is to use conditional termination arguments to segment the state space for the remainder of the analysis. A related idea was used in TRex [25] and HipTNT+ [32], which alternate termination and non-termination proving techniques in their proof search. However, both approaches only use preconditions for *non-termination* to segment the state space, and thus are reliant on non-termination techniques. As finding non-termination arguments is an  $\exists\forall\exists$  problem (there exists a state set such that for all its states there exists a computation leading back to it), these methods tend to be significantly weaker in practice than those based on termination, which is an  $\exists\forall$  problem (there exists a ranking function such that all computations decrease it).

A related idea is counterexample-guided termination proving [10, 16, 26, 28, 36], in which a speculated termination argument is refined until it covers all program executions. Thus, these methods *grow* a set of terminating program states, whereas our method *shrinks* the set of potentially non-terminating states. In practice, “ignoring” the terminating states in a safety prover is often a non-trivial semantic operation, in contrast to the effects of our syntactic *narrowing* operation.

Proving conditional termination has seen less interest than full termination analysis. A first technique combined constraint-based methods for finding potential ranking functions with quantifier elimination [15] to infer preconditions. More recently, policy iteration-based methods [34], backwards reasoning in the abstract interpretation framework [45] and an adaptation of conflict-driven learning from satisfiability solving [17] have been adapted to find conditions for termination. Our algorithm `CondTerm` differs in its relative simplicity (by delegating the majority of the work to a constraint solver), and our procedure `Term` could combine it with or replace it by other approaches. Finally, in a related line of work, decision procedures for conditional termination on restricted programming languages (e.g., only using linear or affine operations) have been developed [7].

*Evaluation.* To evaluate our method, we have implemented Algorithm 2 in the tool `VeryMax`, using it as a safety prover [12] and a non-termination prover [29]. The effectiveness of `VeryMax` depends crucially on the underlying non-linear Max-SMT solver, described in [31]. All experiments were carried out on the StarExec cluster [40], whose nodes are equipped with Intel Xeon 2.4GHz processors.<sup>5</sup> We have compared `VeryMax` with a range of competing termination provers on three benchmark sets. The first two example sets are the benchmark suites `INTEGER TRANSITION SYSTEMS` and `C INTEGER` used in `termCOMP 2016` [41], on which we compare with a superset of the tools [22, 26, 27, 32, 44] that competed in these categories in the 2016 edition<sup>6</sup>. Following the rules of the competition, we use a wall clock timeout of 300s for the `C INTEGER` benchmark set, and a wall clock timeout of 30s for the `INTEGER TRANSITION SYSTEMS` benchmark set. The results of these experiments are displayed in Table 1, where the “Term” (resp. “NTerm”) column indicates the number of examples proven terminating (resp. non-terminating), “Fail” any kind of prover failure, and “TO”

**Table 1.** Experimental results on benchmarks from `termCOMP 2016`.

Tool	C INTEGER					INTEGER TRANSITION SYSTEMS				
	Term	NTerm	Fail	TO	Total (s)	Term	NTerm	Fail	TO	Total (s)
<code>AProVE</code>	210	73	39	13	7547.68	623	386	11	202	9651.05
<code>Ctrl</code>	–	–	–	–	–	348	0	421	453	17229.10
<code>HipTNT+</code>	210	95	25	5	2615.80	–	–	–	–	–
<code>SeaHorn</code>	171	73	19	72	22499.33	–	–	–	–	–
<code>Ultimate</code>	208	98	23	6	4745.79	–	–	–	–	–
<b><code>VeryMax</code></b>	<b>213</b>	<b>100</b>	<b>22</b>	<b>0</b>	<b>2354.94</b>	<b>620</b>	<b>412</b>	<b>103</b>	<b>87</b>	<b>8481.39</b>

<sup>5</sup> A binary of `VeryMax` as well as the detailed results of the experiments can be found at <http://www.cs.upc.edu/~albert/VeryMax.html>.

<sup>6</sup> Due to incompatibilities of input formats, some tools could not be run on some of the benchmark sets. This is indicated in the tables with a dash –.

the number of times the timeout was reached. Finally, “Total (s)” indicates the total time spent on all examples, in seconds.

We additionally evaluated our tool on the examples from the TERMINATION category of the Software Verification Competition 2016, comparing to the participants in 2016 [22, 26, 44] with a CPU timeout of 900 s. As `VeryMax` has no support for recursion and pointers at the moment, we removed 273 examples

using these features and tested the tools on the remaining 358 examples. The results of this experiment are shown in Table 2. Altogether, the overall experimental results show that our method is not only the most powerful combined termination and non-termination prover, but also more efficient than all competing tools.

Moreover, to analyze the effect of our narrowing technique on non-termination proofs, we experimented with the INTEGER TRANSITION SYSTEMS benchmark set. Namely, in Table 3 we compare the performance of `VeryMax` when trying to prove non-termination of narrowed components

(`Narrowing` row) against using the original component (`Original` row). For each case, column “NTerm” indicates the examples proven non-terminating, and column “Exclusive” identifies those that could only be proven with that approach. The results show that removing (conditionally) terminating computations from the analysis significantly improves the effectiveness of the analysis. Still, the more complex narrowed components make the non-termination procedure in `VeryMax` time out in 4 cases that are otherwise proved non-terminating when using the original program components.

Finally, we studied the gain obtained with constraint strengthening, constraint softening and pseudo-invariants of the termination component (see Sect. 3). While constraint softening and pseudo-invariants help in proving termination in few cases at the cost of a time overhead, constraint strengthening significantly improves both the number of problems proved terminating and the time required to do so.

## 6 Conclusions and Future Work

We have proposed a new method for modular termination proofs of integer programs. A program is decomposed into program components, and conditional termination arguments are sought for each component separately. Termination arguments are synthesized iteratively using a template-based approach with a Max-SMT solver as a constraint solving engine. At each iteration, conditional invariants and ranking functions are generated which prove termination for a

**Table 2.** Results on SV-COMP benchmarks.

Tool	Term	NTerm	Fail	TO	Total (s)
AProVE	222	76	41	19	10235.44
SeaHorn	189	75	22	72	34760.69
Ultimate	224	103	25	6	7882.13
<b>VeryMax</b>	<b>231</b>	<b>101</b>	<b>26</b>	<b>0</b>	<b>2444.29</b>

**Table 3.** Impact of narrowing on non-termination proofs.

	NTerm	Exclusive
<code>Narrowing</code>	412	82
<code>Original</code>	334	4

subset of program execution states. The key step of our technique is to exclude these states from the remaining termination analysis. This is achieved by narrowing, i.e., strengthening the transitions of the component and its entry transitions with the negation of the conditional invariant. This operation of narrowing can be viewed as unfolding the program in two phases, namely when the conditional termination argument holds and when it does not, and focusing on the latter, for which termination is not guaranteed yet.

In the future, we want to remove some of the limitations of our method. For example, we do not support the heap at this time, and combining our conditional termination proving procedure with a heap analysis would greatly extend the applicability of our approach. Moreover, as in many other techniques, numbers are treated as mathematical integers, not machine integers. However, a transformation that handles machine integers correctly by inserting explicit normalization steps at possible overflows [19] could be added. We are also interested in formally verifying our technique and to produce certificates for termination that can be checked by theorem provers [11]. Finally, we plan to extend our technique to proving bounds on program complexity. Finding such bounds is closely related to termination proving, and also requires to distinguish different phases of the execution precisely [20,38]. Our termination proving method does this naturally, and an adaption to complexity could thus yield more precise bounds.

## References

1. Albert, E., Arenas, P., Codish, M., Genaim, S., Puebla, G., Zanardini, D.: Termination analysis of Java Bytecode. In: Barthe, G., Boer, F.S. (eds.) FMOODS 2008. LNCS, vol. 5051, pp. 2–18. Springer, Heidelberg (2008). doi:[10.1007/978-3-540-68863-1\\_2](https://doi.org/10.1007/978-3-540-68863-1_2)
2. Alias, C., Darté, A., Feautrier, P., Gonnord, L.: Multi-dimensional rankings, program termination, and complexity bounds of flowchart programs. In: Cousot, R., Martel, M. (eds.) SAS 2010. LNCS, vol. 6337, pp. 117–133. Springer, Heidelberg (2010). doi:[10.1007/978-3-642-15769-1\\_8](https://doi.org/10.1007/978-3-642-15769-1_8)
3. Bagnara, R., Mesnard, F., Pescetti, A., Zaffanella, E.: A new look at the automatic synthesis of linear ranking functions. IC **215**, 47–67 (2012)
4. Bakhirkin, A., Piterman, N.: Finding recurrent sets with backward analysis and trace partitioning. In: Chechik, M., Raskin, J.-F. (eds.) TACAS 2016. LNCS, vol. 9636, pp. 17–35. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-49674-9\\_2](https://doi.org/10.1007/978-3-662-49674-9_2)
5. Ben-Amram, A.M., Genaim, S.: On the linear ranking problem for integer linear-constraint loops. In: POPL (2013)
6. Biere, A., Heule, M.J.H., van Maaren, H., Walsh, T. (eds.): Handbook of Satisfiability. IOS Press, Amsterdam (2009)
7. Bozga, M., Iosif, R., Konečný, F.: Deciding conditional termination. LCMS **10**(3), 1–61 (2014)
8. Bradley, A.R., Manna, Z., Sipma, H.B.: Linear ranking with reachability. In: Etesami, K., Rajamani, S.K. (eds.) CAV 2005. LNCS, vol. 3576, pp. 491–504. Springer, Heidelberg (2005). doi:[10.1007/11513988\\_48](https://doi.org/10.1007/11513988_48)
9. Brockschmidt, M., Cook, B., Fuhs, C.: Better termination proving through cooperation. In: Sharygina, N., Veith, H. (eds.) CAV 2013. LNCS, vol. 8044, pp. 413–429. Springer, Heidelberg (2013). doi:[10.1007/978-3-642-39799-8\\_28](https://doi.org/10.1007/978-3-642-39799-8_28)



10. Brockschmidt, M., Cook, B., Ishtiaq, S., Khlaaf, H., Piterman, N.: T2: temporal property verification. In: Chechik, M., Raskin, J.-F. (eds.) TACAS 2016. LNCS, vol. 9636, pp. 387–393. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-49674-9\\_22](https://doi.org/10.1007/978-3-662-49674-9_22)
11. Brockschmidt, M., Joosten, S.J., Thiemann, R., Yamada, A.: Certifying safety and termination proofs for integer transition systems. In: WST (2016)
12. Brockschmidt, M., Larraz, D., Oliveras, A., Rodríguez-Carbonell, E., Rubio, A.: Compositional safety verification with Max-SMT. In: FMCAD (2015)
13. Brockschmidt, M., Ströder, T., Otto, C., Giesl, J.: Automated detection of non-termination and `NullPointerException`, for JBC. In: Beckert, B., Damiani, F., Gurov, D. (eds.) FoVeOOS 2011. LNCS, vol. 7421, pp. 123–141. Springer, Heidelberg (2011). doi:[10.1007/978-3-642-31762-0\\_9](https://doi.org/10.1007/978-3-642-31762-0_9)
14. Chen, H.-Y., Cook, B., Fuhs, C., Nimkar, K., O’Hearn, P.: Proving nontermination via safety. In: Ábrahám, E., Havelund, K. (eds.) TACAS 2014. LNCS, vol. 8413, pp. 156–171. Springer, Heidelberg (2014). doi:[10.1007/978-3-642-54862-8\\_11](https://doi.org/10.1007/978-3-642-54862-8_11)
15. Cook, B., Gulwani, S., Lev-Ami, T., Rybalchenko, A., Sagiv, M.: Proving conditional termination. In: Gupta, A., Malik, S. (eds.) CAV 2008. LNCS, vol. 5123, pp. 328–340. Springer, Heidelberg (2008). doi:[10.1007/978-3-540-70545-1\\_32](https://doi.org/10.1007/978-3-540-70545-1_32)
16. Cook, B., Podelski, A., Rybalchenko, A.: Termination proofs for systems code. In: PLDI (2006)
17. D’Silva, V., Urban, C.: Conflict-driven conditional termination. In: Kroening, D., Păsăreanu, C.S. (eds.) CAV 2015. LNCS, vol. 9207, pp. 271–286. Springer, Cham (2015). doi:[10.1007/978-3-319-21668-3\\_16](https://doi.org/10.1007/978-3-319-21668-3_16)
18. Falke, S., Kapur, D., Sinz, C.: Termination analysis of C programs using compiler intermediate languages. In: RTA (2011)
19. Falke, S., Kapur, D., Sinz, C.: Termination analysis of imperative programs using bitvector arithmetic. In: Joshi, R., Müller, P., Podelski, A. (eds.) VSTTE 2012. LNCS, vol. 7152, pp. 261–277. Springer, Heidelberg (2012). doi:[10.1007/978-3-642-27705-4\\_21](https://doi.org/10.1007/978-3-642-27705-4_21)
20. Flores-Montoya, A., Hähnle, R.: Resource analysis of complex programs with cost equations. In: Garrigue, J. (ed.) APLAS 2014. LNCS, vol. 8858, pp. 275–295. Springer, Cham (2014). doi:[10.1007/978-3-319-12736-1\\_15](https://doi.org/10.1007/978-3-319-12736-1_15)
21. Ganty, P., Genaim, S.: Proving termination starting from the end. In: Sharygina, N., Veith, H. (eds.) CAV 2013. LNCS, vol. 8044, pp. 397–412. Springer, Heidelberg (2013). doi:[10.1007/978-3-642-39799-8\\_27](https://doi.org/10.1007/978-3-642-39799-8_27)
22. Giesl, J., Brockschmidt, M., Emmes, F., Frohn, F., Fuhs, C., Otto, C., Plücker, M., Schneider-Kamp, P., Ströder, T., Swiderski, S., Thiemann, R.: Analyzing program termination and complexity automatically with AProVE. JAR (2016, to appear)
23. Gonnord, L., Monniaux, D., Radanne, G.: Synthesis of ranking functions using extremal counterexamples. In: PLDI (2015)
24. Gupta, A., Henzinger, T.A., Majumdar, R., Rybalchenko, A., Xu, R.-G.: Proving non-termination. In: POPL (2008)
25. Harris, W.R., Lal, A., Nori, A.V., Rajamani, S.K.: Alternation for termination. In: Cousot, R., Martel, M. (eds.) SAS 2010. LNCS, vol. 6337, pp. 304–319. Springer, Heidelberg (2010). doi:[10.1007/978-3-642-15769-1\\_19](https://doi.org/10.1007/978-3-642-15769-1_19)
26. Heizmann, M., Hoenicke, J., Podelski, A.: Termination analysis by learning terminating programs. In: Biere, A., Bloem, R. (eds.) CAV 2014. LNCS, vol. 8559, pp. 797–813. Springer, Cham (2014). doi:[10.1007/978-3-319-08867-9\\_53](https://doi.org/10.1007/978-3-319-08867-9_53)
27. Kop, C., Nishida, N.: Constrained term rewriting tool. In: Davis, M., Fehnker, A., McIver, A., Voronkov, A. (eds.) LPAR 2015. LNCS, vol. 9450, pp. 549–557. Springer, Heidelberg (2015). doi:[10.1007/978-3-662-48899-7\\_38](https://doi.org/10.1007/978-3-662-48899-7_38)

28. Kroening, D., Sharygina, N., Tsitovich, A., Wintersteiger, C.M.: Termination analysis with compositional transition invariants. In: Touili, T., Cook, B., Jackson, P. (eds.) CAV 2010. LNCS, vol. 6174, pp. 89–103. Springer, Heidelberg (2010). doi:[10.1007/978-3-642-14295-6\\_9](https://doi.org/10.1007/978-3-642-14295-6_9)
29. Larraz, D., Nimkar, K., Oliveras, A., Rodríguez-Carbonell, E., Rubio, A.: Proving non-termination using Max-SMT. In: Biere, A., Bloem, R. (eds.) CAV 2014. LNCS, vol. 8559, pp. 779–796. Springer, Cham (2014). doi:[10.1007/978-3-319-08867-9\\_52](https://doi.org/10.1007/978-3-319-08867-9_52)
30. Larraz, D., Oliveras, A., Rodríguez-Carbonell, E., Rubio, A.: Proving termination of imperative programs using Max-SMT. In: FMCAD (2013)
31. Larraz, D., Oliveras, A., Rodríguez-Carbonell, E., Rubio, A.: Minimal-model-guided approaches to solving polynomial constraints and extensions. In: Sinz, C., Egly, U. (eds.) SAT 2014. LNCS, vol. 8561, pp. 333–350. Springer, Cham (2014). doi:[10.1007/978-3-319-09284-3\\_25](https://doi.org/10.1007/978-3-319-09284-3_25)
32. Le, T.C., Qin, S., Chin, W.: Termination and non-termination specification inference. In: PLDI (2015)
33. Leike, J., Heizmann, M.: Ranking templates for linear loops. In: Ábrahám, E., Havelund, K. (eds.) TACAS 2014. LNCS, vol. 8413, pp. 172–186. Springer, Heidelberg (2014). doi:[10.1007/978-3-642-54862-8\\_12](https://doi.org/10.1007/978-3-642-54862-8_12)
34. Massé, D.: Policy iteration-based conditional termination and ranking functions. In: McMillan, K.L., Rival, X. (eds.) VMCAI 2014. LNCS, vol. 8318, pp. 453–471. Springer, Heidelberg (2014). doi:[10.1007/978-3-642-54013-4\\_25](https://doi.org/10.1007/978-3-642-54013-4_25)
35. Podelski, A., Rybalchenko, A.: A complete method for the synthesis of linear ranking functions. In: Steffen, B., Levi, G. (eds.) VMCAI 2004. LNCS, vol. 2937, pp. 239–251. Springer, Heidelberg (2004). doi:[10.1007/978-3-540-24622-0\\_20](https://doi.org/10.1007/978-3-540-24622-0_20)
36. Podelski, A., Rybalchenko, A.: ARMC: the logical choice for software model checking with abstraction refinement. In: Hanus, M. (ed.) PADL 2007. LNCS, vol. 4354, pp. 245–259. Springer, Heidelberg (2007). doi:[10.1007/978-3-540-69611-7\\_16](https://doi.org/10.1007/978-3-540-69611-7_16)
37. Sharma, R., Dillig, I., Dillig, T., Aiken, A.: Simplifying loop invariant generation using splitter predicates. In: Gopalakrishnan, G., Qadeer, S. (eds.) CAV 2011. LNCS, vol. 6806, pp. 703–719. Springer, Heidelberg (2011). doi:[10.1007/978-3-642-22110-1\\_57](https://doi.org/10.1007/978-3-642-22110-1_57)
38. Sinn, M., Zuleger, F., Veith, H.: A simple and scalable static analysis for bound analysis and amortized complexity analysis. In: Biere, A., Bloem, R. (eds.) CAV 2014. LNCS, vol. 8559, pp. 745–761. Springer, Cham (2014). doi:[10.1007/978-3-319-08867-9\\_50](https://doi.org/10.1007/978-3-319-08867-9_50)
39. Spoto, F., Mesnard, F., Payet, É.: A termination analyser for Java Bytecode based on path-length. *TOPLAS* **32**(3), 8:1–8:70 (2010)
40. Stump, A., Sutcliffe, G., Tinelli, C.: StarExec: a cross-community infrastructure for logic solving. In: Demri, S., Kapur, D., Weidenbach, C. (eds.) IJCAR 2014. LNCS (LNAI), vol. 8562, pp. 367–373. Springer, Cham (2014). doi:[10.1007/978-3-319-08587-6\\_28](https://doi.org/10.1007/978-3-319-08587-6_28)
41. Termination Competition. <http://termination-portal.org/wiki/Termination-Competition>
42. Tsitovich, A., Sharygina, N., Wintersteiger, C.M., Kroening, D.: Loop summarization and termination analysis. In: Abdulla, P.A., Leino, K.R.M. (eds.) TACAS 2011. LNCS, vol. 6605, pp. 81–95. Springer, Heidelberg (2011). doi:[10.1007/978-3-642-19835-9\\_9](https://doi.org/10.1007/978-3-642-19835-9_9)
43. Urban, C.: The abstract domain of segmented ranking functions. In: Logozzo, F., Fähndrich, M. (eds.) SAS 2013. LNCS, vol. 7935, pp. 43–62. Springer, Heidelberg (2013). doi:[10.1007/978-3-642-38856-9\\_5](https://doi.org/10.1007/978-3-642-38856-9_5)

44. Urban, C., Gurfinkel, A., Kahsai, T.: Synthesizing ranking functions from bits and pieces. In: Chechik, M., Raskin, J.-F. (eds.) TACAS 2016. LNCS, vol. 9636, pp. 54–70. Springer, Heidelberg (2016). doi:[10.1007/978-3-662-49674-9\\_4](https://doi.org/10.1007/978-3-662-49674-9_4)
45. Urban, C., Miné, A.: A decision tree abstract domain for proving conditional termination. In: Müller-Olm, M., Seidl, H. (eds.) SAS 2014. LNCS, vol. 8723, pp. 302–318. Springer, Cham (2014). doi:[10.1007/978-3-319-10936-7\\_19](https://doi.org/10.1007/978-3-319-10936-7_19)
46. Velroyen, H., Rümmer, P.: Non-termination checking for imperative programs. In: Beckert, B., Hähnle, R. (eds.) TAP 2008. LNCS, vol. 4966, pp. 154–170. Springer, Heidelberg (2008). doi:[10.1007/978-3-540-79124-9\\_11](https://doi.org/10.1007/978-3-540-79124-9_11)