Taming the Wrapping of Integer Arithmetic

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Abstract. Variables in programs are usually confined to a fixed number of bits and results that require more bits are truncated. Due to the use of 32-bit and 64-bit variables, inadvertent overflows are rare. However, a sound static analysis must reason about overflowing calculations and conversions between unsigned and signed integers; the latter remaining a common source of subtle programming errors. Rather than polluting an analysis with the low-level details of modelling two's complement wrapping behaviour, this paper presents a computationally light-weight solution based on polyhedral analysis which eliminates the need to check for wrapping when evaluating most (particularly linear) assignments.

1 Introduction

Static analysis methods are increasingly used to prove the partial correctness of software [5]. In contrast to formal methods that verify properties of a highlevel specification, a static analysis is complicated by low-level details of source code. For instance, while a specification expresses properties over arbitrary integers, variables in a program are usually confined to finite integer types that are deemed to be large enough to hold all values occurring at run-time. On one hand, the use of 32-bit and 64-bit variables make accidental overflows rare and adding checks to each transfer function of the analysis seems to be excessive considering the infrequency of variable overflows. On the other hand, programmers often inadvertently introduce wrapping when converting between signed and unsigned variables and deliberately exploit the wrapping effects of two's complement arithmetic. Thus, wrapping itself should not be considered harmful, particularly when the objective of an analysis is the verification of a different property, such as absence of out-of-bound memory accesses [8,9].

In fact, there is a danger in flagging all wrapping, since any intentional use of wrapping generates a warning message which the developer immediately dismisses as a false positive. The code in Figure 1 illustrates why this is a problem. The purpose of the shown C function is to print how many times each individual character occurs in the given string ***str**. To this end, the elements of **dist** are initialised to zero by the call to **memset**. In the loop that follows, the *n*th element is incremented each time a character *n* is encountered in **str**. The **for** loop then prints the distribution of printable ASCII characters.

The shown program is correct on platforms where char is unsigned such as Linux on PowerPC. However, for Linux on x86 and MacOS X on PowerPC, char

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Fig. 1. Example C function that counts the occurrences of each character

is signed. In the latter case, the value ***str** which is used to index into **dist** can take on values in the range [-128, 127]. Although the programmer intended to convert the value of ***str** to an unsigned value before the extension to a 4-byte quantity takes place, the C standard [3] dictates that the value of ***str** is first promoted to **int** before the conversion to an unsigned type is performed. Hence, **dist** can be accessed at indices $[2^{32}-128...2^{32}-1] \cup [0...127]$, of which the first range is out-of-bounds. A static analysis that considers all wrapping to be erroneous would flag this statement as possibly faulty. However, since the programmer expects that wrapping does occur (namely when converting from a **char** to an unsigned quantity), the warning about wrapping at the **unsigned int**-level will be dismissed as a false positive. Hence, the analysis above should flag the out-of-bound array access but treat wrapping itself as intentional.

In this work, we propose a re-interpretation of polyhedra in which the wrapping of integer calculations is reflected in the classic polyhedral domain [6]. In particular, we avoid making cross-cutting changes to all transfer functions but refine the approximation relation such that wrapping is mostly implicit, that is, the need for extra polyhedral operations is largely finessed. For the few cases in which wrapping has to be addressed in the transfer functions, we illustrate how to wrap values within the polyhedral domain and propose an algorithm for doing so. To summarise our contributions, this paper presents:

- an approximation relation that implicitly wraps polyhedral variables;
- an algorithm to perform wrapping in the polyhedral domain;
- a formal description of an analysis that faithfully models wrapping and an accompanying correctness argument.

We commence with the definition of a small language and its concrete semantics. Section 3 introduces polyhedral analysis. Sections 4 and 5 explain how wrapping is supported. Section 6 presents a wrapping-aware polyhedral analysis which is discussed in Section 7. We conclude with the related work in Section 8.

2 A Language Featuring Finite Integer Arithmetic

The function shown in Figure 1 demonstrates that it is important to clarify where wrapping can arise in a program. This is particularly true when arguing the correctness of an analysis. To this end we introduce the language $\mathcal{L}(ELang)$ which is a subset of an intermediate language which is used to analyse C programs.

2.1 The Syntax of $\mathcal{L}(ELang)$

 $\mathcal{L}(\text{ELang})$ features linear expressions and casts between integers. In the following grammar of $\mathcal{L}(\text{ELang})$, (T)^{*} denotes the repetition of the non-terminal T.

$$\begin{array}{l} \langle \mathrm{ELang} \rangle :: (\mathrm{Block})^* \\ \langle \mathrm{Block} \rangle ::: \mathbf{i:} (\langle \mathrm{Stmt} \rangle ;)^* \langle \mathrm{Next} \rangle \\ \langle \mathrm{Next} \rangle & ::: \mathbf{jump} \ l; \\ & | \ \mathbf{if} \langle \mathrm{Type} \rangle \ v \ \langle \mathrm{Op} \rangle \ \langle \mathrm{Expr} \rangle \ \mathbf{then} \ \mathbf{jump} \ l; \ \langle \mathrm{Next} \rangle \\ \langle \mathrm{Op} \rangle & :: < | \leq | = | \neq | \geq | > \\ \langle \mathrm{Expr} \rangle & :: n \ | \ n \ * v + \langle \mathrm{Expr} \rangle \\ \langle \mathrm{Stmt} \rangle & :: \langle \mathrm{Size} \rangle \ v = \langle \mathrm{Type} \rangle \ v \\ \langle \mathrm{Type} \rangle & :: (\mathbf{uint} \ | \ \mathbf{int}) \ \langle \mathrm{Size} \rangle \\ \langle \mathrm{Size} \rangle & :: \mathbf{1} \ | \ \mathbf{2} \ | \ \mathbf{4} \ | \ \mathbf{8} \end{array}$$

An ELang program consists of a sequence of basic blocks with execution commencing with the first block. Each basic block consists of a sequence of statements and a list of control-flow instructions. In the sequel, we use lookupBlock(l)and lookupNext(l) to access the statements and control-flow instructions, respectively, of basic block l. The control-flow of a basic block consists of either a **jump** statement or a conditional which itself is followed by more control flow instructions. The $\langle Stmt \rangle$ production is restricted to the two statements of interest, namely the assignment of linear expressions to a variable and a type cast. We require that each variable in the program is used with only one size which is always specified in bytes. In particular, the assignment statement and the conditional require that all occurring variables are of the same size. Note, though, that variables may be used as an **uint** (unsigned integer) in one statement and as an **int** (signed integer) in another.

2.2 The Semantics of $\mathcal{L}(\text{ELang})$

In order to specify the semantics of $\mathcal{L}(\text{ELang})$, we introduce the following notation. Let $\mathbb{B} = \{0, 1\}$ denote the set of Booleans. A vector $\mathbf{b} = \langle b_{w-1}, \ldots b_0 \rangle \in \mathbb{B}^w$ is interpreted as unsigned integer by $val^{w,\text{uint}}(\mathbf{b}) = \sum_{i=0}^{w-1} b_i 2^i$ and as signed integer by $val^{w,\text{int}}(\mathbf{b}) = (\sum_{i=0}^{w-2} b_i 2^i) - b_{w-1} 2^{w-1}$. Conversely, $bin^w : \mathbb{Z} \to \mathbb{B}^w$ converts an integer to the lower w bits of its Boolean representation. Formally, $bin^w(v) = \mathbf{b}$ iff there exists $\mathbf{b}' \in \mathbb{B}^q$ such that $val^{(q+w),\text{int}}(\mathbf{b}' || \mathbf{b}) = v$ where || denotes the concatenation of bit-vectors. For instance, $bin^3(15) = \langle 1, 1, 1 \rangle$ since

Basic Blocks.

$$\llbracket l: s_1; \dots s_n; \ \rrbracket_{\text{Block}}^{\natural} \sigma = \llbracket lookupNext(l) \ \rrbracket_{\text{Next}}^{\natural}(\llbracket s_n \ \rrbracket_{\text{Stmt}}^{\natural}(\dots (\llbracket s_1 \ \rrbracket_{\text{Stmt}}^{\natural} \sigma) \dots))$$

Control Flow.

 $[\![\mathbf{jump}\ l\]\!]_{\mathrm{Next}}^{\natural}\sigma = [\![\ lookupBlock(l)\]\!]_{\mathrm{Block}}^{\natural}\sigma$

 $\begin{bmatrix} \mathbf{if} \ t \ s \ v \ op \ exp \ \mathbf{then} \ \mathbf{jump} \ l \ ; nxt \]_{\mathrm{Next}}^{\natural} \sigma = \\ \begin{cases} \begin{bmatrix} lookupBlock(l) \]_{\mathrm{Block}}^{\natural} \sigma & \mathrm{if} \ val^{8s,t}(\sigma^{s}(addr^{\natural}(v))) \ op \ val^{8s,t}(\llbracket \ exp \]_{\mathrm{Expr}}^{\natural,s} \sigma) \\ \\ \llbracket \ nxt \]_{\mathrm{Next}}^{\natural} \sigma & \mathrm{otherwise} \end{cases}$

$$\begin{split} & \text{Expressions.} \\ & \llbracket n \rrbracket_{\text{Expr}}^{\natural,s} \sigma = bin^{8s}(n) \\ & \llbracket n * v + exp \rrbracket_{\text{Expr}}^{\natural,s} \sigma = bin^{8s}(n) *^{8s} \sigma^{s}(addr^{\natural}(v)) + {}^{8s} \llbracket exp \rrbracket_{\text{Expr}}^{\natural,s} \sigma \end{split}$$

Assignment.

 $[\![s \ v = exp \]\!]_{\mathrm{Stmt}}^{\natural} \sigma = \sigma[addr^{\natural}(v) \stackrel{s}{\mapsto} [\![exp \]\!]_{\mathrm{Expr}}^{\natural,s} \sigma]$

Type Casts. $\llbracket s_1 v_1 = t \ s_2 \ v_2 \ \rrbracket_{\operatorname{Stmt}}^{\natural} \sigma = \sigma [addr^{\natural}(v_1) \stackrel{s_1}{\mapsto} bin^{8s_1}(val^{8s_2,t}(\sigma^{s_2}(addr^{\natural}(v_2))))]$

Fig. 2. Concrete Semantics of $\mathcal{L}(ELang)$

 $val^{5,\text{int}}(\langle 0,1,1,1,1\rangle) = 15$. In order to distinguish calculations on Boolean vectors from standard arithmetic, let $+^{w}, *^{w} : \mathbb{B}^{w} \times \mathbb{B}^{w} \to \mathbb{B}^{w}$ denote addition and multiplication that truncate the result to the lower w bits, for instance $\langle 1,1,1,1\rangle + {}^{4}\langle 0,0,0,1\rangle = \langle 0,0,0\rangle$. Note that the signedness of the arguments of $+^{w}$ and $*^{w}$ do not affect the result of these operations.

 $\mathcal{L}(\text{ELang})$ programs operate in a virtual memory environment which we formalise as a sequence of bytes. Let $\mathcal{B} = \mathbb{B}^8$ denote the set of bytes and $\Sigma = \mathcal{B}^{2^{32}}$ all states of 4 GByte that a program on a 32-bit architecture can take on. Let $\sigma \in \Sigma$ denote a given memory state of a program and let $\sigma^s : [0, 2^{32} - 1] \to \mathcal{B}^s$ denote a read access at the given 32-bit address where $s \in \{1, 2, 4, 8\}$ is the number of bytes to be read. A write operation is formalised as a substitution $\sigma[a \stackrel{s}{\mapsto} v]$. The resulting store $\sigma' = \sigma[a \stackrel{s}{\mapsto} v]$ satisfies $\sigma'^s(a) = v$ and furthermore $\sigma'^1(b) = \sigma^1(b)$ for all addresses $b \notin \{a, \ldots a + s - 1\}$.

Figure 2 presents the concrete semantics (or natural semantics, hence the \natural) of the $\mathcal{L}(\text{ELang})$ language. These definitions use $addr^{\natural}(v) \in [0, 2^{32} - 1]$ which maps the program variable v to its address in memory. We assume that $addr^{\natural}$ maps different variables to non-overlapping memory regions, an assumption that makes $\mathcal{L}(\text{ELang})$ independent of the endianness of an architecture.

The concrete semantics manipulates the store mainly by operations on bitvectors; only in the conditional and in the cast are bit vectors interpreted as numbers. In these cases the signedness of the variables can actually influence the result. In particular, the type t of the cast determines if the source bit-vector is sign-extended (if t = int) or zero-extended (if t = uint) when $s_1 \ge s_2$. We now proceed by abstracting this semantics so as to specify a polyhedral analysis.

3 Polyhedral Analysis of Finite Integers

Two's complement arithmetic exploits the wrapping behaviour of integer variables that are confined to a fixed number of bits. For instance, subtracting 1 from an integer is equivalent to adding the largest representable integer value. In fact, the binary representation of the signed integer -1 is identical to that of the largest, unsigned integer of the same size. In the context of verification, this dichotomy in interpretation cannot be dismissed since $\mathcal{L}(\text{ELang})$ has insufficient information about the signedness of assignments. This omission allows our model to be applied to languages which freely mix signed and unsigned values, e.g. C.

Accessing the same bit sequence as either signed or unsigned integer corresponds to a wrapping behaviour in that the negative range of the signed integer wraps to the upper range of an unsigned integer, see Figure 3. This wrapping behaviour of finite integers creates a mismatch against the infinite range of polyhedral variables. We present our solution to this mismatch in two parts: Section 4 presents a concretisation map between the polyhedral domain and the bit-level representation of variables. This map wraps values of abstract variables implicitly to finite sequences of bits, thereby alleviating the need to check for wrapped values each time a variable is read or written. In contrast, Section 5 details an algorithm that makes the wrapping of program variables explicit in the abstract domain which is important for casts and the conditional statement whose semantics depend on the size and signedness of the operands.

3.1 The Domain of Closed, Convex Polyhedra

In order to make the paper self-contained, this section gives a concise introduction to the notation used in our polyhedral analysis. Let $X = \{x_1, \ldots, x_n\}$ be a finite set of variables, let $\mathbf{x} = \langle x_1, \ldots, x_n \rangle$ and let I be the set of linear inequalities that can be rewritten as $\mathbf{c} \cdot \mathbf{x} \leq d$ where $\mathbf{c} \in \mathbb{Z}^n$ and $d \in \mathbb{Z}$. For brevity we write $e_1 = e_2$ to denote the set of inequalities $\{e_1 \leq e_2, e_2 \leq e_1\}$ where e_i is any linear expression. Furthermore, let $e_1 < e_2$ abbreviate $e_1 \leq e_2 - 1$. Let $\iota \in I$ be an inequality that can be transformed into $\mathbf{c} \cdot \mathbf{x} \leq d$, then $[\![\iota]\!] = \{\mathbf{x} \in \mathbb{R}^n \mid \mathbf{c} \cdot \mathbf{x} \leq d\}$ denotes the induced half-space. Given a finite set of inequalities $E = \{\iota_1, \ldots, \iota_n\} \subseteq I$ the notation $[\![E]\!] = \bigcap_{i=1,\ldots,n} [\![\iota_i]\!]$ denotes the induced convex polyhedron. Let



Fig. 3. The difference between a signed and an unsigned access can be interpreted as a wrap of negative values to the upper range in an unsigned access

Poly be the set of all convex polyhedra (polyhedra for short). Given two polyhedra $P_1, P_2 \in Poly$, let $P_1 \sqsubseteq P_2$ iff $P_1 \subseteq P_2$ and let $P_1 \sqcap P_2 = P_1 \cap P_2$. Note that $P_1 \sqcap P_2 = \llbracket E_1 \cup E_2 \rrbracket$ where $P_i = \llbracket E_i \rrbracket$ and i = 1, 2. We write $P_1 \sqcup P_2$ to denote the closure of the convex hull [6] of two polyhedra P_1 and P_2 which is defined as the smallest polyhedron P such that $P_1 \cup P_2 \sqsubseteq P$. Let $P \in Poly$ be non-empty and $V_i = \{v_i \mid \langle v_0, \dots, v_n \rangle \in P\}$. We write P(x) = [l, u] where $l = \lceil \min(V_i) \rceil$ if the minimum exists, otherwise $l = -\infty$ and $u = \lfloor \max(V_i) \rfloor$ if the maximum exists, otherwise $u = \infty$. Define $\exists_{x_i} : Poly \to Poly$ such that $\exists_{x_i}(P) = \{ \langle v_1, \dots, v_{i-1}, x, v_{i+1}, \dots, v_n \rangle | \langle v_1, \dots, v_n \rangle \in P \land x \in \mathbb{R} \}.$ Let T denote an additional set of temporary variables such that $T \cap X = \emptyset$. All operations above lift from X to $T \cup X$. The operation $P \triangleright x := e \in Poly$ denotes an update of a variable $x \in X$ to the linear expression e and is defined as $\exists_t (\llbracket x = t \rrbracket \sqcap \exists_x (\llbracket t = e \rrbracket \sqcap P))$ where $t \in T$ does not occur in e and $t \neq x$. The rationale for assigning the result to a temporary variable is that e might contain x. Assigning e to a fresh variable t will retain the value of x before it is projected out. If x does not occur in e, the update above is equivalent to $\exists_x(P) \sqcap [x = e]$. In order to argue about updates of polyhedra, observe the following:

Lemma 1. Let $P \in Poly$ and $P' = P \triangleright x_i := \mathbf{c} \cdot \mathbf{x} + d$ with $\mathbf{c} \in \mathbb{Z}^n$ and $d \in \mathbb{Z}$. Then $P' = \{\langle v_1, \ldots, v_{i-1}, v'_i, v_{i+1}, \ldots, v_n \rangle \mid \mathbf{v} = \langle v_1, \ldots, v_n \rangle \in P \land v'_i = \mathbf{c} \cdot \mathbf{v} + d\}.$

4 Implicit Wrapping of Polyhedral Variables

This section formalises the relationship between polyhedral variables and bitsequences that constitute the program state. For simplicity, we assume a one-to-one correspondance between the variable names in the program and the polyhedral variables that represent their values. The values of a program variable are merely bit sequences that are prescribed by the possible values of the polyhedral variable. To illustrate, suppose that **x** is of type **char** and P(x) = [-1, 2]. The represented bit patterns are 11111111, 00000000, 00000001 and 00000010, no matter whether **x** is signed or unsigned. These bit patterns are given by $bin^{8s}(v)$ which turns a value $v \in [-1, 2]$ into a bit sequence of s bytes. Going further, the function $bits_a^s : \mathbb{Z} \to \mathcal{P}(\Sigma)$ produces all concrete stores in which 8s bits at address $a = addr^{\ddagger}(\mathbf{x})$ are set to the value corresponding to $v \in P(x)$ as follows:

$$bits_{a}^{s}(v) = \{ \langle r_{8*2^{32}} \dots r_{8(a+s)} \rangle \| bin^{8s}(v) \| \langle r_{8a-1} \dots r_{0} \rangle | r_{i} \in \mathbb{B} \}$$

Note that this definition only considers the lower 8s bits of the value v. For instance, $bits_a^1(0) = bits_a^1(256)$ since the lower eight bits of 0 and 256 are equal. The mapping $bits_a^s$ can be lifted from the value v of a single variable to the values $\langle v_1, \ldots, v_n \rangle \in \mathbb{Z}^n$ of a vector of variables $\langle x_1, \ldots, x_n \rangle$, resulting in the stores $\bigcap_{i \in [1,n]} bits_{a_i}^{s_i}(v_i)$. Here $a_i \in [0, 2^{32} - 1]$ denotes the address of the variable x_i in the concrete store and $s_i \in \mathbb{N}$ denotes its size in bytes. Observe that, if variables were allowed to overlap, the above intersection might incorrectly collapse to the empty set for certain vectors $\langle v_1, \ldots, v_n \rangle \in \mathbb{Z}^n$. Using this lifting, a polyhedron is now related to a set of stores by γ_a^s : Poly $\rightarrow \mathcal{P}(\Sigma)$ which is defined as

$$\gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P) = \bigcup_{\boldsymbol{v} \in P \cap \mathbb{Z}^n} \left(\bigcap_{i \in [1,n]} bits_{a_i}^{s_i}(v_i) \right)$$

where $\boldsymbol{s} = \langle s_1, \ldots s_n \rangle$, $\boldsymbol{a} = \langle a_1, \ldots a_n \rangle$ and $\boldsymbol{v} = \langle v_1, \ldots v_n \rangle$.

The definition of γ_a^s provides a criterion for judging the correctness of an abstract semantics. In addition, γ_a^s permits linear expressions to be evaluated in the abstract semantics without the need to address overflows since γ_a^s maps the result of calculations in the polyhedral domain to the correctly wrapped result in the actual program. This property is formalised below:

Proposition 1. Let $e \in \mathcal{L}(Expr)$ and $e \equiv \mathbf{c} \cdot \mathbf{x} + d$, that is, e is a reformulation of $\mathbf{c} \cdot \mathbf{x} + d$. If $\sigma \in \gamma_{\mathbf{a}}^{\mathbf{s}}(P)$ then $\sigma[a_i \stackrel{s_i}{\mapsto} \llbracket e \rrbracket_{Expr}^{\natural, s_i}] \in \gamma_{\mathbf{a}}^{\mathbf{s}}(P \triangleright x_i := \mathbf{c} \cdot \mathbf{x} + d)$.

Proof. Define $\pi_i(\langle x_1, \ldots, x_{i-1}, x_i, x_{i+1}, \ldots, x_n \rangle) = x_i$. Since $\sigma \in \gamma_a^s(P)$ there exists $v \in P \cap \mathbb{Z}^n$ such that $\sigma = \bigcap_{i \in [1,n]} bits_{a_i}^{s_i}(\pi_i(v))$. Let $P' = P \triangleright x_i := \mathbf{c} \cdot \mathbf{x} + d$ for some $\mathbf{c} \in \mathbb{Z}^n$ and $d \in \mathbb{Z}$. By Lemma 1, there exists $v' \in P'$ with $\pi_j(v') = \pi_j(v)$ for all $j \neq i$. Since $\{a_i, \ldots, a_i + s_i - 1\} \cap \{a_j, \ldots, a_j + s_j - 1\} = \emptyset$ for all $j \neq i$, there exists $\sigma' \in \gamma_a^s(P')$ such that $\sigma'^1(a) = \sigma^1(a)$ for $a \in [0, 2^{32} - 1] \setminus \{a_i, \ldots, a_i + s_i - 1\}$. Furthermore, the lemma states that $\pi_i(v') = \mathbf{c} \cdot \mathbf{v} + d$ and, by the definition of γ_a^s , it follows that $\sigma'^{s_i}(a_i) = bin^{8s_i}(\mathbf{c} \cdot \mathbf{v} + d)$. To show that $\sigma'^{s_i}(a_i) = [e]_{\text{Expr}}^{\natural,s_i} \sigma$, we find $\mathbf{a} \in \mathbb{Z}^n$, $d \in \mathbb{Z}$ such that $e \equiv \mathbf{c} \cdot \mathbf{x} + d$ and $[e]_{\text{Expr}}^{\natural,s_i} \sigma = bin^{8s_i}(\mathbf{c} \cdot \mathbf{v} + d)$ by induction over e:

- 1. Let e = n. By definition of $[\![\cdot]\!]_{\text{Expr}}^{\natural,s}$, $[\![n]\!]_{\text{Expr}}^{\natural,s_i} \sigma = bin^{8s_i}(n) = bin^{8s_i}(\boldsymbol{c} \cdot \boldsymbol{v} + d)$ where d = n and $\boldsymbol{c} = \langle 0, \ldots 0 \rangle$. Hence $e \equiv \boldsymbol{c} \cdot \boldsymbol{x} + d$.
- 2. Let $e = n * x_j + e'$. Suppose that $\llbracket e' \rrbracket_{\text{Expr}}^{\flat,s_i} \sigma = bin^{8s_i}(\mathbf{c}' \cdot \mathbf{v} + d')$ where $e' \equiv \mathbf{c}' \cdot \mathbf{x} + d'$. By the definition of $\llbracket \cdot \rrbracket_{\text{Expr}}^{\flat,s_i} \pi = bin^{8s_i}(\mathbf{c}' \cdot \mathbf{v} + d')$. By the definition of $\llbracket \cdot \rrbracket_{\text{Expr}}^{\flat,s_i}$, $\llbracket n * x_j + e \rrbracket_{\text{Expr}}^{\flat,s_i} \sigma = bin^{8s_i}(n) *^{8s_i} \sigma^{s_i}(a_j) + {}^{8s_i} \llbracket e' \rrbracket_{\text{Expr}}^{\flat,s_i} \sigma$ where $\sigma^{s_i}(a_j) = bin^{8s_i}(v_j)$. By definition of bin^{8s} , $bin^{8s}(n) *^{8s_i} bin^{8s_i}(v_j) = ((n \mod 2^{8s_i}) * (v_j \mod 2^{8s_i})) \mod 2^{8s_i} = (n * v_j) \mod 2^{8s_i}$, c.f. [7, page 42]. Similarly, $(n * v_j) \mod 2^{8s_i} + {}^{8s_i} \llbracket e' \rrbracket_{\text{Expr}}^{\flat,s_i} \sigma = (n * v_j) \mod 2^{8s_i} + {}^{8s_i} bin^{8s_i}(\mathbf{c}' \cdot \mathbf{v} + d') = (n * v_j + \mathbf{c}' \cdot \mathbf{v} + d') \mod 2^{8s_i}$. Thus, set d = d' and $\langle c_1, \ldots, c_n \rangle = \langle c'_1, \ldots, c'_{i-1}, c'_i + n, c'_{i+1}, \ldots, c'_n \rangle$ where $\mathbf{c} = \langle c_1, \ldots, c_n \rangle$ and $\mathbf{c}' = \langle c'_1, \ldots, c'_n \rangle$. Hence $e \equiv \mathbf{c} \cdot \mathbf{x} + d$.

The force of the above result is that a linear expression $\langle \text{Expr} \rangle$ over finite integer variables can be interpreted as an expression over polyhedral variables without regard for overflows or evaluation order. A prerequisite for this convenience is that all variables occurring in an expression have the same size *s*. In contrast, assignments between different sized variables have to revert to a cast statement. In this case, and in the case of conditionals, wrapping has to be made explicit which is the topic of the next section.

5 Explicit Wrapping of Polyhedral Variables

A consequence of the wrapping behaviour of γ_a^s is that the effect of a guard such as x<=y cannot be modelled as a transformation from a polyhedron P to



Fig. 4. Explicitly wrapping the possible values of x to its admissible range

 $P \sqcap [x \leq y]$. This section explains this problem, discusses possible solutions and proposes an efficient wrapping algorithm *wrap*.

5.1 Wrapping Variables with a Finite Range

In order to illustrate the requirements on the wrap function, consider Figure 4. The thick line in the upper graph denotes $P = [x + 1024 = 8y, -64 \le x \le 448]$ which we suppose feeds into the guard x<=y where x and y both represent variables of type **uint8**. In order to illustrate a peculiarity of modelling the guard, consider the point $\langle x, y \rangle = \langle 384, 176 \rangle \in P$ and let $\sigma \in \gamma_a^s(\{\langle 384, 176 \rangle\})$. Due to implicit wrapping in γ_a^s , the state σ stipulates that $val^{8,uint}(\sigma^1(addr^{\natural}(x))) = 128$ and $val^{8,uint}(\sigma^1(addr^{\natural}(y))) = 176$. Thus, although x<=y is true when interpreting x and y as **uint8** in σ , the polyhedron $\{\langle 384, 176 \rangle\} \sqcap [x \le y]$ is empty. Hence, it is not correct to model the guard in the classic way as $P \sqcap [x \le y]$.

In order to model relational tests correctly, the values of expressions occurring on each side of a relational operator have to be wrapped to the type prescribed in an $\mathcal{L}(\text{ELang})$ conditional. In the example, the expression y is already in the required range [0, 255] whereas the range of x impinges on the two neighbouring quadrants as indicated in the upper graph of Figure 4. These quadrants are obtained by partitioning the state P into $P_{-1} = P \sqcap [-256 \le x \le -1], P_0 =$ $P \sqcap [0 \le x \le 255]$ and $P_1 = P \sqcap [256 \le x \le 511]$. The result of wrapping x can now be calculated by translating P_{-1} by 256 units towards positive xcoordinates and P_1 by 256 units towards negative x-coordinates, yielding P' = $P_0 \sqcup (P_{-1} \triangleright x := x + 256) \sqcup (P_1 \triangleright x := x - 256)$. The contribution of each



Fig. 5. The quest for an efficient wrapping of unbounded variables

partition is shown as a thick line in the lower left graph and the grey region depicts $P' \sqcap [\![x \leq y]\!]$. Observe that a more precise state P'' can be obtained by intersecting each translated state separately with $[\![x \leq y]\!]$, that is, by calculating $(P_0 \sqcap [\![x \leq y]\!]) \sqcup ((P_{-1} \rhd x := x + 256) \sqcap [\![x \leq y]\!]) \sqcup ((P_1 \rhd x := x - 256) \sqcap [\![x \leq y]\!])$. This state, depicted as the grey area in the lower right graph, is smaller than P' since P_{-1} does not contribute at all. Indeed, this example shows that polyhedra are not meet-distributive, that is, $P \sqcap (P_1 \sqcup P_2) \neq (P \sqcap P_1) \sqcup (P \sqcap P_2)$. In this work, we chose to calculate the equivalent of P' in our wrapping function *wrap* as it simplifies the presentation; implementing the refined model is mere engineering.

5.2 Wrapping Variables with Infinite Ranges

In the given example, it was possible to obtain a wrapped representation of the values of x and y by calculating the join of three constituent state spaces. In general, however, wrapping x and y can require the join of an infinite number of constituent state spaces as depicted in Figure 5. Here, the line in the upper graph depicts P = [x+1024 = 8y], that is, P denotes the same linear relation as before, except that x is unbounded. Translating P by i times the range of **uint8** yields $P_i = (P \triangleright x := x + i2^8 \sqcap [0 \le x \le 255]) \sqcup (P \triangleright x := x - i2^8 \sqcap [0 \le x \le 255])$ for $i \ge 0$. A polyhedron that includes the sequence $P'_j = \bigsqcup_{0 \le i \le j} P_i$ can be computed using widening [6], thereby yielding the grey area in the lower right graph. In fact, this region is equivalent to $\exists_x(P) \sqcap [0 \le x \le 255]$ as it contains neither bounds on x nor relational information between x and other variables. This suggests that, rather than wrapping unbounded variables, it is cheaper and



Fig. 6. Precise wrapping of two bounded variables

as precise to set them to the whole range of their type. After wrapping x, it becomes apparent that y is unbounded too, and hence needs wrapping.

5.3 Wrapping Several Variables

Even though the guard $x \le y$ used in the example of Section 5.1 involves two variables, it was only necessary to wrap x to obtain a wrapped representation of both x and y. The example of Section 5.2 hints at the fact that both variables might need wrapping to ensure that both sides of the guard are within range. In particular, it is not possible to translate a guard $x \le y$ to the inequality $x - y \le 0$ and to merely wrap x - y to [0, 255]. To illustrate this, consider the simpler case $x \le 42$ which is satisfied for bit sequences of x that fall within [0, 42]. In order to evaluate $x \le 42$, set x' = x - 42 and wrap x' such that $0 \le x' \le 255$. The intersection with $[x' \le 0]$ constrains x' to 0 which implies x = 42 instead of $x \in [0, 42]$. Thus, both arguments to a guard $x \le y$ need to be wrapped independently.

The example in Figure 5 showed how wrapping the unbounded x leaves y unconstrained which thus has to be wrapped as well. Figure 6 shows a potentially more precise solution for bounded variables in which variables are wrapped simultaneously. Here, the bounded state space shown in grey expands beyond the state P_0 that corresponds to the actual range of the variables. The result of translating each neighbouring quadrant and intersecting it with $x \leq y$ is shown in the graph on the right. Note that the join of these four translated spaces retains no information on either x or y. While it is possible that relational information with other variables is retained, wrapping the variables independently has the same precision if one of the variables is within bounds and, in particular,

Algorithm 1. Explicitly wrapping an expression to the range of a type

procedure wrap(P, t s, x) where $P \neq \emptyset, t \in \{\text{uint, int}\}\$ and $s \in \{1, 2, 4, 8\}$ 1: $b_l \leftarrow 0$ 2: $b_h \leftarrow 2^s$ 3: if t = int then /* Adjust ranges when wrapping to a signed type. */ $b_l \leftarrow b_l - 2^{s-1}$ 4: $b_h \leftarrow b_h - 2^{s-1}$ 5: 6: end if 7: $[l, u] \leftarrow P(x)$ 8: if $l \neq -\infty \land u \neq \infty$ then /* Calculate quadrant indices. */ 9: $\langle q_l, q_u \rangle \leftarrow \langle |(l-b_l)/2^s|, |(u-b_l)/2^s| \rangle$ 10: end if 11: if $l = -\infty \lor u = \infty \lor (q_u - q_l) > k$ then /* Set to full range. */ return $\exists_x(P) \sqcap \llbracket b_l \leq x < b_h \rrbracket$ 12:13: else /* Shift and join quadrants $\{q_1, \ldots, q_u\}$. */ return $\bigsqcup_{q \in [q_l, q_u]} ((P \triangleright x := x - q2^s) \sqcap \llbracket b_l \le x < b_h \rrbracket)$ 14:15: end if

if a variable is compared to a constant. In the 3000 LOC program qmail-smtp that our analysis targets, 427 out of 522 conditionals test a variable against a constant, which motivates our design choice of wrapping variables independently.

5.4 An Algorithm for Explicit Wrapping

Guided by the observations made in the three examples, Algorithm 1 gives a procedure to wrap a polyhedral variable to the range of a given integer type. Due to the observations in the last section, we only present an algorithm to wrap one variable at a time. Thus, both sides of a guard have to be wrapped individually.

The algorithm commences by calculating the bounds of the type t s. A **uint8** type, for instance, will set $b_l = 0$ and $b_h = 2^8 = 256$ while an **int8** type results in the bounds $b_l = 0 - 2^{8-1} = -128$ and $b_h = 2^8 - 2^{8-1} = 128$. Line 7 calculates the bounds of x in P. If one of these bounds is infinite, line 12 removes all information on x and restrains x to $[b_l, b_h - 1]$. In case of finite bounds, line 9 calculates the smallest and largest quadrant into which the values of x impinge. For instance, in the example of Figure 4, these numbers are $q_l = -1$ (for the quadrant [-256, -1]) and $q_h = 1$ (for [256, 511]). Line 11 ensures that the linear expression is simply set to its maximum bounds if more than k quadrants have to be transposed and joined, where k is a limit that can be tuned to the required precision. Line 14 transposes each quadrant and restricts it to the bounds of the type. The correctness of *wrap* is asserted below:

Proposition 2. Given $P \neq \emptyset$ and $P' = wrap(P, t s, x_i)$, the interval $P'(x_i)$ lies in the range of the type t s. Furthermore $\gamma_a^s(P) \subseteq \gamma_a^s(P')$.

Proof. Upon return from lines 12 and 14, x_i is restricted to lie between the bounds b_l and $b_h - 1$ of the type t s, hence $P'(x_i)$ lies in the range of type t s.

Suppose $\boldsymbol{a} = \langle a_1, \ldots, a_n \rangle$, $\boldsymbol{s} = \langle s_1, \ldots, s_n \rangle$ where $a_i = addr^{\natural}(x_i)$ and s_i denotes the size of x_i in bytes. Let $\sigma \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P)$. Then there exists $\langle v_1, \ldots, v_n \rangle \in P \cap \mathbb{Z}^n$ such that $\sigma \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(\{\boldsymbol{v}\})$. We consider two behaviours of *wrap*:

- Suppose that wrap is exited at line 12. Observe that for any $\boldsymbol{b} \in \mathbb{B}^{8s_i}$ there exists $v \in \{b_l, \ldots, b_h 1\}$ such that $bin^{8s_i}(v) = \boldsymbol{b}$. Hence, there exists $\boldsymbol{v}' = \langle v_1, \ldots, v'_i, \ldots, v_n \rangle$ with $v'_i \in [b_l, b_h 1] \cap \mathbb{Z}$ and $bin^{8s_i}(v'_i) = bin^{8s_i}(v_i)$. Observe that $\boldsymbol{v}' \in P' = \exists_x(P) \sqcap [b_l \leq x < b_h]$ and $\boldsymbol{v}' \in \mathbb{Z}^n$. Hence $bits^s_{\boldsymbol{a}}(v_j) = bits^s_{\boldsymbol{a}}(v'_j)$ for all $j \in [1, n]$ and it follows that $\sigma \in \gamma^s_{\boldsymbol{a}}(P')$.
- Suppose now that wrap exits at line 14. Observe that $v_i \in [l, u]$ hence there exists $q \in [q_l, q_u]$ such that $v_i q2^{s_1} \in [b_l, b_h 1]$. Hence, there exists $v' = \langle v_1, \ldots, v'_i, \ldots, v_n \rangle \in P'$ such that $v'_i = v_i q2^{s_i}$. Since $bin^{8s_i}(q2^{s_i}) = \mathbf{0}$ it follows that $bin^{8s_i}(v'_i) = bin^{8s_i}(v_i q2^{s_i}) = bin^{8s_i}(v_i)$. Thus $\sigma \in \gamma^s_{\mathbf{a}}(P')$.

Note the translation of quadrants using $P \triangleright x := x + q2^s$ can be implemented by a potentially cheaper affine transformation [2]. However, the shown solution can be readily implemented using other polyhedral domains [15,17] that do not directly support affine translations.

6 An Abstract Semantics for $\mathcal{L}(ELang)$

This section defines the abstract semantics of $\mathcal{L}(\text{ELang})$ in which a single polyhedron P is calculated for each label l where each label marks the beginning of a basic block. Starting with the unrestricted polyhedron $\mathbb{R}^{|X|}$ for the first basic block and with the empty polyhedron $\emptyset \subseteq \mathbb{R}^{|X|}$ for all others, the semantic function of the basic blocks are repeatedly evaluated until a (post-)fixpoint is reached [4]. We omit a formal definition of this fixpoint for simplicity. Once a fixpoint is reached, each state σ that may arise in the concrete program at l satisfies $\sigma \in \gamma_a^{\mathfrak{s}}(P)$ where P is the polyhedron associated with the label l.

The first rule in Figure 7 specifies how the evaluation of statements feeds into the evaluation of control-flow statements. Specifically, [[lookupNext(l)]][#]_{Next} P_l yields tuples such as $\langle P'_{l'}, l' \rangle$ indicating that $P'_{l'}$ must be joined with the existing state $P_{l'}$ at l'. For instance, **jump** l merely returns the current state paired with the target label. The conditional calculates two new polyhedra P^{then} (which is returned for the label l) and P^{else} (which is used to evaluate other control-flow instructions). The calculation of P^{else} makes use of a function neg which negates a relational operator, for example, $neg('<') = '\geq'$. The auxiliary function cond wraps the two arguments of the relational operator op. Like wrap, this function can only wrap single polyhedral variables which requires that exp is assigned to a temporary variable y which is projected out once the guard is applied.

Observe that enforcing the guard by intersecting with $[x \ op \ y]$ has the same effect as wrapping the expression exp itself since y = exp holds in P'. However, if wrap returns from line 12 in Algorithm 1, the variable y is merely set to the bounds of the type. In this case wrap discards the relational information between y and exp and the intersection with $[x \ op \ y]$ has no effect on P'', thereby ignoring the condition. An alternative treatment for expressions exp that exceed

k quadrants would be to discard any previous information on variables in exp using projection and to modify wrap to intersect P with $[\![b_l \leq exp < b_h]\!]$. In this case the information in the guard could be retained by intersecting with $[\![x \ op \ exp]\!]$ at the cost of discarding any previous bounds on the variables of exp. The following Proposition states the correctness of the conditional:

Proposition 3. If $\sigma \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P)$ and $val^{\otimes,t}(\sigma^{s}(addr^{\natural}(x_{i})))$ op $val^{\otimes,t}(\llbracket exp \rrbracket_{\text{Expr}}^{\natural,s}\sigma)$ then $\langle P', l \rangle \in \llbracket$ if $t \ s \ x_{i}$ op exp then jump $l \ ; nxt \rrbracket_{\text{Next}}^{\natural}P$ and $\sigma \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P')$.

Proof. Since $\sigma \in \gamma_{\mathbf{a}}^{s}(P)$ there exists $\mathbf{v} \in P \cap \mathbb{Z}^{n}$ such that $\sigma \in \gamma_{\mathbf{a}}^{s}(\{\mathbf{v}\})$. Let $\hat{P} = cond(\bar{P}, t \ s, x_{i}, y, op)$ where $\bar{P} = P \triangleright y := \llbracket exp \rrbracket_{\mathrm{Expr}}^{\sharp}$. Then $\langle v_{1}, \ldots, v_{n}, \hat{v} \rangle \in \bar{P} \cap \mathbb{Z}^{n+1}$ where $\langle v_{1}, \ldots, v_{n} \rangle = \mathbf{v}, \ \hat{v} = \mathbf{c} \cdot \mathbf{v} + d$ and $exp \equiv \mathbf{c} \cdot \mathbf{x} + d$. By Proposition 2, there exists $\mathbf{v}' = \langle v_{1}, \ldots, v_{i-1}, v'_{i}, v_{i+1}, \ldots, v_{n}, \hat{v}' \rangle \in \hat{P} \cap \mathbb{Z}^{n+1}$ such that $bin^{8s}(v_{i}) = bin^{8s}(v'_{i}) = \sigma^{8s}(addr^{\natural}(x_{i}))$. By following Proposition 1, $bin^{8s}(\hat{v}) = bin^{8s}(\hat{v}') = \llbracket exp \rrbracket_{\mathrm{Expr}}^{\natural,s} \sigma$. Furthermore, v'_{i} and \hat{v}' lie in the range of $t \ s$ and thus $val^{8s,t}(\sigma^{8s}(addr^{\natural}(x_{i}))) = v'_{i}$ and $val^{8s,t}(\llbracket exp \rrbracket_{\mathrm{Expr}}^{\natural,s}\sigma) = \hat{v}'$. Hence $\mathbf{v}' \in \hat{P} \cap \llbracket x \ op \ y \rrbracket$ for $op \notin \{\neq\}$. With $P' = \exists_{y}(\hat{P} \cap \llbracket x \ op \ y \rrbracket)$ it follows that $\sigma \in \gamma_{\mathbf{a}}^{\mathbf{s}}(P')$. The argument is similar for $op \in \{\neq\}$.

The fall-through case can be shown correct by a similar argument.

Due to the modulo nature of γ_a^s the evaluation of linear expressions and assignments resembles that of classic polyhedral analysis in that linear expressions in the program are simply re-interpreted as expressions over polyhedra variables. This holds true even for casts between different sized variables as long as the target variable is smaller. Assigning smaller variables to larger, on the contrary, requires that wrapping is made explicit since a value that exceeds the range of the smaller source variable would wrap in the actual program whereas it might not exceed the range of the larger target variable.

We conclude this section with a correctness argument for the cast statement:

Proposition 4. Suppose $\sigma \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P)$, $\sigma' = \llbracket s_1 \ x_i = t \ s_2 \ x_j \ \rrbracket_{\text{Stmt}}^{\boldsymbol{\natural}} \sigma$ and let $P' = \llbracket s_1 \ x_i = t \ s_2 \ x_j \ \rrbracket_{\text{Stmt}}^{\boldsymbol{\natural}} P$. Then $\sigma' \in \gamma_{\boldsymbol{a}}^{\boldsymbol{s}}(P')$.

Proof. Since $\sigma \in \gamma_{a}^{s}(P)$ there exists $\boldsymbol{v} = \langle v_{1}, \ldots, v_{n} \rangle \in P$ such that $\sigma \in \gamma_{a}^{s}(\{\boldsymbol{v}\})$. Let $\langle v'_{1}, \ldots, v'_{n} \rangle \in P \vartriangleright x_{i} := x_{j}$ where $v'_{i} = v'_{j}$ and $v'_{k} = v_{k}$ for all $k \neq i$. By Lemma 1, $\sigma'^{s_{k}}(addr^{\natural}(x_{k})) = \sigma^{s_{k}}(addr^{\natural}(x_{k}))$ for all $k \neq i$. By definition of $[\![\cdot\,]]^{\natural}_{\mathrm{Stmt}}$, we need to show that $\sigma'^{s_{1}}(addr^{\natural}(x_{i})) = bin^{8s_{1}}(val^{8s_{2},t}(\sigma^{s_{2}}(addr^{\natural}(x_{j}))))$.

- Suppose $s_1 \leq s_2$. Then $bin^{8s_1}(x) = bin^{8s_1}(val^{8s_2,t}(bin^{8s_2}(x)))$. But $bin^{8s_1}(v'_i) = \sigma'^{s_1}(addr^{\natural}(x_i))$ and $bin^{8s_2}(v_j) = \sigma^{s_2}(addr^{\natural}(x_j))$, thus $\sigma' \in \gamma^s_a(P')$ follows.
- Suppose now that $s_1 > s_2$. By Proposition 2, there exists $\langle v_1, \ldots \hat{v}_i, \ldots v_n \rangle \in P'$ such that $bin^{8s_2}(\hat{v}_i) = bin^{8s_2}(v_j)$ and \hat{v}_i lies in the range of $t s_2$, that is, $val^{8s_2,t}(bin^{8s_2}(\hat{v}_i)) = \hat{v}_i$. But since $bin^{8s_2}(\hat{v}_i) = bin^{8s_2}(v_j) = \sigma'^{s_1}(addr^{\natural}(x_j))$ it follows that $\sigma'^{s_1}(addr^{\natural}(x_i)) = bin^{8s_1}(\hat{v}_i)$ as required.

Basic Blocks. $\begin{bmatrix} l : s_1; \dots s_n; \end{bmatrix}_{\text{Block}}^{\sharp} P = \begin{bmatrix} lookupNext(l) \end{bmatrix}_{\text{Next}}^{\sharp} (\begin{bmatrix} s_n \end{bmatrix}_{\text{Stmt}}^{\sharp} (\dots \begin{bmatrix} s_1 \end{bmatrix}_{\text{Stmt}}^{\sharp} P \dots))$ Control Flow. $\llbracket \mathbf{jump} \ l \ \rrbracket_{\mathrm{Next}}^{\sharp} P = \{ \langle P, l \rangle \}$ $\llbracket \mathbf{if} \ t \ s \ v \ op \ exp \ \mathbf{then} \ \mathbf{jump} \ l \ ; \ nxt \ \rrbracket_{\mathrm{Next}}^{\sharp} P = \{\langle P^{then}, l \rangle\} \cup \llbracket \ nxt \ \rrbracket_{\mathrm{Next}}^{\sharp} P^{else}$ where $P' = P \triangleright y := \llbracket exp \rrbracket_{\text{Expr}}^{\sharp}$ where $y \in T$ fresh $P^{then} = \exists_y (cond(P', t \ s, v, y, op))$ $P^{else} = \exists_{y}(cond(P', t \ s, v, y, neq(op)))$ $cond(P', t \ s, x, y, op) = \begin{cases} (P'' \sqcap \llbracket x < y \rrbracket) \sqcup (P'' \sqcap \llbracket x > y \rrbracket) & \text{if } op \in \{\neq\} \\ (P'' \sqcap \llbracket x \ op \ y \rrbracket) & \text{otherwise} \end{cases}$ otherwise where P'' = wrap(wrap(P', t s, x), t s, y)Expressions. $\llbracket n \rrbracket^{\sharp}_{\mathrm{Expr}} = n$ $\llbracket n * v + exp \rrbracket_{\text{Expr}}^{\sharp} = n v + \llbracket exp \rrbracket_{\text{Expr}}^{\sharp}$ Assignments. $\llbracket s \ v = exp \ \rrbracket_{\mathsf{Stmt}}^{\sharp} P = P \triangleright v := \llbracket exp \ \rrbracket_{\mathsf{Expr}}^{\sharp}$ Type Casts. $[s_1 v_1 = t s_2 v_2]]_{\text{Stmt}}^{\sharp} P = \begin{cases} P' & \text{if } s_1 \leq s_2 \\ wrap(P', t s_2, v_1) & \text{otherwise} \end{cases}$ where $P' = P \triangleright v_1 := v_2$ **Fig. 7.** Abstract semantics of $\mathcal{L}(\text{ELang})$

7 Discussion

The existence of a concretisation map γ_a^s begs the question of whether an abstraction map can be defined. For classic polyhedral analysis [6], it is well-known that no best abstraction exists for certain shapes such as a disc [4]. In the context of our analysis, the set of concrete states Σ is finite. However, a given set of states still has no best abstraction. Consider $\sigma \in \Sigma$ with $\sigma^1(addr^{\natural}(\mathbf{x})) = 1111111$, $P_1 = [x = -1]$ and $P_2 = [x = 255]$. Although $\sigma \in \gamma_a^s(P_1) = \gamma_a^s(P_2)$, P_1 and P_2 are incomparable. As a consequence, the meet operation can only be applied after *wrap* has expressed the polyhedra in the same quadrant and thereby made them comparable. Termination is not compromised as *wrap* is monotonic.

Since different polyhedra can describe the same set of concrete states, care is needed when applying join. Suppose that the loop in Figure 8 is entered with P = [x = -1]: the largest value an unsigned variable can take. As the loop invariant $x \ge 42$ mentions $x, Q = P \sqcup U$ is wrapped to R = wrap(Q, uint8, x) = [x = 255]. A precision loss occurs when P and U = [x = 254] are joined to obtain $[-1 \le x \le 254]$ as x cannot fall below 42. One solution to this particular problem is to unroll the loop once, which avoids the join of different representatives.

Observe that *wrap* is idempotent and, as such, is the identity if the variable is in range. An important consequence is that our solution is as precise as classic polyhedral analysis if all variables remain within the range of their types.



Fig. 8. Precision loss incurred by joining flow paths

An interesting benefit of γ_a^s is that the possible values of a byte x can be represented as either $[-128 \leq x \leq 127]$ or $[0 \leq x \leq 255]$. For example, an analysis of C string buffers [14] does not model individual array elements but tracks a single NUL position (a character with value zero) within the array. Even though **char** is often signed, the range [0, 255] (rather than [-128, 127]) can be returned to indicate an arbitrary value when reading a byte from the array. The unsigned range can then be refined using the NUL position to [1, 255] whenever the access lies in front of the NUL position (see [16] for an example). If a signed range $[-128, -1] \cup [1, 127] = [-128, 127]$ is the best convex approximation. Without this tactic, it can be difficult to prove termination of loops that iterate over strings.

8 Related Work and Conclusion

A sound analysis must reason about overflowing calculations and correctly model conversions between unsigned and signed integers. Rather than contaminating an analysis with the low-level details of two's complement wrapping behaviour, we presented a computationally light-weight solution based on a novel concretization map for polyhedra which eliminates the need to check for wrapping in assignments of linear expression and conversions to smaller integers. Conditionals and other casts require an implementation using the presented algorithm *wrap*. We proved the presented analysis correct and argued the precision is no worse than that of classic polyhedral analysis that warns about every wrapping.

Although a number of works have addressed the modulo properties of congruences [1,11,12,13], little work exists for polyhedral analyses. Blanchet *et al.* use a two-tier approach [5]: For signed integers, any wrapping is erroneous. In this case, each time a variable is set, its range is checked for overflows. Overflows of unsigned integers are assumed to be intentional as wrapping may result from bit-level operations. This approach requires a separation of signed and unsigned variables which incurs false warnings for many programs, such as the C program in our introduction. Indeed, an analysis of our example would lead to the misleading warning about converting from a signed to an unsigned integer.

Further afield is work on tracing the propagation of rounding errors in floating point calculations. In this context, Goubault et al. [10] treat wrapping of integers as a rounding error that is as large as the range of the integer variable.

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