Giant Magnetoresistance (GMR) Magnetometers

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Abstract Since its discovering in 1988, the Giant Magnetoresistance (GMR) effect has been widely studied both from the theoretical and the applications points of view. Its rapid development was initially promoted by their extensive use in the read heads of the massive data magnetic storage systems, in the digital world. Since then, novel proposals as basic solid state magnetic sensors have been continuously appearing. Due to their high sensitivity, small size and compatibility with standard CMOS technologies, they have become the preferred choice in scenarios traditionally occupied by Hall sensors. In this chapter, we analyze the main properties of GMR sensors regarding their use as magnetometers. We will deal about the physical basis, the fabrication processes and the parameters constraining their response. We will also mention about some significant application, including developments at the system level.

1 Physical Background

The electric current in a magnetic multilayer consisting of a sequence of thin magnetic layers separated by thin non-magnetic layers is strongly influenced by the relative orientation of the magnetizations of the magnetic layers [[1,](#page-24-0) [2](#page-24-0)]. More specifically, the resistance of the magnetic multilayer is low when the magnetizations of the magnetics layers are parallel but higher when the magnetizations of the neighbouring magnetic layers are antiparallel. This is due to the spin-dependent scattering. The spontaneous relative orientation between adjacent magnetic layers depend on the the thickness of the spacer layer. Then, by applying an external magnetic field, a change from antiferromagnetic to ferromagnetic (or viceversa) coupling can be achieved, so changing the resultant resistance value.

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The magnetoresistance (MR) ratio is, then, generally defined as:

$$
\frac{\Delta R}{R} = \frac{R^{\uparrow \downarrow} - R^{\uparrow \uparrow}}{R^{\uparrow \uparrow}} \tag{1}
$$

Such behaviour has important applications, initially focusing on magnetic information storage technology. In this sense, P. Grunberg and A. Fert received the Nobel Prize in Physics in 2007 for the discovering of the effect [[3](#page-24-0)].

There are several kind of structures that can display GMR effect [\[4](#page-24-0), [5](#page-24-0)]. In fact, there have been described and used granular materials with such effect [[6\]](#page-24-0). For engineered applications, multilayer structures are preferred due to their integration feasibility [[7\]](#page-24-0). Typical multilayered structures consist of two or more magnetic layers of a Fe–Co–Ni alloy, as can be permalloy, separated by a very thin non magnetic conductive layer, as can be Cu [\[5](#page-24-0)], as sketched in Fig. 1 (left). With magnetic films of about 4–6 nm width and a conductor layer of about 35 nm, magnetic coupling between layers is slightly small. With this configurations, MR levels of about 4–9 % are achieved, and spreading the linear ranges of about 50 Oe [\[5](#page-24-0)], good for sensing applications. The figures of merit of these devices can be improved by continuously repeating the basic structure.

Spin valves are a particular configuration of a sandwich structure. In spin valves, an additional antiferromagnetic (pinning) layer is added to the top or bottom part of the structure, as shown in Fig. 1 (right). In this sort of structures, there is no need of an external excitation to get the antiparallel alignment. In spite of this, the pinned direction (easy axis) is usually fixed by raising the temperature above the knee temperature (at which the antiferromagnetic coupling disappears) and then cooling it within a fixing magnetic field. Obviously, so obtained devices have a temperature limitation below the knee temperature. Typical values displayed by spin valves are a MR of 4–20 % with saturation fields of 0.8–6 kA/m [[4\]](#page-24-0).

For linear applications, and without excitation, pinned (easy axis) and free layers are preferably arranged in a crossed axis configuration (at 90°), as depicted in Fig. [2.](#page-2-0) In this way, the linear range is improved and the sign of the external field is detected without the need of an additional magnetic biasing. The response this structure is given by Freitas et al. [[8\]](#page-24-0):

Fig. 1 (left) Basic GMR multilayer structure, (right) typical response of a basic GMR structure

Fig. 2 Basic spin valve scheme: a multilayer structure in crossed axis configuration, **b** typical implementation, c simplest lithography masks set

$$
\Delta R = \frac{1}{2} \left(\frac{\Delta R}{R} \right) R_{\square} \frac{iW}{h} \cos \left(\Theta_p - \Theta_f \right) \tag{2}
$$

where $(\Delta R/R)$ is the maximum MR level (5–20 %), R_{\Box} is the sensor sheet resistance 15–20 Ω/\square), L is the length of the element, W is its width, h is the thickness, i is the sensor current, and Θ_p and Θ_f are the angle of the magnetization angle of pinned and free layers, respectively. Assuming uniform magnetization for the free and pinned layers, for a linearized output, $\Theta_p = \pi/2$ and $\Theta_f = 0$.

As a practical example, in [\[9](#page-24-0)], the spin valve structure was deposited by ion beam sputtering (IBD) onto 3" $Si/SiO₂ 1500$ Å substrates with a base pressure of $1.0 \times 10^{-8} - 5.0 \times 10^{-8}$ Torr. For IBD deposition, a Xe flow was used for a deposition pressure of 4.1×10^{-5} Torr. The spin valve structure was Ta(20 Å)/ NiFe(30 Å)/CoFe(20 Å)/Cu(22 Å)/CoFe(25 Å)/MnIr(60 Å)/Ta(40 Å). This structure has demonstrated to give magnetoresistance responses of about 6–7 %, linear ranges of about 20 Oe and sheet resistivities of about 10–15 Ω/\Box [\[9](#page-24-0)]. Deposition rates ranged from 0.3 to 0.6 Å/s. A 40 Oe field was applied to the substrates during the deposition step in order to state the easy axis in the pinned and free layers. The wafer was 90° rotated between both depositions to ensure a crossed-axis spin valve configuration.

Nano-oxide layers (NOL) inserted in the pinned layer and above the free layer have been found to increase the magnetoresistance ratio up to 19 % [[10\]](#page-24-0). The enhancement of GMR is attributed to the specular scattering effect of the conduction electrons at the metal/insulator interfaces.

In $[11]$ $[11]$, the specular spin valve structure was Ta(3 nm)/NiFe(3 nm)/MnIr(6 nm)/ CoFe(1.6 nm)//NOL//CoFe(2.5 nm)/Cu(2.5 nm)/CoFe(1.5 nm)/NiFe(2.5 nm)//NOL// CoFe(2.0 nm)/Ta(0.5 nm). NOL layers were formed in a 15 min natural oxidation step at atmospheric pressure in the deposition tool load lock. The natural oxidation process, keeping its simplicity, has proven to be well effective. Finally, the samples were annealed at 270 °C under vacuum and cooled under a 3 kOe magnetic field applied parallel to the pinned and free layer easy axis.

Giant magnetoresistance can also find in other structures. We collect two illustrative examples. Pena et al. [[12\]](#page-24-0) report on giant magnetoresistance in ferromagnet/superconductor superlattices. On the other hand, Pullini et al. [\[13](#page-24-0)] describe GMR in multilayered nanowires. In any case, a magnetic/non-magnetic interface is required in order to allow the spin-electron scattering producing the effect.

2 Fabrication

The fabrication of giant magnetoresistance (GMR) devices involves a sort of techniques including deposition, patterning and encapsulation in a similar fashion to those related to standard CMOS processes. Because doping and implantation are not required, they can be considered as low temperature processes. As a guideline, three to five lithography steps are required for fabricating basic GMR devices. They can be deposited on silicon wafers but glass, sapphire or flexible substrates can also be considered.

On Bi-CMOS processes, silicon, silicon oxide and aluminum are the basis materials, as well as the dopants (Boron, Phosphorus, Arsenic, Antimony and related compounds). In the case of GMR devices, the fabrication of magnetic layers requires the use of additional magnetic materials (Iron, Cobalt, Nickel, Manganese, and their alloys), different metals (e.g. Copper, Ruthenium) and additional oxides $(Al_2O_3, MgO \ldots)$, not usually found in conventional semiconductor facilities. Each of these materials has particular requirements in terms of deposition technology and conditions or system contamination that need to be specifically considered and optimized. As a high-lighting example we should mention the deposition of layers with preferentially aligned magnetic moment which requires the use of a polarizing magnet placed inside the deposition system, therefore not easily compatible with hot deposition tools.

2.1 Deposition

As before mentioned, GMR structures are composed of multilayered engineered structures based on nanometric to sub-nanometric thick layers of ferromagnetic materials (e.g.: Co, CoFe, NiFe) separated by a non-magnetic spacer (Cu). Isolation layers are also commonly required. Therefore, adequate deposition techniques namely those using ultra-high vacuum systems and providing a thorough control of the thickness of the deposited layers are essential for the proper functionality of so obtained devices.

2.1.1 Sputtering

Cathodic sputtering is one of the more common physical vapor deposition technique used for depositing thin films onto substrates. Such sputtering process occurs when an accelerated ion hits a solid target material. If the ion kinetic energy is high enough, atoms are extracted from the matrix. A vacuum reaction chamber (usually lower than 10−⁷ Torr) is required. A high voltage is applied to the target holder so producing an electrical discharge that allows the ionization of the gas and hence leads to the plasma. The produced ions are then attracted toward the cathode, hitting the target. The ions with energy above the threshold can extract atoms from the target material. These atoms are deposited onto the substrate, usually facing the target, and thus forming a layer of material.

Regarding specific GMR devices, this method offers the possibility to deposit from a target composed of different materials (alloy or mosaic target). Due to this, sputtering is one of the preferred techniques to deposit metallic and magnetic layers in GMR de-vices. It is also commonly used for the deposition of metallic nonmagnetic contacts and also insulating oxides.

2.1.2 Ion Beam Deposition (IBD)

The IBD technique is not as extended as traditional sputtering but it provides a good film thickness uniformity and higher deposition control due to the low deposition rates employed, enabling also epitaxial growth under particular conditions and higher deposition textures. Deposition parameters such as ion flux, energy and sputtered species, as well as the angle of incidence, can be more independently controlled. In this case, the plasma is created and confined in an ion gun being then accelerated towards the target through voltage applied into a grid set. Furthermore, the basic configuration of a typical IBD system normally includes an assist gun, used either for assisted deposition or ion-milling etching. An automatically interchangeable target holder (4–8 targets) can be used in GMR multilayer deposition without vacuum break, with deposition rates below 1 nm/s.

2.1.3 Chemical Vapor Deposition (CVD)

CVD thin films deposition is based on the decomposition and/or reaction of different gaseous compounds. In this way, the considered material is directly deposited onto the substrate surface from a gas phase.

Deposition usually occurs at high temperatures >300 °C, therefore not compatible with magnetic multilayers. However, since the deposition rates can be very large (therefore fast deposition) and it is a conformal deposition (thus, excellent step coverage), this method is mainly used in the deposition of insulating and passivation layers (silicon oxide or silicon nitride) leading to good quality layers with moderate cost equipment.

2.2 Patterning

GMR structures can be patterned in a similar way than common devices in typical CMOS processes. Well-known ultraviolet (UV) lithography through hard or software designed masks, together with physical or chemical etching processes can be used. In this way, a good ratio cost/reliability is achieved with defined features down to \sim 1 μm. The patterning process of a GMR device consists of sequential steps of pattern design and transfer as illustrated in Fig. [3](#page-6-0), with typically three lithography steps, including that for opening contacts.

2.2.1 Photolithography

The photolithography process includes three steps: (i) coating of the sample with a proper photoresist (a radiation sensitive polymer solution); (ii) exposure of the resist, patterning a certain design (mask), previously prepared; (iii) development of the transferred pattern.

Coating

The photoresist is deposited onto the surface of the sample by spin-coating, with controlled conditions of speed, time and amount of resist for a proper thickness and homogeneity of the sensitive layer, which is crucial for the lithography resolution [\[14](#page-24-0)]. It is usually required a surface pre-treatment (such as hexamethyldisilizane, HMDS) for promoting the adhesion, and a post-treatment (soft-baking, $80-100$ °C) for removing solvents and stress.

Lithography

By using UV radiation (wavelength typically ranging 0.5–0.1 μm) with focused laser beams (direct write systems) or lamps (hard mask aligners), resolutions below 1 μm can be obtained. Due to the commonly limited production volumes of GMR devices, direct write systems are particularly interesting. In this case, a spot of the light beam moves trough the surface in those zones that need to be illuminated, with the help of a precision X-Y system, together with a switching light mechanism. The fabrication of physical masks is, then, not required. This is a versatile and low cost, but slow process (the full exposure of a 150 mm wafer can take more than 12 h, depending on he particular design). If higher resolutions are demanded (<0.5 μm), X-ray, electron or ion beam systems can be used [\[15](#page-24-0)].

Development

The development is usually assisted by a soft-baking step before the resist developer is sprayed or spin-coated onto the sample surface. With positive resists, exposed regions have turn soluble during the exposure and are removed at this moment. For negative resits, exposed regions turn harder and remain after

developing. In any case, the sample is then washed to stop the development process and dried. The pattern has been printed into the resist layer.

2.2.2 Pattern transfer techniques

We will consider two options: etching and lift-off.

Etching

It is a process concerning the capability of removal undesired portions of a deposited layer. Such a selective property is provided by the patterned resist mask, but also by the characteristics of the involved layers. The starting point is usually the film to be patterned deposited on a substrate with the desired pattern defined in the top resist mask,

Dry etching. Physical (dry) etching is commonly achieved by using plasma etching (reactive etching or an ion beam system) providing a controlled removal of material. Ion beam etching (ion milling), in particular, offers slow (below 0.2 nm/s) but very controlled and stable etching ratios and it is usually used for the patterning of GMR devices [\[15](#page-24-0)]. It is an anisotropic process with etching efficiency depending on the material type and the incident angle [[16\]](#page-25-0).

Wet etching. For chemical (wet) etching, corrosive properties of some substances (usually acids) are used. In this way, wet etching can be patterned with polymer based resists, due to their intrinsic organic nature, resistant to the inorganic acids action. Tables with specific etchers for the different materials, with associated speeds can be found in the literature [[17\]](#page-25-0). Due to its aggressive and isotropic nature, wet etching is not commonly used for patterning GMR structures and is mostly used for processes like opening contacts/vias.

3 Noise

Real performances of GMR magnetometers can only be estimated when compared with their intrinsic noise sources. The noise power spectrum density (PSD) is commonly given in V^2 /Hz. Often, is much more convenient to use the amplitude spectrum density (ASD), expressed in V/\sqrt{Hz} for comparison with voltage signals. The sensitivity for a magnetoresistance signal, S_V is usually given in V/V/T. Typical values for GMR sensors are 20–40 V/V/T, e.g., 20–40 nV/nT when they are biased with 1 V. For comparing different sensors, it is recompensable to use the field equivalent noise power spectra density, sometimes called detectivity. It corresponds to the PSD divided by the sensitivity. For example, if a sensor displays a noise of $10 \text{ nV}/\sqrt{\text{Hz}}$ at a given frequency and a sensitivity of 25 V/V/T, its detectivity will be 400 pT for 1 V bias.

3.1 Types of Noise in GMR Magnetometers

3.1.1 Thermal Noise

The most relevant noise is the thermal noise (also called Johnson-Nyquist noise or white noise), which is directly related to the resistance of the sensor. It is a *white* noise, so it is independent of the frequency. It was first observed by Johnson [\[18](#page-25-0)] and interpreted by Nyquist [[19\]](#page-25-0). It is expressed as:

$$
S_V(\omega) = \sqrt{4Rk_BT} \tag{3}
$$

where R is the sensor resistance, k_B is the Boltzmann constant and T is the temperature. For example, a 1 kΩ resistor at room temperature has $4 \text{ nV}/\sqrt{\text{Hz}}$.

3.1.2 1/f Noise

The origin of the 1/f noise or 'pink' noise or Flicker noise is on resistance fluctuations, so it can only revealed by applying a current into the sensor. Its dependence with the frequency is described by the following phenomenological formula:

$$
S_V(\omega) = \frac{\gamma_H R^2 I^2}{N_C f^{\beta}}
$$
\n(4)

where γ_H is a dimensionless constant proposed by Hooge [[20\]](#page-25-0), R is the sensor resistance, I is the bias current, N_c is the number of current carriers, f is the frequency and β is an exponent typically in the order of 1. 1/f noise can exhibit a non magnetic and a magnetic component with possible different slopes. The size and the shape of the sensors have a strong effect on the 1/f noise. Due to its average nature, and as followed by Ec. 4, small GMR sensors display more 1/f noise than bigger ones. By considering equally thin sensors, the 1/f noise is roughly inversely proportional to their area [\[21](#page-25-0)].

3.2 Noise Measurement in GMR Devices

Noise measurement is a difficult task that needs to be carefully performed. A standard measurement system should comprise the sensor (device-under-test, DUT), a low noise biasing source (usually batteries), a low noise amplifier (it can be composed of different stages), filtering and acquisition/processing system, as this depicted in Fig. [4.](#page-9-0) In some occasions, last two parts can be replaced by a spectrum analyzer. A particular implementation is also shown in Fig. [4](#page-9-0) including a National Instruments data acquisition (DAQ) card (24 bits of resolution, 200 kHz bandwidth

Fig. 4 Noise measurement system: a basic setup, b detail of the bias and shielding of the DUT, c a specifically designed LNA, d aquisition and processing software

and noise spectral density of $8 \text{ nV}/\sqrt{\text{Hz}}$ at 1 kHz) and a low-noise amplifier (2 nV/ \sqrt{Hz} noise in a frequency band from 0.3 Hz to 100 kHz and voltage gain of 1000). Devices and bias batteries are shielded. A LabView program is used for controlling the system and obtaining the ASD.

As a representative example, we will give noise data on spin valves based on multilayered structures [Ta(20 Å)/NiFe(30 Å)/CoFe(20 Å)/Cu(22 Å)/CoFe(25 Å)/ MnIr(60 Å)/Ta(40 Å)] patterned on strips of $3 \times 200 \,\text{\ensuremath{\mu}m}^2$. Measured sensitivity was 20 mV/mT (1 mA bias). Measured bandwidth was above 1 MHz [\[22](#page-25-0)]. The measured noise is shown in Fig. [5a](#page-10-0), b. The 1/f behaviour is clearly observed and the thermal noise limit well defined. If we take into account the measured sensitivity, we can draw the detectivity understood as the field equivalent noise, that is drawn in Fig. [5c](#page-10-0), d. The benefits of the frequency is clearly stated. The increase of the bias current has an impact on the field detectivity at higher frequencies, but there is no effect in the 1/f regime [[21\]](#page-25-0).

Fig. 5 Noise measurement data on $3 \times 200 \,\text{µm}^2$ spin valves as described in [\[22\]](#page-25-0): a low frequency noise, b high frequency noise, c low frequency detectivity, d high frequency detectivity

3.3 Improving the Detectivity

Once the main parameters have been introduced, we follow with some suggestion for improving the detectivity in GMR based magnetometers, as discussed in [[23\]](#page-25-0).

• Structures with high sensitivity (high MR level) should be considered in order to maximize the output signal. Then, the signal to noise ratio (SNR) needs to be calculated. In this sense, tunnel magnetic resistance (TMR) devices display sensitivities higher than those from GMR, but with a noise level that is typically three times higher. Then, a triple MR level is required for achieving the same SNR. On the other hand, linear ranges should be kept as narrow as possible in

	Size	Noise at 1 Hz	White noise	Power consumption
Small GMR	150×4 um ²	10nT	50pT	5 mW
Large GMR	\cdot mm ^{\sim}	100 pT	20pT	100 mW

Table 1 Field equivalent noise of GMR as dependant on the dimensions

order that the pendent in the response (and then the sensitivity) is as high as possible [see Fig. [1](#page-1-0) (right)]

- Due to the statistical nature of noise, this is reduced with the increasing of the sensor size, by means of the Hooge parameter. From [\[21](#page-25-0)], the values in Table 1 can be extracted.
- The frequency of operation should be stated as high as possible in order to minimize the 1/f noise effect. This can hardly done by modulating the measured field with additional loops or by placing the sensing elements onto oscillating cantilevers [[23](#page-25-0)].
- The use of flux guide concentrators allows, in some cases, to have a magnetic field amplification up to one hundred. In addition, the deposition of high permeability materials is compatible with the patterning and deposition processes described above.

4 Thermal Effects

The temperature is always a limiting parameter in electronics. Every electronic device has temperature depending response arising from its physical nature. Regarding specific GMR electrical current sensors, not only the resistance (and then the sensor impedance) varies with the temperature. Also the MR level (and then the sensitivity) does.

The resistance of GMR sensors, like common resistances, is a function of the temperature. For GMR based devices, and in the usual range of utilization, this dependence can be considered as linear, and can be defined by a temperature coefficient (TEMPCO) as following:

$$
TCR(\%) = 100 \times \frac{1}{R_{T_0}} \frac{\Delta R}{\Delta T}
$$
\n⁽⁵⁾

An analogue relationship can be defined for the thermal dependence of sensitivity, as:

$$
TCS(\%) = 100 \times \frac{1}{S_{T_0}} \frac{\Delta S}{\Delta T}
$$
 (6)

When a full bridge configuration is considered, this thermal dependence is partially compensated and is expected to be low. Due to the inherent voltage offset of sensors configured as bridges, the temperature drift of the offset voltage must be specified::

$$
TCV_{off}(\%) = 100 \times \frac{\frac{\Delta V_{off}}{\Delta T}}{V_{off,T_0}} \tag{7}
$$

Moreover, the output voltage has also a thermal dependence, defined as:

$$
TCV_o(\%) = 100 \times \frac{1}{\Delta T} \frac{V_{o,T_i} - V_{o,T_0}}{V_{o,T_0}}
$$

$$
V_{o,T_i} = V_{out,T_i} - V_{off,T_i}
$$
 (8)

Experimental parameters are only related to the nature of the GMR structures, and they have been measured elsewhere. In Fig. 6 we show typical values for full bridge sensors composed of equal spin valve elements, as described in [\[22](#page-25-0)]. From these graphs we can extract $TCR \approx 0.11 \%$ /°C, $TCV_{off} < 10 \text{ }\mu\text{V}$ /°C and $TCS \approx -0.15 \%$ °C.

Compensation techniques

Assumed that thermal effects cannot be completely eliminated, various methods of temperature compensation have been reported in the literature addressed to reduce the thermal drift output of Wheatstone bridge type sensors. These methods can be differentiated as noninvasive and invasive. As noninvasive we mean a technique consisting of the addition of different circuit elements in series or parallel to the bridge in order to reduce its thermal drift, as described, for example in [[24\]](#page-25-0). A temperature sensor, a fixed resistor, some kind of active network (diode or transistor) or a fixed current source have been successfully applied. This way, the addition of one of the above elements results in a change of the bridge supply voltage due to the temperature variation, which produces a valid compensation.

Fig. 6 Experimental thermal parameters of typical GMR structures

A slightly different approach consists of the connection of a temperature variable gain instrumentation amplifier in cascade at the output of the bridge. On the other hand, a Wheatstone bridge can also be temperature compensated by means of the modification of its original configuration. In this case, we should ensure that the terminals of the bridge are externally accessible. This group of techniques can be considered as invasive, due that the conditioning circuitry in common commercial sensors make the bridge terminals often inaccessible. An excellent revision of these works is made in [[25\]](#page-25-0). In addition, in the same work is presented a novel application of the Generalized Impedance Converter (GIC) as a thermal compensating biasing circuit for specific magnetoresistive sensors.

5 Electronic Interfaces

From the macroscopic point of view, a GMR sensor behaves as a resistance. In this sense, in order to get a useful electrical signal, traditional schemes applied to resistive sensors can be considered.

5.1 Resistive Bridges

Although single elements or basic voltage dividers can be also considered, to arrange a resistive senor in a bridge configuration has clear advantages in terms of the signal level, linearization, voltage offset and immunity against external undesired perturbations. As a clear example, we can observe the benefits of such configuration from data in 6. In this sense, we can make use of bridges with a unique sensing element, half bridges or full Wheatstone bridges. For getting half bridge sensors, and due to the requirement of polarization of the magnetic moments of the layers, two of the four sensing elements must be inactive, usually got by depositing patterned magnetic shielding layers [[26\]](#page-25-0). It should be noted that, if a full Wheatstone bridge is considered, the fabrication involves two steps (see Fig. [7\)](#page-14-0).

5.2 Amplification

Due to the low signals involved, low noise amplifiers (LNA) are usually necessary. Noise sources in operational amplifiers can be:

• *Input-referred voltage noise*. It can be modeled with a noise voltage source. As an example, such a noise is typically : $30 \text{ nV}/\sqrt{\text{Hz}}$ @1 kHz in a 741 general purpose opamp and lower than $1 \frac{\mathsf{N}}{\sqrt{\mathrm{Hz}}}$ in an specific LNA.

Fig. 7 Arrangements of GMR elements in bridges: a Single element, b Half bridge, c Full wheatstone bridge

- *Input-referred current noise*. It can be modeled as two noise current sources pumping currents through the two differential input terminals). Its value can range from : $10 \text{ pA}/\sqrt{\text{Hz}}$ @ 1 kHz in general purpose amplifiers to : $10 \text{ fA}/\sqrt{\text{Hz}}$ @1 kHz in specific LNAs.
- Flicker $(1/f)$ noise. Due to the fabrication process, the IC device layout and the device type. It has a rate of \sim 3 dB/oct for CMOS amplifiers, \sim 4.0 dB/oct for bipolar amplifiers and \sim 5 dB/oct for JFET amplifiers,

In this sense, lock-in amplifiers (LIA) $[27]$ $[27]$ and *chopper* amplifiers $[28]$ $[28]$ are the preferred choices.

5.3 Biasing

The correct use of GMR devices implies a proper biasing scheme both from the electric and magnetic point of view. Assuming a resistor bridge configuration, a constant voltage source can be used to feed the sensor, through two opposite vertex of the bridge. The differential output voltage is taken from the remaining pins. Nevertheless, it has been demonstrated that thermal characteristics (temperature drifts) of spin valve based sensors are notably improved by using a constant current source for the sensor feeding [\[29](#page-25-0)]. In addition, it has been demonstrated that an ac biasing applied GMR based devices notably improves their performance in terms of linearity, hysteresis, offset and noise [\[30](#page-25-0)].

Once the sensor is fed and the bias point set, it can be slightly modified by applying an external magnetic field. This external magnetic field adds (with sign) to the measured signal and the operation point is then shifted. In a certain way, an offset correcting coil can also be understood as an additional biasing, as presented here. When no helping coils are present, a permanent magnet can also be used. In this case the system has to be carefully designed. A proper magnetic biasing can, for example, to convert to bipolar a GMR device, by displacing the quiescent point to the middle of the output function [\[30](#page-25-0)].

5.4 Resistance to Time Approaches

Regarding interfacing, in typical resistor-based sensor applications (such as GMR), the resistive sensing devices are usually dc biased. The generated output signal is taken as an analogue dc voltage level, by employing traditional resistance-to-voltage $(R-V)$ conversion approaches and, commonly, by also making use of amplifiers and filters, as previously described. As well known, these voltamperometric solutions usually display undesired voltage offsets that need to be specifically calibrated/corrected or taken into account. When compared with $R-V$ converters, front-end schemes using ac excitation have been demonstrated highly advantageous for wide range devices or with unknown nominal/baseline values by improving the immunity to voltage offset, noise and frequency disturbs [[15\]](#page-24-0). These approaches perform resistance-to-frequency $(R-f)$ or voltage-to-frequency $(V-f)$ conversions so providing a direct quasi-digital output whose frequency depends on the sensor resistance value. In addition, since the ac excitation of the sensor is made through a closed feedback loop, the output frequency is theoretically independent from the power supply level. They generally do not require any calibration procedure and/or manual adjustments and the output signal can be directly connected to the digital part of a system, making these solutions particularly interesting for A/D mixed-signal applications. Moreover, these solutions can be easily used in integrated CMOS designs and SoCs since they are typically implemented with a reduced number of active/passive components.

We can consider several approaches as described in Fig. [8](#page-16-0).

A simple astable multivibrator implemented with transistors (not necessarily bipolar), as this depicted in Fig. [8a](#page-16-0) has an oscillation period given by:

$$
T = \ln(2)(R_2C_1 + R_3C_2)
$$
\n(9)

then, by making R_2 and/or R_3 variable, we get our objective.

An oscillator can also be implemented by using an integrated 555 circuit, as shown in Fig. [8b](#page-16-0). In this case, we get a square wave with a high level during t_1 and a low level during t_2 , being:

$$
t_1 = \ln(2)(R_1 + R_2)C
$$
; $t_2 = \ln(2)(R_2)C$ so $T = \ln(2)(R_1 + 2R_2)C$ (10)

Fig. 8 Differente R-to-t approaches for resistive GMR devices: a basic transistor based astable, b astable with 555 IC, c circuit with operational amplifiers for single resistors, d circuit with operational amplifiers for resistive bridges

More complex approaches can be developed with active elements like opamps as displayed in Fig. [8](#page-16-0)c and detailed in [\[31](#page-25-0)]. In this case,

$$
T = \frac{4C_1}{R_1}R_0(R_0 \pm \Delta R_0) - R_1R_3 \tag{11}
$$

If we are dealing with resistive bridges, we can make use of the circuit in Fig. [8d](#page-16-0), where:

$$
f = \frac{1}{2RC} \left(\frac{R_4}{R_1 + R_4} \frac{R_3}{R_2 + R_3} \right) \tag{12}
$$

Circuits shown in Fig. [8](#page-16-0)c, d have been successfully used with discrete elements and in integrated circuit form and for GMR spin valve sensing elements and bridges for sub-mA electric current measurements, as described in [\[31](#page-25-0)]. Obtained oscillograms are shown in Fig. [9](#page-18-0). As observed, sensitivities of 0.8 Hz/mA and 0.68 Hz/μA are obtained which are excellent numbers for these purposes.

5.5 Arrays

Arrays of sensors are required for specific applications such as non-destructive evaluation/testing (NDE/NDT) [\[32](#page-25-0), [33\]](#page-25-0), bio-technology systems [[34](#page-25-0)–[36\]](#page-25-0) or other magnetic imaging requirements [\[37](#page-25-0), [38](#page-26-0)]. In general, the access to each individual element involves two electrical/physical connections resulting in a total of $2 \times$ $[N \times M]$ connections. In these particular conditions, read out interfaces for such arrays are a matter of concern [[39,](#page-26-0) [40\]](#page-26-0), usually involving analogue multiplexers and shared amplifiers [[41\]](#page-26-0).

5.6 Compatibility with CMOS Technology

Non Volatile Electronics (NVE) was the first company in merging both technologies by using a dedicated 1.5 m BiCMOS technology [[42\]](#page-26-0). Later, Han et al. used chips made by 0.25 m NSC (National Semiconductor Corporation) BiCMOS technology [\[43](#page-26-0)], by applying a post-process that employed reactive ion etching for via opening through the passivation, so allowing access to the buried metal layers. Then, by combining the design rules for CMOS chips with the techniques for GMR device microfabrication allows the full integration of these sensors with the required electronics (e.g., bias and conditioning circuits, signal processing, memory elements, etc.). Recent achievements regarding the monolithic integration of GMR structures onto standard CMOS circuitry is summarized in Fig. [10](#page-19-0). The fabrication of spin valve based magnetic field sensing devices directly onto processed chips

Fig. 9 Experimental oscillograms: a from R-to-t circuit and single GMR devices (Fig. [8](#page-16-0)c), b from V-to-f circuit and GMR Wheatstone bridges (Fig. [8](#page-16-0)d)

(from non-dedicated CMOS standard technologies) is described in [\[44](#page-26-0)] (see Fig. $10a$). Functional devices are successfully developed with an standard 0.35 μ m AMS technology and with a non commercial CNM 2.5 μm technology (see Fig. [10b](#page-19-0)). Due to its extended use, the AMS $0.35 \mu m$ process has also recently used for integrated current sensing at the integrated circuit level [[45,](#page-26-0) [46](#page-26-0)] (see Fig. [10](#page-19-0)c).

Fig. 10 Real monolithic integration of GMR structures onto pre-processed CMOS chips: a wafers from CNM25 non-commercial technology $[44]$ $[44]$, **b** non-dedicated AMS 0.35 μ m technology $[44]$, c specifically designed AMS 0.35 μm chip for sub-mA current sensing [[46](#page-26-0)]

6 Commercially Available Sensors

GMR is a relatively novel technology. At this moment, up to our knowledge, only few companies (NVE, Infineon and Sensitec) have released GMR linear sensors to the market, beyond the preliminary application on read heads. Other companies include anisotropic magnetoresistance (AMR) based sensors to their portfolio (Honeywell, Zetex, Sypris, Philips and ADI).

NVE

NVE is the world leader company in analog GMR sensing technology. It has a complete catalog [[47\]](#page-26-0) with sensors with different magnetic field range applications. Focusing on analog applications, their devices are unipolar (not able to detect sign, see Fig. [11a](#page-20-0)) and they are based on half bridges (two opposite shielded magnetoresistors) with magnetic flux concentrators. Sensitivities range from 5 to 10 mV/V/mT, with linear ranges from ± 0.1 to \pm 7 mT and an input resistance of about 5 kΩ. They have described a good number of successful applications such as general magnetometry, electric current sensing, magnetic media detection and currency detection and validation.

Fig. 11 Output characteristic of two representative GMR commercial sensors: a AA002-02 from NVE (reprinted from [\[47\]](#page-26-0)), b GF705 from Sensitec (reprinted from [\[49\]](#page-26-0))

Infineon

Infineon has developed a line of sensors mainly focusing on the automotive market, with angle sensors and encoders. They release ICs including the associated electronics. A detailed explanation of the functionality can be found in [\[48](#page-26-0)].

Sensitec

Sensitec has recently developed GMR sensors for general magnetic field sensing and magnetic encoding also based on unipolar half bridges (see Fig. 11b). Sensitivity is in the order of 10 mV/V/mT with linear range from ± 1 to \pm 8 mT and input resistance about 5 kΩ.

It is also interesting to compare the noise figure of GMR sensor against those of standard AMR and Hall based, in order to highlight their detectivity level. Such a comparison is made in Fig. 12.

7 Successful Applications

7.1 General Magnetometry

The most of the applications developed with GMR magnetic field sensing is related to the measurement of the Earth's magnetic field perturbations produced by specifically considered ferreous body. This way, a position detecting scheme is always present.

For example, it is possible to use GMR sensors to locally measure the small magnetic perturbations caused by the iron of the car's body over the Earth's magnetic field. Moreover, if we use GMR gradient type sensors, the output signal is only dependent on the magnitude of the magnetic field variation, and no additional external magnetic field compensation is required. This way, a voltage 'signature' is obtained from the differential output of such a sensor when a car is running close to it. Within this scheme, it is easy to incorporate another sensor, placed to a well known distance in order to also measure the car speed. This proposal has been successfully developed by Pelegrí et al. [\[51](#page-26-0)].

The same physical principle can be directly translated to the measurement of vibrations in industrial machines. The small magnetic variations over the Earth's field produced by the vibration of the ferromagnetic pieces in industrial installations can be converted into resistance variations by the use of GMR magnetic field gradient devices. By using three sensors with the appropriated XYZ arrangement, a complete description of the vibration can be obtained. A prototype was developed by PelegrÍ et al. [\[52](#page-26-0)] and successfully tested with a drilling machine.

For linear magnetic position, in addition to the measurement of the Earth's field variations produced by magnetic materials, we can also use, if possible, permanent magnets associated to the moving part of the system. This way, the measurement of the absolute magnetic field is considered. Arana et al. [[6\]](#page-24-0) reported on the design of a high sensitivity linear position sensor using granular GMR devices. Sensitivities above 10 mV/V/mm are demonstrated by the utilization of Nd-Fe-B (0.4 T) magnets.

Angle and circular position detectors are also demanded by the industry: automotive applications, rotational machinery, etc. This kind of sensors are usually designed as contact-less systems in which a magnetic sensor (GMR in our case) detects the relative angular position of a rotationally moving magnet. This is the case presented in [\[53](#page-26-0), [54](#page-26-0)]. In the first case the authors focus on their specifically designed sensor, based on a granular MR. Because of the independence on the magnetic field direction, this technology is optimal for cylindrical symmetry problems. When a NdFeB is used, sensitivities about 0.25 mV/V/ $^{\circ}$ are achieved.

The conservative aerospace sector traditionally used old and well experimented components in its developments. The utilization of brand new technologies in commercial of the shelf (COTS) for space missions is nowadays only in the nearly stage. COTS are cheaper, faster in delivering and with wider reliability. Michelena et al. [[55](#page-26-0)–[57\]](#page-26-0) introduce the possibility of using GMR commercial sensors in space

applications. GMR sensors have not been flown yet but INTA, the Spanish National Institute of Aerospace Technology is working on the adaptation of a miniaturized GMR three axis sensor (HMC2003, from Honeywell) to the attitude control system in the frame of the OPTOS project, which is a $10 \times 10 \times 10 \text{ cm}^3$ Picosat devoted to be technological test bed. The circuitry consist of conditioning and biasing electronics blocks.

7.2 Current Sensing

Electrical current can be indirectly monitored with the measurement of the generated magnetic field by means of GMR sensors. In this way, we can achieve a measurement that is sensitive, isolated and from dc up to the bandwidth of the sensors, theoretically in the range of GHz.

In the medium to high current range, a specific full bridge spin valve sensor for industrial applications is designed, characterized, implemented and tested in [[58\]](#page-26-0). After soldering it onto a PCB strap, it is able to monitor currents up to 10 A. An

Fig. 13 Electric current sensing with GMR devices: a medium to high currents with an hybrid PCB-IC technology $[9]$ $[9]$, **b** integrated low current measurement $[22]$ $[22]$ $[22]$, **c** sub-mA monolithic integrated current measurement in AMS $0.35 \mu m$ chips, **d** detection improvement with the help of V-to-f schemes [\[31\]](#page-25-0)

improved design is presented in [[9\]](#page-24-0) (see Fig. [13](#page-22-0)a), where a meandered strap is designed in order to give the sensor a better performance regarding voltage offset, thermal drifts and immunity against external magnetic fields. Regarding specific applications, GMR sensors have been successfully used in differential current meters [\[59](#page-26-0)], switching regulators [\[60](#page-26-0)], electrical power measurement [[61\]](#page-27-0) and battery management [[62\]](#page-27-0).

GMR based sensors have also been successfully applied to low current measurement, in different scenarios [[63\]](#page-27-0), in particular some compatible with CMOS technology. In this sense, we have also demonstrated the applicability of spin-valve structures [\[11](#page-24-0)] and bridges [[22\]](#page-25-0) (see Fig. [13](#page-22-0)b) to the measurement of such level electrical currents. The detectivity of such sensors can be improved by including Rto-t V-to-f schemes in the measuring process $[31]$ $[31]$ (see Fig. [13c](#page-22-0)). In addition, electrical analog isolators were also designed with a basis of GMR structures [[64\]](#page-27-0). Finally, the potentiality of these devices as milliwattmeters has been also demon-strated [[65\]](#page-27-0).

7.3 Biological

GMR sensors have been proposed for different bioapplications suchs as molecular recognition [\[66](#page-27-0)], bacteria analysis [\[67](#page-27-0)], microfluidic systems [[68\]](#page-27-0), hyperthermia treatments [\[69](#page-27-0)] or neural magnetic field detection [[70](#page-27-0)].

With the rapid development of microfabrication techniques, together with the finding of compatible devices, the concept of Lab On a Chip has become more and more important in the last years. Portable devices have been recently developed which are capable of driving a fluid trough microchannels close to a detecting region, with additional conditioning and acquiring electronics. The usual scheme is the detection of the magnetic fringe field of a magnetically labeled biomolecule interaction with a complementary biomolecule bound to a magnetic field sensor. In this context, magnetoelectronics has emerged as a promising new platform technology for biosensor and biochip development [\[66](#page-27-0)].

8 Conclusions

GMR technology has demonstrated its maturity in its relatively short existence. It gained its popularity in the hard disks market and its success has open new doors. At this moment, only three companies develop general purpose GMR based magnetometers. But specific GMR sensors are nowadays successfully designed for ad hoc applications in the fields of the bio-technology, microelectronics or automotive, among others. Their intrinsic properties regarding high sensitivity, small size and compatibility with CMOS electronics allow us to be optimistic on the next future.

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