# **On Reachability Analysis of Updatable Timed Automata with One Updatable Clock**

Yunqing Wen<sup>1</sup>, Guoqiang Li<sup>1( $\boxtimes$ )</sup>, and Shoji Yuen<sup>2</sup>

<sup>1</sup> School of Software, Shanghai Jiao Tong University, Shanghai, China {wyqwyq,li.g}@sjtu.edu.cn <sup>2</sup> Graduate School of Information Science, Nagoya University, Nagoya, Japan

yuen@is.nagoya-u.ac.jp

**Abstract.** As an extension of *Timed Automata (TAs)*, *Updatable Timed Automata (UTAs)* proposed by Bouyer et al. have the ability to update clocks in a more elaborate way than simply reset them to zero. The reachability of general UTAs is undecidable, by regarding a pair of updatable clocks as counters updatable with incrementation and decrementation operations. This paper investigates the model of subclass of UTAs by restricting the number of updateable clocks. It is shown that the reachability of *UTAs with one updatable clock (UTA1s)* under diagonal-free constraints is decidable. The decidability is proved by treating a region of a UTA1 as an unbounded digiword, and encoding sets of digiwords that are accepted by a pushdown system where regions are generated on-the-fly on the stack.

### **1 Introduction**

*Timed Automata(TAs)*, introduced by Alur and Dill [\[1](#page-13-0)], are one of the moststudied and most-established models for real-time systems. Lots of work has been devoted to extensions of timed automata, with much interest for classes whose emptiness problem remains decidable.

*Updatable Timed Automata(UTAs)* [\[2,](#page-13-1)[3\]](#page-13-2) are extensions of timed automata, based on the possibility to update the clocks in an elaborate way such as increment and decrement operations and assignments to arbitrary values. Their decidability have been investigated in [\[4\]](#page-13-3), and also a quite precise way the thin frontier between decidable and undecidable classes of updatable timed automata has been described. The undecidability is technically shown to simulate the Minsky machine in general, while the decidability is shown by the fact that the constructed regions are finitely many. Our motivation is to investigate an interesting subclass of UTAs existing between them.

This paper gives a positive answer for the reachability of *UTAs with one updatable clock (UTA1s)* with diagonal-free time constraints by constructing regions on-the-fly over the stack of *pushdown systems (PDSs)*. Our result expands the thin frontier between decidable and undecidable classes of updatable timed automata. The model can be effectively used into soft real-time system modelling and analysis, where the updatable clock is used to depict relative

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deadline, which can be flexibly modified according to different conditions and environments.

The rest of the paper is organized as follows. Section [2](#page-1-0) gives an introduction of UTAs and PDSs. Section [3](#page-3-0) introduces UTAs with one updatable clock. Section [4](#page-5-0) introduces the notion of *digiwords*, which is equivalent to region [\[1\]](#page-13-0), but provides us a more concise description. Section [5](#page-6-0) shows the decidable reachability of UTA1s by encoding them to PDSs, based on the notion of digiwords. The related work is presented in Sects. [6](#page-8-0) and [7](#page-9-0) concludes the paper.

# <span id="page-1-0"></span>**2 Preliminaries**

For finite words  $w = \gamma_1 \gamma_2 \dots \gamma_n$ , we denote  $\gamma_i \in w$  for  $0 \leq i \leq n$ .

Let  $\mathbb{R}^{\geq 0}$  and N denote the sets of non-negative real numbers and natural numbers respectively. Let  $\mathbb{N}^{\omega} = \mathbb{N} \cup \{\omega\}$ , where  $\omega$  is the first limit ordinal. Let I denote the set of *intervals* over  $\mathbb{N}^{\omega}$ . An interval can be written as a pair of a lower limit and an upper limit in the form of either  $(a, b), [a, b), [a, c], (a, c]$ , where  $a, c \in \mathbb{N}, b \in \mathbb{N}^{\omega}$ , ('and ')' denote open limits, and '[' and ']' denote closed limits. For a number  $r \in \mathbb{R}^{\geq 0}$  and an interval  $I \in \mathcal{I}$ , we use  $r \in I$  to denote that r belongs to I. Let  $I \setminus I' = \{r \mid r \in I \land r \notin I'\}.$ 

Let  $X = \{x_1, \ldots, x_n\}$  be a finite set of *clocks*. A *clock valuation*  $\nu : X \to \mathbb{R}^{\geq 0}$ , assigns a value to each clock  $x \in X$ .  $\nu_0$  represents all clocks in X assigned to zero. Given a clock valuation  $\nu$  and a time  $t \in \mathbb{R}^{\geq 0}$ ,  $(\nu + t)(x) = \nu(x) + t$ , for  $x \in X$ . A clock assignment function  $\nu[y \leftarrow b]$  is defined by  $\nu[y \leftarrow b](x) = b$  if  $x = y$ , and  $\nu(x)$  otherwise.  $Val(X)$  is used to denote the set of clock valuations of X.

**Definition 1 (Clock Constraint).** *Given a finite set of clocks* X*, we define* diagonal-free constraints con<sub>di</sub> and diagonal constraints con, respectively as fol*lows:*

$$
con_{df} ::= x \in I?
$$
  
con ::=  $x \in I?$  |  $x - y \in I?$ 

*where*  $x, y \in X$  *and*  $I \in \mathcal{I}$ *.* 

### **2.1 Updatable Timed Automata**

*Updatable Timed Automata (UTAs)* [\[2](#page-13-1)[–4\]](#page-13-3), extended from TAs, provide a more flexible way to adjust the value of clock during location switches.

**Definition 2 (Updatable Timed Automata).** *A UTA is a tuple* A =  $\langle Q, q_0, F, X, \Delta \rangle$ , where

- *–* Q is a finite set of control locations, with the initial location  $q_0 \in Q$ ,
- $-F \subseteq Q$  *is the set of final locations,*
- *–* X *is a finite set of clocks,*
- $\Delta \subseteq Q \times Q \times Q$ , where  $\mathcal O$  *is a set of* operations. A transition  $(q_1, \phi, q_2) \in \Delta$ *is written as*  $q_1 \xrightarrow{\phi} q_2$ *, in which*  $\phi$  *is either*

**Local**  $\epsilon$ , an empty *operation*, Test  $x \in I$ ? *or*  $x - y \in I$ ?*, where*  $x, y \in X$  *and*  $I \in \mathcal{I}$ *,* **Assignment**  $x \leftarrow I$ *, where*  $x \in X$  *and*  $I \in \mathcal{I}$ *,* **Increment**  $x := x + 1$ *, where*  $x \in X$  *or* **Decrement**  $x := x - 1$ *, where*  $x \in X$ *.* 

Given a UTA A, we use  $Q(\mathcal{A}), q_0(\mathcal{A}), F(\mathcal{A}), X(\mathcal{A})$  and  $\Delta(\mathcal{A})$  to represent its set of control locations, initial location, set of final locations, set of clocks and set of transitions, respectively. We will use similar notations for other models.

**Definition 3 (Semantics of UTAs).** *Given a UTA*  $A = \langle Q, q_0, F, X, \Delta \rangle$ , a *configuration is a pair*  $(q, \nu)$  *of a control location*  $q \in Q$ *, and a clock valuation*  $\nu$ *on* X*. The transition relation of the UTA is represented as follows,*

- $-$  Progress transition:  $(q, \nu) \xrightarrow{t} (q, \nu + t)$ *, where*  $t \in \mathbb{R}^{\geq 0}$ *.*
- *–* Discrete transition:  $(q_1, \nu_1) \stackrel{\phi}{\rightarrow} \mathscr{A}$   $(q_2, \nu_2)$ *, if*  $q_1 \stackrel{\phi}{\rightarrow} q_2 \in \Delta$ *, and one of the following holds,*
	- Local  $\phi = \epsilon$ , then  $\nu_1 = \nu_2$ .
	- **Test**  $\phi = x \in I$ ? *or*  $\phi = x x' \in I$ ?*,*  $\nu_1 = \nu_2$  *and*  $\nu_2(x) \in I$  *holds or respectively*  $\nu_2(x) - \nu_2(x') \in I$  *holds.*
	- **Assignment**  $\phi = x \leftarrow I$ ,  $\nu_2 = \nu_1[x \leftarrow r]$  where  $r \in I$ .
	- **Increment**  $\phi = x := x + 1$ ,  $\nu_2 = \nu_1[x \leftarrow \nu_1(x) + 1]$ *.*
	- **Decrement**  $\phi = x := x 1$ ,  $\nu_2 = \nu_1[x \leftarrow \nu_1(x) 1]$  and  $\nu_1(x) \ge 1$  holds.

*The initial configuration is*  $(q_0, \nu_0)$ *. The transition relation is*  $\rightarrow$  *and we define*  $\rightarrow = -\frac{t}{\alpha}$   $\rightarrow \infty$ , and define  $\rightarrow^*$  to be the reflexive and transitive closure *of*  $\rightarrow$ *. Without confusion, we will later leave out the subscript*  $\mathscr A$ *.* 

If only diagonal-free constraints appear in test transitions, the corresponding UTAs are called diagonal-free UTAs. Different from TAs, diagonal or diagonalfree constraints heavily affect decidability results of UTAs. We list a few undecidable subclasses of UTAs.

**Proposition 1.** *(Proposition 2 in [\[2](#page-13-1)]) UTAs without increment rules under diagonal-free constraints are undecidable.*

**Proposition 2.** *(Proposition 3 in [\[2](#page-13-1)]) UTAs without decrement rules under diagonal constraints are undecidable.*

#### **2.2 Pushdown Systems**

A *pushdown system* [\[5\]](#page-13-4) is a transition system equipped with a finite set of control locations and a stack. The stack contains a word over some finite stack alphabet, whose length is unbounded. Hence, a pushdown system may have infinitely many reachable states.

**Definition 4 (Pushdown Systems).** *A pushdown system(PDS) is a quadruple*  $\langle P, \Gamma, \Delta \rangle$  *where* 

- *–* P *is a finite set of states,*
- *–* Γ *is finite stack alphabet,*
- $-\Delta \subseteq P \times \Gamma^{\leq 2} \times P \times \Gamma^{\leq 2}$  *is a finite set of transitions, where*  $(p, v, q, w) \in \Delta$  *is denoted by*  $\langle p, v \rangle \hookrightarrow \langle q, w \rangle$ , and

*We use*  $\alpha, \beta, \gamma, \cdots$  *to range over*  $\Gamma$ *, and*  $w, v, \cdots$  *over words in*  $\Gamma^*$ *.* 

*A* configuration *of*  $P$  *is a pair*  $\langle q, w \rangle$ *, where*  $q \in Q$  *and*  $w \in \Gamma^*$ *. A transition*  $relation \implies between configurations of P is defined by$ 

$$
\langle q, \gamma w' \rangle \Rightarrow \langle q', ww' \rangle \text{ if } \langle q, \gamma \rangle \hookrightarrow \langle q', w \rangle
$$

*The reflective and transitive closure of*  $\Rightarrow$  *is denoted by*  $\Rightarrow$ <sup>\*</sup>, and we write  $c \stackrel{\sigma}{\Rightarrow}^n c'$ *if*  $c \stackrel{r_1}{\Rightarrow} c_1 \stackrel{r_2}{\Rightarrow} \ldots c_n \stackrel{r_n}{\Rightarrow} c'$  for any  $n \in \mathbb{N}$  and  $c, c', c_i \in Q \times \Gamma^*$  with  $1 \leq i \leq n$  and  $\sigma = [r_1, r_2, \dots, r_n].$ 

# <span id="page-3-0"></span>**3 Updatable Timed Automata with One Updatable Clock**

<span id="page-3-1"></span>We propose a subclass of UTAs in a different facet by restricting the number of updatable clocks to be one, rather than restricting the ability of updates.

**Definition 5 (Updatable Timed Automata with One Updatable Clock).** *A UTA with one updatable clock (UTA1) is a tuple*  $A =$  $\langle Q, q_0, F, X, c, \Delta \rangle$ *, where* 

- *–* Q is a finite set of control locations, with the initial location  $q_0 \in Q$ ,
- $-F \subseteq Q$  *is the set of final locations,*
- $\overline{X} = \{x_1, \ldots, x_k\}$  *is a finite set of clocks, and c is the singleton updatable clock,*
- $\Delta \subseteq Q \times \mathcal{O} \times Q$ , where  $\mathcal{O}$  *is a set of* operations. A transition  $(q_1, \phi, q_2) \in \Delta$  $i s$  written as  $q_1 \stackrel{\phi}{\longrightarrow} q_2$ , in which  $\phi$  *is either* Local  $\epsilon$ , an empty *operation*, **Test**  $x \in I$ ?*, where*  $x \in X \cup \{c\}$  *is a clock and*  $I \in \mathcal{I}$  *is an interval,* **Assignment**  $x \leftarrow I$ *, where*  $x \in X \cup \{c\}$  *and*  $I \in \mathcal{I}$ *, or* **Increment**  $c := c + 1$ *, or* **Decrement**  $c := c - 1$ *.*

*Remark 1.* UTA1s defined in Definition [5](#page-3-1) are diagonal-free. We do *not* discuss diagonal constraints between clocks, and our following proofs cannot be naturally extended to cover them.

**Definition 6 (Semantics of UTA1s).** *Given a UTA1*  $\mathcal{A} = \langle Q, q_0, F, X, c, \Delta \rangle$ , *a configuration(state) is a pair*  $(q, \nu)$  *of a control location*  $q \in Q$ *, and a clock valuation*  $\nu$  *on*  $X \cup \{c\}$ *. The transition relation of the UTA1 is represented as follows,*

 $-$  Progress transition:  $(q, \nu) \xrightarrow{t} (q, \nu + t)$ *, where*  $t \in \mathbb{R}^{\geq 0}$ *.* 

*−* Discrete transition:  $(q_1, \nu_1) \xrightarrow{\phi} (q_2, \nu_2)$ , if  $q_1 \xrightarrow{\phi} q_2 \in \Delta$ , and one of the follow*ing holds,*

- Local  $\phi = \epsilon$ , then  $\nu_1 = \nu_2$ .
- **Test**  $\phi = x \in I$ ?,  $\nu_1 = \nu_2$  *and*  $\nu_2(x) \in I$  *holds.*
- **Assignment**  $\phi = x \leftarrow I$ ,  $\nu_2 = \nu_1[x \leftarrow r]$  where  $r \in I$ .
- **Increment**  $\phi = c := c + 1$ ,  $\nu_2 = \nu_1[c \leftarrow \nu_1(c) + 1]$ *.*
- **Decrement**  $\phi = c := c 1$ ,  $\nu_2 = \nu_1[c \leftarrow \nu_1(c) 1]$  *and*  $\nu_1(c) \ge 1$  *holds.*

*The initial configuration is*  $(q_0, \nu_0)$ *.* 

*Remark 2.* Although only increment  $(c := c + 1)$  and decrement  $(c := c - 1)$  are considered in the Definition [5,](#page-3-1) arbitrary decrement operation can also be easily encoded by some adjustment to the UTA1s we defined. For example,  $p \xrightarrow{c:=c-d} q$ , where  $d$  is some fixed positive integer, can be encoded in the following way, by introducing an extra clock x' and more locations  $p_1, p_2, \ldots, p_d, p_{d+1}: p \xrightarrow{x' \leftarrow [0,0]}$  $p_1 \xrightarrow{c:=c-1} p_2 \xrightarrow{c:=c-1} p_3 \cdots p_d \xrightarrow{c:=c-1} p_{d+1} \xrightarrow{x' \in [0,0]^2} q.$ 

<span id="page-4-1"></span>*Example [1](#page-4-0).* The UTA1 illustrated in Fig. 1 has three clocks,  $c, x_1$  and  $x_2$ , among which  $c$  is the singleton updatable clock allowed to update in a different way.



<span id="page-4-0"></span>**Fig. 1.** An Example for UTA1s

One run of the UTA1 is as follows:  $(q_0, \nu_0) \xrightarrow{2.5} (q_0, \nu_1) \xrightarrow{x_1 \in (2, +\infty)}$  $(q_1, \nu_2) \xrightarrow{c:=c-1} (q_2, \nu_3) \xrightarrow{x_2 \leftarrow (1, 6)} (q_f, \nu_4) \xrightarrow{5} (q_f, \nu_5)$ , where  $-\nu_0 = {\nu_0(c) = \nu_0(x_1) = \nu_0(x_2) = 0},$  $-\nu_1 = \nu_2 = {\nu_1(c) = \nu_1(x_1) = \nu_1(x_2) = 2.5},$  $-\nu_3 = {\nu_3(c) = 1.5, \nu_3(x_1) = \nu_3(x_2) = 2.5},$  $-\nu_4 = {\nu_4(c) = 1.5, \nu_4(x_1) = 2.5, \nu_4(x_2) = 5.7}, \text{ and}$  $-\nu_5 = {\nu_5(c) = 6.5, \nu_5(x_1) = 7.5, \nu_5(x_2) = 10.7}.$ 

The first transition is a progress transition which elapses 2.5 time units, and each clock grows older for 2.5. The second transition tests whether the value of clock  $x_1$  is greater than 2, and since it is so, a move from location  $q_0$  to location  $q_1$  happens. The sequent transition updates the particular clock c by decreasing one time unit and make a move from location  $q_1$  to location  $q_2$ . The last but one transition randomly picks a value(in this run, 5.7 is picked) from the range  $(1, 6)$  and assigns it to the clock  $x_2$ . The last transition is a progress transition which elapses 5 time units.

**Definition 7 (Reachability Problem).** *Given a UTA1*  $\mathcal{A} = \langle Q, q_0, F, X, c, \Delta \rangle$ *and a state* q*, decide whether there exists a path from the initial configuration such*  $as (q_0, \nu_0) \rightarrow^* (q, \nu)$ *, for some*  $\nu$ *.* 

### <span id="page-5-0"></span>**4 Digiword and Its Operations**

We denote the powerset of D by  $\mathcal{P}(D)$ . Let  $\mathcal{A} = (Q, q_0, F, X, c, \Delta)$  be a UTA1, and let n be the largest integer appearing in  $\Delta$ . For  $v \in \mathbb{R}^{\geq 0}$ , proj $(v) = r_i$  if  $v \in \mathbf{r}_i \in Intv(n)$ , where

$$
Intv(n) = \{ \mathbf{r}_{2i} = [i, i] \mid 0 \le i \le n \} \cup \{ \mathbf{r}_{2i+1} = (i, i+1) \mid 0 \le i < n \} \cup \{ \mathbf{r}_{2n+1} = (n, \omega) \}
$$

The idea of the following digitization is inspired by  $[6-10]$  $[6-10]$ .

**Definition 8.** *Let*  $frac(x,t) = t - floor(t)$  *for*  $(x,t) \in ((X \cup \{c\}) \times \mathbb{R}^{\geq 0})$ *, where* t *is clock* x's value. A digitization digi :  $Val(X \cup \{c\}) \rightarrow (\mathcal{P}((X \cup \{c\}) \times Intv(n)))^*$ *is as follows. For*  $\nu \in Val(X \cup \{c\})$ *, let*  $Y_0, Y_1, \cdots, Y_m$  *be sets that collect*  $(x, proj(t))$ *'s having the same frac* $(x, t)$  *for*  $(x, t) \in (X \cup \{c\}) \times \mathbb{R}^{\geq 0}$ *. Among them,*  $Y_0$  *(which is possibly empty) is reserved for the collection of*  $(x, proj(t))$ *with*  $frac(t) = 0$ *. We assume*  $Y_i$ *'s except for*  $Y_0$  *is non-empty, and*  $Y_i$ *'s are* sorted by the increasing order of  $frac(x,t)$  (i.e.,  $frac(x,t) < frac(x',t')$  for  $(x, proj(t)) \in Y_i$  and  $(x', proj(t')) \in Y_j$ , where  $0 \le i < j \le m$ ).

*Example 2.* In example [1,](#page-4-1)  $n = 6$  and we have 13 intervals illustrated below.



For the clock valuation  $\nu_4$  in Example [1,](#page-4-1)  $digi(\nu_4)$  is  $\{(c, \mathbf{r}_3), (x_1, \mathbf{r}_5)\}\{(x_2, \mathbf{r}_{11})\}.$ Note that, for convenience we do not show the empty set  $Y_0$  in  $\text{diag}(v_4)$ .

A word in  $(\mathcal{P}((X \cup \{c\}) \times Intv(n)))^*$  is called *digiword*. If for a digiword  $\overline{Y}$ there exists an clock valuation  $\nu \in Val(X \cup \{c\})$  such that  $digi(\nu) = \overline{Y}$ , we call the word *well-formed digiword*.

*Remark 3.* For a finite set of clocks  $X \cup \{c\}$ , the set of *well-formed digiword* is finite. This is obvious, since there are a fixed number of clocks, which leads to a fixed number of combinations of digiword. This play an essential role in our encoding, ensuring the PDS has finite states.

**Definition 9.** *Let*  $\overline{Y} = Y_0 \cdots Y_m \in (\mathcal{P}((X \cup \{c\}) \times Intv(n)))^*$ *. We define* digiword operations *as follows.*

*–* **Insert**<sub>I</sub> insert $(\bar{Y}, (x, r_i))$  *for*  $x \in X \cup \{c\}$  *inserts*  $(x, r_i)$  *to*  $\bar{Y}$  *at* 

⎧  $\overline{J}$  $\sqrt{2}$ either put into  $Y_j$  for  $j > 0$ , or put the singleton set  $\{(x, r_i)\}\$ at any place after  $Y_0$  if i is odd put into  $Y_0$  if is even

- *–* **Delete**  $delete(\bar{Y}, x)$  *for*  $x \in X \cup \{c\}$  *is obtained from*  $\bar{Y}$  *by deleting the element*  $(x, r)$  *indexed by x.*
- *–* **Increase**  $increase(\bar{Y}, c)$  *is obtained from*  $\bar{Y}$  *by replacing the element*  $(c, r_i)$ *indexed by* c *with element*  $(c, \mathbf{r}_{min\{i+2,2n+1\}})$ *.*
- *–* **Decrease**  $decrease(\overline{Y}, c, d)$  and  $d \in \mathbb{N}$  *is obtained from*  $\overline{Y}$  *by replacing the element*  $(c, r_i)$  *indexed by* c *with element*  $(c, r_{max{i-d,0}})$ *.*
- *–* **Shift***. Let*  $j \in [0..m]$  and  $0 \leq i \leq 2n + 1$ *. A shift*  $\overline{Y} = Y_0 Y_1 \cdots Y_m \Rightarrow \overline{Y}' = Y_0 Y_1 \cdots Y_m$  $Y_0'Y_1'\cdots Y_{m'}'$  is defined as follows.

$$
\begin{cases}\n\text{either } \bar{Y}' = Y'_0, Y'_1, \cdots, Y'_{m+1} \text{ if } Y_0 \neq \emptyset, Y'_0 = \emptyset, Y'_1 = \{(x, \mathbf{r}_{min}\{i+1, 2n+1\}) \\ & \quad | (x, \mathbf{r}_i) \in Y_0 \} \text{ and } Y'_j = Y_{j-1} \text{ for } j \in [2 \dots m+1]. \\
\text{or } \bar{Y}' = Y'_0, Y'_1, \cdots, Y'_{m-1} \text{ otherwise, } Y'_0 = \{(x, \mathbf{r}_{min}\{i+1, 2n+1\}) \mid (x, \mathbf{r}_i) \in Y_m \}, \\
\text{and } Y'_j = Y_j \text{ for } j \in [1 \dots m-1].\n\end{cases}
$$

As convention, we define  $\Rightarrow^*$  as reflexive transitive closure of  $\Rightarrow$ .

*Example 3.* Consider the digiword in Example [1,](#page-4-1)  $digi(\nu_0) = \{(c, \mathbf{r}_0), (x_1, \mathbf{r}_0),$  $(x_2, r_0)$ .

- after finite times shifts,  $digi(\nu_1) = digi(\nu_2) = \{(c, \mathbf{r}_5), (x_1, \mathbf{r}_5), (x_2, \mathbf{r}_5)\}\$ – after  $decrease(digi(\nu_2), c, 2)$  $digi(\nu_3) = \{(c, \mathbf{r}_3), (x_1, \mathbf{r}_5), (x_2, \mathbf{r}_5)\}\$ – after  $insert(delete(digi(\nu_3), x_2), (x_2, r_{11}))$
- $\text{digi}(\nu_4) = \{(c, \mathbf{r}_3), (x_1, \mathbf{r}_5)\}\{(x_2, \mathbf{r}_{11})\}$
- after finite times shifts,  $digi(\nu_5) = \{(c, \mathbf{r}_{13}), (x_1, \mathbf{r}_{13})\}\{(x_2, \mathbf{r}_{13})\}$

Equivalent to the *regions* [\[1\]](#page-13-0) in nature, *well formed digiwords* are bisimilar to the clock valuations in the following sense:

 $\bar{Y}_1 \Rightarrow^* \bar{Y}_2$  if and only if  $\forall \nu \in [\bar{Y}_1], \exists t \in \mathbb{R}^{\geq 0}$  s.t.  $\nu + t \in [\bar{Y}_2]$ 

where,  $[\bar{Y}] = {\nu | \bar{Y} = digit(\nu)}.$ 

### <span id="page-6-0"></span>**5 Reachability for UTA1s**

In this section, we show that the reachability problem of UTA1s is decidable. discretePDSs, which nicely enjoy decidable property of configuration reachability. The key idea is that when the updatable clock's value exceeds the maximum integer n, we push a special symbol into the stack to record the updates.

**Definition 10.** For a UTA1  $A = \langle Q, q_0, F, X, c, \Delta \rangle$ , there is a PDS  $P = \langle P \times P \rangle$  $(\mathcal{P}((X \cup \{c\}) \times Intv(n)))^*, \{\bullet, \bot\}, \Delta_d\},$  where the set of states  $P = Q \cup \{p' \mid \bot\}$  $p \in Q$  *and the initial configuration*  $\kappa_0 = \langle (q_0, \{(x, r_0)|x \in X \cup \{c\}\}), \perp \rangle$ .  $\Delta_d$ *consists of:*

*– Time Progress*

- *1.*  $\langle (p, \overline{Y}), \perp \rangle \hookrightarrow \langle (p, \overline{Z}), \perp \rangle$ , where  $\overline{Y} \Rightarrow \overline{Z}$ , for  $(c, r_i) \in Y_i \in \overline{Y}$  and  $i <$  $2n - 1$ .
- *2.*  $\langle (p, \overline{Y}), \epsilon \rangle \hookrightarrow \langle (p, \overline{Z}), \bullet \rangle$ , where  $\overline{Y} \Rightarrow \overline{Z}$ , for  $(c, r_i) \in Y_i \in \overline{Y}$  and  $2n \leq i \leq j$  $2n + 1$ .

\n- \n
$$
Local \, (p \xrightarrow{\epsilon} q \in \Delta)
$$
\n $\langle (p, \overline{Y}), \epsilon \rangle \leftrightarrow \langle (q, \overline{Y}), \epsilon \rangle.$ \n
\n- \n $Test \, (p \xrightarrow{x \in \Gamma} q \in \Delta)$ \n $\langle (p, \overline{Y}), \epsilon \rangle \leftrightarrow \langle (q, \overline{Y}), \epsilon \rangle \text{ if } r_i \subseteq I \text{ for } (x, r_i) \in Y_j \in \overline{Y}.$ \n
\n- \n $A \, \text{sigmoid} \, (p \xrightarrow{x \in \Gamma} q \in \Delta)$ \n $1. \, \langle (p, \overline{Y}), \bullet \rangle \leftrightarrow \langle (p, \overline{Y}), \epsilon \rangle.$ \n
\n- \n $2. \, \langle (p, \overline{Y}), \bot \rangle \leftrightarrow \langle (p', \overline{Y}), \bot \rangle.$ \n
\n- \n $3. \, \forall r_i \subseteq I \setminus r_{2n+1}, \langle (p', \overline{Y}), \bot \rangle \leftrightarrow \langle (q, insert(delete(\overline{Y}, c), (c, r_i))), \bot \rangle.$ \n
\n- \n $4. \, \langle (p', \overline{Y}), \bullet \rangle \leftrightarrow \langle (q, insert(delete(\overline{Y}, c), (c, r_{2n+1}))), \bullet \rangle, \text{ if } r_{2n+1} \subseteq I.$ \n
\n- \n $5. \, \langle (p', \overline{Y}), \bullet \rangle \leftrightarrow \langle (q, insert(delete(\overline{Y}, c), (c, r_{2n+1}))), \bullet \rangle, \text{ if } r_{2n+1} \subseteq I.$ \n
\n- \n $A \, \text{sigmoid} \, (p \xrightarrow{x \in \bot} q \in \Delta, \text{ where } x \in X)$ \n
\n- \n $\forall r_i \subseteq I, \langle (p, \overline{Y}), \epsilon \rangle \leftrightarrow \langle (q, insert(delete(\overline{Y}, x), (x, r_i))), \epsilon \rangle.$ \n
\n- \n $I \, \text{measured} \, (p \xrightarrow{x \in \bot} q \in \Delta)$ \n
\n- \n $1. \, \langle ($

In our encoding, we abuse the symbol  $\epsilon$  in the left hand of the transition rule of PDS  $P$  to indicate that whatever the topmost symbol in the stack is, the transition can occur. This leads to a concise description for our encoding, which is equivalent to its counterpart with only standard transition rules. For example,  $\langle (p, \overline{Y}), \epsilon \rangle \hookrightarrow \langle (q, \overline{Y}), \epsilon \rangle$  can be replaced with two transition rules since there are only 2 symbols in the alphabet: (1)  $\langle (p, Y), \perp \rangle \hookrightarrow \langle (q, Y), \perp \rangle$  and (2)  $\langle (p, Y), \bullet \rangle \hookrightarrow \langle (q, Y), \bullet \rangle.$ 

Given a UTA1  $\mathcal{A}$ , for each kind of its transitions, we have its counterpart in the  $\Delta_d(\mathcal{P})$ . Some transitions of  $\mathcal{P}$ , at the first look, may seem hard to understand. The following give a simple explanation for simulating *assignment* and *decrement*.

For an assignment of the form  $x \leftarrow I$ , PDS  $\mathcal{P}$  proceed with two different cases: (1)  $x = c$ ; (2)  $x \neq c$ . For the first case,  $\mathcal{P}$  first need to pop all symbol • out of stack, then push a certain number of symbols • if needed, and finally perform delete and insert operations. For the latter case, much simpler, just directly perform *delete* and *insert* operations.

For a decrement of the form  $c := c - 1$ , PDS  $P$  proceed with four different cases: (1)  $\nu(c) > n+1$ ; (2)  $\nu(c) = n+1$ ; (3)n  $\lt \nu(c) < n+1$ ; (4)  $1 \leq \nu(c) \leq n$ . For the first case,  $\mathcal P$  merely pops out two symbols of  $\bullet$ . For the second and third cases, P pops out one or two symbol(s) of • and performs *decrease* operation. The difference between them is how much it decreases. For the last case,  $P$ merely performs *decrease* operation.

The following example shows the discretization of assignment transitions, decrement transitions.

*Example 4.* Consider the run in the Example [1.](#page-4-1)

- $(q_1, \nu_2) \xrightarrow{c:=c-1} (q_2, \nu_3)$ , where  $\nu_2 = {\nu_2(c) = \nu_2(x_1) = \nu_2(x_2) = 2.5}$ and  $\nu_3 = {\nu_3(c) = 1.5, \nu_3(x_1) = \nu_3(x_2) = 2.5}.$  Corresponding simulation:  $\langle (q_1, digi(\nu_2)), \perp \rangle \hookrightarrow \langle (q_2, decrease(digi(\nu_2), c, 2)), \perp \rangle.$
- $(q_2, \nu_3) \xrightarrow{x_2 \leftarrow (1,6)} (q_f, \nu_4)$ , where  $\nu_3$  is defined above and  $\nu_4 = {\nu_4(c) = 1.5, \nu_4(x_1) = 2.5, \nu_4(x_2) = 5.7}.$  Corresponding simulation:  $\langle (q_2, digi(\nu_3)), \perp \rangle \hookrightarrow \langle (q_f, insert(delete(digi(\nu_3), x_2), (x_2, r_{11}))), \perp \rangle.$

<span id="page-8-1"></span>**Definition 11.** Let  $\varrho$  be any configuration of a UTA1 such that  $\varrho_0 = (q_0, \nu_0) \hookrightarrow^*$  $\rho = (q, \nu)$ *. Define*  $\llbracket \varrho \rrbracket$  to be  $\langle (q, digi(\nu)), \bullet^k \perp \rangle$ *, where*  $k = 2 \times floor(\nu(c) - n) +$ ceiling(frac( $\nu(c)$ )) *if*  $\nu(c) > n$  *otherwise*  $k = 0$ *. A configuration*  $\kappa$  *of PDS P with some*  $\rho$  *and*  $\kappa = [\![\rho]\!]$  *is called an* encoded configuration.

*Example 5.* Consider the configuration  $(q_f, \nu_5)$  in Example [1,](#page-4-1) where  $\nu_5 = \{c =$  $6.5, x_1 = 7.5, x_2 = 10.7$ . By Definition  $11, [[(q_f, \nu_5)]] = \langle (q_f, digi(\nu_5)), \bullet \bot \rangle,$  $11, [[(q_f, \nu_5)]] = \langle (q_f, digi(\nu_5)), \bullet \bot \rangle,$ where  $digi(\nu_5) = \{(c, \mathbf{r}_{13}), (x_1, \mathbf{r}_{13})\}\{(x_2, \mathbf{r}_{13})\}.$ 

*Remark 4.* For a UTA1's time progress transition  $(p, \nu) \xrightarrow{t} (q, \nu')$ , where  $t \in \mathbb{R}^{\geq 0}$ , there has a transition sequence of  $[\![ (p, \nu) ]\!] \hookrightarrow \langle (p, \overline{Z}_1), w_1 \rangle \hookrightarrow \langle (p, \overline{Z}_2), w_2 \rangle \cdots \hookrightarrow$  $\left[\!\left[ \left( q,\nu' \right) \right]\!\right]$  to simulate it, which consists of finite many time *time progress* transitions of PDS.

<span id="page-8-2"></span>**Lemma 1.** *Given a UTA1* A*, its associated PDS* P*, and any configuration ,*  $\rho'$  of  $\mathcal A$ *.* 

 $(\textbf{Preservation}) \ \textit{If} \ \varrho \to \varrho' \ \textit{then} \ \llbracket \varrho \rrbracket \hookrightarrow^* \llbracket \varrho' \rrbracket.$ **(Reflection)** *If*  $[\![\varrho]\!] \hookrightarrow^* \kappa$ ,

- *1. there exists*  $\varrho'$  such that  $\kappa = [\![\varrho']\!]$  and  $\varrho \to^* \varrho'$ , or
- 2. κ *is not an encoded configuration, and there exists*  $\varrho'$  such that  $\kappa \hookrightarrow^* [\![\varrho']\!]$ *by transitions (of*  $P$ *) and*  $\rho \rightarrow^* \rho'$ .

With Lemma [1,](#page-8-2) we have the following theorem.

**Theorem 1.** *The reachability of a UTA1 is decidable.*

The proof is given in Appendix [A.](#page-10-0)

#### <span id="page-8-0"></span>**6 Related Work**

After *timed automata(TAs)* [\[1\]](#page-13-0) had been proposed by Alur and Dill, a lot of work has been devoted to extensions of TAs.

The extension that allows to compare the sum of two clocks with a constant has also been investigated in [\[1\]](#page-13-0). It leads to an undecidable class of automata. Periodic clock constraints defined in [\[11](#page-13-7)], can express properties like "the value of a clock is even" or "the value of a clock is of the form  $0.5+3n$  where n is some integer. The corresponding class of automata is strictly more powerful than TAs if silent transitions are not allowed but otherwise coincides with the original model.

*Controlled real-time automata*, a parameterized family of TAs with some additional features like clock stopping, variable clock velocities and periodic tests has been proposed in [\[12](#page-13-8)]. Due to carefully chosen restrictions, controlled real-time automata remains decidable.

*Recursive timed automata(RTAs)* [\[13](#page-13-9)] is an extension of TAs with recursive structure. It has clocks by the mechanism of "pass-by-value". When the condition of "glitch-freeness",i.e. all the clocks of components are uniformly either by "pass-by-value" or by "pass-by-reference", the reachability is shown to be decidable.

*Nested timed automata (NeTAs)* [\[9,](#page-13-10)[10](#page-13-6)[,14](#page-14-0)] extend TAs with recursive structure in another way, which allow clocks of some TAs in the stack elapse simultaneously with the current running clocks during time passage. Those clocks are named *local clocks*, while clocks in other TAs kept unaltered clocks during time passage are named *frozen clocks*. It is proved that the reachability of NeTAs with both types of clocks and a singleton *global clock* that can be observed by all TAs is decidable, while that with two or more global clocks is undecidable [\[10](#page-13-6)].

The *updatable timed automata (UTAs)* [\[4](#page-13-3)] is a natural syntactic extension of TA. It enjoyed the possibility of updating the clocks in a more elaborate way than just simple reset in TA. The value of a clock could be reassigned to a basic arithmetic computation result of values of other clocks. The paper gave undecidability and decidability results for several specific cases. The decidability results were obtained through a generalization of the region graph proposed by Alur and Dill, while the undecidability results were obtained by reducing an undecidable problem on Minsky Machine [\[15](#page-14-1)] to the emptiness problem for a subclass of UTAs. The expressiveness of the UTAs was also investigated in the paper. Our model UTA1s, is actually a specific subclass of general UTAs by restricting the number of updatable clock to one.

A forward analysis of UTAs has been proposed in [\[16\]](#page-14-2) for specific subclass of UTAs that do not use comparisons between clocks. Recently, a refined algorithm for specific subclass of UTAs with diagonal constraints has been proposed in [\[17\]](#page-14-3).

### <span id="page-9-0"></span>**7 Conclusion**

This paper has investigated the reachability of UTA1s, UTAs with one updatable clock. By restricting the number of updatable clocks to one, the reachability of UTA1s is decidable, under the diagonal-free constraints. The decidability is proved by encoding the clock behavior of UTA1s to PDSs based on the notion of *digiword*. The key idea is to use a stack to record the time interval exceeding the maximum constant integer. As a result, the transitions in pushdown systems are finer than that of UTA1s.

UTA1s defined in Definition [5](#page-3-1) only allow diagonal-free clock constraints. Our proof can not be extended to cover the UTA1s with diagonal clock constraints. We will further investigate the reachability for the UTA1s with diagonal clock constraints as a future work.

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## <span id="page-10-0"></span>**A A Proof of Lemma 1**

*Proof.* Let  $\varrho = (q, \nu)$ . Then  $[\![\varrho]\!] = \langle (q, digi(\nu)), w \rangle$ , where  $w = \bullet^k \perp$  for some k. For preservation part, By case analysis of  $\rho \to \rho'$ .

- 1. **Time Progress**:  $\varrho \xrightarrow{t} \varrho'$ . By the digiword's region-like property, we have  $di\dot{q}i(\nu) \Rightarrow^{*} di\dot{q}i(\nu+t)$ . Proceed with two subcases:
	- (a) If no stack operations involved (i.e.  $\nu(c) + t \leq n$ ), then we have  $[\![\varrho]\!] =$  $\langle (q, digi(\nu)), w \rangle \hookrightarrow^* [\![\varrho']\!] = \langle (q, digi(\nu + t)), w \rangle$  by applying the first transition rule of time progress rules finite times. Note that in this subcase,  $w = \perp$ .
	- (b) If  $\nu(c)+t>n$ , then we have  $[\![\varrho]\!] = \langle (q, digi(\nu)), w \rangle \hookrightarrow^* [\![\varrho']\!] = \langle (q, digi(\nu +$  $(t)$ ),  $w'$  by applying the first time progress rule finite times (maybe zero times if  $\nu(c) \geq n$ ) and then applying the second time progress rule finite times.
- 2. **Local**:  $\varrho = (p, \nu) \stackrel{\epsilon}{\rightarrow} \varrho' = (q, \nu)$ . Then with the *Local* transition of PDS,  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \hookrightarrow [\![\varrho']\!] = \langle (q, digi(\nu)), w \rangle.$
- 3. **Test**:  $\rho = (p, \nu) \xrightarrow{x \in I?} \rho' = (q, \nu)$ . Then with the *Test* transition of PDS,  $[\![\varrho]\!] = \langle (p, \operatorname{d}\!\mathit{igi}(\nu)), w \rangle \hookrightarrow [\![\varrho']\!] = \langle (q, \operatorname{d}\!\mathit{igi}(\nu)), w \rangle,$  since  $\exists (x, \mathbf{r}_i) \in Y_j \in \operatorname{d}\!\mathit{igi}(\nu)$ such that  $\mathbf{r}_i \subseteq I$ , where  $\text{d}\text{igi}(\nu) = Y_1 Y_2 \cdots Y_i \cdots Y_m$ .
- 4. **Assignment**:  $\rho = (p, \nu) \xrightarrow{x \leftarrow I} \rho' = (q, \nu[x \leftarrow d])$ , where  $d \in I$ . We proceed with 2 cases:
	- (a) If  $x \neq c$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \hookrightarrow [\![\varrho']\!] = \langle (q, digi(\nu[\nu \leftarrow \neg \varrho])) \rangle]$ d)), w) by applying the first assignment rule of PDS  $\mathcal{P}$ :  $\langle (p, \overline{Y}), \epsilon \rangle \hookrightarrow$  $\langle (q, insert(delete(\bar{Y}, x), (x, \mathbf{r}_i))), \epsilon \rangle$ , where  $\bar{Y} = digi(\nu)$  and  $d \in \mathbf{r}_i$ .
	- (b) Otherwise,  $x = c$ , proceed with two subcases,  $d \le n$  and  $d > n$ .
		- $d \le n$ : If  $w = \cdot^k \perp$  for  $k > 0$ , we first need to pop all symbols of • out of stack, by repeatedly applying the second assignment rule of PDS k times, having  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bullet^k \bot \rangle \hookrightarrow^* \kappa =$  $\langle (p, digi(\nu)), \perp \rangle$ , otherwise define  $\kappa = [\![\varrho]\!]$  since the stack already has no symbols of •. Then by applying the third and fourth assignment rule, we have  $\kappa \hookrightarrow \kappa' = \langle (p', digi(\nu)), \bot \rangle \hookrightarrow [ \![ \varrho' ]\!] = \langle (q, digi(\nu[x \leftarrow \bot)))) \rangle$  $d(\mathbf{)}), \perp$ .
			- $-$  **d** > **n**: If  $w = \cdot^k$  ⊥ for  $k > 0$ , we first need to pop all symbols of  $\bullet$  out of stack, by repeatedly applying the second assignment rule of PDS k times, having  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bullet^k \bot \rangle \hookrightarrow^* \kappa = \langle (p, digi(\nu)), \bot \rangle,$ otherwise define  $\kappa = [\![\varrho]\!]$ , since the stack already has no symbols of •. Next, we have  $\kappa \hookrightarrow \kappa' = \langle (p', digi(\nu)), \bot \rangle$  by the third assignment rule. Then, by repeatedly applying the fifth assignment rule of  $P$  until we have  $k = 2 \times floor(d-n) + ceiling(frac(d))$  symbols of • in stack,

we have  $\kappa' \hookrightarrow^* \kappa'' = \langle (p', digi(\nu)), \bullet^k \bot \rangle$ . Finally, by applying the last assignment rule of mathcalP, we have  $\kappa'' \hookrightarrow ||\varrho'|| = \langle (q, digi(\nu | c \leftarrow$  $d$ ),  $\bullet^k \perp$ ) $\angle$ .

- 5. **Increment**:  $\rho = (p, \nu) \xrightarrow{c:=c+1} \rho' = (q, \nu[c \leftarrow \nu(c) + 1])$ . We proceed with 3 subcases:
	- (a) If  $\nu(c) \leq n-1$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \leftrightarrow [\![\varrho']\!] =$  $\langle (q, increase(diai(\nu), c)), w \rangle = \langle (q, diai(\nu[c \leftarrow \nu(c) + 1]), w \rangle)$  by applying the first transition increment rule of PDS  $\mathcal{P}$ .
	- (b) If  $n-1 < \nu(c) < n$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \hookrightarrow [\![\varrho']\!] =$  $\langle (q, increase(digi(\nu), c)), \bullet w \rangle = \langle (q, digit(\nu[c \leftarrow \nu(c) + 1])), \bullet w \rangle \text{ by apply-} \rangle$ ing the second transition increment rule of PDS  $\mathcal{P}$ .
	- (c) If  $\nu(c) \geq n$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \leftrightarrow [\![\varrho']\!] =$  $\langle (q, increase(digi(\nu), c)), \bullet \bullet w \rangle = \langle (q, digi(\nu[c \leftarrow \nu(c) + 1])), \bullet \bullet w \rangle \text{ by }$ applying the third transition increment rule of PDS  $\mathcal{P}$ .
- 6. **Decrement**:  $\rho = (p, \nu) \xrightarrow{c:=c-1} \rho' = (q, \nu[c \leftarrow \nu(c) 1])$ . Note that only when  $\nu(c) \geq 1$ , can this transition happen. We proceed with 4 subcases:
	- (a) If  $\nu(c) > n+1$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bullet \bullet \bullet w' \rangle \hookrightarrow [\![\varrho']\!] =$  $\langle (q, digi(\nu)), \bullet w' \rangle = \langle (q, digi(\nu[c \leftarrow \nu(c)-1])), \bullet w' \rangle \text{ by applying the first}) \rangle$ transition decrement rule of PDS P.
	- (b) If  $\nu(c) = n + 1$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bullet \bullet \bot \rangle \leftrightarrow [\![\varrho']\!] =$  $\langle (q, decrease(digi(\nu), c, 1)), \perp \rangle = \langle (q, digi(\nu[c \leftarrow \nu(c)-1))), \perp \rangle$  by applying the second transition decrement rule of PDS  $\mathcal{P}$ .
	- (c) If  $n < \nu(c) < n+1$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bullet \bot \rangle \hookrightarrow [\![\varrho']\!] =$  $\langle (q, increase(digi(\nu), c, 2)), \perp \rangle = \langle (q, digit(\nu[c \leftarrow \nu(c) - 1])), \perp \rangle)$  by applying the third transition decrement rule of PDS  $P$ .
	- (d) If  $1 \leq \nu(c) \leq n$ , then we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), \bot \rangle \leftrightarrow [\![\varrho']\!] =$  $\langle (q, increase(digi(\nu), c, 2)), \perp \rangle = \langle (q, digi(\nu[c \leftarrow \nu(c)-1))), \perp \rangle \rangle$  by applying the fourth transition decrement rule of PDS  $P$ .

For *reflection* part, by induction on the steps of  $\hookrightarrow^*$ . **Base step:** Consider the case of  $[\![\varrho]\!] \hookrightarrow \kappa$ :

- 1. **Time Progress.** Obviously,  $[\![\varrho]\!] \hookrightarrow \kappa$  by one of two time progress rules of PDS P. Since digiwords have the region-like property,  $diqil(\nu) \Rightarrow \overline{Y}$  implies that there exists a clock valuation  $\nu' \in Val(X \cup \{c\})$  such that  $\nu' = \nu + t$ and  $\nu \in [\bar{Y}]$  for a real number t(more precisely,  $0 < t < 1$ ). Proceed with two cases:
	- (a) If  $\nu(c) < n$ , then  $[\![\varrho]\!] \hookrightarrow \kappa$  by using the first time progress rule of PDS  $\mathcal{P}$ . In such case, we have  $\kappa = \langle (p, \bar{Y}, \bot) \rangle = [\![\varrho' ]\!]$ , where  $\varrho = (p, \nu) \stackrel{t}{\rightarrow} \varrho' = (p, \nu').$
	- (b) If  $\nu(c) \geq n$ , then  $[\![\varrho]\!] \hookrightarrow \kappa$  by using the second time progress rule of PDS P. In such case, we have  $\kappa = \langle (p, \bar{Y}), \bullet w \rangle = [\![ \varrho' ]\!]$ , where  $\varrho = (p, \nu) \stackrel{t}{\rightarrow} \varrho' =$  $(p, \nu').$
- 2. **Local.** If  $[\![\varrho]\!] \leftrightarrow \kappa$  for  $p \stackrel{\epsilon}{\rightarrow} q$  being a transition in A, then  $\kappa =$  $\langle (q, digi(\nu)), w \rangle = [\![\varrho' ]\!]$ , where  $\varrho' = (q, \nu)$ .
- 3. **Test.** Similar with the case for **Local**.
- 4. **Assignment.** We only need to proceed with the first three cases, the other cases can not happen since the configuration is a *encoded configuration*(i.e.  $[\![\varrho]\!])$ .
	- (a) If  $\llbracket \varrho \rrbracket \hookrightarrow \kappa$  by applying the first assignment transition rule of PDS  $\mathcal{P}$ , then  $\kappa$  is a *encoded configuration*, since we have  $[\![\varrho]\!] = \langle (p, digi(\nu)), w \rangle \hookrightarrow \kappa =$  $\llbracket \varrho' \rrbracket = \langle (q, insert(detetedigi(\nu), x), (x, \mathbf{r}_i))), w \rangle$ , where  $\varrho = (p, \nu) \xrightarrow{x \leftarrow I}$  $\rho' = (q, \nu[x \leftarrow d])$  for  $d \in \mathbf{r}_i \in I$ .
	- (b) If  $\llbracket \varrho \rrbracket \leftrightarrow \kappa$  by applying the second assignment transition rule of PDS P, then the clock involved is c and κ may not be a *encoded configuration*. However, by applying the same rules finite times and then third assignment rule,  $\kappa \hookrightarrow^* \kappa_1 = \langle (p, digi(\nu)), \perp \rangle \hookrightarrow \kappa_2 =$  $\langle (p', digi(\nu)), \perp \rangle$ , and then if  $\mathbf{r}_i \in I$  for  $i \leq 2n$ , we have  $\kappa_2 \hookrightarrow \llbracket \varrho' \rrbracket =$  $\langle (q, insert(delete(digi(\nu), c), (c, r_i)), \perp \rangle$ , by applying the fourth assignment rule, otherwise, we have  $\kappa_2 \leftrightarrow^* \kappa_3 = \langle (p', digi(\nu)), \bullet^k \perp \rangle \hookrightarrow [ \![ \varrho' ]\!]$  $\langle (q, insert(delete(igi(\nu), c), (c, r_i)), \bullet^k \perp \rangle$  by applying finite times of fifth assignment rule first and then the last assignment rule.
	- (c) If  $\llbracket \varrho \rrbracket \hookrightarrow \kappa$  by applying the second assignment transition rule of PDS  $\mathcal{P}$ , the proof is similar to the above case.
- 5. **Increment.** We proceed with three cases:
	- (a) If it takes the first increment transition rule, then we can infer that  $\nu(c) \leq n-1$ . Thus we have  $[\![\varrho]\!] \hookrightarrow \kappa = \langle (q, increase(digi(\nu), c)), w \rangle =$  $[\![\varrho']\!]$ , where  $\varrho \xrightarrow{c:=c+1} \varrho'.$
	- (b) If it takes the second increment transition rule, then we can infer that  $n 1 < \nu(c) < n$ . Then we have  $[\![\varrho]\!] \hookrightarrow \kappa = \langle (q, increase(digi(\nu), c)), \bullet w \rangle =$  $[\![\varrho']\!]$ , where  $\varrho \xrightarrow{c:=c+1} \varrho'.$
	- (c) If it takes the third increment transition rule, then we can infer that  $\nu(c) \geq n$ . Then we have  $[\![\varrho]\!] \hookrightarrow \kappa = \langle (q, increase(digi(\nu), c)), \bullet \bullet \omega \rangle = [\![\varrho']\!]$ , where  $\varrho \xrightarrow{c:=c+1} \varrho'.$
- 6. **Decrement.** We proceed with four cases:
	- (a) If it takes the first decrement transition rule, then we can infer that  $\nu(c) > n + 1$ . Then we have  $[\![\varrho]\!] = \langle (q, digi(\nu)), \bullet \bullet \bullet w' \rangle \hookrightarrow \kappa =$  $\langle (q, digi(\nu)), \bullet w' \rangle = \llbracket \varrho' \rrbracket$ , where  $\varrho \xrightarrow{c:=c-1} \varrho'$ . Note that  $digi(\nu) =$  $diagi(\nu[\nu(c) \leftarrow \nu(c) - 1]),$  since  $\nu(c) > n + 1.$
	- (b) If it takes the second decrement transition rule, then we can infer that  $\nu(c) = n + 1$ . Then we have  $[\![\varrho]\!] = \langle (q, digi(\nu)), \bullet \bullet \bot \rangle \leftrightarrow \kappa =$  $\langle (q, decrease(digi(\nu), c, 1)), \perp \rangle = [q']$ , where  $q \xrightarrow{c:=c-1} q' = (q, \nu')$  and  $\nu'(c) = n.$
	- (c) If it takes the third decrement transition rule, then we can infer that  $n \langle \nu(c) \langle n+1. \text{ Then we have } [\![\varrho]\!] = \langle (q, digi(\nu)), \bullet \bot \rangle \hookrightarrow \kappa =$  $\langle (q, decrease(digi(\nu), c, 2)), \perp \rangle = [q']$ , where  $q \xrightarrow{c:=c-1} q' = (q, \nu')$  and  $n-1 < \nu'(c) < n.$
	- (d) If it takes the fourth decrement transition rule, then we can infer that  $1 \leq \nu(c) \leq n$ . Then we have  $[\![\varrho]\!] = \langle (q, digi(\nu)), \bot \rangle \leftrightarrow \kappa =$  $\langle (q, decrease (digi(\nu), c, 2)), \perp \rangle = [\![\varrho' ]\!]$ , where  $\varrho \xrightarrow{c:=c-1} \varrho' = (q, \nu').$

**Induction step**: Assume  $[\![\varrho]\!] \hookrightarrow^* \kappa' \hookrightarrow \kappa$ . We proceed with two cases:

- 1.  $\kappa'$  is an *encoded configuration*, and by induction hypothesis  $[\![\varrho]\!] \hookrightarrow^* \kappa' = [\![\varrho']\!]$ . Then the proof is similar to the base step.
- 2.  $\kappa'$  is not an *encoded configuration*. Note that for our encoding, we have encoded configurations except for the second, third and fifth assignment rule. Here, we give a proof for the fifth assignment rule. The other cases are similar. Assume  $\kappa' = \langle (p', \bar{Y}), w \rangle$  is obtained by applying the fifth assignment rule, we have  $\kappa' \hookrightarrow \kappa = \langle (q, digi(\nu')), w \rangle = ||\varrho'|| = ||(q, \nu')||$  by applying the last assignment rule, where  $\exists \nu \in [\overline{Y}]$  such that  $\nu' = \nu[c \leftarrow d]$  for  $d \in \mathbf{r}_{2n+1}$ . Finally, put together  $\varrho \to^* \varrho'$ .

# **References**

- <span id="page-13-0"></span>1. Alur, R., Dill, D.L.: A theory of timed automata. Theor. Comput. Sci. **126**, 183– 235 (1994)
- <span id="page-13-1"></span>2. Bouyer, P., Dufourd, C., Fleury, E., Petit, A.: Are timed automata updatable? In: Emerson, E.A., Sistla, A.P. (eds.) CAV 2000. LNCS, vol. 1855. Springer, Heidelberg (2000)
- <span id="page-13-2"></span>3. Bouyer, P., Dufourd, C., Fleury, E., Petit, A.: Expressiveness of updatable timed ´ automata. In: Nielsen, M., Rovan, B. (eds.) MFCS 2000. LNCS, vol. 1893, pp. 232–242. Springer, Heidelberg (2000)
- <span id="page-13-3"></span>4. Bouyer, P., Dufourd, C., Fleury, E., Petit, A.: Updatable timed automata. Theor. Comput. Sci. **321**, 291–345 (2004)
- <span id="page-13-4"></span>5. Schwoon, S.: Model-checking pushdown system. Ph.D. thesis, Technical University of Munich (2000)
- <span id="page-13-5"></span>6. Ouaknine, J., Worrell, J.: On the language inclusion problem for timed automata: closing a decidability gap. In: Proceedings of the 19th IEEE Symposium on Logic in Computer Science (LICS'04), IEEE Computer Society, pp. 54–63 (2004)
- 7. Abdulla, P.A., Jonsson, B.: Verifying Networks of Timed Processes (Extended Abstract). In: Steffen, B. (ed.) TACAS 1998. LNCS, vol. 1384, p. 298. Springer, Heidelberg (1998)
- 8. Abdulla, P., Jonsson, B.: Model checking of systems with many identical time processes. Theor. Comput. Sci. **290**, 241–264 (2003)
- <span id="page-13-10"></span>9. Li, G., Cai, X., Ogawa, M., Yuen, S.: Nested timed automata. In: Braberman, V., Fribourg, L. (eds.) FORMATS 2013. LNCS, vol. 8053, pp. 168–182. Springer, Heidelberg (2013)
- <span id="page-13-6"></span>10. Li, G., Ogawa, M., Yuen, S.: Nested timed automata with frozen clocks. In: Sankaranarayanan, S., Vicario, E. (eds.) FORMATS 2015. LNCS, vol. 9268, pp. 189–205. Springer, Heidelberg (2015)
- <span id="page-13-7"></span>11. Choffrut, C., Goldwurm, M.: Timed automata with periodic clock constraints. J. Automata, Lang. Comb. **5**, 371–404 (2000)
- <span id="page-13-8"></span>12. Demichelis, F., Zielonka, W.: Controlled timed automata. In: Sangiorgi, D., de Simone, R. (eds.) CONCUR 1998. LNCS, vol. 1466, pp. 455–469. Springer, Heidelberg (1998)
- <span id="page-13-9"></span>13. Trivedi, A., Wojtczak, D.: Recursive timed automata. In: Bouajjani, A., Chin, W.- N. (eds.) ATVA 2010. LNCS, vol. 6252, pp. 306–324. Springer, Heidelberg (2010)
- <span id="page-14-0"></span>14. Wen, Y., Li, G., Yuen, S.: An over-approximation forward analysis for nested timed automata. In: Liu, S., Duan, Z. (eds.) SOFL+MSVL 2014. LNCS, vol. 8979, pp. 62–80. Springer, Heidelberg (2015)
- <span id="page-14-1"></span>15. Minsky, M.: Computation: Finite and Infinite Machines. Prentice-Hall, Upper Saddle River (1967)
- <span id="page-14-2"></span>16. Bouyer, P.: Forward analysis of updatable timed automata. Formal Methods in System Design **24**, 281–320 (2004)
- <span id="page-14-3"></span>17. Fang, B., Li, G., Fang, L., Xiang, J.: A refined algorithm for reachability analysis of updatable timed automata. In: Proceedings of the 1st IEEE International Workshop on Software Engineering and Knowledge Management (SEKM 2015 @ QRS 2015), IEEE Computer Society, pp. 230–236 (2015)