When a Formal Model Rhymes with a Graphical Notation

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Abstract. Formal methods are based on mathematical notations which allow to rigorously reason about a model and ensure its correctness by proofs and/or model-checking. Unfortunately, these notations are complex and often difficult to understand from a human point of view especially for engineers who are not familiar with formal methods. Several research works have proposed tools to support formal models using graphical views. On the one hand, such views are useful to make formal documents accessible to humans, and on the other hand they ease the verification of some behavioral properties. However, links between graphical and formal models proposed by these approaches are often difficult to put into practice and depend on the targeted formal language. In this paper, we discuss these links from a practical approach and show how a behavioral description can be computed from a formal model based on two complementary paradigms: under-approximation (or animationbased) and over-approximation (or proof-based). We applied these paradigms in order to produce behavioural state/chart views from B models and we carried out an empirical study to assess the quality and relevance of these graphical representations for humans.

Keywords: B method · Symbolic LTS · Animation · Abstraction

1 Introduction

Several research works are devoted to bridge the gap between formal and semiformal methods considering their complementary aspects and cross contributions. Indeed, on the one hand, semi-formal methods (thanks to their support for graphical notations such as UML) are synthetic, structuring and more intuitive for humans, and on the other hand, formal methods (thanks to their mathematical notations) are precise and support automated reasonings. These works were widely interested by translations from a semi-formal UML model to a formal specification: from UML to B $[15]$ $[15]$, from UML to Z $[10]$, from UML to Alloy $[3]$, etc. Their main motivations are to provide precise semantics to UML notations in order to remedy the lack of tools for formally analyzing UML models.

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Despite of these numerous tools dedicated to such translation, several companies have an established software development process entirely based on a formal method. For example, Siemens Transport [\[7](#page-13-1)], Clearsy [\[13](#page-14-2)], Gemplus [\[5\]](#page-13-2) have used the B method as its core development method without any accompanying UML model. Indeed, since the targeted formal language is not object oriented, translations from UML often lead to a complex specification which is, on the one hand, far from what a developer could write directly, and on the other hand incomplete for a safety critical system. This motivates other kind of works to define a formal link between a formal model and its behavioral representation. For example, works of C. Snook and M. Butler [\[15](#page-14-0)] ant its support tool iUML-B [\[14](#page-14-3)], provide a graphical front end, used conjointly with a formal B specification in order to keep the distance between both formal and graphical models as thin as possible. We can also cite the ProB tool [\[11](#page-14-4)] which is an animator and a model-checker able to draw an accessibility graph after an exhaustive exploration of the specification state space. However, it considers concrete states rather than symbolic ones and the resulting graphical representation is complex because of the combinatorial explosion problem. In order to remedy this shortcoming, [\[12\]](#page-14-5) provides some heuristics to reduce the accessibility graph size by using a symmetry analysis technique. Furthermore, it was not dedicated to make the focus on the understanding of some particular properties, since the abstract state space could not be provided by an expert user.

In this paper, the starting point is a B or Event-B formal model [\[1](#page-13-3)[,2](#page-13-4)]. Our aim is to provide tools able to extract graphical views representing some properties of the formal model and hence increase its understanding by humans who are not trained with such a formal notation. We discuss and compare two paradigms: **under-approximation** (or animation-based) and **over-approximation** (or proof-based). We applied these paradigms in order to produce behavioural views from B models and then we carried out an empirical study to assess the quality and relevance of these graphical representations for humans.

In Sect. [2](#page-1-0) we give a simple example in order to illustrate contributions of this paper. Section [3](#page-3-0) discusses the under-approximation approach and presents an algorithm which improves automation of this technique. Section [4](#page-6-0) describes the over approximation technique and presents the *GénéSyst*-tool. Results of our empirical study are discussed in Sect. [5.](#page-9-0) Finally, Sect. [6](#page-12-0) summarizes our comparative study of both techniques and draws the conclusions and perspectives of this work.

2 Case-Study

Figure [1](#page-2-0) gives a simple scheduler specification taken from [\[6](#page-13-5)] and written in B. It models exclusive access of processes to a unique resource. Variables *waiting*, *ready* and *active* model states of processes managed by the system. The set of all processes is an abstract set (set *PID*). An idle process which doesn't request access to the unique resource is introduced by the system using the *waiting* variable. Variable *ready* is the set of processes that have requested access to the resource. Finally, variable *active* contains the active process to which the resource is assigned. Evolutions of these variables are performed by three events. Event $NEW(pp)$ creates a new waiting process. Event $READY(pp)$ changes process pp from the waiting state to the ready state. If there is no active process it directly activates process pp. Finally, event SWAP puts the active process in the waiting state and activates non-deterministically some ready process.

```
SYSTEM SCHEDULER
SETS
  PID
VARIABLES
  active, ready, waiting
INVARIANT
  active \subseteq PID \land ready \subseteq PID \land waiting \subseteq PID \landready \cap waiting =\emptyset \wedge active \cap waiting =\emptyset \wedge active \cap ready =\emptyset \wedgecard(active) \leq 1 \wedge (active = \emptyset \Rightarrow ready = \emptyset)INITIALISATION
  active, ready, waiting := \emptyset, \emptyset, \emptysetEVENTS
 NEW(pp) \triangleqSELECT (pp \in PID) \wedge (pp \notin (ready \cup waiting \cup active))
                      THEN
                                  waiting := waiting \cup {pp}
                       END;
 READV(pp) \triangleq SELECT pp \in waiting
                                  waiting := (wating - {pp})THEN
                                  IF (active = \emptyset) THEN active := {pp}
                                  ELSE ready := ready \cup {pp}
                                  END
                       END:
 SWAP \triangleqSELECT active \neq \emptysetTHEN
                                  waiting := waiting \cup active ||IF (ready = \emptyset) THEN active := \emptysetELSE
                                     ANY pp WHERE pp \in ready THEN
                                             active := \{pp\} \parallel ready := ready - \{pp\}END
                                  END
                       END
END
```
Fig. 1. Scheduler Specification from [\[6\]](#page-13-5)

A palette of graphical representations that can be issued from the scheduler example can be found in [\[8](#page-13-6)]. These representations provide a graphical documentation of the behaviour of B specifications and allow to identify different viewpoints potentially useful for humans. IFor example, a B analyst may be interested by a graphical representation of the SCHEDULER that intuitively show a process life cycle. Hence, the abstract graphical view may deal with

three states corresponding to the fact that a process p_i (such that $p_i \in PID$) is in state *waiting*, *ready* or *active*. In other words, states of the abstract view are: (1) $p_i \in waiting$, (2) $p_i \in ready$, and (3) $p_i \in active$. Figure [2,](#page-3-1) built manually, gives an abstract view of the SCHEDULER system based on these three states.

Fig. 2. Example of an intuitive abstract view of the SCHEDULER system

From a documentation point of view the interest of this representation is to emphasize graphically some intrinsic properties of the SCHEDULER system, for example:

- The process equity property, indicating that every process may be activated, is not verified by the specification. Indeed, Fig. [2](#page-3-1) shows that in state $p_i \in ready$, SWAP has a non-deterministic behaviour justified by the existence of two transitions with the same label. This means that event *SWAP* can block a process p_i indefinitely in state $p_i \in ready$.
- The non-blocking property, indicating that after being active a process does not stop the system is verified by the specification. Indeed, in Fig. [2](#page-3-1) the transition *SWAP* is triggered on from the state ($p_i \in active$) and always leads to state $(p_i \in waiting)$.

This paper shows how these graphical representations can be extracted automatically using two kinds of techniques: **under-approximation** (or animationbased) and **over-approximation** (or proof-based).

3 Under-Approximation Approach

Under-approximation is based on exploration of a useful subset of the state space. We apply this technique in order to draw a graphical representation which is useful from a documentation point of view but which may miss some behaviours.

3.1 Construction Method and Usability Constraints

One way to build an under-approximating graphical abstraction is to exhaustively explore the concrete state space of the B specification and then to apply an abstraction algorithm to group concrete states. For a bounded state space,

animators such as ProB [\[11\]](#page-14-4) can help to explore all states. In other cases, such as for the SCHEDULER example, we must start by bounding unbounded elements (i.e. specifying *PID* with a bounded set). If we introduce only two processes p_1 and p_2 in the system, we obtain ten accessible states (Fig. [3\)](#page-4-0). If the number of processes increases, the accessibility graph becomes too large and difficult to understand. For example, having $PID = \{p_1, p_2, p_3\}$ we obtained thirty five accessible states with numerous transitions.

3.2 Graph Abstraction Algorithm

We note $\mathcal{G} = (N, T)$ an accessibility graph issued from a B system, where N is the set of concrete states of graph G , and T is the set of transitions between states of *N*. A concrete state S_v ($S_v \in N$) gives particular values assigned to state variables v ($v = \{v_1, \ldots, v_n\}$) of the B system. Consequently, each state *s*

Fig. 3. Accessibility Graph of the SCHEDULER for $PID = \{p_1, p_2\}$

can be formally expressed by a predicate $P(S_v)$ as the conjunction of equality predicates that associate to each state variable v_i its value in S_i :

$$
P(S_v) \triangleq \bigwedge_{i=1}^n (v_i = val_j)
$$

Where val_j is a value of v_i allowed by the invariant. A concrete state S_v satisfies an abstract state $S_{abstract}$ (noticed $S_v \vdash S_{abstract}$), defined by a predicate R (*e.g.* $p_1 \in ready$), if and only if we can prove that $P(S_v) \Rightarrow R$.

Hence, according to an accessibility graph and a set of abstract states, the following algorithm can produce a symbolic representation by grouping concrete states satisfying a same abstract state predicate. The inputs are: (*i*) an accessibility graph $G = (N, T)$ and *(ii)* a set of abstract state predicates $N_{abstract}$ (Fig. [4\)](#page-5-0).

```
abstract state S_{abstract} from N_{abstract} do
For
       each
       N := N \cup \{S_{abstract}\}For each concrete state S_v such that
                     S_v \in N - N_{abstract} \land S_v \vdash S_{abstract}do
              For each state S and transition t such that
                     S \in N \land t \in T \land t = S \stackrel{o}{\longrightarrow} S_vdo
                     t := (S \stackrel{o}{\longrightarrow} S_{abstract})\mathbf{L}End do
              For each state S and transition t such that
                     S \in N \land t \in T \land t = S_v \xrightarrow{o} Sdo
                     t:=(S_{abstract} \xrightarrow{o} S)End do
              N := N - \{S_v\}End do
       Delete redundant transitions
End do
```
Fig. 4. Under approximation algorithm.

The algorithm checks each concrete state against each abstract state predicate, using the *AtelierB* prover. If the proof succeeds, then an abstract state has been found for the concrete state. The next step in the construction of the abstract state-transition diagram is to identify the transitions. Since each node of the concrete graph corresponds to a node of the abstract diagram, each transition of the concrete graph can be translated into a transition in the abstract diagram. In order to decrease the number of transitions, the tool groups all transitions which correspond to the same pair of nodes, and to the same B event.

Our algorithm links concrete states to abstract ones, and hence the nodes of the abstract state-transition diagram are: (*a*) the abstract state predicates given by the user, and (*b*) the concrete nodes which don't appear in the domain of the abstraction function. This guarantees that each concrete node will correspond to a node of the abstract diagram. Furthermore, in order to obtain a relevent abstract view, two conditions should be verified:

- 1. abstract state predicates are disjoint, *i.e.* each concrete state corresponds to at most one abstract state.
- 2. abstract state predicates cover all the state space allowed by the invariant, *i.e.* the nodes of the abstract diagram only correspond to the abstract predicates.

The abstract view of Fig. [2](#page-3-1) respects only the first condition because it misses all concrete states reached from the initialization. These states can be grouped in an abstract state $p_i \notin (waiting \cup active \cup ready)$ which is reached when the system is initialized. The left hand side of Fig. [6](#page-9-1) shows the result of this technique when applied to accessibility graph of Fig. [3](#page-4-0) in which set PID contains two processes p_1 and p_2 .

4 Over-Approximation Approach

The under-approximation technique is useful when the accessibility graph explores a relevant finite subset of state space from which we can exhibit a useful abstract view for a documentation purpose. If some interesting behaviours are not included in the concrete graph, they will not appear in the abstract diagram. An over-approximation technique is then more interesting because it allows to produce a symbolic transition system that represent a potentially infinite set of values. Such tools reason on event *enabledness* and state *reachability* properties.

4.1 Construction Method and Usability Constraints

Our objective is to directly compute an abstract view from the B model properties, rather than to reason on a concrete graph. For instance, if an overapproximation view shows that a state is not reached by any transition, then one can conclude that associated concrete valuations could not be reachable by any execution of the B model.

Our approach tries first to prove, for each event *e* and each couple of abstract states S_1 and S_2 , that no execution of event *e* from state S_1 can reach state S_2 . This goal is a proof obligation (PO) assuming that if state predicate $P(S_1)$ is true then event *e* establishes the negation of state predicate $P(S_2)$:

$$
P(S_1) \Rightarrow [e] \neg P(S_2)
$$

This first step allows to identify by proofs, all uncrossable transitions between states S_1 and S_2 . In fact, if the above PO is solved, then we assert that event e never reaches S_2 from S_1 . Variations of this PO allow to compute whether S_2 is always or possibly reached by *e* from *S*1:

- − S_2 always reached from S_1 : $P(S_1)$ ⇒ $[e]P(S_2)$
- $-S_2$ possibly reached from *S*₁: *P*(*S*₁) ⇒ ¬[*e*]¬*P*(*S*₂).

For example, the following proofs (but not only) should succeed for event $SWAP¹$ $SWAP¹$ $SWAP¹$:

- it always deactivate an active process: (*pⁱ* ∈ *active*) ⇒ [SWAP](*pⁱ* ∈ *waiting*)
- it never activate a waiting process: (*pⁱ* ∈ *waiting*) ⇒ [SWAP](*pⁱ* ∈ *active*)
- it may activate a ready process: (*pⁱ* ∈ *ready*) ⇒ ¬[SWAP](*pⁱ* ∈ *active*)

As for the under-approximation approach, two conditions must be verified: abstract state predicates are disjoint and cover all the state space allowed by the invariant. The first condition avoids states overlapping and the second one allows to have a global view on the complete system. An important proof obligation is then to establish the completeness of the state predicates according to the invariant:

$$
I \implies \bigvee_{i=1}^{n} P(S - v)
$$

4.2 The *G´en´eSyst* **Tool**

The $G\acute{e}n\acute{e}Syst$ tool^{[2](#page-7-1)} [\[4\]](#page-13-7) implements the ideas of this approach. It computes a Symbolic Labelled Transition System (SLTS) describing all possible behaviours of a given event-B model, according to a given set of disjoint state predicates. Generated proof obligations are discharged by means of the *AtelierB* automatic prover.

The overall *G´en´eSyst* algorithm is presented in Fig. [5,](#page-8-0) where we distinguish transitions from the initialization, and transitions associated to other events. In this algorithm, conditions are written under a negative form (*i.e.* if $\neg A$ can not be established), since a formula that has not been proved is not necessarily true. In this algorithm, no any information is presented to consider simplification of the conditions. The reader can refer to $\left[4\right]$ $\left[4\right]$ $\left[4\right]$ for further semantical details.

In order to restrict the undecidability problem of proofs, heuristics are used to compute the over-approximation graph (the SLTS). One of them is to split proofs into two parts: *enabledness* and *reachability*. In this approach, for each pair of abstract states S_1 and S_2 , and each event e , a transition t of the SLTS is defined by $(S_1 \stackrel{(D,A,e)}{\longrightarrow} S_2)$, where *D* is the *enabledness* condition (condition under which the event e can be triggered from S_1) and A is the *reachability* condition (condition under which the event e can reach the state S_2). We define *enabledness* and *reachability* as follows:

- *Enabledness* condition D : *P*(*S*1) ⇒ (*D* ⇔ *guard*(*e*))
- *Reachability* condition A : *P*(*S*1) ∧ *D* ⇒ (*A* ⇔ ¬[*action*(*e*)]¬*P*(*S*2)).

¹ These properties are not all properties of event SWAP.

² *GénéSyst*: [http://perso.citi.insa-lyon.fr/nstouls/?ZoomSur=Logiciels.](http://perso.citi.insa-lyon.fr/nstouls/?ZoomSur=Logiciels)

```
// Initialization of the result: set of transitions is empty
T := \emptyset// Defining states reachable by the initialization
      each state S, except S_{init} do
For// Define the condition A under which the initialization reaches SA := \neg [init] \neg P(S)If \neg A can not be established then
             T := T \cup \{ (S_{init} \stackrel{(true, A, init)}{\longrightarrow} S) \}End if
End do
// Defining existing transitions
Foreach state S_1, except S_{init}, and each event e do
      //Define the condition D under which e can be trigger from S_1D := guard(e)If (P(S_1) \Rightarrow \neg D) can not be established then
                   each state S_2, except S_{init}, do
             For
                   // Define the condition A under which event e reaches S_2A := \neg [action(e)] \neg P(S_2)If (P(S_1) \wedge D \Rightarrow \neg A) can not be established then
                          T := T \cup \{(S_1 \stackrel{(D,A,e)}{\longrightarrow} S_2)\}\End if
             End do
      End if
End do
```
Fig. 5. *GénéSyst* main algorithm

In the context of the starting state, the *enabledness* condition *D* is equivalent to the B event guard (denoted $guard(e)$). Technically, the tool asks *AtelierB* to prove if *D* can be reduced to *true*, by trying to proof the assertion $I \wedge P(S_1) \Rightarrow guard(e)$, where *I* is the invariant. If this proof succeeds, it concludes that *e* can always be enabled from *S*1; otherwise it asks *AtelierB* to prove if *D* can be reduced to *false*, by trying to proof $I \wedge P(S_1) \Rightarrow \neg guard(e)$. If this second proof succeeds then the tool concludes that *e* can't be enabled from *S*1. The same principle is applied for the *reachability* property. Condition *A* is equivalent to $\neg [action(e)] \neg P(S_2)$ which means that if *A* is reduced to true, then *e* may reach *S*2. But if it is reduced to *f alse* then *e* cannot reach *S*2. By the way, a transition $(S_1 \xrightarrow{(D,A,e)} S_2)$ is said valid if and only if $\exists x \cdot (P(S_1) \land D \land A)$.

Right hand side of Fig. [6](#page-9-1) is produced by this technique without any bounding of set PID. It describes all possible behaviours around states focused on a process life-cycle. The two crossing conditions between brackets represent respectively conditions *D* and *A*, for *enabledness* and *reachability*. Empty brackets mean that the condition is proved true. A cross X on a condition means that this condition has not been proved neither true, nor false.

Fig. 6. Results of Under and Over-Approximation Techniques

Compared to the under-approximation diagram produced for two processes, some transitions exhibited by $G\acute{e}n\acute{e}Syst$ don't correspond to any transition of the concrete representation (*New*, *ready* and *swap* transitions, reflexive on state $P_i \in Ready$). Indeed, limited to two processes for this example the underapproximation technique didn't explore a sufficient number of states.

5 Human Oriented Empirical Study

Techniques discussed in the previous sections allow to produce behavioural views from a formal B specification depending on the abstraction chosen by the analyst. We have conducted experimentally a qualitative study with students from a master's degree specialized on software engineering, and who have finished a detailed course about the B method. We formed two groups of 17 students to which we provided two different specifications: the scheduler example discussed in this paper, and a B specification modelling access control mechanisms to buildings. We applied our tools to these specifications and produced various

Fig. 7. Diagrams reduce significantly error rate for some questions

diagrams in order to graphically document them. Every group had two lists of questions about two different specifications where only one was supported by diagrams. Our intension was to evaluate the error rate variations of answers to quiet simple questions about these specifications when diagrams are provided. This study allowed to observe an error rate decrease from 26*.*14 % to 15*.*60 % when specifications are documented graphically. A variation about 11% is interesting, because it hightlights the contribution of diagrams to the understanding of B specifications, but it may not seem very promising. We believe that the inverse would be surprising because specifications provided to students are not complex and should be accessible for them. Indeed, a global error rate near 26 % may be acceptable for persons who are not skilled with formal techniques, but 15% is better. More specifically, we observed that diagrams reduce significantly the number of wrong answers for several questions. Figure [7](#page-10-0) gives wrong answers proportions with and without diagrams and shows that the error rate can be divided by three and sometimes it is reduced from around 50 % to zero.

Questions G5-Q23 will be detailed further. 30% of students to whom we didn't provide diagrams, misunderstood the equity property and considered that a process can't be bloqued indefinitely in the ready state (question Q11

Fig. 8. State/Transition diagram focused on active processes

Fig. 9. Did the diagrams help you to understand specifications?

Fig. 10. Self-rated familiarity with B and UML for unfavourable students

in Fig. [7\)](#page-10-0). However, when the diagram of Fig. [2](#page-3-1) is provided, only 10% gave the wrong answer. We believe that such a property is somehow difficult to perceive from a human point of view. Indeed, in order to be verifyied, the equity property needs more automated tool analysis or other formal languages, such as LTL, because it is a kind of behavioural properties not explicit in the B model. Nevertheless, invariant properties can be illustrated graphically using state transition diagrams. For example, Fig. [8,](#page-10-1) produced by our tools, shows that state $card(active) > 1$ is not reached by any transition and hence it is conformant to invariant $card(active) < 1$.

Without this diagram, about 40% of students said that there may be several active processes at the same time (question Q5 in Fig. [7\)](#page-10-0). Although invariant $card(active) \leq 1$ is clearly mentioned in the scheduler specification, students were not able to attest that the scheduler operations preserve such a trivial property. This result emphasizes the interest to document graphically an invariant property for a better human understanding. Indeed, we obtained 100 % of good answers when Fig. [8](#page-10-1) is provided.

An overall appreciation of the graphical views is given in Fig. [9](#page-11-0) and shows that two out of three students think that diagrams helped them to understand specifications and the remaining one third expresses an unfavourable opinion.

Fig. 11. Self-rated familiarity with B and UML for favourable students

In these two proportions, 13.33% of students say that diagrams didn't help them at all and 13*.*33 % of them have the opposite opinion. In order to refine these results we asked students to evaluate their knowledge of B and UML notations (Figs. [10](#page-11-1) and [11\)](#page-12-1). A great part of students who disagree with the interest of diagrams seem to be uncomfortable with UML notations and has a better familiarity with the formal B notation. Basing on this self-rated familiarity with B and UML, one may conclude that although graphical views seem to be a way for making a formal specification more accessible, they can have the inverse effect because they also require some knowledge. This observation is confirmed by the proportion of students who appreciated diagrams and who has obviously a better mastering of graphical UML notations.

6 Conclusion

It is commonly known that formal specifications are complex because of notations that need a great mathematical background. In this paper, we focused our interest on a B specification which is based on a verbose notation, close to a programming language, and which should be more affordable than other formal notations. Our empirical study showed that the language itself is not the main reason to be less at ease with a formal method. Obviously, the difficulty for humans is to have an overall view on the formal model.

This paper has presented two complementary approaches providing a behavioural abstract view from a formal specification, in order to ease its understanding. Figure [6](#page-9-1) shows an example of results issued from under and over-approximation techniques. We can observe from these diagrams that the *G´en´eSyst* tool associates guards to events in order to describe their *enabledness* and *reachability* properties. However, reflexive transitions SWAP, READY and NEW in state $p_i \in ready$ are not possible when the scheduler system deals with only two processes. For this particular set of processes the graph abstraction tool produced a more precise diagram. *G´en´eSyst* being based on proof techniques, it suffers the usual limitations of automatic provers: some theorems cannot be proved automatically and require user interaction. Furthermore, if the underapproximation approach can be used to verify reachability properties, then the

over-approximation approach is mainly interesting in case of safety properties. Both techniques have some restrictions such as a limited state space for the first one and a too large abstraction in case of leak of proof for the second one.

We also measured the computational time of each approach and we noticed that the graph abstraction tool produced the state/transition diagram of a process life cycle in 7s for $PID = \{p_1, p_2\}$ and 13s for $PID = \{p_1, p_2, p_3\}$; while the $G\acute{e}n\acute{e}Syst$ tool produced this diagram in 80 s for an unbounded state space. This confirms that under-approximation tools are interesting when the state space can be reduced to a small finite space. Furthermore, if some interesting behaviours are not included in the concrete graph, they will not appear in the abstract diagram. Given sets, such as *PID*, can be turned into enumerated sets but numerical data structures such as *NAT* are less easy to address. Overapproximation tools are much more interesting for such complex data structures because they may be used to provide more formal evidence on the diagram transitions.

Over-approximation, can be dedicated to verify safety properties as proposed in [\[4](#page-13-7)] and [\[16\]](#page-14-6). It has the advantage to preserve infinite concrete state space without any constraint, and hence safety properties could be established on the symbolic transition system. The resulting LTS could also be used like a test oracle which brings some interesting perspectives [\[9\]](#page-13-8).

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