

Speeding Up Non-archimedean Numerical Computations Using AVX-512 SIMD Instructions

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Abstract. This work presents the acceleration of a Bounded Algorithmic Number (BAN) library exploiting vector instructions in generalpurpose processors. With the use of this encoding, it is possible to represent non-Archimedean numbers that are not only finite (like real numbers) but also infinite or infinitesimal. The tremendous growth in non-Archimedean numerical computations over the past 20 years and the resulting applications spurred this study's development. Enabling acceleration of BANs processing can significantly increase the throughput of non-Archimedean numerical computations, enlarging the spectrum of possible applications to industrial and real-time ones.

Keywords: Non-archimedean fields · Alpha theory · Bounded Algorithmic Number (BAN) · Single Instruction Multiple Data $(SIMD) \cdot AVX-512$ instruction set

1 Introduction

Numerical non-Archimedean computations have been pioneered by Sergeyev and his Grossone Methodology [\[1](#page-5-0)], and allow for the use of infinitely large and infinitely small numbers in machines, other than finite ones as usual. From their advent, numerous applications benefited, especially in the domain of multiobjective optimisation, e.g., linear programming [\[2,](#page-5-1)[3](#page-5-2)], quadratic programming [\[4](#page-5-3)], evolutionary algorithms [\[5](#page-5-4)], game theory $[6]$ $[6]$, artificial intelligence $[7,8]$ $[7,8]$ $[7,8]$, etc.

The reference framework of this study is the non-Archimedean model built upon the Alpha Theory [\[9\]](#page-5-8), which introduces the set of Euclidean numbers and their associated numerical encoding called Bounded Algorithmic Number (BAN) format. The BAN encoding is a fixed length representation, guaranteeing that any operation involving two BANs outputs a result that occupies the same memory as the operands, exactly as happens with computations between 32-bit IEEE 754 floats, where the result of addition, subtraction, addition, and division is again a 32-bit float. However, since the Euclidean numbers form a superset of the real ones, their numerical representation is heavier than the one of floats, making the processing of BANs cumbersome. CPU vectorization can be exploited to optimise computations since BANs encoding can be implemented through multiple fixed-length coefficients, thus fitting them inside vector registers and efficiently computing operations in a single clock cycle.

Using vector instructions had already proved to be a good solution for handling non-native data types that do not have hardware acceleration [\[10](#page-5-9)[,11](#page-5-10)]. In this paper, we present an optimisation of a C++ BAN library called *BANcpp* [\[12](#page-5-11)]. The goal is to optimise the library to leverage CPU vectorization when dealing with BAN coefficients. In particular, we focused on BAN numbers with eight 64-bit coefficients and 512-bit vector instruction sets (e.g., Intel AVX-512). We also present a benchmark application that consists of several iterations of a non-Archimedean optimisation problem. We evaluate the goodness of automatic vectorization as a baseline model and then we enhance the automatic vectorization whenever the compiler fails to automatically optimise the code.

The paper is organised as follows: (i) Sect. [2](#page-1-0) briefly introduces Alpha Theory and the Euclidean numbers; (ii) Sect. [3](#page-2-0) details the BAN format; (iii) Sect. [4](#page-2-1) explains the choices made to implement the enhanced vectorization of the library; (iv) Sect. [5](#page-3-0) shows the application benchmark and the results obtained in terms of timing performance and throughput.

2 Alpha Theory and The Euclidean Numbers

Alpha Theory reference set of non-Archimedean numbers is indicated with the symbol E and it is called the set of α -Euclidean numbers, or, in brief, just Euclidean numbers. The peculiar name of the theory comes from the definition of a reference infinite value within E and it is indicated by the symbol α . Then, any Euclidean number can be represented as a function of α , and only functions of α are numbers in E, which guarantees that the Euclidean numbers and the mathematical operations among them behave according to their counterparts in R, i.e., commutative operations continue to be commutative, differentiable functions are still differentiable, etc. For instance, the following are all Euclidean numbers:

$$
\alpha^3, \qquad \frac{1}{\alpha}, \qquad \frac{1}{\alpha^2} - e^{\alpha}, \qquad -\frac{1}{2^{\alpha}}, \qquad -\ln\left(\frac{1}{\alpha}\right). \tag{1}
$$

As opposed to Archimedean Mathematics the concepts of infinite and infinitesimal numbers are sharply defined rather than vague concepts.

Definition 1. Given $\xi \in \mathbb{E}$, then

 $- \xi$ is infinite $\Longleftrightarrow \forall n \in \mathbb{N}, |\xi| > n$ ξ is finite $\Longleftrightarrow \exists n \in \mathbb{N}, \frac{1}{n} < |\xi| < n$ $- \xi$ is infinitesimal $\Longleftrightarrow \forall n \in \mathbb{N}, |\xi| < \frac{1}{n}$.

Therefore, in [\(1\)](#page-1-1) the first and fifth numbers are positive and infinite, the third is negative and infinite, the second is positive and infinitesimal, while the fourth is negative and infinitesimal. A more detailed presentation of Alpha Theory and the set $\mathbb E$ can be found in [\[13](#page-5-12)].

3 The BAN Format

The BAN encoding consists in a finite length representation for Euclidean numbers; however, as for IEEE 754 floating point numbers, it cannot represent the whole set E because it would require infinitely many binary possible representations, i.e., an infinite computer memory. Any Euclidean number compliant with the following representation can be represented as a BAN:

$$
\xi = \sum_{i=1}^{L} r_i \alpha^{p-i},
$$

where $L \in \mathbb{N}$ is the encoding length, $r_i \in \mathbb{R}$ and $p \in \mathbb{Z}$. Changing perspective, one can define a BAN as a Euclidean number that can be represented by a linear combination of L subsequent integer powers of α .

From the very first glimpse, one may notice that the BAN representation of a Euclidean number is very similar to the one of a polynomial, suggesting how cumbersome can be to execute algebraic operations between BANs. An example of addition and multiplication between BANs with $L = 3$ follows:

$$
(3.2\alpha^2 - 0.5\alpha + 1.4) + (0.2\alpha + 1 - 1.5\alpha^{-1}) = 3.2\alpha^2 - 0.3\alpha + 2.4 - 1.5\alpha^{-1}
$$

$$
(3.2\alpha^2 - 0.5\alpha + 1.4) \times (0.2\alpha + 1 - 1.5\alpha^{-1}) = 0.64\alpha^3 + 3.1\alpha^2 - 5.02\alpha - 2.15 - 2.1\alpha^{-1}
$$

Both computations output a result that is not a BAN since it requires more than three consecutive powers of α to be represented. Therefore, there is the need to approximate them considering only the first three highest powers of α , the most significant ones somehow, that is executing a truncation of the result. Below we report the numerical execution of the previous two operations, along with the division, realized by a software simulator of a BAN Processing Unit (BPU, whose hardware design has been recently proposed in [\[12\]](#page-5-11)). The latter manipulates and outputs BANs in the normal form [\[13](#page-5-12)], i.e., in the standardized format which guarantees the uniqueness of the representation.

Operands: $\alpha^2(3.2 - 0.5η^1 + 1.4η^2)$ and $\alpha^2(0.2 + 1η^1 - 1.5η^2)$

Sum: $α^2(3.2 - 0.3η^1 - 2.4η^2)$ Product: α ³(0.64 - 3.1η²1 - 5.02η²) Division: $α^1(16 - 82.5η^1 + 539.5η^2)$

4 Vectorization of BANcpp Library

We vectorized the *BANcpp* library by mixing two approaches: (i) leveraging the automatic vectorization offered by the compiler; (ii) enhancing vectorization manually whenever the automatic optimisation of the compiler fails. In particular, we needed to implement manual vectorization in the following cases:

- when a for-loop contains control-flow instructions on the BAN coefficients: due to possible branches and FPU exceptions the compiler refuses to insert vector instructions for these loops. The solution is to provide vectorization manually exploiting masked instructions based on comparisons. This is the case of comparison between BANs or checks on BAN values.
- when we have an outer and inner for-loop whose indexes are depending on each other. The solution is to implement vectorization manually leveraging the "geometry" of the problem. This is the case of multiplication between two BANs, which ends up in a one-dimensional convolution.

Listing 1. Manually vectorized 8 *×* 8 1-D convolution for AVX512 SIMD.

```
void convmul(const double* a, const double* b, double* dst) {
     _{2}_{m}512 va0 = _{mm}512_{set1}_{pd(a[0])};
     ...
     _{2}_{m}512 va7 = _{mm}512<sub>2</sub>set1<sub>-pd</sub>(a[7]);
     _{2}_{m}512 vb07 = _{mm}512_{1}loadu_{pd}(&b[0]);
     _{--}m512 vb17 = _{mm}512_{mul\_pd}(vb07, masked1);
     ...
     _{--}m512 vb77 = _{mm}512_{mul\_pd}(vb07, masked7);
     _{--}m512 va0b = _{mm}512_{mu}l<sub>pd</sub>(va0, vb07);
     _{\texttt{m512 valb}} = _{\texttt{mm512}}slide(_{\texttt{mm512}}mul_pd(va1, vb17),1);
     __m512 va2b = _mm512_slide(_mm512_mul_pd(va2 , vb27),2);
     ...
     __m512 va7b = _mm512_slide(_mm512_mul_pd(va7 , vb77),7);
     _{--}m512 accr = _{mm}512_{add\_pd}(va0b,va1b);
     accr = _mm512_add_pd(accr, va2b);
     ...
     accr = \texttt{mm512\_add\_pd}(\text{accr}, \text{va7b});_mm512_storeu_pd (&dst[0], accr);
}
```
5 Benchmark Application and Results

The problem used as a benchmark in this study is one of the first ever used for testing and showing the efficacy of non-Archimedean numerical computations, namely Kite [\[3](#page-5-2)]. It consists of a bi-objective lexicographic linear programming problem, i.e., an optimisation problem of two linear functions, ordered by strict priority, over a linearly defined domain. To solve the problem, we adopted a Simplex-like non-Archimedean algorithm [\[3](#page-5-2)], precisely tailored to this type of task. To make it more realistic, we wrapped the problem within the I-Big-M framework [\[14\]](#page-5-13), which adds a third objective to generalize the optimisation to the case of unknown starting feasible basis.

We ran the benchmark application for 10^5 steps with both the auto-vectorized (namely, *baseline*) and the enhanced-auto-vectorized (namely *enhanced*) versions of the BAN library, collecting the time spent for each iteration. We smoothed the data by a 200-steps moving average window and computed a least square fit to plot the metric trends for the average time spent during an iteration and the average throughput (in terms of iterations per second). The benchmark was run on an Intel Xeon Gold 6238R processor running at 2.2 GHz with eight 64-bit BAN coefficients. Figure [1](#page-4-0) shows the comparison between the two versions in terms of average time spent per iteration and overall throughput (iterations per second). Mean value and standard deviation of the two are reported in Table [1.](#page-4-1)

Fig. 1. Comparison between time spent for each iteration (**left**) and throughput (iterations per second, **right**) in the two different versions of the BAN library with the associated fitted curve.

Table 1. Mean value and standard deviation over 10^5 iterations of the benchmark application, 8 64-bit BAN Coefficient with AVX-512.

		Time (ns, $\times 10^4$) Throughput (iter/s, $\times 10^5$)
Baseline (Non-vector) $ 9.44 \pm 1.11$		$1.07 + 0.87$
Baseline (Vector)	5.18 ± 0.21	$1.93 + 0.04$
Enhanced	4.68 ± 0.19	$2.14 + 0.05$

6 Conclusions

In this work, we presented the acceleration of a C_{++} library for Bounded Algorithmic Numbers (BAN) exploiting vector instructions, testing it on a non-Archimedean optimisation benchmark. The results showed how manually enhancing the automatic vectorization produced by the compiler can improve the performance of such applications even without complete hardware support for BANs. We have found that the performance of compiler automatic vectorization is significantly inferior to that achieved by manually optimising which intrinsics to use (and in which order) and how to load the information (again, in which order and according to which scheme). This can be helpful for the community of compiler developers too since it means that there is room for improving the compilers for handling the specific use case tackled in this work.

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