

Propositional Projection Temporal Logic Specification Mining

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Abstract. This paper proposes a dynamic approach of specification mining for Propositional Projection Temporal Logic (PPTL). To this end, a pattern library is built to collect some common temporal relation among events. Further, several algorithms of specification mining for PPTL are designed. With our approach, PPTL specifications are mined from a trace set of a target program by using patterns in the library. In addition, a specification mining tool PPTLMiner supporting this approach is developed. In practice, given a trace set and user selected patterns, PPTLMiner can capture PPTL specifications of target programs.

Keywords: Propositional projection temporal logic \cdot Pattern \cdot Trace \cdot Specification mining

1 Introduction

A software system specification is a formal description of the system requirements. Formal languages are often employed to write specifications so as to prevent the ambiguity written in natural languages. The common used formal languages include Temporal Logic (TL) and Finite State Automata (FSA). Software system specification can be used to test and verify the correctness and reliability of software systems [\[13\]](#page-13-0). However, due to various kinds of reasons, a great number of software systems lack formal specifications. In particular, for most of legacy software systems, formal specifications are missed. This makes the maintenance of software systems difficult. To fight this problem, various kinds of specification mining approaches are proposed $[10-12, 14, 15, 17, 19-21]$ $[10-12, 14, 15, 17, 19-21]$ $[10-12, 14, 15, 17, 19-21]$ $[10-12, 14, 15, 17, 19-21]$ $[10-12, 14, 15, 17, 19-21]$ $[10-12, 14, 15, 17, 19-21]$.

Walkinshaw et al. [\[19](#page-14-2)] present a semi-automated approach to inferring FSAs from dynamic execution traces that builds on the QSM algorithm [\[8\]](#page-13-4). This algorithm infers a finite state automaton by successively merging states. Lo et al.

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propose Deep Specification Mining (DSM) approach that performs deep learning for mining FSA-based specifications [\[11\]](#page-13-5). FSA specifications are intuitive and easily to be used for verifying and testing programs. However, most of FSA specification mining approaches suffer from accuracy and correctness for representing properties of programs. Yang et al. [\[21\]](#page-14-3) present an interesting work on mining two-event temporal logic rules (i.e., of the form $G(a \to XF(b))$, where G, X and F are LTL operators, which are statistically significant with respect to a user-defined "satisfaction rate". Wasylkowski et al. [\[20](#page-14-4)] mine temporal rules as Computational Tree Logic (CTL) properties by leveraging a model checking algorithm and using concept analysis. Lemieux et al. [\[12\]](#page-13-2) propose an approach to mine LTL properties of arbitrary length and complexity. Similar to the above research work, most of specification mining approaches employ LTL and CTL as the property description languages. Due to the limitation of the expressiveness of LTL and CTL, some temporal properties such as periodic repetition properties cannot be characterized.

Since the expressiveness of Propositional Projection Temporal Logic (PPTL) is full regular $[3,18]$ $[3,18]$ $[3,18]$, in this paper, we propose a dynamic approach to mining PPTL properties based on a pattern library. PPTL contains three primitive temporal operators: next (\bigcap) , projection (prj) and chop-plus $(+)$. Apart from some common temporal properties that can be formalized in LTL and CTL, PPTL is able to describe two other kinds of properties: interval sensitive properties and periodic repetition properties. With the proposed approach, we abstract API/method calls as events. A trace is a sequence of API/method calls occurred during program execution. Daikon [\[1](#page-13-7)] is used to generate raw traces first, then a tool *DtraceFilter* we developed is employed to further refine the traces. Patterns are used to characterize common temporal relations among events. Two categories of patterns, Occurrence and Order, are used. These patterns are predefined in a pattern library. The proposed mining algorithms require two inputs: an instantiated pattern formula P and a refined execution trace τ . To obtain an instantiated pattern formula, we need to specify a pattern formula which can be either a user-defined one or a predefined one in the library. The pattern is instantiated by substituting atomic propositions with concrete events. After pattern instantiation, several mining algorithms based on PPTL normal form $[3-7]$ $[3-7]$ are employed to recursively check whether τ satisfies P.

The contribution of the paper is three-fold. First, we propose a PPTL temporal rule specification mining approach so that full regular properties can be mined. Second, we develop a tool PPTLMiner which supports the proposed mining approach. Third, we build a pattern library to cover all common patterns accumulated from literatures and abstracted from the existing software systems. The library is open, user-editable and in constant expansion and growth.

This paper is organized as follows. In the next section, PPTL is briefly introduced. In Sect. [3,](#page-3-0) the trace generation and the construction of the pattern library are presented. In Sect. [4,](#page-6-0) the overall framework of PPTLMiner and key algorithms are elaborated. Finally, conclusions are drawn in Sect. [5.](#page-13-9)

2 Propositional Projection Temporal Logic

In this section, we briefly introduce our underlying logic, Propositional Projection Temporal Logic (PPTL), including its syntax and semantics. It is used to describe specifications of programs. For more detail, please refer to [\[3](#page-13-6)[,7](#page-13-8)].

Syntax of PPTL. Let *Prop* be a set of atomic propositions and $p \in Prop$. The syntax of PPTL is inductively defined as follows.

$$
P ::= p | \bigcirc P | \neg P | P \vee Q | (P_1, ..., P_m) \text{ prj } Q | P^+
$$

where $P_1, ..., P_m, P$ and Q are well-formed PPTL formulas. Here, \bigcap (next), prj (projection) and + (chop-plus) are primitive temporal operators.

Semantics of PPTL. Let $B = \{true, false\}$ and N be the set of non-negative integers. Let ω denote infinity. PPTL formulas are interpreted over intervals. An interval σ is a finite or infinite sequence of states, denoted by $\sigma = \langle s_0, s_1, \ldots \rangle$. A state s_i is a mapping from *Prop* to B. An interpretation $\mathcal{I} = (\sigma, k, j)$ is a subinterval $\langle s_k, \ldots, s_j \rangle$ of σ with the current state being s_k . An auxiliary operator \downarrow is defined as $\sigma \downarrow (r_1, \ldots, r_m) = \langle s_{t_1}, s_{t_2}, \ldots, s_{t_n} \rangle$, where t_1, \ldots, t_n are obtained from r_1, \ldots, r_m by deleting all duplicates. That is, t_1, \ldots, t_n is the longest strictly increasing subsequence of r_1, \ldots, r_m . The semantics of PPTL formulas is inductively defined as a satisfaction relation below.

- (1) $\mathcal{I} \models p$ **iff** $s_k[p] = true$.
- (2) $\mathcal{I} \models \bigcap P$ **iff** $(\sigma, k+1, j) \models P$.
- (3) $\mathcal{I} \models \neg P$ **iff** $\mathcal{I} \not\models P$.

(4) $\mathcal{I} \models P \vee Q$ **iff** $\mathcal{I} \models P$ or $\mathcal{I} \models Q$.

(5) $\mathcal{I} \models (P_1,\ldots,P_m)$ pr j Q **iff** there exist m integers $k = r_0 \leq r_1 \leq \ldots \leq r_m \leq j$ such that $(\sigma, r_{l-1}, r_l) \models P_l$ for all $1 \leq l \leq m$, and one of the following two cases holds:

• if $r_m = j$, there exists r_h such that $0 \leq h \leq m$ and $\sigma \downarrow (r_0, \ldots, r_h) \models Q;$

• if $r_m < j$, then $\sigma \downarrow (r_0, \ldots, r_m) \cdot \sigma_{(r_m+1..j)} \models Q$.

(6) $\mathcal{I} \models P^+$ **iff** there exist m integers $k = r_0 \le r_1 \le \ldots \le r_m = j \ (m \in N)$ such that $(\sigma, r_{l-1}, r_l) \models P$ for all $1 \leq l \leq m$.

Derived Formulas. Some derived formulas in PPTL are defined in Table [1.](#page-3-1)

Operator Priority. To avoid an excessive number of parentheses, the prece-dence rules shown in Table [2](#page-3-2) are used, where $1 =$ highest and $9 =$ lowest.

Definition 1 (PPTL Normal Formal). Let Q be a PPTL formula and Q_p denote the set of atomic propositions appearing in Q . Q is in normal form if Q has been rewritten as

$$
Q \equiv \bigvee_{j=1}^{n_0} (Q_{ej} \wedge \epsilon) \vee \bigvee_{i=1}^{n} (Q_{ci} \wedge \bigcirc Q_i')
$$

where $Q_{ej} \equiv \bigwedge_{k=1}^{m_0} q_{jk}, \ Q_{ci} \equiv \bigwedge_{h=1}^{m} q_{ih}, \ l = |Q_p|, \ 1 \leq n \leq 3^l, \ 1 \leq n_0 \leq 3^l,$ $1 \leq m \leq l, 1 \leq m_0 \leq l$; $q_j k, q_i h \in Q_p$, for any $r \in Q_p$, \dot{r} means r or $\neg r$; Q_i' is a general PPTL formula.

A1		$\varepsilon \stackrel{\text{def}}{=} \neg \bigcirc true$	A2		more $\stackrel{\text{def}}{=}$ \bigcirc true
A ₃	$\bigcap^0 P \stackrel{\text{def}}{=} P$		A ₄		$\bigcap{}^n P \stackrel{\text{def}}{=} \bigcirc (\bigcirc^{n-1} P)(n > 0)$
A ₅	$\bigcap P \stackrel{\text{def}}{=} \varepsilon \vee \bigcirc P$		A6		$P; Q \stackrel{\text{def}}{=} (P, Q) \text{ pr } j \in$
A7	$\Diamond P \stackrel{\text{def}}{=} true; P$				A8 $\Box P \stackrel{\text{def}}{=} \neg \Diamond \neg P$
A ₉	$len(n) \stackrel{\text{def}}{=} \bigcap^n \varepsilon$		A10		$skip \stackrel{\text{def}}{=} len(1)$
A11		$P^* \stackrel{\text{def}}{=} P^+ \vee \varepsilon$			A12 $P \parallel Q \stackrel{\text{def}}{=} (P; true) \land Q \lor P \land (Q; true)$
A13	$fin \stackrel{\text{def}}{=} \Diamond \varepsilon$		A14		$inf \stackrel{\text{def}}{=} \neg fin$

Table 1. Derived formulas

Table 2. Operator priority

		$1. \neg 2. +, * 3. \bigcirc, \bigcirc, \Diamond, \Box$
	4. $ \wedge 5.$; 6. \vee	
		$7. prj 8. 9. \rightarrow, \leftrightarrow$

In some circumstances, for convenience, we write $Q_e \wedge \epsilon$ instead of $\bigvee_{j=1}^{n_0} (Q_{e_j} \wedge$ ϵ) and $\bigvee_{i=1}^{r}(Q_i \wedge \bigcirc Q'_i)$ instead of $\bigvee_{i=1}^{n}(Q_{ci} \wedge \bigcirc Q'_i)$. Thus,

$$
Q \equiv (Q_e \wedge \epsilon) \vee \bigvee_{i=1}^r (Q_i \wedge \bigcirc Q_i')
$$

where Q_e and Q_i are state formulas. The algorithm of translating a PPTL formula into its normal form can be found in $[4-6]$ $[4-6]$.

3 Pattern Library Construction and Trace Generation

Our specification mining algorithm relies on two inputs: a pattern and a program execution trace. A pattern is a property template in which the atomic proposition symbols need to be instantiated as events (namely, API or method calls) occurred during program execution. A trace is a sequence of method calls in the execution of a program. In this section, we present how to build the pattern library and traces.

3.1 Pattern and Pattern Library

Patterns are abstracted from common software behaviors and used to describe occurrence of events or states during program execution [\[9](#page-13-12)]. A pattern is a logical representation of certain event relation. The *APIs* and methods in a target program are defined as events. We say that an event occurs whenever it is called in the execution of the program. In the following, we define a quadruples to represent and store patterns.

Definition 2 (Pattern). A pattern $T = \langle C, N, R, A \rangle$ is a tuple where C is a pattern category indicating occurrence or order of events, N a pattern name, R a PPTL formula, and A an annotation.

Following Dwyer et al.'s SPS [\[9\]](#page-13-12) and Autili et al.'s PSP framework [\[2\]](#page-13-13), we also classify patterns into two categories, *Occurrence* and *Order*.

The *Occurrence* category contains 18 patterns that indicate presence or absence of certain events or states during program execution. For instance, (1) *Absence* means that an event never happens; (2) *Universality* indicates that an event always occurs during program execution; (3) *Existence* shows that an event occurs at least once during program execution; and (4) *Bounded Existence* tells us that an event has a limited number of occurrences during program execution, e.g. event f.open() occurs twice.

The *Order* category contains 19 patterns that represent relative temporal orders among multiple events or states occurred during program execution. For example, (1) "s precedes p" indicates that if event p occurs, event s definitely occurs before p; (2) "s responds p" means that if event p occurs, event s definitely occurs after p; (3) *Chain* (s, t) means that a combination chain of events s and t. (s, t) precedes p means that if event p happens, chain events (s, t) certainly happen before p, and (s, t) responds p means that if event p happens, (s, t) certainly responds to $p \ [2, 9]$ $p \ [2, 9]$ $p \ [2, 9]$.

Pattern Library. A pattern library L is a set containing all patterns p we collected. After an in-depth investigation of the existing literature and programs specified behavior characteristics, we build a pattern library and some patterns are shown in Table [3](#page-4-0) and Table [4.](#page-5-0)

		No. Pattern Name PPTL Formula Annotation	
	Universality	$\Box p$	Event p always occurs
$\overline{2}$	Absence	$\Box \neg p$	Event p never occur
3	Existence	$\Diamond p$	Event p occurs at least once
$\overline{4}$	Frequency	$\square \Diamond p$	Event p occurs frequently
5	Both Occur	$\Diamond p \land \Diamond q$	Events p and q both occur
6	Simultaneity	$\Diamond(p \land q)$	Events p and q occur at the same time
7	Prefix of Trace \Box more		Event p occurs frequently at a prefix of a trace
8	Suffix of Trace $\Diamond \Box p$		Event p occurs continuously at a suffix of a trace

Table 3. Pattern library - occurrence category

3.2 Trace Generation

We concern only specifications of temporal relations among the methods or API calls occurred during program execution.

	No. Pattern Name	PPTL Formula Annotation	
-1.	Precedence $(1-1)$	$\Diamond p \rightarrow (\Box \neg p; s)$	Event s takes precedence over event p
$\overline{2}$	$Response(1-1)$	$\Box(s \rightarrow \bigcirc \Diamond p)$	Event p responds to event s
3	Until	$(\Box p; \bigcirc s) \vee s$	Event p occurs until event s occurs
$\overline{4}$	Response Invariance $\square(p \to \bigcap \square s)$		If p has occurred, then in response s holds continually
5	Chop	\square_p ; $\bigcirc \square_q$	There exists a time point t such that event p occurs continuously before t and event q continuously after t
-6	Never Follow		$\Box(p \rightarrow \bigcirc \Box \neg q)$ Event p is never followed by event q

Table 4. Pattern library - order category

Definition 3 (Trace). A trace is a sequence of methods or API calls (namely events) with parameters.

Example 1. A trace of a program using stack structure.

trace $\tau_1 = \langle StackAr(int), isFull(), isEmpty(), top(), isEmpty(), topAndPop(),$ $isEmpty(), isFull(), isEmpty(), top(), isEmpty(), push(java.lang.Object),$ $is Full()$

Example 2. A trace of a program manipulating files.

trace $\tau_2 = \langle open(f1), write(f1), read(f1), close(f1), open(f2), delete(f1),$ $read(f2), write(f2), write(f2), read(f2), close(f2), delete(f2)$

We use Daikon [\[1\]](#page-13-7) as an auxiliary tool to generate traces. Daikon can dynamically detect program invariants. A program invariant is a property that remains unchanged at one or more positions of program execution. The common invariants are APIs, functions, global or local variables, arguments, return values and so on. Invariants can be used to analyze behavior of a program. Dynamic invariant detection refers to a process of running a program so as to check variables and assertions detected in the program execution [\[16](#page-14-6)].

Daikon generates a sequence containing all invariants and stores it in a *dtrace* file in which the invariants are stored line by line. The program execution traces we need are contained in this sequence. Since there exists an amount of redundant information, the *dtrace* file needs to be further refined.

The whole process of generating a trace is shown in Fig. [1.](#page-5-1)

Fig. 1. The process of trace generation

Step 1. Generating sequences of program invariants

A source program and its arguments are input to Daikon so that a sequence of program invariants is generated. The sequence is written in a file *f.dtrace* in the *dtrace* format. When the program is executed with different arguments for a desired number n of times, we obtain a set $Pool_1 = \{f_i. dtrace | i = 1, \ldots, n\}$ of program traces.

Step 2. Filtering of sequences of program invariants

A filter tool *DtraceFilter* has been developed to filter out redundant information, including parameters, variables, return values and useless spaces, in each file $f_i.dtrac$ of $Pool_1$. As a result, sequences consisting of only $APIs$ and method calls constitute a new set $Pool_2 = \{f_i.trac|i = 1, \ldots, n\}.$

*Step 3. Parsing traces in Pool*₂

Each trace $f_i trace$ in $Pool_2$ needs to be parsed so as to obtain a API/methodname list $f_i.event$. These lists constitute a set $Event = \{f_i.event | i = 1, ..., n\}$.

Step 4. Optimizing traces in Pool₂

We can specify desired API/method names from the lists in $Event$ according to the requirements. *DtraceFilter* can be used to select the events we concern from each list in Event to build a positive list f_i pevent of events, and generate a set $PositiveEvent = \{f_i.pevent | i = 1, ..., n\}.$

Based on $PositiveEvent$, *DtraceFilter* further refines each f_i .trace in $Pool_2$ to get a positive trace f_i .ptrace consisting of only the events in f_i .pevent, and obtain a set $PositiveTrace = \{f_i, phrase | i = 1, ..., n\}.$

We can also specify undesired API/method names from the lists in Event. In a similar way, *DtraceFilter* can be used to build a negative list f*i*.nevent of events and generate $NegativeEvent = \{f_i.newent | i = 1, ..., n\}$. After deleting the negative events from each trace f_i .trace in $Pool_2$, *DtraceFilter* builds a set $NegativeTrace = \{f_i. ntrace | i = 1, \ldots, n\}.$

4 PPTL Specification Mining

Based on the Pattern Library and set of refined traces presented in the previous section, an approach to PPTL specification mining is proposed and a specification mining tool, PPTLMiner, is developed. In this section, the framework of PPTLMiner and some key algorithms are presented in detail.

4.1 The Framework of PPTLMiner

The integrated design of PPTLMiner is shown in Fig. [2.](#page-7-0) It consists of the following six parts.

(1) Pattern Library. The Pattern Library covers all patterns we obtain after investigating literatures and programs. Our Pattern Library is open, usereditable and in constant expansion and growth. New patterns can be inserted into the library from time to time. For more details, refer to Sect. [3.1.](#page-3-3)

Fig. 2. The framework of PPTLMiner

(2) Trace Generator. The function of the Trace Generator is to generate traces from an executable program. To do so, an executable program and its arguments are input into Daikon to produce raw traces (dtrace files). Then *Dtracefilter* is employed to filter out redundant information in dtrace files to obtain trace files, which are further refined to obtain positive and negative traces. For more details, refer to Sect. [3.2.](#page-4-1)

(3) PPTL Parser. The input of PPTL Parser is a PPTL formula. PPTL Parser is developed by means of Flex and Bison. It can be used to generate a PPTL syntax tree for any PPTL formula.

(4) Trace Parser. The function of Trace Parser is two-fold. The first is to parse traces generated by the Trace Generator and restore them in an appropriate data structure so that the traces can conveniently be used by PPTL Pattern Checker. The second is to calculate a set $E = \{e_1, e_2, \ldots, e_n\}$ of events appeared in the traces so as to instantiate PPTL patterns.

(5) PPTL Pattern Formula Instantiator. The instantiator requires two inputs: (a) a PPTL pattern formula P , and (b) E , the set of events produced by Trace Parser. The function of the instantiator is to instantiate a pattern formula P by substituting atomic propositions in P by events in Events.

(6) PPTL Pattern Checker. PPTL Pattern Checker also requires two inputs: (a) a trace τ produced by Trace Generator, and (b) an instantiated pattern formula P generated by PPTL Pattern Formula Instantiator. The function of the Checker is to decide whether trace τ satisfies P.

4.2 Mining Process and Algorithms

In this subsection, we present the mining process and algorithms in detail.

(1) Syntax Tree of PPTL Formula

By syntax analysis, a PPTL Pattern Formula is parsed into a syntax tree. A syntax tree consists of a root node and two child nodes. The root node is of two attributes, NODETYPE and STRING, which indicate the type and name of the root node, respectively. All nodes having two null child nodes in the syntax tree of a PPTL pattern formula P constitute a set $S(P)$ of atomic propositions. For instance, for an atomic proposition p , its NODETYPE is "atomic proposition" while its STRING is "p". Two child nodes are all null. For formula $P_1; P_2$, its NODETYPE is "chop" while its STRING is ";". It has two non-null child nodes, *child*₁ and *child*₂, where *child*₁ is the root of P_1 while *child*₂ is the root of P_2 . $S(P_1; P_2) = S(P_1) \cup S(P_2)$. More Examples are shown in Fig. [3.](#page-8-0)

Fig. 3. PPTL syntax tree

(2) Instantiating PPTL Pattern Formulas

Based on the set $S(P)$ of atomic propositions and set E collected by Trace Parser, a PPTL Pattern Formula P is instantiated by Algorithm [1.](#page-9-0)

(3) PPTL Pattern Check

We use Algorithm [2,](#page-10-0) Algorithm [3](#page-11-0) and Algorithm [4](#page-12-0) to check whether τ satisfies Q, where τ is a refined trace generated in Sect. [3.2](#page-4-1) while Q is an instantiated PPTL pattern formula obtained in part (2). These algorithms are based on PPTL Normal Form.

In particular, Algorithm [2,](#page-10-0) i.e. CheckBasedonNF(P, τ), first checks the satisfiability of P. If P is satisfiable, it is translated into its normal form P_{nf} by calling the existing external function $NF(\cdot)$ given in [\[3](#page-13-6)]. Then Algorithm [3](#page-11-0) NFCheckTrace(P_{nf}, τ) is called to decide whether τ satisfies P_{nf} .

In function NFCheckTrace(P_{nf}, τ), the first disjunct $P_{nf}.child_1$ is first checked. If NFCheckTrace($P_{nf}.child_1, \tau$) is true, P_{nf} is already satisfied by τ . Otherwise the rest disjuncts $P_{nf}.child_2$ are further checked.

Algorithm 1. function Instantiator (E, S, P)

Input: E: a set of events; **Input:** S: a set of atomic propositions appearing in P; **Input:** P: a syntax tree of a PPTL pattern formula; **Output:** P_s : a set of instantiated PPTL pattern formulas. 1: **begin** 2: $P_s \leftarrow null;$ 3: m is a patttern instance; 4: $\frac{1}{n}$ $m = \{(ap_i, ep_i) \mid ap_i \in S \& ep_i \in E \& 1 \leq i \leq |S| \& ap_i \neq ap_j \text{ if } i \neq j)\}$ */ 5: *M* is a set of pattern instances; $\frac{1}{2}$ $M = \{m_1, m_2, ...\}$ $\frac{1}{2}$ 6: $M \leftarrow null$; 7: Count is used for count the number of m; 8: $Count \leftarrow 0;$
 $(s, (E.size())!$ 9: /* (*E.size*())! (*E.size*()−*S.size*())! is the total number of non-duplicate pattern instances */ 10: **while** $Count \leq \frac{(E.size())!}{(E.size() - S.size())!}$ **do** 11: E_1 is a set used to store $ep \in E$ has been checked;
12: $m \leftarrow null$: 12: $m \leftarrow null;$
13: $E_1 \leftarrow null;$ 13: $E_1 \leftarrow null;$
14: for all ap i 14: **for all** ap in S **do** 15: **while** true **do** 16: ep is an event randomly selected from E ; 17: **if** ep not in E_1 **then** 18: $m.insert(ap, ep)$; /* ap is mapped to ep */ 19: $E_1.insert(ep)$; /* ep is labeled */ 20: break; 21: **end if** 22: **end while** 23: **end for**/* build m */ 24: **if** m not in M **then** 25: $M.insert(m);$ 26: $count + +$; 27: **end if** 28: **end while**/* build M */ 29: **for all** m in M **do** 30: P_{ins} is a copy of P ; /* P_{ins} is used for instantiation */ 31: $P_{ins} \leftarrow P$;
32: **for all** no for all node in P_{ins} do 33: **if** node.type == AtomicProp **then** 34: **for all** m*ⁱ* in m **do** 35: **if** $m_i.ap == node.name$ **then** 36: $node.name \leftarrow m_i . ep;$
37: **end if** 37: **end if** 38: **end for** 39: **end if** 40: **end for** 41: $P_s.insert(P_{ins});$ /* insert pattern instance P_{ins} into set P_s */ 42: **end for** 43: **return** P*^s* 44: **end**

Algorithm 2. function CheckBasedonNF(P **,** τ **)**

Input: P: An instantiated PPTL pattern formula; **Input:** τ : A program execution trace; **Output:** True if τ satisfies P, False otherwise. 1: **begin** 2: q is a boolean variable; 3: $q = \text{CheckSatisfiability}(P);$ /* check satisfiability of P [\[5](#page-13-14)] */ 4: **if** $\neg q$ **then**
5: **return** return False; 6: **else** 7: $P_{nf} = NF(P);$ /* transform P into its normal form $[5]$ $[5]$ */ 8: **return** NFCheckTrace(P_{nf}, τ); 9: **end if** 10: **end**

To check a disjunct, two cases need to be considered: $(1)P_e \wedge \varepsilon$ and $(2)P_c \wedge$ $\bigcap P_f$. For the first case, the function checks whether τ satisfies P_e and whether τ is empty. If both are true, $P_e \wedge \varepsilon$ is satisfied by τ . For the second case, the function checks whether τ satisfies P_c and whether $tail(\tau)$ satisfies P_f . If both are true, $P_c \wedge \bigcirc P_f$ is satisfied by τ . In checking whether τ satisfies the state formula P_e or P_c , Algorithm [4](#page-12-0) StateFormulaCheck(P_s , τ) is called. For doing so, function StateFormulaCheck (P_s, τ) is simply to check the satisfiability of state formula P_s over τ by considering several syntax constructs of P_s .

```
Algorithm 3. function NFCheckTrace(P_{nf}, \tau)Input: P_{nf}: A PPTL formula in its normal form;
Input: \tau: A program execution trace;
Output: True if \tau satisfies P_{nf}, False otherwise.
1: begin
2: \tau_{bak} = \tau;
3: switch Pnf .type do
4: case OrProp5: q_1 is a boolean variable;
 6: q_1 = \text{NFCheckTrace}(P_{nf}.child_1, \tau);7: if q_1 then /* first disjunct is satisfied by \tau */
8: return True:
9: else/* select another disjunct */
10: \tau = \tau_{bak};11: return NFCheckTrace(P_{nf}.child_2, \tau);
12: end if
13: case AndProp
14: P_c = P_{nf}.child_1; /* if P_{nf}.child_2 is \varepsilon, P_c stands for P_e */
15: q_2 is a boolean variable;
16: q_2 = \text{StateFormulaCheck}(P_c, \tau); /* check satisfiability of P_c over \tau^{*}/17: if q_2 then
18: if P_{nf}.child_2.type is \varepsilon then
19: if |\tau| == 0 then /* check whether the trace is empty */<br>20: return True:
                   return True;
21: else
22: return False;
23: end if
24: else
25: P_f = P_{nf}.child_2.child_1; /* obtain next formula P_f */
26: if |\tau| == 0 then<br>27: return Falsereturn False;
28: else
29: \tau = tail(\tau); /* update \tau by its first proper suffix */
30: return CheckBasedOnNF(P_f, \tau);
31: end if
32: end if
33: else
34: return False;
35: end if
36: end
```

```
Algorithm 4. function StateFormulaCheck(P_s, \tau)Input: Ps: A state PPTL formula;
Input: \tau: A program execution trace;
Output: True if \tau satisfies P_s, False otherwise.
 1: begin
 2: switch Ps.type do
 3: case OrProp \nmid^* P_s \equiv P_1 \vee P_2 \nmid^* P_4<br>4: P_1 = P_s \nmid^* P_5P_1 = P_s.child<sub>1</sub>;
 5: P_2 = P_s \n{.} \n{child}_2;6: q_1 is a boolean variable;
 7: q_1 = \text{StateFormulaCheck}(P_1, \tau);8: if q_1 then
 9: return True;10: else
11: return StateFormulaCheck(P_2, \tau)
12: end if
13: case And Prop \nmid * P_s \equiv P_1 \wedge P_2 * /<br>14: P_1 = P_2 \nmid A_1:
           P_1 = P_s.child<sub>1</sub>:
15: P_2 = P_s \n{child_2};16: q_2 is a boolean variable;
17: q_2 = \text{StateFormulaCheck}(P_1, \tau);18: if q_2 then
19: return StateFormulaCheck(P<sub>2</sub>, τ)
20: else
21: return False;
22: end if
23: case NegationProp \frac{*}{P_s} \equiv \neg P_1 \frac{*}{24}<br>24: P_1 = P_s \text{.child}_1;
           P_1 = P_s.child<sub>1</sub>;
25: if StateFormulaCheck(P_1, \tau) then
26: return False;
27: else
28: return True;29: end if
30: case AtomicProp /* P_s \equiv p */<br>31: if head(\tau) satisfies P_s then
           if head(\tau) satisfies P_s then
32: return True;33: else
34: return False;
35: end if
36: case TrueProp \nmid^* P_s \equiv true \nmid^* /<br>37: return True:
           return True;
38: case FalseProp \nmid^* P_s \equiv false * /<br>39: return False:
           return False;
40: end
```
5 Conclusion

This paper presents an approach to mining PPTL specification from program execution traces. A tool PPTLMiner has been developed to support the proposed approach. This allows us to mine full regular temporal rules represented by PPTL formulas from traces. However, a mined PPTL formula has to be checked over all traces so as to ensure its validity. This is not a easy job since there might be error traces involved.

In the future, we will investigate how to evaluate the mined properties so that desired properties can be found. Further, we will optimize PPTLMiner to improve its mining quality and efficiency.

References

- 1. The Daikon Invariant Detector. <http://plse.cs.washington.edu/daikon/>
- 2. Autili, M., Grunske, L., Lumpe, M., Pelliccione, P., Tang, A.: Aligning qualitative, real-time, and probabilistic property specification patterns using a structured English grammar. IEEE Trans. Softw. Eng. **41**(7), 1 (2015)
- 3. Duan, Z.: Temporal logic and Temporal Logic Programming. Science Press, Beijing (2005)
- 4. Duan, Z., Tian, C.: A practical decision procedure for propositional projection temporal logic with infinite models. Theoret. Comput. Sci. **554**, 169–190 (2014)
- 5. Duan, Z., Tian, C., Zhang, L.: A decision procedure for propositional projection temporal logic with infinite models. Acta Informatica **45**(1), 43–78 (2008)
- 6. Duan, Z., Tian, C., Zhang, N.: A canonical form based decision procedure and model checking approach for propositional projection temporal logic. Theor. Comput. Sci. **609**, 544–560 (2016)
- 7. Duan, Z., Zhang, N., Koutny, M.: A complete proof system for propositional projection temporal logic. Theor. Comput. Sci. **497**, 84–107 (2013)
- 8. Dupont, P., Lambeau, B., Damas, C., Lamsweerde, A.: The QSM algorithm and its application to software behavior model induction. Appl. Artif. Intell. $22(1&2)$, 77–115 (2008)
- 9. Dwyer, M.B., Avrunin, G.S., Corbett, J.C.: Patterns in property specifications for finite-state verification. In: Proceedings of the 1999 International Conference on Software Engineering (IEEE Cat. No.99CB37002), pp. 411–420 (1999)
- 10. Iegorov, O., Fischmeister, S.: Mining task precedence graphs from real-time embedded system traces. pp. 251–260 (2018)
- 11. Le, T.B., Lo, D.: Deep specification mining. In: Proceedings of the 27th ACM SIGSOFT International Symposium on Software Testing and Analysis, pp. 106– 117 (2018)
- 12. Lemieux, C., Park, D., Beschastnikh, I.: General LTL specification mining (T). In: Proceedings of the 2015 IEEE/ACM International Conference on Automated Software Engineering (ASE), pp. 81–92 (2015)
- 13. Li, H., Shen, L.M., Ma, C., Liu, M.Y.: Role behavior detection method of privilege escalation attacks for android applications. Int. J. Perform. Eng. **15**(6), 1631–1641 (2019)
- 14. Narayan, A., Cutulenco, G., Joshi, Y., Fischmeister, S.: Mining timed regular specifications from system traces. ACM Trans. Embed. Comput. Syst. **17**(2), 1–21 (2018)
- 15. Pradel, M., Gross, T.R.: Automatic generation of object usage specifications from large method traces. In: Proceedings of the 2009 IEEE/ACM International Conference on Automated Software Engineering, pp. 371–382 (2009)
- 16. Ratcliff, S., White, D., Clark, J.: Searching for invariants using genetic programming and mutation testing. In: Proceedings of the 2011 Annual Genetic and Evolutionary Computation Conference, pp. 1907–1914 (2011)
- 17. Reger, G., Havelund, K.: What is a trace? A runtime verification perspective. In: Margaria, T., Steffen, B. (eds.) ISoLA 2016. LNCS, vol. 9953, pp. 339–355. Springer, Cham (2016). [https://doi.org/10.1007/978-3-319-47169-3](https://doi.org/10.1007/978-3-319-47169-3_25) 25
- 18. Tian, C., Duan, Z.: Expressiveness of propositional projection temporal logic with star. Theor. Comput. Sci. **412**(18), 1729–1744 (2011)
- 19. Walkinshaw, N., Bogdanov, K., Holcombe, M., Salahuddin, S.: Reverse engineering state machines by interactive grammar inference. In: Proceedings of the 2007 Working Conference on Reverse Engineering, pp. 209–218 (2007)
- 20. Wasylkowski, A., Zeller, A.: Mining temporal specifications from object usage. In: Proceedings of the 2009 IEEE/ACM International Conference on Automated Software Engineering, pp. 295–306 (2009)
- 21. Yang, J., Evans, D., Bhardwaj, D., Bhat, T., Das, M.: Perracotta: mining temporal API rules from imperfect traces. In: Proceedings of the 2006 International Conference on Software Engineering, pp. 282–291 (2006)