Fred Mesnard Peter J. Stuckey (Eds.)

# Logic-Based Program Synthesis and Transformation

28th International Symposium, LOPSTR 2018 Frankfurt/Main, Germany, September 4–6, 2018 Revised Selected Papers



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## Logic-Based Program Synthesis and Transformation

28th International Symposium, LOPSTR 2018 Frankfurt/Main, Germany, September 4–6, 2018 Revised Selected Papers



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#### Preface

This volume contains a selection of the papers presented at LOPSTR 2018, the 28th International Symposium on Logic-Based Program Synthesis and Transformation held during September 4–6, 2018 at the the Goethe University Frankfurt am Main, Germany. It was co-located with PPDP 2018, the 20th International ACM SIGPLAN Symposium on Principles and Practice of Declarative Programming, and WFLP 2018, the 26th International Workshop on Functional and Logic Programming. The co-location of these related conferences has occurred several times and has been stimulating and cross-fertilizing.

Previous LOPSTR symposia were held in Namur (2017), Edinburgh (2016), Siena (2015), Canterbury (2014), Madrid (2013 and 2002), Leuven (2012 and 1997), Odense (2011), Hagenberg (2010), Coimbra (2009), Valencia (2008), Lyngby (2007), Venice (2006 and 1999), London (2005 and 2000), Verona (2004), Uppsala (2003), Paphos (2001), Manchester (1998, 1992, and 1991), Stockholm (1996), Arnhem (1995), Pisa (1994), and Louvain-la-Neuve (1993). More information about the symposium can be found at: http://ppdp-lopstr-18.cs.uni-frankfurt.de/lopstr18.html.

The aim of the LOPSTR series is to stimulate and promote international research and collaboration on logic-based program development. LOPSTR is open to contributions on all aspects of logic-based program development, all stages of the software life cycle, and issues of both programming-in-the-small and programming-in-the-large. LOPSTR traditionally solicits contributions, in any language paradigm, in the areas of synthesis, specification, transformation, analysis and verification, specialization, testing and certification, composition, program/model manipulation, optimization, transformational techniques in software engineering, inversion, applications, and tools. LOPSTR has a reputation for being a lively, friendly forum that allows for the presentation and discussion of both finished work and work in progress. Formal proceedings are produced only after the symposium so that authors can incorporate the feedback from the conference presentation and discussion.

In response to the calls for papers, 29 contributions were submitted from ten countries. The Program Committee accepted seven full papers for immediate inclusion in the formal proceedings, and four more papers presented at the symposium were accepted after a revision and another round of reviewing. Each submission was reviewed by at least three Program Committee members or external referees. The paper "Proving Program Properties as First-Order Satisfiability" by Salvador Lucas won the best paper award, sponsored by Springer. In addition to the 11 contributed papers, this volume includes the abstracts of the invited talks by three outstanding speakers: Philippa Gardner (Imperial College London, UK) and Jorge A. Navas (SRI International, USA), whose talks were shared with WFLP. We also had two invited tutorials: Fabio Fioravanti (University of Chieti-Pescara, Italy) presented "The VeriMAP System

for Program Transformation and Verification" and Manuel Hermenegildo (IMDEA Software Institute and Technical University of Madrid, Spain) summarized "25 Years of Ciao."

We want to thank the Program Committee members, who worked diligently to produce high-quality reviews for the submitted papers, as well as all the external reviewers involved in the paper selection. We are very grateful to the local organizer, David Sabel, and his team for the great job they did in managing the symposium. Many thanks also to Peter Thiemann, the Program Committee chair of PPDP, and Josep Silva, the Program Committee chair of WFLP, with whom we interacted for coordinating the events. We would also like to thank Andrei Voronkov for his excellent EasyChair system that automates many of the tasks involved in chairing a conference.

Special thanks go to the invited speakers and to all the authors who submitted and presented their papers at LOPSTR 2018. We also thank our sponsors, the Goethe University Frankfurt am Main, the Deutsche Forschungsgemeinschaft (DFG, German Research Foundation), and Springer for their cooperation and support in the organization of the symposium.

January 2019

Fred Mesnard Peter J. Stuckey

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## **Abstracts of Invited Talks**

## Formal Methods for JavaScript

Philippa Gardner

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**Abstract.** We present a novel, unified approach to the development of compositional symbolic execution tools, which bridges the gap between traditional symbolic execution and compositional program reasoning based on separation logic. We apply our approach to JavaScript, providing support for full verification, whole-program symbolic testing, and automatic compositional testing based on bi-abduction.

### **Constrained Horn Clauses for Verification**

Jorge Navas

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Abstract. Developing scalable software verification tools is a very difficult task. First, due to the undecidability of the verification problem, these tools, must be highly tuned and engineered to provide reasonable efficiency and precision trade-offs. Second, different programming languages come with very diverse assortments of syntactic and semantic features. Third, the diverse encoding of the verification problem makes the integration with other powerful solvers and verifiers difficult. This talk presents SeaHorn - an open source automated Constrained Horn clause-based reasoning framework. SeaHorn combines advanced automated solving techniques based on Satisfiability Modulo Theory (SMT) and Abstract Interpretation. SeaHorn is built on top of LLVM using its front-end(s) to deal with the idiosyncrasies of the syntax and it highly benefits from LLVM optimizations to reduce the verification effort. SeaHorn uses Constrained Horn clauses (CHC) which are a uniform way to formally represent a broad variety of transition systems while allowing many encoding styles of verification conditions. Moreover, the recent popularity of CHC as an intermediate language for verification engines makes it possible to interface SeaHorn with a variety of new and emerging tools. All of these features make SeaHorn a versatile and highly customizable tool which allows researchers to easily build or experiment with new verification techniques.

## Experiences in Designing Scalable Static Analyses

Laure Gonnord

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**Abstract.** Proving the absence of bugs in a given software (problem which has been known to be intrinsically hard since Turing and Cook) is not the only challenge in software development. Indeed, the ever growing complexity of software increases the need for more trustable optimisations. Solving these two problems (reliability, optimisation) implies the development of safe (without false negative answers) and efficient (wrt memory and time) analyses, yet precise enough (with few false positive answers). In this talk I will present some experiences in the design of scalable static analyses inside compilers, and try to make a synthesis about the general framework we, together with my coauthors, used to develop them. I will also show some experimental evidence of the impact of this work on real-world compilers, as well as future perspective for this area of research.

## **Abstracts of Invited Tutorials**

## The VeriMAP System for Program Transformation and Verification

Fabio Fioravanti

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**Abstract.** Constrained Horn Clauses (CHC) are becoming very popular for representing programs and verification problems, and several tools have been developed for checking their satisfiability. In this tutorial we will survey recent work on satisfiability-preserving transformation techniques for CHC and we will show how the VeriMAP system can be used effectively to (i) generate CHC verification conditions from the programming language semantics, (ii) prove safety properties of imperative programs manipulating integers and arrays, (iii) prove relational program properties, such as program equivalence and non-interference, (iv) check the satisfiability of CHC with inductively-defined data structures (e.g. lists and trees), (v) prove safety and controllability properties of time-aware business processes.

## 25 Years of Ciao

#### Manuel Hermenegildo

#### IMDEA Software Institute and Technical University of Madrid, Spain herme@fi.upm.es

Abstract. Ciao is a logic-based, multi-paradigm programming language which has pioneered over the years many interesting language- and programming environment-related concepts. An example is the notion of programming languages as modular language-building tools rather than closed designs. Another is the idea of dynamic languages that can optionally and gradually offer formal guarantees, which is also a solution for the classic dichotomy between dynamic and static typing: Ciao has many dynamic features (e.g., dynamically typed, dynamic program modification) but includes an assertion language for (optionally) declaring program properties and powerful tools for static inference and static/dynamic checking of such assertions, testing, documentation, etc. We will provide a hands-on overview of these features, concentrating on the novel aspects, the motivations behind their design and implementation, their evolution over time, and, specially, their use. In particular, we will show how the system can be used not only as a programming tool and as a language design tool, but also as a general-purpose program analysis and verification tool, based on the technique of translating program semantics (ranging from source to bytecode, LLVM, or assembly) into Horn-clause representation, and idea which Ciao also introduced early on. Finally, we will present some recent work in areas such as scalability, incrementality, or static vs. dynamic costs, as well as some future plans and ideas.

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## Analysis of Term Rewriting



## Proving Program Properties as First-Order Satisfiability

Salvador Lucas $^{(\boxtimes)}$ 

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Abstract. Program semantics can often be expressed as a (manysorted) first-order theory S, and program properties as sentences  $\varphi$  which are intended to hold in the *canonical model* of such a theory, which is often incomputable. Recently, we have shown that properties  $\varphi$  expressed as the existential closure of a boolean combination of atoms can be *disproved* by just finding a model of S and the *negation*  $\neg \varphi$  of  $\varphi$ . Furthermore, this idea works quite well in practice due to the existence of powerful tools for the automatic generation of models for (many-sorted) first-order theories. In this paper we extend our previous results to *arbitrary* properties, expressed as sentences without any special restriction. Consequently, one can prove a program property  $\varphi$  by just *finding a model* of an appropriate theory (including S and possibly something else) and an appropriate first-order formula related to  $\varphi$ . Beyond its possible theoretical interest, we show that our results can also be of practical use in several respects.

Keywords: First-order logic · Logical models · Program analysis

#### 1 Introduction

Given a first-order theory S and a sentence  $\varphi$ , finding a model A of  $S \cup \{\neg\varphi\}$ , i.e., such that  $A \models S \cup \{\neg\varphi\}$  holds, shows indeed that  $\varphi$  is *not* a logical consequence of S: there is at least one model of S (e.g., A) which does not satisfy  $\varphi$  (as it satisfies  $\neg\varphi$ ). Provability of  $\varphi$  in S, i.e.,  $S \vdash \varphi$ , implies (by correctness of the proof calculus) that  $\varphi$  is a logical consequence of S (written  $S \models \varphi$ ). Thus,  $A \models S \cup \{\neg\varphi\}$  disproves  $\varphi$  regarding S; this can be written  $\neg(S \vdash \varphi)$  by using some metalevel notation. In general, this does not allow us to conclude that  $\neg\varphi$ is proved, i.e.,  $S \vdash \neg\varphi$ , or is a logical consequence of S, i.e.,  $S \models \neg\varphi$ . What can be concluded about  $\neg\varphi$  regarding S from the fact that  $A \models S \cup \{\neg\varphi\}$  holds? Can this be advantageously used in a 'logic-based' approach to program analysis?

In [14], some answers to these questions are given: a sentence  $\varphi$  which is an *Existentially Closed Boolean Combination of Atoms* (ECBCA for short) does not

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hold in the initial model  $\mathcal{I}_{\mathcal{S}}$  of a theory  $\mathcal{S}$  consisting of a set of ground atoms if we find a model  $\mathcal{A}$  of  $\mathcal{S} \cup \{\neg\varphi\}$  [14, Corollary 2]. This is useful in program analysis when considering programs P that are given a theory  $\overline{P}$  representing its operational semantics so that the execution of P is described as a set  $\mathcal{I}_P$ of (ground) atoms A which can be proved from  $\overline{P}$  (i.e.,  $\mathcal{I}_P$  is the *initial* model of  $\overline{P}$  in the usual first-order sense; in the following, we often refer to it as its *canonical* model [11, Sect. 1.5]). Actually, rather than being logical consequences of  $\overline{P}$ , the intended meaning of first-order sentences  $\varphi$  that represent properties of P is that they hold in the *initial model* of  $\overline{P}$ , see [4, Chap. 4], for instance.

In [14, 16] we applied this approach to prove computational properties of rewriting-based systems in practice. This includes Term Rewriting Systems (TRSs [1]) and more general rewriting-based formalisms [3,9,18,19].

*Example 1.* Consider the following TRS  $\mathcal{R}$  with the well-known rules defining the addition and product of natural numbers in Peano's notation:

$$\operatorname{\mathsf{add}}(0,x) \to x$$
 (1)

$$\operatorname{\mathsf{add}}(\operatorname{\mathsf{s}}(x), y) \to \operatorname{\mathsf{s}}(\operatorname{\mathsf{add}}(x, y))$$
 (2)

$$\mathsf{mul}(\mathbf{0}, x) \to \mathbf{0} \tag{3}$$

$$\operatorname{\mathsf{mul}}(\operatorname{\mathsf{s}}(x), y) \to \operatorname{\mathsf{add}}(y, \operatorname{\mathsf{mul}}(x, y))$$
 (4)

The associated theory  $\overline{\mathcal{R}}$  is the following:

$$\begin{array}{cccc} (\forall x) \ x \rightarrow^* x & (\forall x) \ \mathrm{add}(0, x) \rightarrow x \\ (\forall x, y, z) \ x \rightarrow y \wedge y \rightarrow^* z \Rightarrow x \rightarrow^* z & (\forall x, y) \ \mathrm{add}(\mathsf{s}(x), y) \rightarrow \mathsf{s}(\mathsf{add}(x, y)) \\ (\forall x, y) \ x \rightarrow y \Rightarrow \mathsf{s}(x) \rightarrow \mathsf{s}(y) & (\forall x) \ \mathrm{mul}(0, x) \rightarrow 0 \\ (\forall x, y, z) \ x \rightarrow y \Rightarrow \ \mathrm{add}(x, z) \rightarrow \ \mathrm{add}(y, z) & (\forall x, y) \ \mathrm{mul}(\mathsf{s}(x), y) \rightarrow \mathsf{add}(y, \mathsf{mul}(x, y)) \\ (\forall x, y, z) \ x \rightarrow y \Rightarrow \ \mathrm{add}(z, x) \rightarrow \ \mathrm{add}(z, y) \\ (\forall x, y, z) \ x \rightarrow y \Rightarrow \ \mathrm{mul}(x, z) \rightarrow \ \mathrm{mul}(y, z) \\ (\forall x, y, z) \ x \rightarrow y \Rightarrow \ \mathrm{mul}(z, x) \rightarrow \ \mathrm{mul}(z, y) \end{array}$$

The first sentence in the first column represents *reflexivity* of many-step rewriting, with predicate symbol  $\rightarrow^*$ ; the second sentence shows how one-step rewriting, with predicate symbol  $\rightarrow$ , contributes to  $\rightarrow^*$ . The next sentences describe the *propagation* of rewriting steps to (arguments of) symbols **s**, add and mul. The second column describes the rules of  $\mathcal{R}$ . More details can be found in [14, Sect. 4]. In the initial or *least Herbrand model*  $\mathcal{I}_{\mathcal{R}}$  of  $\overline{\mathcal{R}}$ ,  $\rightarrow$  and  $\rightarrow^*$  are interpreted as the sets  $(\rightarrow)^{\mathcal{I}_{\mathcal{R}}}$  and  $(\rightarrow^*)^{\mathcal{I}_{\mathcal{R}}}$  of all pairs (s, t) of ground terms s and t such that  $s \rightarrow_{\mathcal{R}} t$  and  $s \rightarrow_{\mathcal{R}}^* t$ , respectively. Now, we can express the property "the double of some natural number can be an odd number" as an ECBCA:

$$(\exists x)(\exists y)(\exists z) \text{ add}(x,x) \to^* z \land \mathsf{s}(\mathsf{mul}(\mathsf{s}(\mathsf{s}(\mathsf{0})),y)) \to^* z \tag{5}$$

With the automatic model generator Mace4 [17] we find a model of  $\overline{\mathcal{R}} \cup \{\neg(5)\}$  with domain  $\mathcal{A} = \{0, 1\}$ . Function symbols are interpreted as follows:  $\mathbf{0}^{\mathcal{A}} = 0$ ;

 $s^{\mathcal{A}}(x) = 1 - x$ ; add $^{\mathcal{A}}(x, y)$  returns 0 if x = y and 1 otherwise;  $\mathsf{mul}^{\mathcal{A}}(x, y)$  returns 1 if x = y = 1 and 0 otherwise. Predicates  $\rightarrow$  and  $\rightarrow^*$  are both interpreted as the Aphequality. Thus, we have *proved* that (5) does *not* hold for  $\mathcal{R}$ .

Our approach in [14] relies on the notion of *preservation* of a formula under homomorphisms h between interpretations. Roughly speaking, a homomorphism h preserves a formula  $\varphi$  if  $\varphi$  is satisfied in the target interpretation of h whenever  $\varphi$  is satisfied in its domain interpretation [11, Sect. 2.4]. Homomorphisms preserve ECBCA [11, Theorem 2.4.3(a)]; the results in [14] rely on this fact. In this paper we extend [14] to deal with more general program properties. Homomorphisms preserve other first-order sentences if further requirements are imposed: (i) positive sentences (where connective ' $\neg$ ' is absent) are preserved under surjective homomorphisms and (ii) arbitrary sentences are preserved under embeddings [11, Theorem 2.4.3]. In contrast to [14] (and [11]), here we focus on many-sorted logic [23] (see Sect. 2). This has an important advantage: since homomorphisms in many-sorted logic with set of sorts S are actually a *family*  $h_s$  of homomorphisms between components of sort s for each  $s \in S$ , the preservation requirements for  $h_s$  depend on the specific quantification of variables x : s for such a sort. In Sect. 3 we provide a *unique* preservation theorem that subsumes the results in [14], and even improves [11]. Section 4 investigates how to guarantee surjectivity of homomorphisms. Section 5 shows several application examples taken from Table 1, which shows some properties of rewriting-based systems that could not be captured in [14] but we are able to handle now. Here, t(x) is a term with variables x (or just t if it is ground), C (and D) are the constructor (resp. defined) symbols in the TRS, and  $\xrightarrow{\Lambda}$  is topmost rewriting. Section 6 discusses the possibility of providing more information about *disproved* properties by means of refutation witnesses, i.e., (counter) examples of sentences which are synthesized from the models that are used to disprove the property. Section 7 shows how to deal with completely general sentences by means of a simple example. Section 8 discusses some related work. Section 9 concludes.

Property	$\varphi$
Ground reducible	$(orall x) (\exists y) \ t(x)  ightarrow y$
Completely defined symbol $f$	$(\forall x)(\exists y) \ f(x_1,\ldots,x_k) \to y$
Completely defined TRS	$(\forall \boldsymbol{x})(\exists \boldsymbol{y}) \bigwedge_{f \in \mathcal{D}} f(x_1, \dots, x_{ar(f)}) \to y_f$
Productive	$(\forall x)(\exists y) \bigvee_{c \in \mathcal{C}} x \to^* c(y_1, \dots, y_k)$
Nonterminating	$(\exists x)(\forall n \in \mathbb{N})(\exists y) \ x \to^n y$
Infinitely root-reducible	$(\exists x)(\forall n \in \mathbb{N})(\exists y) \ x(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^n y$
Normalizing term	$(\exists x) \ (t \to^* x \land \neg (\exists y) \ x \to y)$
Normalizing TRS (WN)	$(\forall x)(\exists y) \ (x \to^* y \land \neg(\exists z) \ y \to z)$
Locally confluent (WCR)	$(\forall x, y, z) \ x \to y \land x \to z \Rightarrow (\exists u) \ x \to^* u \land z \to^* u$
Confluent (CR)	$(\forall x, y, z) \ x \to^* y \land x \to^* z \Rightarrow (\exists u) \ x \to^* u \land z \to^* u$

Table 1. Some properties about rewriting-based systems

#### 2 Many-Sorted First-Order Logic

Given a set of sorts S, a (many-sorted) signature (with predicates)  $\Omega = (S, \Sigma, \Pi)$ consists of a set of sorts S, an  $S^* \times S$ -indexed family of sets  $\Sigma = \{\Sigma_{w,s}\}_{(w,s)\in S^*\times S}$ containing function symbols  $f \in \Sigma_{s_1 \cdots s_k, s}$ , with a rank declaration  $f : s_1 \cdots s_k \to$ s (constant symbols c have rank declaration  $c: \lambda \to s$ , where  $\lambda$  denotes the *empty* sequence), and an S<sup>+</sup>-indexed family of sets  $\Pi = {\Pi_w}_{w \in S^+}$  of ranked predicates P: w. Given an S-sorted set  $\mathcal{X} = \{\mathcal{X}_s \mid s \in S\}$  of mutually disjoint sets of variables (which are also disjoint from  $\Sigma$ ), the set  $\mathcal{T}_{\Sigma}(\mathcal{X})_{s}$  of terms of sort s is the least set such that  $\mathcal{X}_s \subseteq \mathcal{T}_{\Sigma}(\mathcal{X})_s$  and for each  $f: s_1 \dots s_k \to s$  and  $t_i \in \mathcal{T}_{\Sigma}(\mathcal{X})_{s_i}, 1 \leq i \leq k, f(t_1, \ldots, t_k) \in \mathcal{T}_{\Sigma}(\mathcal{X})_s$ . If  $\mathcal{X} = \emptyset$ , we write  $\mathcal{T}_{\Sigma}$  rather than  $\mathcal{T}_{\Sigma}(\emptyset)$  for the set of ground terms. The set  $\mathcal{T}_{\Sigma}(\mathcal{X})$  of many-sorted terms is  $\mathcal{T}_{\Sigma}(\mathcal{X}) = \bigcup_{s \in S} \mathcal{T}_{\Sigma}(\mathcal{X})_s$ . For  $w = s_1 \cdots s_n \in S^+$ , we write  $\mathcal{T}_{\Sigma}(\mathcal{X})_w$  rather than  $\mathcal{T}_{\Sigma}(\mathcal{X})_{s_1} \times \cdots \times \mathcal{T}_{\Sigma}(\mathcal{X})_{s_n}$  and even write  $\mathbf{t} \in \mathcal{T}_{\Sigma}(\mathcal{X})_w$  rather than  $t_i \in \mathcal{T}_{\Sigma}(\mathcal{X})_{s_i}$ for each  $1 \leq i \leq n$ . The formulas  $\varphi \in Form_{\Omega}$  of a signature  $\Omega$  are built up from atoms P(t) with  $P \in \Pi_w$  and  $t \in \mathcal{T}_{\Sigma}(\mathcal{X})_w$ , logic connectives  $(\neg, \land, \text{ and also})$  $\lor, \Rightarrow,...$ ) and quantifiers ( $\forall$  and  $\exists$ ) in the usual way. A closed formula, i.e., one whose variables are all universally or existentially quantified, is called a *sentence*. In the following, substitutions  $\sigma$  are assumed to be S-sorted mappings such that for all sorts  $s \in S$ , we have  $\sigma(x) \in \mathcal{T}_{\Sigma}(\mathcal{X})_{s}$ .

An  $\Omega$ -structure  $\mathcal{A}$  consists of (i) a family  $\{\mathcal{A}_s \mid s \in S\}$  of sets called the *carriers* or *domains* together with (ii) a function  $f_{w,s}^{\mathcal{A}} \in \mathcal{A}_w \to \mathcal{A}_s$  for each  $f \in \Sigma_{w,s}$  ( $\mathcal{A}_w$  is a one point set when  $w = \lambda$  and hence  $\mathcal{A}_w \to \mathcal{A}_s$  is isomorphic to  $\mathcal{A}_s$ ), and (iii) an assignment to each  $P \in \Pi_w$  of a subset  $P_w^{\mathcal{A}} \subseteq \mathcal{A}_w$ ; if the identity predicate  $\_=\_:ss$  is in  $\Pi_{ss}$ , then  $(=)_{ss}^{\mathcal{A}} = \{(a,a) \mid a \in \mathcal{A}_s\}$ , i.e.,  $\_=\_:ss$  is interpreted as the identity on  $\mathcal{A}_s$ .

Let  $\mathcal{A}$  and  $\mathcal{A}'$  be  $\Omega$ -structures. An  $\Omega$ -homomorphism  $h: \mathcal{A} \to \mathcal{A}'$  is an Ssorted function  $h = \{h_s: \mathcal{A}_s \to \mathcal{A}'_s \mid s \in S\}$  such that for each  $f \in \Sigma_{w,s}$  and  $P \in \Pi_w$  with  $w = s_1, \ldots, s_k$ , (i)  $h_s(f_{w,s}^{\mathcal{A}}(a_1, \ldots, a_k)) = f_{w,s}^{\mathcal{A}'}(h_{s_1}(a_1), \ldots, h_{s_k}(a_k))$ and (ii) if  $\mathbf{a} \in P_w^{\mathcal{A}}$ , then  $h(\mathbf{a}) \in P_w^{\mathcal{A}'}$ . Given an S-sorted valuation mapping  $\alpha: \mathcal{X} \to \mathcal{A}$ , the evaluation mapping  $[-]_{\mathcal{A}}^{\alpha}: \mathcal{T}_{\Sigma}(\mathcal{X}) \to \mathcal{A}$  is the unique  $(S, \Sigma)$ homomorphism extending  $\alpha$ . Finally,  $[-]_{\mathcal{A}}^{\alpha}: Form_{\Omega} \to Bool$  is given by:

- 1.  $[P(t_1, \ldots, t_n)]^{\alpha}_{\mathcal{A}} = true \text{ (with } P \in \Pi_w) \text{ if and only if } ([t_1]^{\alpha}_{\mathcal{A}}, \ldots, [t_n]^{\alpha}_{\mathcal{A}}) \in P^{\mathcal{A}}_w;$ 2.  $[\neg \varphi]^{\alpha}_{\mathcal{A}} = true \text{ if and only if } [\varphi]^{\alpha}_{\mathcal{A}} = false;$
- 3.  $[\varphi \land \psi]^{\alpha}_{\mathcal{A}} = true$  if and only if  $[\varphi]^{\alpha}_{\mathcal{A}} = true$  and  $[\psi]^{\alpha}_{\mathcal{A}} = true$ ; and
- 4.  $[(\forall x:s) \varphi]^{\alpha}_{\mathcal{A}} = true \text{ if and only if for all } a \in \mathcal{A}_s, [\varphi]^{\alpha[x \mapsto a]}_{\mathcal{A}} = true.$

A valuation  $\alpha \in \mathcal{X} \to \mathcal{A}$  satisfies  $\varphi$  in  $\mathcal{A}$  (written  $\mathcal{A} \models \varphi[\alpha]$ ) if  $[\varphi]^{\alpha}_{\mathcal{A}} = true$ . We then say that  $\varphi$  is satisfiable. If  $\mathcal{A} \models \varphi[\alpha]$  for all valuations  $\alpha$ , we write  $\mathcal{A} \models \varphi$ and say that  $\mathcal{A}$  is a model of  $\varphi$  or that  $\varphi$  is true in  $\mathcal{A}$ . We say that  $\mathcal{A}$  is a model of a set of sentences  $\mathcal{S} \subseteq Form_{\Omega}$  (written  $\mathcal{A} \models \mathcal{S}$ ) if for all  $\varphi \in \mathcal{S}, \mathcal{A} \models \varphi$ . Given a sentence  $\varphi$ , we write  $\mathcal{S} \models \varphi$  iff  $\mathcal{A} \models \varphi$  holds for all models  $\mathcal{A}$  of  $\mathcal{S}$ .

#### **3** Preservation of Many-Sorted First-Order Sentences

Every set S of ground atoms has an *initial model*  $\mathcal{I}_S$  (or just  $\mathcal{I}$  if no confusion arises) which consists of the usual (many-sorted) *Herbrand Domain* of ground terms modulo the equivalence ~ generated by the equations in S. There is a unique homomorphism  $h: \mathcal{I} \to \mathcal{A}$  from  $\mathcal{I}$  to any model  $\mathcal{A}$  of S [9, Sect. 3.2]. In the following, h refers to such a homomorphism. If S contains no equation, then  $\mathcal{I}$  is the (many-sorted) *Least Herbrand Model* of S and  $\mathcal{I}_s$  is  $\mathcal{T}_{\Sigma_S}$  for each sort  $s \in S$ . In the following, we consider sentences in *prenex* form as follows:

$$(Q_1x_1:s_1)\cdots(Q_kx_k:s_k)\bigvee_{i=1}^m\bigwedge_{j=1}^{n_i}L_{ij}$$
(6)

where (i) for all  $1 \leq i \leq m$  and  $1 \leq j \leq n_i$ ,  $L_{ij}$  are *literals*, i.e.,  $L_{ij} = A_{ij}$ or  $L_{ij} = \neg A_{ij}$  for some atom  $A_{ij}$  (in the first case, we say that  $L_{ij}$  is *positive*; otherwise, it is *negative*), (ii)  $x_1, \ldots, x_k$  for some  $k \geq 0$  are the variables occurring in those literals (of sorts  $s_1, \ldots, s_k$ , respectively), and (iii)  $Q_1, \ldots, Q_k$  are universal/existential quantifiers. A sentence  $\varphi$  (equivalent to) (6) is said to be *positive* if all literals are.

**Theorem 1.** Let  $\Omega$  be a signature, S be a set of ground atoms,  $\varphi$  be a sentence (6), and A be a model of S such that (a) for all q,  $1 \leq q \leq k$ , if  $Q_q = \forall$  then  $h_{s_q}$  is surjective<sup>1</sup> and (b) for all negative literals  $L_{ij} = \neg P(\mathbf{t})$ , with  $P \in \Pi_w$ , and substitutions  $\sigma$ , if  $h(\sigma(\mathbf{t})) \in P^A$  then  $\sigma(\mathbf{t}) \in P^T$ . Then,  $\mathcal{I}_S \models \varphi \Longrightarrow \mathcal{A} \models \varphi$ .

In order to achieve condition (b) in Theorem 1, given  $P \in \Pi_w$ , let  $N(P) = \mathcal{I}_w - P^{\mathcal{I}}$  be the complement of the (Herbrand) interpretation of P. Let  $\mathcal{N}(P) = \{\neg P(\mathbf{t}) \mid \mathbf{t} \in N(P)\}$  (cf. Reiter's *Closed World Assumption* [20]). In general,  $\mathcal{N}(P)$  is infinite and incomputable. In some simple cases, though, we can provide a *finite description* of  $\mathcal{N}(P)$  for the required predicates P (see Sect. 7).

**Proposition 1.** Let  $\Omega$  be a signature, S be a set of ground atoms,  $\varphi$  be a sentence (6), A be a model of S, and  $\mathcal{N} = \bigcup_{L_{ij}=\neg P(t)} \mathcal{N}(P)$  be such that  $A \models \mathcal{N}$ . Let  $L_{ij} = \neg P(t)$  be a negative literal and  $\sigma$  be a substitution. If  $h(\sigma(t)) \in P^A$ , then  $\sigma(t) \in P^{\mathcal{I}}$ .

Consider a theory S and let  $S^{\vdash}$  be the set of ground atoms obtained as the *deductive closure* of S, i.e., the set of *atoms*  $P(t_1, \ldots, t_n)$  for each *n*-ary predicate symbol P and ground terms  $t_1, \ldots, t_n$ , such that  $S \vdash P(t_1, \ldots, t_n)$ . The following result is the basis of the practical applications discussed in the following sections.

**Corollary 1** (Semantic criterion). Let  $\Omega$  be a signature,  $S_0$  be a theory,  $S = S_0^{\perp}$ ,  $\varphi$  be a sentence (6), and  $\mathcal{A}$  be a model of  $S_0$  such that (a) for all  $q, 1 \leq q \leq k$ , if  $Q_q = \forall$  then  $h_{s_q}$  is surjective and (b) for all negative literals  $L_{ij} = \neg P(t)$ , with  $P \in \Pi_w$  and substitutions  $\sigma$ , if  $h(\sigma(t)) \in P^{\mathcal{A}}$  then  $\sigma(t) \in P^{\mathcal{I}}$ . If  $\mathcal{A} \models \neg \varphi$ , then  $\mathcal{I}_S \models \neg \varphi$ .

<sup>&</sup>lt;sup>1</sup> A mapping  $f: A \to B$  is *surjective* if for all  $b \in B$  there is  $a \in A$  such that f(a) = b.

In the following, we will not distinguish between theories S and their ground deductive closure  $S^{\vdash}$ ; we rather use S in both cases.

Remark 1 (Proofs by satisfiability). We can prove an arbitrary sentence  $\varphi$  valid in  $\mathcal{I}_{\mathcal{S}}$  by satisfiability in some model  $\mathcal{A}$  of  $\mathcal{S}$ . First define  $\overline{\varphi}$  as the negation  $\neg \varphi$ of  $\varphi$ . Then, find an appropriate structure  $\mathcal{A}$  satisfying (a) and (b) (with regard to  $\overline{\varphi}$ ) and such that  $\mathcal{A} \models \mathcal{S} \cup \{\neg \overline{\varphi}\}$ . By Corollary 1,  $\mathcal{I} \models \neg \overline{\varphi}$  holds. Since  $\neg \overline{\varphi}$  is equivalent to  $\varphi$ ,  $\mathcal{I} \models \varphi$  holds.

Models  $\mathcal{A}$  to be used in Corollary 1 can be automatically generated from the theory  $\mathcal{S}$  and sentence  $\varphi$  by using a tool like AGES [10] or Mace4. In the following section, we investigate how to ensure *surjectivity* when required in Corollary 1.

#### 4 Surjective Homomorphisms

Given  $\Omega = (S, \Sigma, \Pi), s \in S$  and  $T \subseteq \mathcal{T}_{\Sigma s}$ , consider the following sentences:

$$(\forall x:s)\bigvee_{t\in T} x = t \tag{7}$$

$$\bigwedge_{t,u\in T, t\neq u} \neg(t=u) \tag{8}$$

In the following, we write  $(7)_s$  to make sort s referred in (7) explicit. We do the same in similar formulas below.

**Proposition 2.** Let  $\Omega$  be a signature, S be a theory, A be a model of S,  $s \in S$ , and  $T \subseteq \mathcal{T}_{\Sigma_S}$ . (a) If  $T \neq \emptyset$  and  $A \models (7)_s$ , then  $h_s$  is surjective and  $|\mathcal{A}_s| \leq |T|$ . (b) If  $\mathcal{A}_s \neq \emptyset$  and  $A \models (8)$ , then  $|\mathcal{A}_s| \geq |T|$ .

In view of Proposition 2(a), denote  $(7)_s$  as  $\mathsf{SuH}_s^T(\Omega)$  (or just  $\mathsf{SuH}_s^T$  or  $\mathsf{SuH}^T$  if no confusion arises). Whenever T is finite, Proposition 2(a) imposes that the interpretation domain  $\mathcal{A}_s$  for sort s is finite. This is appropriate for tools like **Mace4** which generate structures with finite domains only. However, the *choice* of T in Proposition 2, when used together with a theory  $\mathcal{S}$  imposing further requirements on symbols, can be crucial for Corollary 1 to succeed. Restricting the attention to finite domains can also be a drawback. In the following, we investigate a different approach which avoids any choice of terms T and is valid for infinite structures as well. Consider the following sentence:

$$(\forall x:s)(\exists n:Nat) \ term_s(x,n) \tag{9}$$

where Nat is a new sort, to be interpreted as the set  $\mathbb{N}$  of natural numbers, and  $term_s : s \ Nat$  is a new predicate for each  $s \in S$ . The intended meaning of  $(9)_s$  is

that, for all  $x \in \mathcal{A}_s$ , there is  $t \in \mathcal{T}_{\Sigma s}$  of height at most n such that  $x = t^{\mathcal{A}}$ . We substantiate this, for each sort  $s \in S$ , by means of two (families of) formulas:

$$(\forall x:s)(\forall n:Nat) term_s(x,0) \Rightarrow \bigvee_{c \in \Sigma_{\lambda,s}} x = c$$
 (10)

$$(\forall x:s)(\forall n: Nat)(\exists m: Nat) \ (n > 0 \land term_s(x, n)) \Rightarrow$$

$$n > m \land \left( term_s(x, m) \lor \bigvee_{\substack{f \in \Sigma_{w,s} \\ w \in S^+}} (\exists y: w) \left( x = f(y) \land \bigwedge_{s_i \in w} term_{s_i}(y_i, m) \right) \right)$$

$$(11)$$

Thus, by  $(10)_s$ , values x satisfying  $term_s(x,0)$  will be represented by some constant symbol c of sort s. Similarly, by  $(11)_s$ , values x satisfying  $term_s(x,n)$  for some n > 0 will be represented by some ground term s of height m for some m < n, or by a term  $t = f(t_1, \ldots, t_k)$ , where f has rank  $w \to s$  for some  $w \in S^+$  and  $t_1, \ldots, t_k$  have height m at most.

The set K(s) of *s*-relevant sorts is the least set satisfying: (i)  $s \in K(s)$  and (ii) if  $f \in \Sigma_{s_1 \dots s_k, s'}$  and  $s' \in K(s)$ , then  $\{s_1, \dots, s_n\} \subseteq K(s)$ . Let  $\Omega_{Nat,s} = (S_{Nat}, \Sigma_{Nat}, \Pi_{Nat,K(s)})$  be an extension of  $\Omega$  where  $S_{Nat} = S \cup \{Nat\}, \Sigma_{Nat}$ extends  $\Sigma$  with a new constant  $0 : \lambda \to Nat$ , and  $\Pi_{Nat,K(s)}$  extends  $\Pi$  with > : Nat Nat and a predicate  $term_{s'} : s' Nat$  for each  $s' \in K(s)$ . We let

$$\mathsf{SuH}_s = \{(9)_{s'}, (10)_{s'}, (11)_{s'} \mid s' \in K(s)\}$$
(12)

**Proposition 3.** Let  $\Omega$  be a signature, S be a theory,  $s \in S$ , and A be an  $\Omega_{Nat,s}$ structure which is a model of S. Assume that  $A_{Nat} = \mathbb{N}$ ,  $0^{\mathcal{A}} = 0$ , and  $\mathfrak{m} >^{\mathcal{A}}$   $\mathfrak{n} \Leftrightarrow \mathfrak{m} >_{\mathbb{N}} \mathfrak{n}$  for all  $\mathfrak{m}, \mathfrak{n} \in \mathcal{A}_{Nat}$ . If  $A \models SuH_s$ , then  $h_{s'}$  is surjective for all  $s' \in K(s)$ .

Given an extension  $\Omega'$  of a signature  $\Omega$ , every  $\Omega'$ -structure  $\mathcal{A}'$  defines an  $\Omega$ structure  $\mathcal{A}$ : just take  $\mathcal{A}_s = \mathcal{A}'_s$  for all  $s \in S$ , and then  $f_{w,s}^{\mathcal{A}} = f_{w,s}^{\mathcal{A}'}$  and  $P_w^{\mathcal{A}} = P_w^{\mathcal{A}'}$ for all  $w \in S^*$ ,  $s \in S$ ,  $f \in \Sigma_{w,s}$ , and  $P \in \Pi_w$ . Thus, Proposition 3 is used to
guarantee surjectivity of  $h: \mathcal{T}_{\Sigma s'} \to \mathcal{A}_{s'}$ , rather than  $h: \mathcal{T}_{\Sigma_{Nat}s'} \to \mathcal{A}_{s'}$ .

#### 5 Examples of Application with Positive Sentences

In this section we exemplify the use of Corollary 1 together with the approach in Sect. 4 to deal with *positive* sentences (6), i.e., all literals are positive.

#### 5.1 Complete Definedness and Commutativity

Consider the following Maude specification (hopefully self-explained, but see [5]) for the arithmetic operations in Example 1 together with function head, which returns the head of a list of natural numbers:

```
mod ExAddMulHead is
sorts N LN . *** Sorts for numbers and lists of numbers
op Z : -> N . op suc : N -> N . ops add mul : N N -> N .
op head : LN -> N . op nil : -> LN . op cons : N LN -> LN .
vars x y : N . var xs : LN .
rl add(Z,x) => x . rl add(suc(x),y) => suc(add(x,y)) .
rl mul(Z,x) => Z . rl mul(suc(x),y) => add(y,mul(x,y)) .
rl head(cons(x,xs)) => x .
endm
```

(1) Complete definedness. We claim head to be completely defined as follows:

$$(\forall xs: LN)(\exists x: N) \text{ head}(xs) \to x$$
 (13)

We disprove (13) by using Corollary 1. Due to the universal quantification of xs in (13), we need to ensure that  $h_{LN} : \mathcal{T}_{\Sigma LN} \to \mathcal{A}_{LN}$  is surjective for any structure  $\mathcal{A}$  we may use. We use Proposition 3. Since  $K(LN) = \{N, LN\}$  due to cons, whose first argument is of sort N,  $SuH_{LN}$  consists of the following sentences:

$$\begin{array}{ll} (\forall x: \mathbb{N})(\exists n: Nat) \ term_{\mathbb{N}}(x, n) & (9)_{\mathbb{N}} \\ (\forall x: \mathbb{N}) \ term_{\mathbb{N}}(x, 0) \Rightarrow x = \mathbb{Z} & (10)_{\mathbb{N}} \\ (\forall x: \mathbb{N})(\forall n: Nat)(\exists m: Nat)(\exists y: \mathbb{N})(\exists z: \mathbb{N})(\exists ys: \mathbb{L}\mathbb{N}) & (11)_{\mathbb{N}} \\ n > 0 \land term_{\mathbb{N}}(x, n) \Rightarrow n > m \land [term_{\mathbb{N}}(x, m) \lor \\ (term_{\mathbb{N}}(y, m) \land term_{\mathbb{N}}(z, m) \land term_{\mathbb{L}\mathbb{N}}(ys, m) \land \\ (x = \operatorname{suc}(y) \lor x = \operatorname{add}(y, z) \lor x = \operatorname{mul}(y, z) \lor x = \operatorname{head}(ys)))] \\ (\forall xs: \mathbb{L}\mathbb{N})(\exists n: Nat) \ term_{\mathbb{L}\mathbb{N}}(xs, n) & (9)_{\mathbb{L}\mathbb{N}} \\ (\forall xs: \mathbb{L}\mathbb{N}) \ term_{\mathbb{L}\mathbb{N}}(xs, 0) \Rightarrow xs = \operatorname{nil} & (10)_{\mathbb{L}\mathbb{N}} \\ (\forall xs: \mathbb{L}\mathbb{N})(\forall n: Nat)(\exists m: Nat)(\exists y: \mathbb{N})(\exists ys: \mathbb{L}\mathbb{N}) & (11)_{\mathbb{L}\mathbb{N}} \\ n > 0 \land term_{\mathbb{N}}(x, n) \Rightarrow n > m \land [term_{\mathbb{N}}(x, m) \lor \\ (term_{\mathbb{N}}(y, m) \land term_{\mathbb{L}\mathbb{N}}(ys, m) \land xs = \operatorname{cons}(y, ys))] \end{array}$$

We obtain a model  $\mathcal{A}$  of  $\overline{\mathsf{ExAddMulHead}} \cup \mathsf{SuH}_{LN} \cup \{\neg(13)\}$  with AGES. Sorts are interpreted as follows:  $\mathcal{A}_{N} = \mathcal{A}_{LN} = \{-1, 0\}$  and  $\mathcal{A}_{Nat} = \mathbb{N}$ . For function symbols:

For predicates,  $x \to_{\mathbb{N}}^{\mathcal{A}} y \Leftrightarrow x \ge y \land x \ge 0$ ,  $x \to_{\mathbb{LN}}^{\mathcal{A}} y \Leftrightarrow x = y = -1$ , and both  $x(\to_{\mathbb{N}}^*)^{\mathcal{A}}y$  and  $x(\to_{\mathbb{LN}}^*)^{\mathcal{A}}y$  are *true*. We can check surjectivity of  $h_s: \mathcal{T}_{\Sigma_s} \to \mathcal{A}_s$  (for  $s \in \{\mathbb{N}, \mathbb{LN}\}$ ). For instance, we have:

$$\begin{split} [\mathtt{Z}]_{\mathcal{A}} &= -1 \quad [\mathtt{add}(\mathtt{Z},\mathtt{Z})]_{\mathcal{A}} = 0 \quad \text{for sort } \mathtt{N} \\ [\mathtt{cons}(\mathtt{Z},\mathtt{nil})]_{\mathcal{A}} &= -1 \quad [\mathtt{nil}]_{\mathcal{A}} = 0 \quad \text{for sort } \mathtt{LN} \end{split}$$

(2) Commutativity. It is well-known that both add and mul as defined by the rules of  $\mathcal{R}$  in Example 1 are commutative on ground terms, i.e., for all ground terms s and t,  $\mathsf{add}(s,t) =_{\mathcal{R}} \mathsf{add}(t,s)$  and  $\mathsf{mul}(s,t) =_{\mathcal{R}} \mathsf{mul}(t,s)$ , where  $=_{\mathcal{R}}$  is the equational theory induced by the rules  $\ell \to r$  in  $\mathcal{R}$  treated as equations  $\ell = r$ . Actually, by using Birkhoff's theorem and the fact that  $\mathcal{R}$  is confluent, we can rephrase commutativity of add as joinability as follows:

$$(\forall x)(\forall y)(\exists z) \; \mathsf{add}(x,y) \to^* z \land \mathsf{add}(y,x) \to^* z$$
 (14)

Remark 2. Proving commutativity of add and mul when defined by  $\mathcal{R}$  in Example 1 by using Corollary 1 is possible (see Remark 1) but unlikely. We should first define  $\overline{\varphi}$  as  $\neg(14)$ , i.e.,  $\overline{\varphi}$  is

$$(\exists x)(\exists y)(\forall z) \neg (\mathsf{add}(x, y) \to^* z) \lor \neg (\mathsf{add}(y, x) \to^* z)$$
(15)

Since (15) contains two negative literals, Corollary 1 requires the use of  $\mathcal{N}(\to^*)$ .

Since head is not completely defined, add and mul are *not* commutative in ExAddMulHead. We prove this fact by disproving the sorted version of (14), i.e.,

$$(\forall x: \mathbb{N})(\forall y: \mathbb{N})(\exists z: \mathbb{N}) \text{ add}(x, y) \to^* z \land \text{ add}(y, x) \to^* z$$
(16)

Due to the universal quantification of x and y in (16), we need to ensure that  $h_{\mathbb{N}} : \mathcal{T}_{\Sigma_{\mathbb{N}}} \to \mathcal{A}_{\mathbb{N}}$  is surjective. Since  $K(\mathbb{N}) = \{\mathbb{N}, \mathbb{LN}\}$  due to head, we have  $\mathsf{SuH}_{\mathbb{N}} = \mathsf{SuH}_{\mathbb{LN}}$  as above. AGES obtain a model  $\mathcal{A}$  of ExAddMulHead  $\cup \mathsf{SuH}_{\mathbb{N}} \cup \{\neg(16)\}$  as follows:  $\mathcal{A}_{\mathbb{N}} = \{0, 1\}, \mathcal{A}_{\mathbb{LN}} = \{-1, 0\}$  and  $\mathcal{A}_{Nat} = \mathbb{N}$ . Also,

$$\begin{array}{cccc} \mathbf{Z}^{\mathcal{A}} = 1 & \mathsf{nil}^{\mathcal{A}} = -1 & \mathsf{suc}^{\mathcal{A}}(x) = x & \mathsf{add}^{\mathcal{A}}(x,y) = y \\ \mathsf{mul}^{\mathcal{A}}(x,y) = x & \mathsf{cons}^{\mathcal{A}}(x,xs) = x - 1 & \mathsf{head}^{\mathcal{A}}(xs) = xs + 1 \\ x \to_{\mathbb{N}}^{\mathcal{A}} y \Leftrightarrow x = y & x(\to_{\mathbb{N}}^{*})^{\mathcal{A}} y \Leftrightarrow x = y & x \to_{\mathbb{LN}}^{\mathcal{A}} y \Leftrightarrow x = y & x(\to_{\mathbb{LN}}^{*})^{\mathcal{A}} y \Leftrightarrow true \end{array}$$

#### 5.2 Top-Termination

A TRS  $\mathcal{R}$  is *top-terminating* if no infinitary reduction sequence performs infinitely many rewrites at topmost position  $\Lambda$  [7]. From a computational point of view, top-termination is important in the semantic description of lazy languages as it is an important ingredient to guarantee that every initial expression has an infinite normal form [7,8]. Accordingly, given a *dummy* sort S, the *negation* of

$$(\exists x: \mathbf{S})(\forall n \in \mathbb{N})(\exists y: \mathbf{S}) \ x(\to^* \circ \stackrel{\Lambda}{\to})^n y \tag{17}$$

(which claims for the existence of a term with infinitely many rewriting steps at top) captures top-termination. We introduce a new predicate  $\rightarrow_{\star,\Lambda}$  for the composition  $\rightarrow^* \circ \xrightarrow{\Lambda}$  of the many-step rewriting relation  $\rightarrow^*$  (defined as usual, i.e., by the whole theory  $\overline{\mathcal{R}}$  associated to  $\mathcal{R}$ ) and topmost rewriting  $\xrightarrow{\Lambda}$  defined by a theory  $\mathcal{R}_{\Lambda} = \{ (\forall \boldsymbol{x} : \mathbf{S}) \ \ell \xrightarrow{\Lambda} r \mid \ell \to r \in \mathcal{R} \}$ . Sequences  $s \to_{\star,\Lambda}^{n} t$  meaning that  $s \to_{\star,\Lambda}$ -reduces into t in  $n+1 \to_{\star,\Lambda}$ -steps are defined as follows:

$$(\forall x, y, z: \mathbf{S}) \ x \to^* y \land y \xrightarrow{\Lambda} z \Rightarrow x \to^0_{\star,\Lambda} z$$
(18)

$$(\forall x, y, z: \mathbf{S})(\forall n \in \mathbb{N}) \ x \to_{\star,\Lambda}^{0} y \land y \to_{\star,\Lambda}^{n} z \Rightarrow x \to_{\star,\Lambda}^{n+1} z$$
(19)

Overall, the sentence  $\varphi$  to be disproved is:

$$(\exists x: \mathbf{S})(\forall n: Nat)(\exists y: \mathbf{S}) \ x \to_{\star,\Lambda}^{n} y$$
(20)

Remark 3. We use  $\mathbb{N}$  in (17) but Nat in (20). Indeed, (17) is not a valid sentence because  $\mathbb{N}$  is not first-order axiomatizable, see, e.g. [11, Sect. 2.2]. This is consistent with the well-known fact that termination (or top-termination) cannot be encoded in first-order logic [22, Sect. 5.1.4]. We can use (20) together with Corollary 1 provided that Nat is interpreted as  $\mathbb{N}$ . This is possible with AGES.

*Example 2.* Consider the following (nonterminating) TRS  $\mathcal{R}$  [8, Sect. 9.5]:

$$\operatorname{non} \to f(g, f(\operatorname{non}, g))$$
 (21)

$$g \rightarrow a$$
 (22)

$$f(a, x) \to a \tag{23}$$

$$\mathbf{f}(\mathbf{b}, \mathbf{b}) \to \mathbf{b} \tag{24}$$

$$f(b,a) \rightarrow b$$
 (25)

The associated theory  $\mathcal{R}_{topT}$  is  $\mathcal{R}_{topT} = \overline{\mathcal{R}} \cup \mathcal{R}_{\Lambda} \cup \{(18), (19)\}$ , where  $\mathcal{R}_{\Lambda}$  is

$$\operatorname{non} \stackrel{\Lambda}{\to} f(g, f(\operatorname{non}, g)) \tag{26}$$

$$g \xrightarrow{\Lambda} a$$
 (27)

$$(\forall x: \mathbf{S}) \ \mathbf{f}(\mathbf{b}, x) \xrightarrow{\Lambda} \mathbf{b}$$
 (28)

$$f(b,b) \xrightarrow{\Lambda} b \tag{29}$$

$$\mathbf{f}(\mathbf{b}, \mathbf{a}) \xrightarrow{\Lambda} \mathbf{b} \tag{30}$$

Note that (20) only requires that the homomorphism mapping terms of sort *Nat* to  $\mathbb{N}$  is surjective, which is automatically achieved by AGES. The structure  $\mathcal{A}$  with  $\mathcal{A}_{s} = \{-1, 0, 1\}$ ,  $\mathcal{A}_{Nat} = \mathbb{N}$ , function symbols interpreted by:  $\mathbf{a}^{\mathcal{A}} = 1$ ,  $\mathbf{b}^{\mathcal{A}} = 1$ ,  $\mathbf{g}^{\mathcal{A}} = 0$ ,  $\operatorname{non}^{\mathcal{A}} = -1$ , and  $\mathbf{f}^{\mathcal{A}}(x) = 0$ ; and predicate symbols as follows:

$$\begin{array}{ll} x \rightarrow^{\mathcal{A}} y \Leftrightarrow y \geq x \wedge x + y \geq -1 & \quad x(\rightarrow^{*})^{\mathcal{A}} y \Leftrightarrow y \geq x \\ x(\stackrel{\Lambda}{\rightarrow})^{\mathcal{A}} y \Leftrightarrow y > x & \quad x(\rightarrow^{n}_{\star,\Lambda})^{\mathcal{A}} y \Leftrightarrow y > x + n \end{array}$$

is a model of  $\mathcal{R}_{topT} \cup \{\neg(20)\}$  and proves top-termination of  $\mathcal{R}$ .

#### 6 Refutation Witnesses

In logic, a witness for an existentially quantified sentence  $(\exists x)\varphi(x)$  is a specific value b to be substituted by x in  $\varphi(x)$  so that  $\varphi(b)$  is true (see, e.g., [2, p. 81]). Similarly, we can think of a value b such that  $\neg\varphi(b)$  holds as a witness of  $(\exists x)\neg\varphi(x)$  or as a refutation witness for  $(\forall x)\varphi(x)$ ; we can also think of b as a counterexample to  $(\forall x)\varphi(x)$  [13, p. 284]. Note, however, that witnesses that are given as values b belonging to an interpretation domain  $\mathcal{A}$  can be meaningless for the user who is acquainted with the first-order language  $\Omega$  but not so much with abstract values from  $\mathcal{A}$  (which is often automatically synthesized by using some tool). Users can be happier to deal with terms t which are somehow connected to witnesses b by a homomorphism, so that  $t^{\mathcal{A}} = b$ . Corollary 1 permits a refutation of  $\varphi$  by finding a model  $\mathcal{A}$  of  $\neg\varphi$  to conclude that  $\mathcal{I} \models \neg\varphi$ . We want to obtain instances of  $\varphi$  to better understand unsatisfiability of  $\varphi$ . In this section we investigate this problem.

The negation  $\neg(6)$  of (6), i.e., of  $(Q_1x_1:s_1)\cdots(Q_kx_k:s_k)\bigvee_{i=1}^m\bigwedge_{j=1}^{n_i}L_{ij}$  is

$$(\overline{Q}_1 x_1 : s_1) \cdots (\overline{Q}_k x_k : s_k) \bigwedge_{i=1}^m \bigvee_{j=1}^{n_i} \neg L_{ij}(x_1, \dots, x_k)$$
(31)

where  $\overline{Q}_i$  is  $\forall$  whenever  $Q_i$  is  $\exists$  and  $\overline{Q}_i$  is  $\exists$  whenever  $Q_i$  is  $\forall$ . We assume  $\eta \leq k$ universal quantifiers in (31) with indices  $U = \{v_1, \ldots, v_\eta\} \subseteq \{1, \ldots, k\}$  and hence  $k - \eta$  existential quantifiers with indices  $E = \{\epsilon_1, \ldots, \epsilon_{k-\eta}\} = \{1, \ldots, k\} - U$ . In the following  $\overline{\eta}$  denotes  $k - \eta$ . For each  $\epsilon \in E$ , we let  $U_{\epsilon} = \{v \in U \mid v < \epsilon\}$  be the (possibly empty) set of indices of universally quantified variables in (31) occurring before  $x_{\epsilon}$  in the quantification prefix of (31). Let  $\eta_{\epsilon} = |U_{\epsilon}|$ . Note that  $U_{\epsilon_1} \subseteq U_{\epsilon_2} \subseteq \cdots \subseteq U_{\epsilon_{\overline{\eta}}}$ . Let  $U_{\exists}$  be the set of indices of universally quantified variables occurring before some existentially quantified variable in the quantification prefix of (31). Note that  $U_{\exists}$  is empty whenever  $v_1 > \epsilon_{k-\eta}$ (no existential quantification after a universal quantification); otherwise,  $U_{\exists} = \{v_1, \ldots, v_{\exists}\}$  for some  $v_{\exists} \leq v_{\eta}$ . Accordingly,  $U_{\forall} = U - U_{\exists} = \{\epsilon_{\overline{\eta}} + 1, \ldots, k\}$  is the set of indices of universally quantified variables occurring after all existentially quantified variables in the quantification prefix of (31). Note that  $U_{\forall}$  is empty whenever  $\epsilon_1 > v_{\eta}$  (no universal quantification after an existential quantification).

Most theorem provers transform sentences into universally quantified formulas by Skolemization (see, e.g., [12]). Thus, if  $k > \eta$ , i.e., (31) contains existential quantifiers, we need to introduce *Skolem function symbols*  $sk_{\epsilon} : w_{\epsilon} \to s_{\epsilon}$  for each  $\epsilon \in E$ , where  $w_{\epsilon}$  is the (possibly empty) sequence of  $\eta_{\epsilon}$  sorts indexed by  $U_{\epsilon}$ . Note that  $sk_{\epsilon}$  is a *constant* if  $\eta_{\epsilon} = 0$ . The *Skolem normal form* of (31) is

$$(\forall x_{\upsilon_1} : s_{\upsilon_1}) \cdots (\forall x_{\upsilon_\eta} : s_{\upsilon_\eta}) \bigwedge_{i=1}^m \bigvee_{j=1}^{n_i} \neg L_{ij}(e_1, \dots, e_k)$$
(32)

where for all  $1 \leq q \leq k$ , (i)  $e_q \equiv x_q$  if  $q \in U$  and (ii)  $e_q \equiv sk_q(\boldsymbol{x}_{\eta_q})$  if  $q \in E$ , where  $\boldsymbol{x}_{\eta_q}$  is the sequence of variables  $x_{\nu_1}, \ldots, x_{\nu_{\eta_q}}$ . If  $E \neq \emptyset$  (i.e., (31) and (32) differ), then (32) is a sentence of an *extended* signature  $\Omega^{sk} = (S, \Sigma^{sk}, \Pi)$  where  $\Sigma^{sk}$  extends  $\Sigma$  with skolem functions. Since (32) logically implies (31) [2, Sect. 19.2], every model  $\mathcal{A}$  of (33) is a model of (32) as well.

**Definition 1 (Set of refutation witnesses).** Using the notation developed in the previous paragraphs, let  $\mathcal{A}$  be an  $\Omega^{sk}$ -structure such that  $h_{sq}$  is surjective for all  $q \in U_{\exists} \cup E$ . The  $\Omega^{sk}$ -sentence (32) is given a set of refutation witnesses  $\Phi$  consisting of  $\Omega$ -sentences  $\phi_{\alpha}$  for each valuation  $\alpha$  of the variables  $x_{v_1}, \ldots, x_{v_{\exists}}$ indexed by  $U_{\exists}$ ; each  $\phi_{\alpha}$  is (nondeterministically) defined as follows:

$$(\forall x_{\epsilon_{\overline{\eta}}+1} : s_{\epsilon_{\overline{\eta}}+1}) \cdots (\forall x_k : s_k) \bigwedge_{i=1}^m \bigvee_{j=1}^{n_i} \neg L_{ij}(e'_1, \dots, e'_k)$$
(33)

where for all  $1 \leq q \leq k$ , (i)  $e'_q \equiv x_q$  if  $q \in U_{\forall}$  and (ii)  $e'_q \equiv t$  if  $q \in U_{\exists} \cup E$  and  $t \in \mathcal{T}_{\Sigma s_q}$  is such that  $[t]_{\mathcal{A}} = [e_q]^{\alpha}_{\mathcal{A}}$ .

Note that, in Definition 1 we could emphfail to find the necessary terms  $t \in \mathcal{T}_{\Sigma s_q}$  if  $h_{s_q}$  is not surjective. Note also that, whenever E is empty,  $\Phi$  is a singleton consisting of (33) which coincides with (32). We have the following:

**Proposition 4.** For every  $\Omega^{sk}$ -structure  $\mathcal{A}, \mathcal{A} \models (33)$  if and only if  $\mathcal{A} \models \Phi$ .

Refutation witnesses are built from symbols in the original signature  $\Omega$  only. We can use them as more intuitive *counterexamples* to the refuted property  $\varphi$ .

**Proposition 5.** Let  $\Omega$  be a signature, S be a theory,  $\varphi$  be a sentence (6), and  $\mathcal{A}$  be a model of S such that for all negative literals  $L_{ij} = \neg P(\mathbf{t})$  with  $P \in \Pi_w$  and substitutions  $\sigma$ , if  $h(\sigma(\mathbf{t})) \in P^{\mathcal{A}}$  then  $\sigma(\mathbf{t}) \in P^{\mathcal{I}}$ . For all  $\phi \in \Phi$ ,  $\mathcal{I} \models \phi$ .

**Corollary 2.** If (6) is positive, then for all refutation witnesses  $\phi \in \Phi$ ,  $\mathcal{I} \models \phi$ .

*Example 3.* Consider ExAddMulHead in Sect. 5. The *refutation* of (13) using AGES actually proceeds by skolemization of the negation of (13), i.e., of

$$(\exists xs: \texttt{LN})(\forall x: \texttt{N}) \neg(\texttt{head}(xs) \rightarrow x)$$
(34)

With regard to (34), we have  $E = \{1\}$ ,  $U_{\exists} = \emptyset$  and  $U_{\forall} = \{2\}$ , where 1 and 2 refer to variables xs and x, respectively. Accordingly,  $v_{\exists} = 0$ . The only sort involved in the variables indexed by  $U_{\exists} \cup E$  is LN. Since variables of sort LN are universally quantified in (13), the application of Corollary 1 in Sect. 5 already required surjectivity of  $h_{\text{LN}}$ . The *Skolem normal form* of (34) is:

$$(\forall x: \mathbb{N}) \neg (\texttt{head}(\mathsf{sk}_{xs}) \to x) \tag{35}$$

where  $\mathsf{sk}_{xs}$  is a new constant of sort LN. The structure  $\mathcal{A}$  computed by AGES is actually a model of  $\overline{\mathcal{R}} \cup \mathsf{SuH}_{\mathsf{LN}} \cup \{(36)\}$ , for  $\mathsf{SuH}_{\mathsf{LN}}$  in Sect. 5. For  $\mathsf{sk}_{xs}$ , we have  $\mathsf{sk}_{xs}^{\mathcal{A}} = 0$ . There is a single (empty) valuation  $\alpha$  of variables indexed by  $U_{\exists}$  (which is empty). Hence,  $\Phi = \{\phi_{\alpha}\}$  is a singleton. According to Definition 1, since  $[\mathtt{nil}]_{\mathcal{A}} = 0 = [\mathtt{sk}_{xs}]_{\mathcal{A}}$ , the following sentence could be associated to the refutation witness  $\phi_{\alpha}$ :  $(\forall x : \mathbb{N}) \neg (\mathtt{head}(\mathtt{nil}) \rightarrow x)$ . *Example 4.* With regard to the computation of refutation witnesses for  $\mathcal{R}$  in Example 2, we start with the negation of (17), i.e.,

$$(\forall x: \mathbf{S})(\exists n: Nat)(\forall y: \mathbf{S}) \neg (x(\rightarrow^* \circ \xrightarrow{\Lambda})^n y)$$
(36)

We have  $E = \{2\}, U_{\exists} = \{1\}$  and  $U_{\forall} = \{3\}$ . The Skolem normal form of (36) is

$$(\forall x: \mathbf{S})(\forall y: \mathbf{S}) \neg (x(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^{\mathsf{sk}_n(x)}y)$$
(37)

where  $\mathsf{sk}_n : \mathsf{S} \to Nat$  is a new (monadic) function symbol. Since the sorts for variables indexed by  $U_\exists \cup E$  are  $\mathsf{S}$  and Nat, we require surjectivity of  $h_{\mathsf{S}}$  and  $h_{Nat}$ . This is achieved by using  $\mathsf{SuH}_{\mathsf{S}}$  and interpreting Nat as  $\mathbb{N}$  as done in AGES. The structure  $\mathcal{A}$  in Example 2 is a model of  $\mathcal{R}_{top T} \cup \mathsf{SuH}_{\mathsf{S}} \cup \{(38)\}$ . The interpretation obtained for  $\mathsf{sk}_n$  is

$$\mathsf{sk}_n^{\mathcal{A}}(x) = 1 - x$$

Now we can compute refutation witnesses for (37). Since  $U_{\epsilon} = \{1\}$  is a singleton whose index refers to a variable x of sort S and  $\mathcal{A}_{S} = \{-1, 0, 1\}$ , we have to deal with three valuation functions for the only variable x to be considered:

$$\alpha_{-1}(x) = -1$$
  $\alpha_0(x) = 0$   $\alpha_1(x) = 1$ 

We have  $\Phi = \{\phi_{\alpha_{-1}}, \phi_{\alpha_0}, \phi_{\alpha_1}\}$ , where  $\phi_{\alpha_{-1}}$  is  $(\forall y : \mathbf{S}) \neg (\mathsf{non}(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^2 y), \phi_{\alpha_0}$  is  $(\forall y : \mathbf{S}) \neg (\mathbf{g}(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^1 y)$ , and  $\phi_{\alpha_1}$  is  $(\forall y : \mathbf{S}) \neg (\mathbf{a}(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^0 y)$ . Note that, since  $\mathbf{f}^{\mathcal{A}}(x) = 0$ , we could also write  $\phi_{\alpha_0}$  as  $(\forall y : \mathbf{S}) \neg (\mathbf{f}(t)(\rightarrow^* \circ \stackrel{\Lambda}{\rightarrow})^1 y)$  for every ground term t. This gives additional, complementary information.

#### 7 Example of Application with General Sentences

Consider a well-known example of a *locally confluent* but *nonconfluent* TRS  $\mathcal{R}$ :

 $b \to \mathsf{a} \qquad b \to \mathsf{c} \qquad \mathsf{c} \to \mathsf{b} \qquad \mathsf{c} \to \mathsf{d}$ 

Example 5 (Local confluence of  $\mathcal{R}$ ). Local confluence corresponds to  $\varphi_{WCR}$  in Table 1. As explained in Remark 1, we start with  $\overline{\varphi}_{WCR} = \neg \varphi_{WCR}$  i.e.,

$$(\exists x, y, z: \mathbf{S})(\forall u: \mathbf{S}) \ (x \to y \land x \to z \land \neg(x \to^* u)) \lor (x \to y \land x \to z \land \neg(z \to^* u))$$
(38)

Due to the universal quantifier,  $h_{s} : \mathcal{T}_{\Sigma s} \to \mathcal{A}_{s}$  must be surjective. We can achieve this by adding the following sentence  $\mathsf{SuH}_{s}^{T}$  for  $T = \{\mathsf{a}, \mathsf{b}, \mathsf{c}, \mathsf{d}\}$ :

$$(\forall x: \mathbf{S}) \ x = \mathbf{a} \lor x = \mathbf{b} \lor x = \mathbf{c} \lor x = \mathbf{d}$$
(39)

Due to the negative literals  $\neg(x \to^* u)$  and  $\neg(z \to^* u)$ , we consider  $\mathcal{N}$ , representing the forbidden many-step rewriting steps, explicitly given by:

$$\mathcal{N} = \{ \neg (\mathsf{a} \to^* \mathsf{b}), \neg (\mathsf{a} \to^* \mathsf{c}), \neg (\mathsf{a} \to^* \mathsf{d}), \neg (\mathsf{d} \to^* \mathsf{a}), \neg (\mathsf{d} \to^* \mathsf{b}), \neg (\mathsf{d} \to^* \mathsf{c}) \}$$

We apply Corollary 1 to prove that  $\neg \overline{\varphi}_{WCR}$  (i.e.,  $\varphi_{WCR}$ ) holds by obtaining a model of  $\overline{\mathcal{R}} \cup \mathsf{SuH}_{\mathsf{S}}^T \cup \mathcal{N} \cup \{\varphi_{WCR}\}$  with Mace4.<sup>2</sup> The structure has domain  $\mathcal{A}_{\mathsf{S}} = \{0, 1, 2, 3\}$ ; constants are interpreted as follows:  $\mathsf{a}^{\mathcal{A}} = 0$ ,  $\mathsf{b}^{\mathcal{A}} = 1$ ,  $\mathsf{c}^{\mathcal{A}} = 3$ , and  $\mathsf{d}^{\mathcal{A}} = 2$ . With regard to predicate symbols, we have:

$$x \to^{\mathcal{A}} y = \{(1,0), (1,3), (3,1), (3,2)\} \quad x(\to^*)^{\mathcal{A}} y = \{(1,x), (3,x) \mid x \in \mathcal{A}_{\mathsf{S}}\}$$

This proves  $\mathcal{R}$  locally confluent.

Example 6 (Nonconfluence of  $\mathcal{R}$ ). In order to disprove confluence of  $\mathcal{R}$ , which is represented by  $\varphi_{CR}$  in Table 1, we first write  $\varphi_{CR}$  in the form (6), i.e.,

$$(\forall x, y, z: \mathbf{S})(\exists u: \mathbf{S}) \neg (x \to^* y) \lor \neg (x \to^* z) \lor (y \to^* u \land z \to^* u)$$
(40)

Due to the universal quantification and negative literals, we use  $\mathsf{SuH}_{\mathsf{S}}^T$  and  $\mathcal{N}$  as in Example 5. We obtain a model  $\mathcal{A}$  of  $\overline{\mathcal{R}} \cup \mathsf{SuH}_{\mathsf{S}}^T \cup \mathcal{N} \cup \{\neg \varphi_{CR}\}$  with Mace4. The domain is  $\mathcal{A}_{\mathsf{S}} = \{0, 1, 2\}$  and symbols are interpreted by:  $\mathsf{a}^{\mathcal{A}} = 0$ ,  $\mathsf{b}^{\mathcal{A}} = \mathsf{c}^{\mathcal{A}} = 1$ ,  $\mathsf{d}^{\mathcal{A}} = 2$ ,  $x \to^{\mathcal{A}} y \Leftrightarrow x = 1$ , and  $x(\to^*)^{\mathcal{A}} y \Leftrightarrow x = y \lor x = 1$ . This proves nonconfluence of  $\mathcal{R}$ . With regard to the refutation witnesses,  $\neg \varphi_{CR}$  is

$$(\exists x, y, z: \mathbf{S})(\forall u: \mathbf{S}) \ x \to^* y \land x \to^* z \land \neg(y \to^* u \land z \to^* u)$$
(41)

and its Skolem normal form is

$$(\forall u: \mathbf{S}) \ \mathsf{sk}_x \to^* \mathsf{sk}_y \land \mathsf{sk}_x \to^* \mathsf{sk}_z \land \neg(\mathsf{sk}_y \to^* u \land \mathsf{sk}_z \to^* u)$$
(42)

Mace4 yields  $\mathsf{sk}_x^{\mathcal{A}} = 1$ ,  $\mathsf{sk}_y^{\mathcal{A}} = 0$  and  $\mathsf{sk}_z^{\mathcal{A}} = 2$ ;  $\Phi$  consists of a single sentence; e.g.,

$$(\forall u: \mathbf{S}) \ \mathbf{b} \to^* \mathbf{a} \land \mathbf{b} \to^* \mathbf{d} \land \neg(\mathbf{a} \to^* u \land \mathbf{d} \to^* u)$$
(43)

but also:  $(\forall u : \mathbf{S}) \in \mathbf{c} \to^* \mathbf{a} \land \mathbf{c} \to^* \mathbf{d} \land \neg(\mathbf{a} \to^* u \land \mathbf{d} \to^* u)$ . Indeed, they represent the two possible cases of nonconfluent behavior in  $\mathcal{R}$ .

Example 7 (Normalizing TRS).  $\mathcal{R}$  is not terminating, but we can prove it normalizing (i.e., every term has a normal form) by disproving  $\overline{\varphi}_{WN}$ , for  $\varphi_{WN}$  in Table 1. Therefore,  $\overline{\varphi}_{WN}$  is  $(\exists x : \mathbf{S})(\forall y : \mathbf{S})(\exists z : \mathbf{S}) (\neg(x \to^* y) \lor y \to z)$ . We guarantee surjectivity by using  $\mathbf{SuH}_{\mathbf{S}}^T$  in Example 5; we also use  $\mathcal{N}$  in Example 5. Mace4 obtains a model  $\mathcal{A}$  of  $\overline{\mathcal{R}} \cup \mathbf{SuH}_{\mathbf{S}}^T \cup \mathcal{N} \cup \{\varphi_{WN}\}$  with  $\mathcal{A}_{\mathbf{S}} = \{0, 1, 2\}$ ,  $\mathbf{a}^{\mathcal{A}} = 0$ ,  $\mathbf{b}^{\mathcal{A}} = \mathbf{c}^{\mathcal{A}} = 1$ ,  $\mathbf{d}^{\mathcal{A}} = 2$ ,  $x \to^{\mathcal{A}} y \Leftrightarrow x = 1$ , and  $x(\to^*)^{\mathcal{A}}y \Leftrightarrow x = y \lor x = 1$ .

#### 8 Related Work

In [14, Sect. 6] we already compared our approach to existing techniques and tools for the so-called First-Order Theory of Rewriting [6], which applies to restricted classes of TRSs and formulas. In [16], we show that our semantic approach is practical when applied to arbitrary (Conditional) TRSs.

<sup>&</sup>lt;sup>2</sup> This proves  $\mathcal{R}$  ground locally confluent, i.e., variables in  $\varphi_{WCR}$  refer to ground terms only; since  $\mathcal{R}$  is a ground TRS, local confluence and ground local confluence coincide.

McCune's Prover9/Mace4 are popular automated systems for theorem proving in first-order and equational logic. Given a theory S and a goal or statement  $\varphi$ , Prover9 tries to prove that  $S \vdash \varphi$  holds. The generator of models Mace4 complements Prover9 as follows: "If the statement is the denial of some conjecture, any structures found by Mace4 are counterexamples to the conjecture".<sup>3</sup> Accordingly, the user introduces  $\varphi$  in the goal section of Mace4, but the system seeks a model of  $S \cup \{\neg\varphi\}$ . Indeed, as discussed in Sect. 1, if  $A \models S \cup \{\neg\varphi\}$  holds, then  $S \vdash \varphi$  does not hold. But, unless  $\varphi$  is an ECBCA, this does not necessarily mean that  $\varphi$  does not hold of a program P with  $S = \overline{P}!$  Consider the following 'misleading' session with Mace4 that 'disproves' commutativity of the addition.

Example 8. Consider  $\mathcal{R}$  in Example 1. Mace4 obtains a model  $\mathcal{A}$  of  $\overline{\mathcal{R}} \cup \{\neg(14)\}$  with domain  $\mathcal{A} = \{0, 1\}$ , and function and predicate symbols as follows:  $\mathbf{0}^{\mathcal{A}} = 0$ ,  $\mathbf{s}^{\mathcal{A}}(x) = x$ ,  $\operatorname{add}^{\mathcal{A}}(x, y) = \begin{cases} 1 \text{ if } x = 0 \land y = 1 \\ 0 \text{ otherwise} \end{cases}$ ,  $\operatorname{mul}^{\mathcal{A}}(x, y) = 0$ , and  $\rightarrow^{\mathcal{A}}$  and  $(\rightarrow^*)^{\mathcal{A}}$  both interpreted as the equality. Additionally, Mace4 also displays the following:  $\mathbf{c1}^{\mathcal{A}} = 0$  and  $\mathbf{c2}^{\mathcal{A}} = 1$ . These c1 and c2 are *new* Skolem symbols (but *unexpected* for most users!). In practice, Mace4 finds a model for the *Skolem normal form* of  $\neg(14)$ , which is

$$(\forall z) \neg (\mathsf{add}(\mathsf{c1}, \mathsf{c2}) \to^* z \land \mathsf{add}(\mathsf{c2}, \mathsf{c1}) \to^* z) \tag{44}$$

Indeed,  $\mathcal{A}$  is a model of  $\overline{\mathcal{R}} \cup \{(45)\}$ . But we *should not* conclude (as suggested by the aforementioned sentences in Mace4 manual) that add is *not* commutative!

The problem in Example 8 is that  $h: \mathcal{I}_{\overline{\mathcal{R}}} \to \mathcal{A}$  is not surjective. For instance, no ground term  $t \in \mathcal{T}_{\Sigma}$  satisfies  $t^{\mathcal{A}} = 1$ ; note that  $c1, c2 \notin \Sigma$ . Since proving validity in  $\mathcal{I}_{\mathcal{S}}$  is not the main purpose of Mace4, no warning in its documentation prevents the prospective user to give credit to the 'refutation' of (ground) commutativity for the addition computed by Mace4. We believe that our work is helpful to clarify the use of such tools, and even improve it by adding (for instance) sentences reinforcing surjectivity to avoid the problem discussed above. For instance, Mace4 obtains no model of  $\overline{\mathcal{R}} \cup \mathsf{SuH}^T \cup \{(45)\}$  with, e.g.,  $T = \{0, \mathsf{s}(0)\}.$ 

**Proofs by Satisfiability vs. Theorem Proving.** In order to further clarify the differences between our approach and the use of first-order theorem proving tools, consider the CTRS  $\mathcal{R}$  in [14, Example 1], consisting of the rules

$$b \rightarrow a$$
 (45)

$$\mathsf{a} \to \mathsf{b} \Leftarrow \mathsf{c} \to \mathsf{b} \tag{46}$$

<sup>&</sup>lt;sup>3</sup> https://www.cs.unm.edu/~mccune/prover9/manual/2009-11A/mace4.html.

Its associated Horn theory  $\overline{\mathcal{R}}$  is:

$$(\forall x) \ x \to^* x \tag{47}$$

$$(\forall x, y, z) \ x \to y \land y \to^* z \Rightarrow x \to^* z \tag{48}$$

$$b \rightarrow a$$
 (49)

$$\mathsf{c} \to^* \mathsf{b} \Rightarrow \mathsf{a} \to \mathsf{b} \tag{50}$$

We consider some simple tests regarding goals  $\mathbf{b} \to \mathbf{a}$  and  $\mathbf{a} \to \mathbf{b}$  and their negations. We tried such four goals with the following theorem provers: Alt-Ergo,<sup>4</sup> Prover9/Mace4, PDL-tableau,<sup>5</sup> and Princess<sup>6</sup> (most of them with a web-interface). Besides attempting a *proof* of each goal with respect to  $\overline{\mathcal{R}}$ , tools Alt-Ergo, Mace4, and Princess can also generate *models* of the negation of the tested goal when the proof attempt fails. The following table summarizes the results of our test:

#	Goal		Alt-Ergo		Mace4		PDL-tableau		Princess	
	$\varphi$	$\mathcal{I}_{\mathcal{R}} \models \varphi$	$\overline{\mathcal{R}} \vdash \varphi$	$\mathcal{A}\!\models\!\neg\varphi$	$\overline{\mathcal{R}} \vdash \varphi$	$\mathcal{A} \!\models\! \neg \varphi$	$\overline{\mathcal{R}} \vdash \varphi$	$\mathcal{A}\!\models\!\neg\varphi$	$\overline{\mathcal{R}} \vdash \varphi$	$\mathcal{A}\!\models\!\neg\varphi$
1	$b \to a$	true	Y	Ν	Y	Ν	Y	_	Y	Ν
2	$\neg(b\toa)$	false	Ν	Y	Ν	Y	Ν	_	Ν	Y
3	$a \to b$	false	Ν	Y	Ν	Υ	Ν	_	Ν	Y
4	$\neg(a\tob)$	true	Ν	Y	Ν	Y	Ν	_	Ν	Y

Goal  $\neg(\mathbf{a} \to \mathbf{b})$  in row 4 is *not* directly proved by any tool. Indeed, since  $\neg(\mathbf{a} \to \mathbf{b})$ is *not* a logical consequence of  $\overline{\mathcal{R}}$  (see [14, Example 2]),  $\overline{\mathcal{R}} \vdash \neg(\mathbf{a} \to \mathbf{b})$  does *not* hold. Our *satisfiability* approach can be used to formally prove that  $\mathcal{R}$  cannot reduce  $\mathbf{a}$  into  $\mathbf{b}$ , i.e., that  $\mathcal{I}_{\mathcal{R}} \models \neg(\mathbf{a} \to \mathbf{b})$  (or  $\mathbf{a} \not\rightarrow_{\mathcal{R}} \mathbf{b}$ ) holds: from row 3 we see that  $\mathcal{A} \models \neg(\mathbf{a} \to \mathbf{b})$  holds for the models  $\mathcal{A}$  of  $\overline{\mathcal{R}}$  computed by some of the tools. By Corollary 1, the desired conclusion  $\mathbf{a} \not\rightarrow_{\mathcal{R}} \mathbf{b}$  follows. Note also that row 4 reports on the ability of some tools to obtain models of  $\mathbf{a} \to \mathbf{b}$ . However, Corollary 1 cannot be used to conclude that  $\mathbf{a} \to_{\mathcal{R}} \mathbf{b}$  holds (which is obviously wrong): since  $\varphi$  in row 4 is a *negative* literal, condition (b) in Corollary 1 must be fulfilled before being able to conclude  $\mathcal{I}_{\mathcal{R}} \models \mathbf{a} \to \mathbf{b}$  from  $\mathcal{A} \models \mathbf{a} \to \mathbf{b}$  for some model  $\mathcal{A}$  of  $\overline{\mathcal{R}}$ . But this is *not* the case in our test set.

Although Remark 1 explains how an arbitrary program property  $\varphi$  can be proved by using Corollary 1 (see also Sect. 7), from a practical point of view we better think of our approach as *complementary* to the use of first-order proof techniques and tools. Provability of  $\varphi$  (i.e.,  $\mathcal{S} \vdash \varphi$ ) implies that  $\mathcal{I}_{\mathcal{S}} \models \varphi$  holds. Thus, as usual, a proof of  $\varphi$  with respect to  $\mathcal{S}$  implies that a program P with  $\mathcal{S} = \overline{P}$  has property  $\varphi$ . However, as discussed above, showing that  $\mathcal{S} \vdash \varphi$  or  $\mathcal{S} \vdash \neg \varphi$  holds is often impossible. We can try to prove  $\mathcal{I}_{\mathcal{S}} \models \neg \varphi$  by using Corollary 1, though. For positive sentences  $\varphi$ , this is often affordable.

<sup>&</sup>lt;sup>4</sup> https://alt-ergo.ocamlpro.com/.

<sup>&</sup>lt;sup>5</sup> http://www.cs.man.ac.uk/~schmidt/pdl-tableau/.

<sup>&</sup>lt;sup>6</sup> http://www.philipp.ruemmer.org/princess.shtml.

#### 9 Conclusions and Future Work

We have shown how to prove properties  $\varphi$  of computational systems whose semantics can be given as a first-order theory S. Our proofs by satisfiability proceed (see Remark 1) by just finding a model  $\mathcal{A}$  of  $\mathcal{S} \cup \mathcal{Z} \cup \{\varphi\}$  where  $\mathcal{Z}$ is an *auxiliary* theory representing the requirements (a) and (b) in Corollary 1 (referred to  $\neg \varphi$ ), so that  $\mathcal{A} \models \mathcal{S} \cup \mathcal{Z} \cup \{\varphi\}$  implies  $\mathcal{I}_{\mathcal{S}} \models \varphi$ . Surjectivity of the interpretation homomorphisms (requirement (a) in Corollary 1) is ensured if  $\mathcal{Z}$  includes the appropriate theory SuH (see Sect. 4); and requirement (b), for dealing with negative literals, is fulfilled if  $\mathcal{Z}$  includes  $\mathcal{N}$  in Proposition 1. Our results properly subsume the ones in [14], which concern *existentially closed boolean combinations of atoms* only. We have also introduced the notion of *refutation witness* which is useful to obtain counterexamples by using the symbols in the first-order language rather than values of the computed model.

From a theoretical point of view, the idea of proving program properties as satisfiability (see Remark 1) is appealing as it emphasizes the role of abstraction (introduced by semantic structures) in theorem proving and logic-based program analysis. However, the requirement of surjectivity of the interpretation homomorphisms and the use of theories  $\mathcal{N}$  with negative information about some of the predicates introduce additional difficulties in the model generation process. Investigating methods for the practical implementation of our techniques, and also finding specific areas of application where our approach can be useful (as done in [16], for instance) is an interesting subject for future work.

Also, our research suggests that further investigation on the generation of models for many-sorted theories that combines the use of finite and infinite domains is necessary. For instance, [15] explains how to generate such models by interpreting the sort, function, and predicate symbols by using linear algebra techniques. This is implemented in AGES. Domains are defined as the solutions of matrix inequalities, possibly restricted to an underlying set of values (e.g.,  $\mathbb{Z}$ ); thus, finite and infinite domains can be obtained as particular cases of the same technique. Since piecewise definitions are allowed, we could eventually provide fully detailed descriptions of functions and predicates by just adding more pieces to the interpretations. However, such a flexibility is expensive. In contrast, Mace4 is based on a different principle (similar to [12]) and it is really fast, but only finite domains can be generated. This is a problem, for instance, when using Proposition 3 to guarantee surjectivity of homomorphisms  $h_s: \mathcal{T}_{\Sigma_s} \to \mathcal{A}_s$ . Even though  $\mathcal{A}_s$  is finite, we still need to be able to interpret Nat as N, which is not possible with Mace4. For this reason, the examples in Sect. 5 (where the computed structures  $\mathcal{A}$  have finite domains for the 'proper' sorts N, LN, and S, and only *Nat* is interpreted as an infinite set) could not be handled with Mace4. or with similar tools that are able to deal with sorts (e.g., SEM [24] or the work in [21]) but which generate finite domains only.

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# Guided Unfoldings for Finding Loops in Standard Term Rewriting

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**Abstract.** In this paper, we reconsider the unfolding-based technique that we have introduced previously for detecting loops in standard term rewriting. We modify it by *guiding* the unfolding process, using disagreement pairs in rewrite rules. This results in a partial computation of the unfoldings, whereas the original technique consists of a thorough computation followed by a mechanism for eliminating some rules. We have implemented this new approach in our tool NTI and conducted successful experiments on a large set of term rewrite systems.

**Keywords:** Term rewrite systems  $\cdot$  Dependency pairs  $\cdot$  Non-termination  $\cdot$  Loop  $\cdot$  Unfolding

#### 1 Introduction

In [13], we have introduced a technique for finding *loops* (a periodic, special form of non-termination) in standard term rewriting. It consists of unfolding the term rewrite system (TRS)  $\mathcal{R}$  under analysis and of performing a semi-unification [10] test on the unfolded rules for detecting loops. The unfolding operator  $U_{\mathcal{R}}$  which is applied processes both forwards and backwards and considers *every* subterm of the rules to unfold, including variable subterms.

*Example 1.* Let  $\mathcal{R}$  be the TRS consisting of the following rules (x is a variable):

$$R_1 = \underbrace{\mathsf{f}(\mathsf{s}(\mathsf{0}),\mathsf{s}(1),x)}_l \to \underbrace{\mathsf{f}(x,x,x)}_r \qquad R_2 = \mathsf{h} \to \mathsf{0} \qquad R_3 = \mathsf{h} \to \mathsf{1}.$$

Note that  $\mathcal{R}$  is a variation of a well-known example by Toyama [18]. Unfolding the subterm 0 of l backwards with the rule  $R_2$ , we get the unfolded rule  $U_1 = f(\mathbf{s}(\mathbf{h}), \mathbf{s}(1), x) \rightarrow f(x, x, x)$ . Unfolding the subterm x (a variable) of l backwards with  $R_2$ , we get  $U_2 = f(\mathbf{s}(0), \mathbf{s}(1), \mathbf{h}) \rightarrow f(0, 0, 0)$ . Unfolding the first (from the left) occurrence of x in r forwards with  $R_2$ , we get  $U_3 = f(\mathbf{s}(0), \mathbf{s}(1), \mathbf{h}) \rightarrow f(0, \mathbf{h}, \mathbf{h})$ . We have  $\{U_1, U_2, U_3\} \subseteq U_{\mathcal{R}}(\mathcal{R})$ . Now, if we unfold the subterm 1 of  $U_1$  backwards with  $R_3$ , we get  $f(\mathbf{s}(\mathbf{h}), \mathbf{s}(\mathbf{h}), x) \rightarrow f(x, x, x)$ , which is an element of  $U_{\mathcal{R}}(U_{\mathcal{R}}(\mathcal{R}))$ . The left-hand side  $l_1$  of this rule semi-unifies with its right-hand side  $r_1$  *i.e.*,  $l_1\theta_1\theta_2 = r_1\theta_1$  for the substitutions  $\theta_1 = \{x/\mathbf{s}(\mathbf{h})\}$  and  $\theta_2 = \{\}$ . Therefore, © Springer Nature Switzerland AG 2019

F. Mesnard and P. J. Stuckey (Eds.): LOPSTR 2018, LNCS 11408, pp. 22–37, 2019. https://doi.org/10.1007/978-3-030-13838-7\_2  $l\theta_1 = f(s(h), s(h), s(h))$  loops with respect to  $\mathcal{R}$  because it can be rewritten to itself using the rules of  $\mathcal{R}$  (the redex is underlined at each step):

$$\mathsf{f}(\mathsf{s}(\underline{\mathsf{h}}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h})) \mathop{\rightarrow}_{R_2} \mathsf{f}(\mathsf{s}(\mathsf{0}),\mathsf{s}(\underline{\mathsf{h}}),\mathsf{s}(\mathsf{h})) \mathop{\rightarrow}_{R_3} \underbrace{\mathsf{f}(\mathsf{s}(\mathsf{0}),\mathsf{s}(\mathsf{1}),\mathsf{s}(\mathsf{h}))}_{R_1} \mathop{\rightarrow}_{R_1} \mathsf{f}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h})).$$

Iterative applications of the operator  $U_{\mathcal{R}}$  result in a combinatorial explosion which significantly limits the approach. In order to reduce it, a mechanism is introduced in [13] for eliminating unfolded rules which are estimated as *useless* for detecting loops. Moreover, in practice, three analyses are run in parallel (in different threads): one with forward unfoldings only, one with backward unfoldings only and one with forward and backward unfoldings together.

So, the technique of [13] roughly consists in computing all the rules of  $U_{\mathcal{R}}(\mathcal{R})$ ,  $U_{\mathcal{R}}(U_{\mathcal{R}}(\mathcal{R}))$ , ... and removing some useless ones, until the semi-unification test succeeds on an unfolded rule or a time limit is reached. Therefore, this approach corresponds to a breadth-first search for a loop, as the successive iterations of  $U_{\mathcal{R}}$  are computed thoroughly, one after the other. However, it is not always necessary to compute all the elements of each iteration of  $U_{\mathcal{R}}$ . For instance, in Example 1 above,  $U_2$  and  $U_3$  do not lead to an unfolded rule satisfying the semi-unification criterion. This is detected by the eliminating mechanism of [13], but only after these two rules are generated. In order to avoid the generation of these useless rules, one can notice that l and r differ at the first argument is  $\mathfrak{s}(0)$  while in r it is x. We say that  $\langle \mathfrak{s}(0), x \rangle$  is a disagreement pair of l and r. Hence, one can first concentrate on resolving this disagreement, unfolding this pair only, and then, once this is resolved, apply the same process to another disagreement pair.

*Example 2 (Example 1 continued).* There are two ways to resolve the disagreement pair  $\langle s(0), x \rangle$  of l and r (*i.e.*, make it disappear).

The first way consists in unifying s(0) and x, *i.e.*, in computing  $R_1\theta$  where  $\theta$  is the substitution  $\{x/s(0)\}$ , which gives  $V_0 = f(s(0), s(1), s(0)) \rightarrow f(s(0), s(0), s(0))$ . The left-hand side of  $V_0$  does not semi-unify with its right-hand side.

The other way is to unfold s(0) or x. We decide not to unfold variable subterms, hence we select s(0). As it occurs in the left-hand side of  $R_1$ , we unfold it backwards. The only possibility is to use  $R_2$ , which results in

$$V_1 = \mathsf{f}(\mathsf{s}(\mathsf{h}), \mathsf{s}(1), x) \to \mathsf{f}(x, x, x).$$

Note that this approach only generates two rules  $(V_0 \text{ and } V_1)$  at the first iteration of the unfolding operator. In comparison, the approach of [13] produces 14 rules (before elimination), as all the subterms of  $R_1$  are considered for unfolding.

Hence, the disagreement pair  $\langle \mathsf{s}(\mathsf{0}), x \rangle$  has been replaced with the disagreement pair  $\langle \mathsf{s}(\mathsf{h}), x \rangle$  in  $V_1$ . Unifying  $\mathsf{s}(\mathsf{h})$  and x *i.e.*, computing  $V_1\theta'$  where  $\theta'$  is the substitution  $\{x/\mathsf{s}(\mathsf{h})\}$ , we get  $V'_1 = \mathsf{f}(\mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{1}), \mathsf{s}(\mathsf{h})) \to \mathsf{f}(\mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}))$ . So, the disagreement  $\langle \mathsf{s}(\mathsf{0}), x \rangle$  is solved: it has been replaced with  $\langle \mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}) \rangle$ . Now,  $\langle 1, \mathsf{h} \rangle$  is a disagreement pair in  $V'_1$  (here we mean the second occurrence of  $\mathsf{h}$  in the right-hand side of  $V'_1$ ). Unfolding 1 backwards with  $R_3$ , we get  $W = \mathsf{f}(\mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h})) \to \mathsf{f}(\mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}), \mathsf{s}(\mathsf{h}))$  and unfolding  $\mathsf{h}$  forwards with  $R_3$ , we get  $W' = f(s(h), s(1), s(h)) \rightarrow f(s(h), s(1), s(h))$ . The semi-unification test succeeds on both rules: we get the looping terms f(s(h), s(h), s(h), s(h)) and f(s(h), s(1), s(h)) from W and W', respectively.

In the approach sketched in Example 2, the iterations of  $U_{\mathcal{R}}$  are not thoroughly computed because only some selected disagreement pairs are considered for unfolding, unlike in our previous approach [13] which tries to unfold all the subterms in rules. Hence, now the unfoldings are guided by disagreement pairs. In this paper, we formally describe the intuitions presented above (Sects. 3–5). We also report experiments on a large set of rewrite systems from the TPBD [17] (Sect. 6). The results we get in practice with the new approach are better than those obtained with the approach of [13] and we do not need to perform several analyses in parallel, nor to unfold variable subterms, unlike [13].

#### 2 Preliminaries

If Y is an operator from a set E to itself, then for any  $e \in E$  we let

 $(Y \uparrow 0)(e) = e$  and  $\forall n \in \mathbb{N} : (Y \uparrow n+1)(e) = Y((Y \uparrow n)(e))$ .

We refer to [4] for the basics of rewriting. From now on, we fix a finite signature  $\mathcal{F}$  together with an infinite countable set  $\mathcal{V}$  of variables with  $\mathcal{F} \cap \mathcal{V} = \emptyset$ . Elements of  $\mathcal{F}$  (symbols) are denoted by f, g, h, 0, 1, ... and elements of  $\mathcal{V}$  by  $x, y, z, \ldots$  The set of terms over  $\mathcal{F} \cup \mathcal{V}$  is denoted by  $\mathcal{T}(\mathcal{F}, \mathcal{V})$ . For any  $t \in \mathcal{T}(\mathcal{F}, \mathcal{V})$ , we let root(t) denote the root symbol of t: root(t) = f if  $t = f(t_1, \ldots, t_m)$  and  $root(t) = \bot$  if  $t \in \mathcal{V}$ , where  $\bot$  is a new symbol not occurring in  $\mathcal{F}$  and  $\mathcal{V}$ . We let Var(t) denote the set of variables occurring in t and Pos(t) the set of positions of t. For any  $p \in Pos(t)$ , we write  $t|_p$  to denote the subterm of t at position p and  $t[p \leftarrow s]$  to denote the term obtained from t by replacing  $t|_p$  with a term s. For any  $p, q \in Pos(t)$ , we write  $p \leq q$  iff p is a prefix of q and we write p < q iff  $p \leq q$  and  $p \neq q$ . We also define the set of non-variable positions of t which either are a prefix of p or include p as a prefix:

$$NPos(t,p) = \{ q \in Pos(t) \mid q \le p \lor p \le q, \ t|_q \notin \mathcal{V} \}.$$

A disagreement position of terms s and t is a position  $p \in Pos(s) \cap Pos(t)$ such that  $root(s|_p) \neq root(t|_p)$  and, for every q < p,  $root(s|_q) = root(t|_q)$ . The set of disagreement positions of s and t is denoted as DPos(s,t). A disagreement pair of s and t is an ordered pair  $\langle s|_p, t|_p \rangle$  where  $p \in DPos(s,t)$ .

Example 3. Let s = f(s(0), s(1), y), t = f(x, x, x),  $p_1 = 1$ ,  $p_2 = 2$  and  $p_3 = 3$ . Then,  $\{p_1, p_2\} \subseteq DPos(s, t)$  and  $\langle s|_{p_1}, t|_{p_1} \rangle = \langle s(0), x \rangle$  and  $\langle s|_{p_2}, t|_{p_2} \rangle = \langle s(1), x \rangle$  are disagreement pairs of s and t. However,  $p_3 \notin DPos(s, t)$  because  $\langle s|_{p_3}, t|_{p_3} \rangle = \langle y, x \rangle$  and  $root(y) = root(x) = \bot$ .

We write substitutions as sets of the form  $\{x_1/t_1, \ldots, x_n/t_n\}$  denoting that for each  $1 \le i \le n$ , variable  $x_i$  is mapped to term  $t_i$  (note that  $x_i$  may occur in  $t_i$ ). The empty substitution (identity) is denoted by *id*. The application of a substitution  $\theta$  to a syntactic object *o* is denoted by  $o\theta$ . We let mgu(s,t) denote the (up to variable renaming) most general unifier of terms *s* and *t*. We say that *s* semi-unifies with *t* when  $s\theta_1\theta_2 = t\theta_1$  for some substitutions  $\theta_1$  and  $\theta_2$ .

A rewrite rule (or rule) over  $\mathcal{F} \cup \mathcal{V}$  has the form  $l \to r$  with  $l, r \in \mathcal{T}(\mathcal{F}, \mathcal{V})$ ,  $l \notin \mathcal{V}$  and  $Var(r) \subseteq Var(l)$ . A term rewriting system (TRS) over  $\mathcal{F} \cup \mathcal{V}$  is a finite set of rewrite rules over  $\mathcal{F} \cup \mathcal{V}$ . Given a TRS  $\mathcal{R}$  and some terms s and t, we write  $s \xrightarrow{\rightarrow} t$  if there is a rewrite rule  $l \to r$  in  $\mathcal{R}$ , a substitution  $\theta$  and  $p \in Pos(s)$  such that  $s|_p = l\theta$  and  $t = s[p \leftarrow r\theta]$ . We let  $\stackrel{+}{\mathcal{R}}$  (resp.  $\stackrel{*}{\mathcal{R}}$ ) denote the transitive (resp. reflexive and transitive) closure of  $\xrightarrow{\sim}$ . We say that a term t is non-terminating with respect to  $(w.r.t.) \mathcal{R}$  when there exist infinitely many terms  $t_1, t_2, \ldots$  such that  $t \xrightarrow{\sim}_{\mathcal{R}} t_1 \xrightarrow{\sim}_{\mathcal{R}} t_2 \xrightarrow{\sim}_{\mathcal{R}} \cdots$ . We say that  $\mathcal{R}$  is non-terminating if there exists a non-terminating term w.r.t. it. A term t loops  $w.r.t. \mathcal{R}$  when  $t \xrightarrow{+}_{\mathcal{R}} C[t\theta]$  for some context C and substitution  $\theta$ . Then  $t \xrightarrow{+}_{\mathcal{R}} C[t\theta]$  is called a loop for  $\mathcal{R}$ . We say that  $\mathcal{R}$  is looping when it admits a loop. If a term loops  $w.r.t. \mathcal{R}$ 

The unfolding operators that we define in Sect. 3 of this paper use *narrowing*. We say that a term s narrows forwards (resp. backwards) to a term t w.r.t. a TRS  $\mathcal{R}$  when there exists a non-variable position p of s and a rule  $l \to r$  of  $\mathcal{R}$ renamed with new variables not previously met such that  $t = s[p \leftarrow r]\theta$  (resp.  $t = s[p \leftarrow l]\theta$ ) where  $\theta = mgu(s|_p, l)$  (resp.  $\theta = mgu(s|_p, r)$ ).

We refer to [3] for details on dependency pairs. The defined symbols of a TRS  $\mathcal{R}$  over  $\mathcal{F} \cup \mathcal{V}$  are  $\mathcal{D}_{\mathcal{R}} = \{root(l) \mid l \to r \in \mathcal{R}\}$ . For every  $f \in \mathcal{F}$  we let  $f^{\#}$  be a fresh tuple symbol with the same arity as f. The set of tuple symbols is denoted as  $\mathcal{F}^{\#}$ . The notations and definitions above with terms over  $\mathcal{F} \cup \mathcal{V}$  are naturally extended to terms over  $(\mathcal{F} \cup \mathcal{F}^{\#}) \cup \mathcal{V}$ . Elements of  $\mathcal{F} \cup \mathcal{F}^{\#}$  are denoted as  $f, g, \ldots$  If  $t = f(t_1, \ldots, t_m) \in \mathcal{T}(\mathcal{F}, \mathcal{V})$ , we let  $t^{\#}$  denote the term  $f^{\#}(t_1, \ldots, t_m)$ , and we call  $t^{\#}$  an  $\mathcal{F}^{\#}$ -term. An  $\mathcal{F}^{\#}$ -rule is a rule whose left-hand and right-hand sides are  $\mathcal{F}^{\#}$ -terms. The set of dependency pairs of  $\mathcal{R}$  is

 $\{l^{\#} \to t^{\#} \mid l \to r \in \mathcal{R}, t \text{ is a subterm of } r, root(t) \in \mathcal{D}_{\mathcal{R}}\}.$ 

A sequence  $s_1 \to t_1, \ldots, s_n \to t_n$  of dependency pairs of  $\mathcal{R}$  is an  $\mathcal{R}$ -chain if there exists a substitution  $\sigma$  such that  $t_i \sigma \stackrel{*}{\underset{\mathcal{R}}{\rightarrow}} s_{i+1} \sigma$  holds for every two consecutive pairs  $s_i \to t_i$  and  $s_{i+1} \to t_{i+1}$  in the sequence.

#### **Theorem 1** ([3]). $\mathcal{R}$ is non-terminating iff there exists an infinite $\mathcal{R}$ -chain.

The dependency graph of  $\mathcal{R}$  is the graph whose nodes are the dependency pairs of  $\mathcal{R}$  and there is an arc from  $s \to t$  to  $u \to v$  iff  $s \to t, u \to v$  is an  $\mathcal{R}$ chain. This graph is not computable in general since it is undecidable whether two dependency pairs of  $\mathcal{R}$  form an  $\mathcal{R}$ -chain. Hence, for automation, one constructs an estimated graph containing all the arcs of the real graph. This is done by computing *connectable terms*, which form a superset of those terms s, t where  $s\sigma \stackrel{*}{\underset{\mathcal{R}}{\to}} t\sigma$  holds for some substitution  $\sigma$ . The approximation uses the transformations CAP and REN where, for any  $t \in \mathcal{T}(\mathcal{F} \cup \mathcal{F}^{\#}, \mathcal{V})$ , CAP(t) (resp. REN(t)) results from replacing all subterms of t with defined root symbol (resp. all variable occurrences in t) by different new variables not previously met. More formally:

$$\begin{aligned} \operatorname{CAP}(x) &= x \text{ if } x \in \mathcal{V} \\ \operatorname{CAP}(f(t_1, \dots, t_m)) &= \begin{cases} \text{a new variable not previously met if } f \in \mathcal{D}_{\mathcal{R}} \\ f(\operatorname{CAP}(t_1), \dots, \operatorname{CAP}(t_m)) & \text{ if } f \notin \mathcal{D}_{\mathcal{R}} \end{cases} \\ \operatorname{REN}(x) &= \text{a new variable not previously met} \\ & \text{ if } x \text{ is an occurrence of a variable} \end{cases} \\ \operatorname{REN}(f(t_1, \dots, t_m)) &= f(\operatorname{REN}(t_1), \dots, \operatorname{REN}(t_m)) \end{aligned}$$

A term s is connectable to a term t if  $\operatorname{REN}(\operatorname{CAP}(s))$  unifies with t. An  $\mathcal{F}^{\#}$ -rule  $l \to r$  is connectable to an  $\mathcal{F}^{\#}$ -rule  $s \to t$  if r is connectable to s. The *estimated* dependency graph of  $\mathcal{R}$  is denoted as  $DG(\mathcal{R})$ . Its nodes are the dependency pairs of  $\mathcal{R}$  and there is an arc from N to N' iff N is connectable to N'. We let  $SCC(\mathcal{R})$  denote the set of strongly connected components of  $DG(\mathcal{R})$  that contain at least one arc. Hence, a strongly connected component consisting of a unique node is in  $SCC(\mathcal{R})$  only if there is an arc from the node to itself.

*Example 4.* Let  $\mathcal{R}$  be the TRS of Example 1. We have  $SCC(\mathcal{R}) = \{\mathcal{C}\}$  where  $\mathcal{C}$  consists of the node  $N = f^{\#}(s(0), s(1), x) \rightarrow f^{\#}(x, x, x)$  and of the arc (N, N).

Example 5. Let  $\mathcal{R}' = \{f(0) \to f(1), f(2) \to f(0), 1 \to 0\}$ . We have  $SCC(\mathcal{R}') = \{\mathcal{C}'\}$  where  $\mathcal{C}'$  consists of the nodes  $N_1 = f^{\#}(0) \to f^{\#}(1)$  and  $N_2 = f^{\#}(2) \to f^{\#}(0)$  and of the arcs  $\{N_1, N_2\} \times \{N_1, N_2\} \setminus \{(N_2, N_2)\}$ . The strongly connected component of  $DG(\mathcal{R}')$  which consists of the unique node  $f^{\#}(0) \to 1^{\#}$  does not belong to  $SCC(\mathcal{R}')$  because it has no arc.

Finite sequences are written as  $[e_1, \ldots, e_n]$ . We let :: denote the concatenation operator over finite sequences. A *path* in  $DG(\mathcal{R})$  is a finite sequence  $[N_1, N_2, \ldots, N_n]$  of nodes where, for each  $1 \leq i < n$ , there is an arc from  $N_i$ to  $N_{i+1}$ . When there is also an arc from  $N_n$  to  $N_1$ , the path is called a *cycle*. It is called a *simple cycle* if, moreover, there is no repetition of nodes (modulo variable renaming).

# 3 Guided Unfoldings

In the sequel of this paper, we let  $\mathcal{R}$  denote a TRS over  $\mathcal{F} \cup \mathcal{V}$ .

While the method sketched in Example 2 can be applied directly to the TRS  $\mathcal{R}$  under analysis, we use a refinement based on the estimated dependency graph of  $\mathcal{R}$ . The cycles in  $DG(\mathcal{R})$  are over-approximations of the infinite  $\mathcal{R}$ -chains *i.e.*, any infinite  $\mathcal{R}$ -chain corresponds to a cycle in the graph but some cycles in the graph may not correspond to any  $\mathcal{R}$ -chain. Moreover, by Theorem 1, if we find

an infinite  $\mathcal{R}$ -chain then we have proved that  $\mathcal{R}$  is non-terminating. Hence, we concentrate on the cycles in  $DG(\mathcal{R})$ . We try to *solve* them, *i.e.*, to find out if they correspond to any infinite  $\mathcal{R}$ -chain. This is done by iteratively unfolding the  $\mathcal{F}^{\#}$ -rules of the cycles. If the semi-unification test succeeds on one of the generated unfolded rules, then we have found a loop.

**Definition 1 (Syntactic loop).** A syntactic loop in  $\mathcal{R}$  is a finite sequence  $[N_1, \ldots, N_n]$  of distinct (modulo variable renaming)  $\mathcal{F}^{\#}$ -rules where, for each  $1 \leq i < n, N_i$  is connectable to  $N_{i+1}$  and  $N_n$  is connectable to  $N_1$ . We identify syntactic loops consisting of the same (modulo variable renaming) elements, not necessarily in the same order.

Note that the simple cycles in  $DG(\mathcal{R})$  are syntactic loops. For any  $\mathcal{C} \in SCC(\mathcal{R})$ , we let s-cycles( $\mathcal{C}$ ) denote the set of simple cycles in  $\mathcal{C}$ . We also let

$$s\text{-}cycles(\mathcal{R}) = \cup_{\mathcal{C} \in SCC(\mathcal{R})} s\text{-}cycles(\mathcal{C})$$

be the set of simple cycles in  $\mathcal{R}$ . The rules of any simple cycle in  $\mathcal{R}$  are assumed to be pairwise variable disjoint.

Example 6 (Examples 4 and 5 continued). We have

$$s$$
-cycles $(\mathcal{R}) = \{[N]\}$  and  $s$ -cycles $(\mathcal{R}') = \{[N_1], [N_1, N_2]\}$ 

with, in s-cycles( $\mathcal{R}'$ ),  $[N_1, N_2] = [N_2, N_1]$ .

The operators we use for unfolding an  $\mathcal{F}^{\#}$ -rule R at a disagreement position p are defined as follows. They are guided by a given term u and they only work on the non-variable subterms of R. They unify a subterm of R with a subterm of u, see (1) in Definitions 2–3. This corresponds to what we did in Example 2 for generating  $V'_1$  from  $V_1$ , but in the definitions below we do not only consider p, we consider all its prefixes. The operators also unfold R using narrowing, see (2) in Definitions 2–3: there,  $l' \to r' \ll \mathcal{R}$  means that  $l' \to r'$  is a new occurrence of a rule of  $\mathcal{R}$  that contains new variables not previously met. This corresponds to what we did in Example 2 for generating  $V_1$  from  $R_1$ . In contrast to (1), the positions that are greater than p are also considered in (2); for instance in Example 2, we unfolded the inner subterm 0 of the disagreement pair component s(0).

**Definition 2 (Forward guided unfoldings).** Let  $l \to r$  be an  $\mathcal{F}^{\#}$ -rule, s be an  $\mathcal{F}^{\#}$ -term and  $p \in DPos(r, s)$ . The forward unfoldings of  $l \to r$  at position p, guided by s and  $w.r.t. \mathcal{R}$  are

$$F_{\mathcal{R}}(l \to r, s, p) = \left\{ U \middle| \begin{array}{l} q \in NPos(r, p), \ q \leq p \\ \theta = mgu(r|_q, s|_q), \ U = (l \to r)\theta \end{array} \right\}^{(1)} \cup \\ \left\{ U \middle| \begin{array}{l} q \in NPos(r, p), \ l' \to r' \ll \mathcal{R} \\ \theta = mgu(r|_q, l'), \ U = (l \to r[q \leftarrow r'])\theta \end{array} \right\}^{(2)}$$

**Definition 3 (Backward guided unfoldings).** Let  $s \to t$  be an  $\mathcal{F}^{\#}$ -rule, r be an  $\mathcal{F}^{\#}$ -term and  $p \in DPos(r, s)$ . The backward unfoldings of  $s \to t$  at position p, guided by r and  $w.r.t. \mathcal{R}$  are

$$B_{\mathcal{R}}(s \to t, r, p) = \left\{ U \middle| \begin{array}{l} q \in NPos(s, p), \ q \leq p \\ \theta = mgu(r|_q, s|_q), \ U = (s \to t)\theta \end{array} \right\}^{(1)} \cup \\ \left\{ U \middle| \begin{array}{l} q \in NPos(s, p), \ l' \to r' \ll \mathcal{R} \\ \theta = mgu(s|_q, r'), \ U = (s[q \leftarrow l'] \to t)\theta \end{array} \right\}^{(2)}$$

Example 7 (Examples 4 and 6 continued). [N] is a simple cycle in  $\mathcal{R}$  with

$$N = \underbrace{\mathsf{f}^{\#}(\mathsf{s}(0), \mathsf{s}(1), x)}_{s} \to \underbrace{\mathsf{f}^{\#}(x, x, x)}_{t}.$$

Let r = t. Then  $p = 1 \in DPos(r, s)$ . Moreover,  $q = 1.1 \in NPos(s, p)$  because  $p \leq q$  and  $s|_q = 0$  is not a variable. Let  $l' \rightarrow r' = h \rightarrow 0 \in \mathcal{R}$ . We have  $id = mgu(s|_q, r')$ . Hence, by (2) in Definition 3, we have

$$U_1 = \underbrace{\mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(1),x)}_{s_1} \to \underbrace{\mathsf{f}^{\#}(x,x,x)}_{t_1} \in B_{\mathcal{R}}(N,r,p).$$

Let  $r_1 = t_1$ . Then,  $p = 1 \in DPos(r_1, s_1)$ . Moreover,  $p \in NPos(s_1, p)$  with  $s_1|_p = \mathsf{s}(\mathsf{h}), p \leq p$  and  $r_1|_p = x$ . As  $\{x/\mathsf{s}(\mathsf{h})\} = mgu(r_1|_p, s_1|_p)$ , by (1) in Definition 3 we have

$$U_1' = \underbrace{\mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(1),\mathsf{s}(\mathsf{h}))}_{s_1'} \to \underbrace{\mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}))}_{t_1'} \in B_{\mathcal{R}}(U_1,r_1,p).$$

Let  $r'_1 = t'_1$ . Then,  $p' = 2.1 \in DPos(r'_1, s'_1)$  with  $p' \in NPos(s'_1, p')$ . Let  $l'' \to r'' = h \to 1 \in \mathcal{R}$ . We have  $id = mgu(s'_1|_{p'}, r'')$ . Hence, by (2) in Definition 3, we have

$$U_1^{''} = \mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h})) \to \mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h})) \in B_{\mathcal{R}}(U_1',r_1',p').$$

Our approach consists of iteratively unfolding syntactic loops using the following operator.

**Definition 4 (Guided unfoldings).** Let X be a set of syntactic loops of  $\mathcal{R}$ . The guided unfoldings of X w.r.t.  $\mathcal{R}$  are defined as

$$\begin{aligned} GU_{\mathcal{R}}(X) &= \left\{ L :: [U] :: L' \mid L :: [l \to r, s \to t] :: L' \in X, \ \theta = mgu(r, s) \\ U &= (l \to t)\theta, \ L :: [U] :: L' \ is \ a \ syntactic \ loop \right\}^{(1)} \cup \\ &\left\{ L :: [U, s \to t] :: L' \mid L' \mid L :: [l \to r, s \to t] :: L' \in X \\ p \in DPos(r, s), \ U \in F_{\mathcal{R}}(l \to r, s, p) \\ L :: [U, s \to t] :: L' \ is \ a \ syntactic \ loop \right\}^{(2)} \cup \end{aligned} \end{aligned}$$

$$\left\{ L :: [l \to r, U] :: L' \middle| \begin{array}{l} L :: [l \to r, s \to t] :: L' \in X \\ p \in DPos(r, s), \ U \in B_{\mathcal{R}}(s \to t, r, p) \\ L :: [l \to r, U] :: L' \ is \ a \ syntactic \ loop \end{array} \right\}^{(3)} \cup \\ \left\{ \begin{bmatrix} U \\ U \end{bmatrix} \middle| \begin{array}{l} [l \to r] \in X, \ p \in DPos(r, l) \\ U \in F_{\mathcal{R}}(l \to r, l, p) \cup B_{\mathcal{R}}(l \to r, r, p) \\ \begin{bmatrix} U \end{bmatrix} s \ a \ syntactic \ loop \end{array} \right\}^{(4)} .$$

The general idea is to *compress* the syntactic loops into singletons by iterated applications of this operator. The semi-unification criterion can then be applied to these singletons, see Theorem 2 below. Compression takes place in case (1) of Definition 4: when the right-hand side of a rule unifies with the left-hand side of its successor, then both rules are merged. When merging two successive rules is not possible yet, the operators  $F_{\mathcal{R}}$  and  $B_{\mathcal{R}}$  are applied to try to transform the rules into mergeable ones, see cases (2) and (3). Once a syntactic loop has been compressed to a singleton, we keep on unfolding (case (4)) to try reaching a compressed form satisfying the semi-unification criterion. Note that after an unfolding step, we might get a sequence which is not a syntactic loop: the newly generated rule U might be identical to another rule in the sequence or it might not be connectable to its predecessor or successor. So, (1)–(4) require that the generated sequence is a syntactic loop.

The guided unfolding semantics is defined as follows, in the style of [1, 13].

**Definition 5 (Guided unfolding semantics).** The guided unfolding semantics of  $\mathcal{R}$  is the limit of the unfolding process described in Definition 4, starting from the simple cycles in  $\mathcal{R}$ :  $gunf(\mathcal{R}) = \bigcup_{n \in \mathbb{N}} gunf(\mathcal{R}, n)$  where, for all  $n \in \mathbb{N}$ ,

$$gunf(\mathcal{R}, n) = (GU_{\mathcal{R}} \uparrow n)(s - cycles(\mathcal{R})).$$

This semantics is very similar to the overlap closure [7] of  $\mathcal{R}$  (denoted by  $OC(\mathcal{R})$ ). A difference is that for computing  $gunf(\mathcal{R})$  one starts from dependency pairs of  $\mathcal{R}$  (s-cycles( $\mathcal{R}$ )), whereas for computing  $OC(\mathcal{R})$  one starts directly from the rules of  $\mathcal{R}$ . In case (1) of Definition 4, we merge two unfolded rules. Similarly, for computing  $OC(\mathcal{R})$  one overlaps closures with closures. However, in cases (2)–(4) the operators  $F_{\mathcal{R}}$  and  $B_{\mathcal{R}}$  narrow an unfolded rule with a rule of  $\mathcal{R}$ , not with another unfolded rule, unlike in the computation of  $OC(\mathcal{R})$ .

Example 8. By Example 7 and (4) in Definition 4, we have  $[U_1^{''}] \in gunf(\mathcal{R}, 3)$ .

Example 9. Let  $\mathcal{R} = \{f(0) \rightarrow g(1), g(1) \rightarrow f(0)\}$ . Then,  $SCC(\mathcal{R}) = \{\mathcal{C}\}$  where  $\mathcal{C}$  consists of the nodes  $N_1 = f^{\#}(0) \rightarrow g^{\#}(1)$  and  $N_2 = g^{\#}(1) \rightarrow f^{\#}(0)$  and of the arcs  $(N_1, N_2)$  and  $(N_2, N_1)$ . Moreover, s-cycles $(\mathcal{R}) = \{[N_1, N_2]\}$ . As  $id = mgu(g^{\#}(1), g^{\#}(1))$  and  $(f^{\#}(0) \rightarrow f^{\#}(0))id = f^{\#}(0) \rightarrow f^{\#}(0)$ , by (1) in Definition 4 we have  $[f^{\#}(0) \rightarrow f^{\#}(0)] \in gunf(\mathcal{R}, 1)$ .

**Proposition 1.** For any  $n \in \mathbb{N}$  and  $[s^{\#} \to t^{\#}] \in gunf(\mathcal{R}, n)$  there exists some context C such that  $s \stackrel{+}{\xrightarrow{\mathcal{P}}} C[t]$ .

*Proof.* For some context C, we have  $s \to C[t] \in unf(\mathcal{R})$  where  $unf(\mathcal{R})$  is the unfolding semantics defined in [13]. So, by Prop. 3.12 of [13],  $s \xrightarrow{+}{\mathcal{R}} C[t]$ .

#### 4 Inferring Terms that Loop

As in [13], we use semi-unification [10] for detecting loops. Semi-unification encompasses both matching and unification, and a polynomial-time algorithm for it can be found in [8].

**Theorem 2.** For any  $n \in \mathbb{N}$ , if there exist  $[s^{\#} \to t^{\#}] \in gunf(\mathcal{R}, n)$  and some substitutions  $\theta_1$  and  $\theta_2$  such that  $s\theta_1\theta_2 = t\theta_1$ , then the term  $s\theta_1$  loops w.r.t.  $\mathcal{R}$ .

*Proof.* By Proposition 1,  $s \xrightarrow{+}{\mathcal{R}} C[t]$  for some context C. Since  $\xrightarrow{\to}{\mathcal{R}}$  is stable, we have

$$s\theta_1 \xrightarrow{+}{\mathcal{R}} C[t]\theta_1 \quad i.e., \quad s\theta_1 \xrightarrow{+}{\mathcal{R}} C\theta_1[t\theta_1] \quad i.e., \quad s\theta_1 \xrightarrow{+}{\mathcal{R}} C\theta_1[s\theta_1\theta_2].$$

Hence,  $s\theta_1$  loops w.r.t.  $\mathcal{R}$ .

Example 10 (Example 8 continued). We have

$$\underbrace{[\mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h})) \to \mathsf{f}^{\#}(\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}),\mathsf{s}(\mathsf{h}))]}_{U_{1}^{\prime\prime}} \in gunf(\mathcal{R},3)$$

with  $f(s(h), s(h), s(h))\theta_1\theta_2 = f(s(h), s(h), s(h))\theta_1$  for  $\theta_1 = \theta_2 = id$ . Consequently,  $f(s(h), s(h), s(h))\theta_1 = f(s(h), s(h), s(h))$  loops w.r.t.  $\mathcal{R}$ .

Example 11 (Example 9 continued).  $[f^{\#}(0) \rightarrow f^{\#}(0)] \in gunf(\mathcal{R}, 1)$  with  $f(0)\theta_1\theta_2 = f(0)\theta_1$  for  $\theta_1 = \theta_2 = id$ . Hence,  $f(0)\theta_1 = f(0)$  loops w.r.t.  $\mathcal{R}$ .

The substitutions  $\theta_1$  and  $\theta_2$  that we use in the next example are more sophisticated than in Examples 10 and 11.

Example 12. Let  $\mathcal{R} = \{f(g(x,0), y) \rightarrow f(g(0,x), h(y))\}$ . Then,  $SCC(\mathcal{R}) = \{\mathcal{C}\}$ where  $\mathcal{C}$  consists of the node  $N = f^{\#}(g(x,0), y) \rightarrow f^{\#}(g(0,x), h(y))$  and of the arc (N, N). Moreover, s-cycles $(\mathcal{R}) = \{[N]\}$  hence  $[N] \in gunf(\mathcal{R}, 0)$ . Therefore, as  $f(g(x,0), y)\theta_1\theta_2 = f(g(0,x), h(y))\theta_1$  for  $\theta_1 = \{x/0\}$  and  $\theta_2 = \{y/h(y)\}$ , by Theorem 2 we have that  $f(g(x,0), y)\theta_1 = f(g(0,0), y)$  loops w.r.t.  $\mathcal{R}$ .

We do not have an example where semi-unification is necessary for detecting a loop. In every example that we have considered, matching or unification were enough. However, semi-unification sometimes allows us to detect loops earlier in the unfolding process than with matching and unification. This is important in practice, because the number of unfolded rules can grow rapidly from iteration to iteration.

Example 13 (Example 12 continued). Semi-unification allows us to detect a loop at iteration 0 of  $GU_{\mathcal{R}}$ . But a loop can also be detected at iteration 1 using matching. Indeed, we have

$$N = \underbrace{\mathsf{f}^{\#}(\mathsf{g}(x,0),y)}_{l} \to \underbrace{\mathsf{f}^{\#}(\mathsf{g}(0,x),\mathsf{h}(y))}_{r}$$

with  $p = 1.1 \in DPos(r, l)$ . Hence,

$$U = \underbrace{\mathsf{f}^{\#}(\mathsf{g}(\mathbf{0},\mathbf{0}),y)}_{s^{\#}} \to \underbrace{\mathsf{f}^{\#}(\mathsf{g}(\mathbf{0},\mathbf{0}),\mathsf{h}(y))}_{t^{\#}} \in F_{\mathcal{R}}(l \to r,l,p).$$

So, by (4) in Definition 4, we have  $[U] \in gunf(\mathcal{R}, 1)$ . Notice that  $s\theta = t$  for  $\theta = \{y/h(y)\}$ , so s matches t. Moreover, by Theorem 2, s = f(g(0,0), y) loops w.r.t.  $\mathcal{R}$  (take  $\theta_1 = id$  and  $\theta_2 = \theta$ ).

#### 5 Further Comparisons with the Approach of [13]

The approach that we have presented in [13] relies on an unfolding operator  $U_{\mathcal{R}}$  (where  $\mathcal{R}$  is the TRS under analysis) which is also based on forward and backward narrowing (as  $F_{\mathcal{R}}$  and  $B_{\mathcal{R}}$  herein). But, unlike the technique that we have presented above, it directly unfolds the rules (not the dependency pairs) of  $\mathcal{R}$  and it does not compute any SCC. Moreover, it consists of a thorough computation of the iterations of  $U_{\mathcal{R}}$  followed by a mechanism for eliminating rules that cannot be further unfolded to a rule  $l \rightarrow r$  where l semi-unifies with r. Such rules are said to be *root-useless*. The set of root-useless rules is an overapproximation of the set of useless rules (rules that cannot contribute to detecting a loop), hence the elimination technique of [13] may remove some rules which are actually useful for detecting a loop. Our non-termination analyser which is based on [13] uses a time limit. It stops whenever it has detected a loop within the limit (then it answers NO, standing for No, this TRS does not terminate as in the Termination Competition [15]) or when the limit has been reached (then it answers TIME OUT) or when no more unfolded rule could be generated at some point within the limit  $((U_{\mathcal{R}} \uparrow n)(\mathcal{R}) = \emptyset$  for some n). In the last situation, either the TRS under analysis is not looping (it is terminating or non-looping non-terminating) or it is looping but a loop for it cannot be captured by the approach (for instance, the elimination mechanism has removed all the useful rules). In such a situation, our analyser answers DON'T KNOW.

Example 14. Consider the terminating TRS  $\mathcal{R} = \{0 \rightarrow 1\}$ . As the left-hand (resp. right-hand) side of the rule of  $\mathcal{R}$  cannot be narrowed backwards (resp. forwards) with  $\mathcal{R}$  then we have  $(U_{\mathcal{R}} \uparrow 1)(\mathcal{R}) = \emptyset$ .

In contrast, the approach that we have presented in Sects. 3–4 above avoids the generation of some rules by only unfolding disagreement pairs. Currently, in terms of loop detection power, we do not have any theoretical comparison between this new technique and that of [13]. Our new non-termination analyser also uses a time limit and answers NO, TIME OUT or DON'T KNOW when no more unfolded rules are generated at some point  $(gunf(\mathcal{R}, n) = \emptyset$  for some n, as in Example 14). Moreover, it allows the user to fix a selection strategy of disagreement pairs: in Definition 4, the conditions  $p \in DPos(r, s)$  (cases (2)–(3)) and  $p \in DPos(r, l)$  (case (4)) are replaced with  $p \in select_{\mathcal{R}}(l \to r, s \to t)$  and  $p \in select_{\mathcal{R}}(l \to r, l \to r)$  respectively, where  $select_{\mathcal{R}}$  can be one of the following functions. Selection of all the pairs:  $select_all_{\mathcal{R}}(l \rightarrow r, s \rightarrow t) = DPos(r, s)$ .

**Leftmost selection:** if  $DPos(r, s) = \emptyset$  then  $select\_lm_{\mathcal{R}}(l \to r, s \to t) = \emptyset$ , otherwise  $select\_lm_{\mathcal{R}}(l \to r, s \to t) = \{p\}$  where p is the leftmost disagreement position of r and s.

#### Leftmost selection with non-empty unfoldings:

 $select\_lmne_{\mathcal{R}}(l \rightarrow r, s \rightarrow t) = \{p\}$ 

where p is the leftmost disagreement position of r and s such that

$$F_{\mathcal{R}}(l \to r, s, p) \cup B_{\mathcal{R}}(s \to t, r, p) \neq \emptyset.$$

If such a position p does not exist then  $select\_lmne_{\mathcal{R}}(l \rightarrow r, s \rightarrow t) = \emptyset$ .

Example 15. Let  $\mathcal{R} = \{f(s(0), s(1), z) \to f(x, y, z)\}$  and  $l = f^{\#}(s(0), s(1), z)$  and  $r = f^{\#}(x, y, z)$ . Then, we have  $select\_all_{\mathcal{R}}(l \to r, l \to r) = DPos(r, l) = \{1, 2\}$ . Moreover, 1 is the leftmost disagreement position of r and l because  $r|_1 = x$  occurs to the left of  $r|_2 = y$  in r and  $l|_1 = s(0)$  occurs to the left of  $l|_2 = s(1)$  in l. Therefore, we have  $select\_lm_{\mathcal{R}}(l \to r, l \to r) = \{1\}$ .

Example 16. Let  $\mathcal{R} = \{f(x, x) \to f(g(x), h(x)), h(x) \to g(x)\}$ . Then,  $SCC(\mathcal{R}) = \{\mathcal{C}\}$  where  $\mathcal{C}$  consists of the node  $N = l \to r = f^{\#}(x, x) \to f^{\#}(g(x), h(x))$  and of the arc (N, N). Then,  $DPos(r, l) = \{1, 2\}$  and  $select\_lm_{\mathcal{R}}(N, N) = \{1\}$ . As  $F_{\mathcal{R}}(N, l, 1) \cup B_{\mathcal{R}}(N, r, 1) = \emptyset$  and  $F_{\mathcal{R}}(N, l, 2) \cup B_{\mathcal{R}}(N, r, 2) \neq \emptyset$  (for instance,  $f^{\#}(x, x) \to f^{\#}(g(x), g(x)) \in F_{\mathcal{R}}(N, l, 2)$  is obtained from narrowing  $r|_2 = h(x)$  forwards with  $h(x) \to g(x)$ ), then  $select\_lmne_{\mathcal{R}}(N, N) = \{2\}$ .

As the approach of [13], and depending on the strategy used for selecting disagreement pairs, our new technique is not complete in the sense that it may miss some loop witnesses.

Example 17 (Example 16 continued). We have  $gunf(\mathcal{R}, 0) = s$ -cycles $(\mathcal{R}) = \{[N]\}$ . As l does not semi-unify with r, no loop is detected from  $gunf(\mathcal{R}, 0)$ , so we go on and compute  $gunf(\mathcal{R}, 1)$ . Only case (4) of Definition 4 is applicable to [N]. First, suppose that  $select_{\mathcal{R}} = select\_lm_{\mathcal{R}}$ . Then,  $select_{\mathcal{R}}(N,N) = \{1\}$  and, as  $F_{\mathcal{R}}(N,l,1) \cup B_{\mathcal{R}}(N,r,1) = \emptyset$ , case (4) does not produce any rule. Consequently, we have  $gunf(\mathcal{R}, 1) = \emptyset$ , hence no loop is detected for  $\mathcal{R}$ . Now, suppose that  $select_{\mathcal{R}} = select\_lmne_{\mathcal{R}}$ . Then,  $select_{\mathcal{R}}(N,N) = \{2\}$ . Narrowing  $r|_2 = h(x)$  forwards with  $h(x) \rightarrow g(x)$ , we get the rule  $N' = f^{\#}(x,x) \rightarrow f^{\#}(g(x),g(x))$  which is an element of  $F_{\mathcal{R}}(N,l,2)$ . Hence,  $[N'] \in gunf(\mathcal{R},1)$ . As in N' we have that f(x,x) semi-unifies with f(g(x),g(x)) (take  $\theta_1 = id$  and  $\theta_2 = \{x/g(x)\}$ ), then f(x,x) loops  $w.r.t. \mathcal{R}$ . This loop is also detected by the approach of [13].

#### 6 Experiments

We have implemented the technique of this paper in our analyser  $NTI^1$  (Non-Termination Inference). For our experiments, we have extracted from the directory TRS\_Standard of the TPBD [17] all the valid rewrite systems<sup>2</sup> that were

<sup>&</sup>lt;sup>1</sup> http://lim.univ-reunion.fr/staff/epayet/Research/NTI/NTI.html.

<sup>&</sup>lt;sup>2</sup> Surprisingly, the subdirectory **Transformed\_CSR\_04** contains 60 files where a pair  $l \rightarrow r$  with  $Var(r) \not\subseteq Var(l)$  occurs. These pairs are not valid rewrite rules.

either proved looping or unproved<sup>3</sup> during the Termination Competition 2017 (TC'17) [15]. Otherwise stated, we removed from TRS\_Standard all the non-valid TRSs and all the TRSs that were proved terminating or non-looping non-terminating by a tool participating in the competition. We ended up with a set S of 333 rewrite systems. We let  $\mathcal{L}$  (resp.  $\mathcal{U}$ ) be the subset of S consisting of all the systems that were proved looping (resp. that were unproved) during TC'17. Some characteristics of  $\mathcal{L}$  and  $\mathcal{U}$  are reported in Table 1. Note that the complete set of simple cycles of a TRS may be really huge, hence NTI only computes a subset of it. The simple cycle characteristics given in Table 1 relate to the subsets computed by NTI.

$\mathcal{S} = \mathcal{L} \uplus \mathcal{U}$ (333 TRSs)	$\mathcal{L}$ (173	TRSs)		$\mathcal{U}$ (160 TRSs)				
	Min	Max	Average	Min	Max	Average		
TRS size	1 [17]	104 [1]	11.08	1 [9]	837 [1]	64.78		
Number of SCCs	1 [101]	$12 \ [1]$	1.95	1 [70]	130 [1]	6.14		
SCC size	1 [96]	$192 \ [1]$	4.44	$1 \ [54]$	473 [1]	10.79		
Number of simple cycles	1 [47]	$185 \ [1]$	8.55	$2 \ [15]$	1,176 [1]	69.02		
Simple cycle size	1 [157]	9 [2]	2.23	$1 \ [157]$	9 [4]	2.21		
Number of symbols	$1 \ [4]$	66 [1]	9.09	$2 \ [7]$	259 [1]	24.14		
Symbol arity	$0 \ [153]$	5 [2]	1.07	$0 \ [150]$	12 [2]	1.94		
Number of defined symbols	$1 \ [28]$	58 [1]	5.21	$1 \ [15]$	132 [2]	17.19		
Defined symbol arity	0 [74]	5 [2]	1.38	0 [28]	12 [2]	2.27		

**Table 1.** Some characteristics of the analysed TRSs. Sizes are in number of rules. In square brackets, we report the number of TRSs with the corresponding min or max.

We have run our new approach (NTI'18) and that of [13] (NTI'08) on the TRSs of S. The results are reported in Tables 2 and 3. We used an Intel 2-core i5 at 2 GHz with 8 GB of RAM and the time limit fixed for a proof was 120 s. For every selection strategy, NTI'18 issues more successful proofs (NO) and generates less unfolded rules than NTI'08. Moreover, as it avoids the generation of some rules instead of computing all the unfolding and then eliminating some rules (as NTI'08 does), its times are better. At the bottom of the tables, we give the numbers of TRSs proved looping by both approaches and by one approach only. NTI'18 succeeds on all the TRSs of  $\mathcal{L}$  on which NTI'08 succeeds, but it fails on one TRS of  $\mathcal{U}$  on which NTI'08 succeeds. This is due to our simplified computation of the set of simple cycles: our algorithm does not generate the cycle that would allow NTI'18 to succeed and NTI'18 times out, trying to unfold syntactic loops from which it cannot detect anything. Another point to note is that the implementation of the new approach does not need to run several

<sup>&</sup>lt;sup>3</sup> By *unproved* we mean that no tool succeeded in proving that these TRSs were terminating or non-terminating (all the tools failed on these TRSs).

analyses in parallel to achieve the results presented in Tables 2 and 3. One single thread of computation is enough. On the contrary, for the approach of [13], 3 parallel threads are necessary: one with forward unfoldings only, one with backward unfoldings only and one with forward and backward unfoldings together. The results get worse if NTI'08 only runs one thread performing forward and backward unfoldings together. In Tables 2 and 3, we report in square brackets the number of successes of NTI'08 when it only runs one thread performing both forward and backward unfoldings.

$\mathcal{L}$ (173 TRSs)	NTI'08	NTI'18						
		$select\_all$	$select\_lm$	$select\_lmne$				
NO	$152 \ [149]$	157	157	158				
DON'T KNOW	0	0	1	0				
TIME OUT	21	16	15	15				
Time	$2{,}966\mathrm{s}$	$2{,}194{\rm s}$	$1,\!890\mathrm{s}$	$1,\!889\mathrm{s}$				
Generated rules	$11,\!167,\!976$	9,030,962	8,857,421	8,860,560				
$NO(NTI'08) \cap N$	O(NTI'18)	152	151	152				
$NO(NTI'08) \setminus NO(NTI'08)$	O(NTI'18)	0	1	0				
$NO(NTI'18) \setminus NO(NTI'18)$	D(NTI'08)	5	6	6				

Table 2. Analysis results on the TRSs of  $\mathcal{L}$ .

Table 3. Analysis results on the TRSs of  $\mathcal{U}$ .

$\mathcal{U}$ (160 TRSs)	NTI'08	NTI'18					
		$select\_all$	$select\_lm$	$select\_lmne$			
NO	4 [3]	6	6	6			
DON'T KNOW	0	0	1	0			
TIME OUT	156	154	153	154			
Time	$18{,}742\mathrm{s}$	$18{,}563\mathrm{s}$	$18{,}414\mathrm{s}$	$18{,}534\mathrm{s}$			
Generated rules	$64,\!011,\!002$	$53,\!134,\!334$	$61,\!245,\!705$	$63,\!300,\!604$			
$NO(NTI'08) \cap N$	O(NTI'18)	3	3	3			
$NO(NTI'08) \setminus NO(NTI'08)$	O(NTI'18)	1	1	1			
$NO(NTI'18) \setminus NO(NTI'18)$	D(NTI'08)	3	3	3			

Four tools participated in the category *TRS Standard* of TC'17: AProVE [2,5], MU-TERM [11], NaTT [12] and WANDA [9]. The numbers of TRSs proved looping by each of them during the competition is reported in Table 4. An important point to note here is that the time limit fixed in TC'17 was 300s, whereas in our experiments with NTI'18 and NTI'08 it was 120s. Moreover, the machine we

$\mathcal{S} = \mathcal{L} \uplus \mathcal{U} (333 \text{ TRSs})$	TC'17 (1	time limit $=$	$300\mathrm{s})$	(time limit $= 120  s$ )			
	AProVE	MU-TERM	NaTT	WANDA	NTI'08	$NTI'18(select\_lmne)$	
$\mathcal{L}$ (173 TRSs)	172	81	109	0	152	158	
$\mathcal{U}$ (160 TRSs)	0	0	0	0	4	6	

Table 4. Number of successes (NO) on  $\mathcal{L}$  and  $\mathcal{U}$  obtained during TC'17 and those obtained by NTI during our experiments.

used (an Intel 2-core i5 at 2 GHz with 8 GB of RAM) is much less powerful than the machine used during TC'17 (the StarExec platform [14] running on an Intel Xeon E5-2609 at 2.4 GHz with 129 GB of RAM). All the tools of TC'17 failed on all the rewrite systems of  $\mathcal{U}$ . In contrast, NTI'18 (resp. NTI'08) finds a loop for 6 (resp. 4) of them. Regarding  $\mathcal{L}$ , AProVE was able to prove loopingness of 172 out of 173 TRSs. The only TRS of  $\mathcal{L}$  on which AProVE failed<sup>4</sup> was proved looping by NaTT. In comparison, our approach succeeds on 158 systems of  $\mathcal{L}$ , less than AProVE but more than the other tools of TC'17. Similarly to our approach, AProVE handles the SCCs of the estimated dependency graph independently, but it first performs a termination analysis. The non-termination analysis is then only applied to those SCCs that could not be proved terminating. On the contrary, NTI only performs non-termination proofs. If an SCC is terminating, it cannot prove it and keeps on trying a non-termination proof, unnecessarily generating unfolded rules at the expense of the analysis of the other SCCs. The loop detection techniques implemented in AProVE and NTI'18 are based on the idea of searching for loops by forward and backward narrowing of dependency pairs and by using semi-unification to detect potential loops. This idea has been presented in [6] where heuristics are used to select forward or backward narrowing. Note that in constrast, the technique that we present herein does not use any heuristics and proceeds both forwards and backwards.

#### 7 Conclusion

We have reconsidered and modified the unfolding-based technique of [13] for detecting loops in standard term rewriting. The new approach uses disagreement pairs for guiding the unfoldings, which now are only partially computed, whereas the technique of [13] consists of a thorough computation followed by a mechanism for eliminating some rules. Two theoretical questions remain open: in terms of loop detection, is an approach more powerful than the other and does semiunification subsume matching and unification?

We have implemented the new approach in our tool NTI and compared it to [13] on a set of 333 rewrite systems. The new results are better (better times, more successful proofs, less unfolded rules). Moreover, the approach compares well to the tools that participated in TC'17. However, the number of generated

<sup>&</sup>lt;sup>4</sup> Ex6\_15\_AEL02\_FR.xml in the directory TRS\_Standard/Transformed\_CSR\_04.

rules is still important. In an attempt to reduce it, during our experiments we added the elimination mechanism of [13] to the new approach, but the results we recorded were not satisfactory (an equivalent, slightly smaller, number of generated rules but, due to the computational overhead, bigger times and less successes); hence, we removed it. Termination analysis may help to reduce the number of unfolded rules by detecting terminating SCCs in the estimated dependency graph *i.e.*, SCCs on which it is useless to try a non-termination proof. In other words, we could use termination analysis as an elimination mechanism. Several efficient and powerful termination analysers have been implemented so far [16] and one of them could be called by NTI. A final idea to improve our approach would be to consider more sophisticated strategies for selecting disagreement pairs.

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# Homeomorphic Embedding Modulo Combinations of Associativity and Commutativity Axioms

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Abstract. The Homeomorphic Embedding relation has been amply used for defining termination criteria of symbolic methods for program analysis, transformation, and verification. However, homeomorphic embedding has never been investigated in the context of ordersorted rewrite theories that support symbolic execution methods *modulo* equational axioms. This paper generalizes the symbolic homeomorphic embedding relation to order–sorted rewrite theories that may contain various combinations of associativity and/or commutativity axioms for different binary operators. We systematically measure the performance of increasingly efficient formulations of the homeomorphic embedding relation modulo associativity and commutativity axioms. From our experimental results, we conclude that our most efficient version indeed pays off in practice.

# 1 Introduction

Homeomorphic Embedding is a control mechanism that is commonly used to ensure termination of symbolic methods and program optimization techniques. Homeomorphic embedding is a structural preorder relation under which a term t' is greater than (i.e., it embeds) another term t represented by  $t \leq t'$  if t can be obtained from t' by deleting some symbols of t'. For instance, v = s(0 + s(X)) \* s(X + Y) embeds u = s(X) \* s(Y). The usefulness of homeomorphic embedding for ensuring termination is given by the following well-known property of wellquasi-orderings: given a finite signature, for every infinite sequence of terms

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 $t_1, t_2, \ldots$ , there exist i < j such that  $t_i \leq t_j$ . Therefore, if we iteratively compute a sequence  $t_1, t_2, \ldots, t_n$ , we can guarantee finiteness of the sequence by using the embedding as a whistle: whenever a new expression  $t_{n+1}$  is to be added to the sequence, we first check whether  $t_{n+1}$  embeds any of the expressions that are already in the sequence. If that is the case, the computation must be stopped because the whistle ( $\leq$ ) signals (potential) non-termination. Otherwise,  $t_{n+1}$  can be safely added to the sequence and the computation proceeds.

In [2], an extension of homeomorphic embedding modulo equational axioms, such as associativity and commutativity, was defined as a key component of the symbolic partial evaluator Victoria. Unfortunately, the formulation in [2] was done with a concern for simplicity in mind and degrades the tool performance because the proposed implementation of equational homeomorphic embedding did not scale well to realistic problems. This was not unexpected since other equational problems (such as equational matching, equational unification, or equational least general generalization) are typically much more involved than their corresponding "syntactic" counterparts, and achieving efficient implementations has required years of significant investigation.

**Our Contribution.** In this paper, we introduce four different formulations of homeomorphic embedding modulo axioms in rewrite theories that may contain sorts, subsort polymorphism, overloading, and rewriting with (conditional) rules and equations modulo a set B of equational axioms, and we compare their performance. We propose an equational homeomorphic embedding formulation  $\trianglelefteq_B^{sml}$  that runs up to 5 orders of magnitude faster than the original definition of  $\trianglelefteq_B$  in [2]. For this improvement in performance, we take advantage of Maude's powerful capabilities such as the efficiency of deterministic computations with equations versus non-deterministic computations with rewriting rules, or the use of non-strict definitions of the boolean operators versus more speculative standard boolean definitions [5].

Plan of the Paper. After some preliminaries in Sect. 2, Sect. 3 recalls the homeomorphic equational embedding relation of [2] that extends the "syntactically simpler" homeomorphic embedding on nonground terms to the order-sorted case modulo equational axioms. Section 4 provides two goal-driven formulations for equational homeomorphic embedding: first, a calculus for embeddability goals that directly handles the algebraic axioms in the deduction system, and then a reachability oriented characterization that cuts down the search space by taking advantage of pattern matching modulo associativity and commutativity axioms. Section 5 is concerned with an efficient meta-level formulation of equational homeomorphic embedding that relies on the classical flattening transformation that canonizes terms w.r.t. associativity and/or commutativity axioms (for instance, 1 + (2 + 3) gets flattened to +(1, 2, 3)). An improvement of the algorithm is also achieved by replacing the classical boolean operators by shortcircuit, strategic versions of these operators. We provide an experimental performance evaluation of the proposed formulations showing that we can efficiently deal with realistic embedding problems modulo axioms.

### 2 Preliminaries

We introduce some key concepts of order-sorted rewriting logic theories, see [5] for further details.

Given an order-sorted signature  $\Sigma$ , with a finite poset of sorts  $(S, \leq)$ , we consider an S-sorted family  $\mathscr{X} = \{\mathscr{X}_s\}_{s\in S}$  of disjoint variable sets.  $\mathscr{T}_{\Sigma}(\mathscr{X})_s$  and  $\mathscr{T}_{\Sigma_s}$  denote the sets of terms and ground terms of sorts s, respectively. We also write  $\mathscr{T}_{\Sigma}(\mathscr{X})$  and  $\mathscr{T}_{\Sigma}$  for the corresponding term algebras. In order to simplify the presentation, we often disregard sorts when no confusion can arise. A position p in a term t is represented by a sequence of natural numbers ( $\Lambda$  denotes the empty sequence, i.e., the root position).  $t|_p$  denotes the subterm of t at position p, and  $t[u]_p$  denotes the result of replacing the subterm  $t|_p$  by the term u. A substitution  $\sigma$  is a sorted mapping from a finite subset of  $\mathscr{X}$  to  $\mathscr{T}_{\Sigma}(\mathscr{X})$ . The application of a substitution  $\sigma$  to a term t is called an instance of t and is denoted by  $t\sigma$ .

A  $\Sigma$ -equation is an unoriented pair t = t', where  $t, t' \in \mathscr{T}_{\Sigma}(\mathscr{X})_{s}$  for some sort  $s \in S$ . Given  $\Sigma$  and a set E of  $\Sigma$ -equations, order-sorted equational logic induces a congruence relation  $=_E$  on terms  $t, t' \in \mathscr{T}_{\Sigma}(\mathscr{X})$  (see [4]). An equational theory  $(\Sigma, E)$  is a pair with  $\Sigma$  being an order-sorted signature and E a set of  $\Sigma$ -equations. A substitution  $\theta$  is more (or equally) general than  $\sigma$  modulo E, denoted by  $\theta \leq_E \sigma$ , if there is a substitution  $\gamma$  such that  $\sigma =_E \theta \gamma$ , i.e., for all  $x \in \mathscr{X}, x\sigma =_E x\theta\gamma$ . A substitution  $\sigma$  is called a renaming if  $\sigma = \{X_1 \mapsto$  $Y_1, \ldots, X_n \mapsto Y_n\}$ , the sorts of  $X_i$  and  $Y_i$  coincide, and variables  $Y_1, \ldots, Y_n$  are pairwise distinct. The renaming substitution  $\sigma$  is a renaming for expression E if  $(\mathscr{V}ar(E) - \{X, \ldots, X_n\}) \cap \{Y_1, \ldots, Y_n\} = \emptyset$ .

A rewrite theory is a triple  $\mathscr{R} = (\Sigma, E, R)$ , where  $(\Sigma, E)$  is the equational theory modulo that we rewrite and R is a set of rewrite rules. Rules are of the form  $l \to r$  where terms  $l, r \in \mathscr{T}_{\Sigma}(\mathscr{X})_{\mathsf{s}}$  for some sort  $\mathsf{s}$  are respectively called the *left-hand side* (or *lhs*) and the *right-hand side* (or *rhs*) of the rule and  $\mathscr{V}ar(r) \subseteq \mathscr{V}ar(l)$ . Let  $\to \subseteq A \times A$  be a binary relation on a set A. We denote its transitive closure by  $\to^+$ , and its reflexive and transitive closure by  $\to^*$ . We define the relation  $\to_{R,E}$  on  $\mathscr{T}_{\Sigma}(\mathscr{X})$  by  $t \to_{p,R,E} t'$  (or simply  $t \to_{R,E} t'$ ) iff there is a non-variable position  $p \in Pos_{\Sigma}(t)$ , a rule  $l \to r$  in R, and a substitution  $\sigma$  such that  $t|_p =_E l\sigma$  and  $t' = t[r\sigma]_p$ .

#### 2.1 Pure Homeomorphic Embedding

The pure (syntactic) homeomorphic embedding relation known from term algebra [10] was introduced by Dershowitz for variable-arity symbols in [6] and for fixed-arity symbols in [7]. In the following, we consider only fixed-arity symbols.

**Definition 1 (Homeomorphic embedding, Dershowitz** [7]). The homeomorphic embedding relation  $\blacktriangleleft$  over  $\mathscr{T}_{\Sigma}$  is defined as follows, with  $n \ge 0$ :

$$\frac{\exists i \in \{1, \dots, n\} : s \blacktriangleleft t_i}{s \blacktriangleleft f(t_1, \dots, t_n)} \qquad \qquad \frac{\forall i \in \{1, \dots, n\} : s_i \blacktriangleleft t_i}{f(s_1, \dots, s_n) \blacktriangleleft f(t_1, \dots, t_n)}$$

Roughly speaking, the left inference rule deletes subterms, while the right inference rule deletes context. We write  $s \leq t$  if s is derivable from t using the above rules. When  $s \leq t$ , we say that s is (syntactically) *embedded* in t (or t syntactically *embeds* s). Note that  $\equiv \subseteq \triangleleft$ , where  $\equiv$  denotes syntactic identity.

A well-quasi ordering  $\leq$  is a transitive and reflexive binary relation such that, for any infinite sequence of terms  $t_1, t_2, \ldots$  with a finite number of operators, there exist j, k with j < k and  $t_j \leq t_k$ .

**Theorem 1 (Tree Theorem, Kruskal** [10]). The embedding relation  $\blacktriangleleft$  is a well-quasi-ordering on  $\mathscr{T}_{\Sigma}$ .

The derivability relation given by  $\blacktriangleleft$  is mechanized in [14] by introducing the following term rewriting system  $Emb(\Sigma)$  as follows:  $t \blacktriangleleft t'$  if and only if  $t' \rightarrow_{Emb(\Sigma)}^{*} t$ .

**Definition 2 (Homeomorphic embedding rewrite rules, Middeldorp** [14]). Let  $\Sigma$  be a signature. The homeomorphic embedding can be decided by a rewrite theory  $Emb(\Sigma) = (\Sigma, \emptyset, R)$  such that R consists of rewrite rules of the form  $f(X_1, \dots, X_n) \to X_i$  where  $f \in \Sigma$  is a function symbol of arity  $n \ge 1$  and  $i \in \{1, \dots, n\}$ .

Definition 1 can be applied to terms of  $\mathscr{T}_{\Sigma}(\mathscr{X})$  by simply regarding the variables in terms as constants. However, this definition cannot be used when existentially quantified variables are considered. The following definition from [11, 15] adapts the pure (syntactic) homeomorphic embedding from [6] by adding a simple treatment of logical variables where all variables are treated as if they were identical, which is enough for many symbolic methods such as the partial evaluation of [2]. Some extensions of  $\trianglelefteq$  dealing with varyadic symbols and infinite signatures are investigated in [12].

**Definition 3 (Variable-extended homeomorphic embedding, Leuschel** [11]). The extended homeomorphic embedding relation  $\trianglelefteq$  over  $\mathscr{T}_{\Sigma}(\mathscr{X})$  is defined in Fig. 1, where the Variable inference rule allows dealing with free (unsorted) variables in terms, while the Diving and Coupling inference rules are equal to the pure (syntactic) homeomorphic embedding definition.

VariableDivingCoupling
$$x \leq y$$
 $\exists i \in \{1, ..., n\} : s \leq t_i$  $\forall i \in \{1, ..., n\} : s_i \leq t_i$  $x \leq y$  $\exists i \in \{1, ..., n\} : s \leq t_i$  $\forall i \in \{1, ..., n\} : s_i \leq t_i$ 

Fig. 1. Variable-extended homeomorphic embedding

The extended embedding relation  $\leq$  is a well-quasi-ordering on the set of terms  $\mathscr{T}_{\Sigma}(\mathscr{X})$  [11,15]. An alternative characterization without the hassle of explicitly handling variables can be proved as follows.

**Lemma 1** (Variable-less characterization of  $\leq$ ). Given a signature  $\Sigma$ , let  $\Sigma^{\sharp}$  be an extension of  $\Sigma$  with a new constant  $\sharp$ , and let  $t^{\sharp}$  denote the (ground) instance of t where all variables have been replaced by  $\sharp$ . Given terms  $t_1$  and  $t_2$ ,  $t_1 \leq t_2$  iff  $t_1^{\sharp} \leq t_2^{\sharp}$  iff  $t_1^{\sharp} \leq t_2^{\sharp}$ .

Moreover, Lemma 1 above allows the variable-extended relation  $\trianglelefteq$  of Definition 3 to be mechanized in a way similar to the rewriting relation  $\rightarrow^*_{Emb(\Sigma)}$  used in Definition 2 for the embedding  $\blacktriangleleft$  of Definition 1:  $t_1 \trianglelefteq t_2$  if and only if  $t_2^{\ddagger} \rightarrow^*_{Emb(\Sigma^{\ddagger})} t_1^{\ddagger}$ . By abuse of notation, from now on, we will indistinctly consider either terms with variables or ground terms with  $\ddagger$ , whenever one formulation is simpler than the other.

# 3 Homeomorphic Embedding Modulo Equational Axioms

The following definition given in [2] extends the "syntactically simpler" homeomorphic embedding relation on nonground terms to the order-sorted case *modulo* a set of axioms *B*. The (order-sorted) relation  $\trianglelefteq_B$  is called *B*-embedding (or embedding modulo *B*). We define  $v \stackrel{ren}{=}_B v'$  iff there is a renaming substitution  $\sigma$  for v' such that  $v =_B v' \sigma$ .

**Definition 4 ((Order-sorted) homeomorphic embedding modulo** *B*). We define the *B*-embedding relation  $\leq_B$  (or embedding modulo *B*) as  $\binom{ren}{=}_B$ . $(\leq)$ .  $\binom{ren}{=}_B$ .

*Example 1.* Consider the following rewrite theory (written in Maude syntax) that defines the signature of natural numbers, with sort Nat and constructor operators 0, and suc for sort Nat. We also define the associative and commutative addition operator symbol \_+\_.

```
fmod NAT is sort Nat .
  op 0 : -> Nat .
  op suc : Nat -> Nat .
  op _+_ : Nat Nat -> Nat [assoc comm] .
endfm
```

We have  $+(1, X:Nat) \leq_B +(Y:Nat, +(1, 3))$  because +(Y:Nat, +(1, 3)) is equal to +(1, +(Y:Nat, 3)) modulo AC, and  $+(1, X:Nat) \leq +(1, +(Y:Nat, 3))$ .

The following result extends Kruskal's Tree Theorem for the equational theories considered in this paper. We have to restrict it to the class of finite equational theories in order to prove the result.  $\mathscr{B}$  is called *class-finite* if all  $\mathscr{B}$ -equivalence classes are finite. This includes the class of permutative equational theories. An equational theory  $\mathscr{E}$  is permutative if for all terms t, t', the fact that  $t =_{\mathscr{E}} t'$ implies that the terms t and t' contain the same symbols with the same number of occurrences [9]. Permutative theories include any theory with any combination of symbols obeying any combination of associativity and commutativity axioms. **Theorem 2.** For class-finite theories, the embedding relation  $\trianglelefteq_B$  is a well-quasi ordering of the set  $\mathscr{T}_{\Sigma}(\mathscr{X})$  for finite  $\Sigma$ , that is,  $\trianglelefteq_B$  is a quasi-order.

Function symbols with variable arity are sometimes seen as associative operators. Let us briefly discuss the homeomorphic embedding modulo axioms  $\leq_B$ of Definition 4 in comparison to the variadic extension  $\leq^{\nu}$  of Definition 1 as given in [6]:

Diving  

$$\frac{\exists i \in \{1, \dots, n\} : s \underline{\blacktriangleleft}^{v} t_{i}}{s \underline{\blacktriangleleft}^{v} f(t_{1}, \dots, t_{n})} \qquad \frac{\forall i \in \{1, \dots, m\} : s_{i} \underline{\blacktriangleleft}^{v} t_{j_{i}}, \text{with } 1 \leq j_{1} < j_{2} < \dots < j_{m} \leq n}{f(s_{1}, \dots, s_{m}) \underline{\blacktriangleleft}^{v} f(t_{1}, \dots, t_{n})}$$

Example 2. Consider a variadic version of the addition symbol + of Example1 that allows any number of natural numbers to be used as arguments; for instance, +(1,2,3). On the one hand,  $+(1) \leq (+(1,2,3))$  whereas  $+(1) \leq R_B$ +(1,2,3), with B consisting of the associativity and commutativity axioms for the operator + (actually, +(1) is ill-formed). On the other hand, we have both  $+(1,2) \leq (+(1,0,3,2))$  and  $+(1,2) \leq (+(1,0,3,2))$ . This is because any well-formed term that consists of the addition (in any order) of the constants 0, 1, 2, and 3 (for instance, +(+(1,0),+(3,2))) can be given a flat representation +(1,0,2,3). Note that there are many other equivalent terms, e.g., +(+(1,2),+(3,0)) or +(+(1,+(3,2)),0), all of which are represented by the flattened term +(0, 1, 2, 3). Actually, because of the associativity and commutativity of symbol +, flattened terms like +(1, 0, 2, 3) can be further simplified into a single<sup>1</sup> canonical representative +(0, 1, 2, 3), hence also  $+(1, 2) \leq_B +(0, 1, 2, 3)$ . A more detailed explanation of flat terms can be found in Sect. 5. However, note that  $+(2,1) \leq_B +(1,0,3,2)$  but  $+(2,1) \not\leq^{v} +(1,0,3,2)$  because the  $\leq^{v}$  does not consider the commutativity of symbol +.

Roughly speaking, in the worst case, the homeomorphic embedding modulo axioms of Definition 4,  $t \leq_B t'$ , amounts to considering all the elements in the *B*-equivalence classes of t and t' and then checking for standard homeomorphic embedding,  $u \leq u'$ , every pair u and u' of such terms, one term from each class. According to Definition 2, checking  $u \leq u'$  essentially boils down to the reachability analysis given by  $u' \rightarrow_{Emb(\Sigma)}^* u$ . Unfortunately, the enumeration of all terms in a *B*-equivalence class is impractical, as shown in the following example.

Example 3. Consider the AC binary symbol + of Example 1 and the terms t = +(1, 2) and t' = +(2, +(3, 1)). The AC-equivalence class of t contains two terms whereas the AC-equivalence class of t' contains 12 terms. This implies computing 24 reachability problems  $u' \rightarrow_{Emb(\Sigma)}^{*} u$  in order to decide  $t \leq_{AC} t'$ , in the worst case. Moreover, we know a priori that half of these reachability tests will fail (those in which 1 and 2 occur in different order in u' and u; for instance u' = +(1, +(2, 3)) and u = +(2, 1).

<sup>&</sup>lt;sup>1</sup> Maude uses a term lexicographic order for the arguments of flattened terms [8].

A more effective rewriting characterization of  $\leq_B$  can be achieved by lifting Definition 2 to the order-sorted and *modulo* case in a natural way. However, ill-formed terms can be produced by naïvely applying the rules  $f(X_1, \ldots, X_n) \to X_i$  of Definition 2 to typed (i.e., order-sorted) terms. For example, " $(0 \leq 1)$  or true"  $\to$  "0 or true".

In the order-sorted context we can overcome this drawback as follows. Assume that  $\Sigma$  has no ad-hoc overloading. We can extend  $\Sigma$  to a new signature  $\Sigma^{\mathscr{U}}$  by adding a new top sort  $\mathscr{U}$  that is bigger than all other sorts. For each  $f : A_1, \ldots, A_n \to A$  in  $\Sigma$ , we add the rules  $f(X_1: \mathscr{U}, \ldots, X_n: \mathscr{U}) \to X_i: \mathscr{U}, 1 \leq i \leq n$ . In this way, rewriting with  $\to_{Emb(\Sigma^{\mathscr{U}}),B}^*$  becomes a relation between well-formed  $\Sigma^{\mathscr{U}}$ -terms, see [2].

**Definition 5.** ((Order-sorted) homeomorphic embedding rewrite rules modulo B [2]). Let  $\Sigma$  be an order-sorted signature and B be a set of axioms. Let us introduce the following signature transformation  $\Sigma \ni (f : \mathbf{s}_1 \dots \mathbf{s}_n \to \mathbf{s}) \mapsto (f : \mathcal{U} \ \overset{n}{\ldots} \ \mathcal{U} \to \mathcal{U}) \in \Sigma^u$ , where  $\mathcal{U}$  conceptually represents a universal supersort of all sorts in  $\Sigma$ . Also, for any  $\Sigma$ -term t,  $t^u$  leaves the term t unchanged but regards all its variable as unsorted (i.e., of sort  $\mathcal{U}$ ). We define the TRS  $Emb(\Sigma)$  that consists of all rewrite rules  $f(X_1:\mathcal{U}, \dots, X_n:\mathcal{U}) \to X_i:\mathcal{U}$  for each  $f : A_1, \dots, A_n \to A$  in  $\Sigma$  and  $i \in \{1, \dots, n\}$ .

In the sequel, we consider equational theories B that may contain any combination of associativity and/or commutativity axioms for any binary symbol in the signature. Also, for the sake of simplicity we often omit sorts when no confusion can arise.

**Proposition 1.** Given  $\Sigma$ , B, and t, t' in  $\mathscr{T}_{\Sigma}(\mathscr{X}), t \trianglelefteq_B t'$  iff  $(t'^u)^{\sharp} \to^* Emb((\Sigma^{\mathscr{U}})^{\sharp}), B(t^u)^{\sharp}$ .

*Example 4.* Consider the order-sorted signature for natural numbers of Example 1. Let us represent by sort U in Maude the unique (top) sort of the transformed signature:

```
fmod NAT-U is sort U .
   op 0 : -> U .
   op suc : U -> U .
   op _+_ : U U -> U [assoc comm] .
endfm
```

Likewise, the terms expressed in  $\Sigma$  must also be transformed to be expressed as  $\Sigma^{\mathscr{U}}$ -terms. For instance, given the  $\Sigma$ -terms  $t = X: \operatorname{Nat}^2$  and  $t' = \operatorname{suc}(Y: \operatorname{Nat})$ , the corresponding  $\Sigma^{\mathscr{U}}$ -terms are t = X: U and  $\operatorname{suc}(Y: U)$ , respectively.

The associated TRS  $Emb(\Sigma)$  contains the following two rules: + $(X_1:U, X_2:U) \rightarrow X_1:U$  and + $(X_1:U, X_2:U) \rightarrow X_2:U$ . However, since the rules of  $Emb(\Sigma)$  are applied modulo the commutativity of symbol +, in practice, we can get rid of either of the two rules above since only one is required in Maude.

 $<sup>^2</sup>$  The expression  $X{:}S$  represents an explicit definition of a variable X of sort S in Maude.

Example 5. Following Example 3, instead of comparing pairwisely all terms in the equivalence classes of t and t', we choose  $Emb(\Sigma)$  to contain just the rewrite rule  $+(X_1:U, X_2:U) \rightarrow X_2:U$ , we use it to prove the rewrite step  $+(2, +(3, 1)) \rightarrow_{Emb(\Sigma),B} +(2, 1)$ , and finally we check that  $+(2, 1) =_B +(1, 2)$ , with  $B = \{A, C\}$ . However, there are six alternative rewriting steps stemming from the initial term +(2, +(3, 1)), all of which result from applying the very same rewrite rule above to the term (modulo AC), five of which are useless for proving the considered embedding (the selected redex is underlined):

$+(2, \underline{+(3,1)}) \rightarrow_{Emb(\Sigma),B} +(2,1)$	$\underline{+(2,+(3,1))} \rightarrow_{Emb(\Sigma),B} 1$
$+(2, \underline{+(3,1)}) \rightarrow_{Emb(\Sigma),B} +(2,3)$	$\underline{+(2,+(3,1))} \to_{Emb(\Sigma),B} 2$
$+(2,+(3,1)) \rightarrow_{Emb(\Sigma),B} +(3,1)$	$+(2,+(3,1)) \rightarrow_{Emb(\Sigma),B} 3$

For a term with k addends, we have  $(2^k) - 2$  rewriting steps. This leads to a huge combinatorial explosion when considering the complete rewrite search tree.

Moreover, there are three problems with Definition 5. First, the intrinsic nondeterminism of the rules may unnecessarily produce an extremely large search space. Second, as shown in Example 5, this intrinsic non-determinism in the presence of axioms is intolerable, that is, unfeasible to handle. Third, the associated reachability problems do not scale up to complex embedding problems so that a suitable search strategy must be introduced. We address these problems stepwisely in the sequel.

#### 4 Goal-Driven Homeomorphic Embedding Modulo B

The formulation of homeomorphic embedding as a reachability problem by using the rewrite rules of Definition 5 generates a blind search that does not take advantage of the actual terms t and t' being compared for embedding. In this section, we provide a more refined formulation of homeomorphic embedding modulo axioms that is *goal driven* in the sense that, given an embedding problem (or *goal*),  $t \leq_B t'$ , it inductively processes the terms t and t' in a top-down manner.

First, we introduce in the following section a calculus that extends the homeomorphic embedding relation of Definition 3 to the order-sorted equational case.

#### 4.1 An Homeomorphic Embedding Calculus Modulo B

Let us introduce a calculus for embeddability goals  $t \leq_B^{gd} t'$  that directly handles in the deduction system the algebraic axioms of B, with B being any combination of A and/or C axioms for the theory operators. Roughly speaking, this is achieved by specializing w.r.t. B the coupling rule of Definition 3.

**Definition 6.** (Goal-driven homeomorphic embedding modulo B). The homeomorphic embedding relation modulo B is defined as the smallest relation that satisfies the inference rules of Definition 3 together with the new inference

rules given in Fig. 2: (i) the three inference rules (Variable, Diving, and Coupling) of Definition 3 for any function symbol; (ii) one extra coupling rule for the case of a commutative symbol with or without associativity (Coupling<sub>C</sub>); (iii) two extra coupling rules for the case of an associative symbol with or without commutativity (Coupling<sub>A</sub>); and (iv) two extra coupling rules for the case of an associative-commutative symbol (Coupling<sub>AC</sub>).

$$\begin{aligned} \mathbf{Coupling}_{C} & \frac{s_{0} \leq_{B}^{gd} t_{1} \wedge s_{1} \leq_{B}^{gd} t_{0}}{f(s_{0},s_{1}) \leq_{B}^{gd} f(t_{0},t_{1})} \\ \\ \mathbf{Coupling}_{A} & \frac{f(s_{0},s_{1}) \leq_{B}^{gd} t_{0} \wedge s_{2} \leq_{B}^{gd} t_{1}}{f(s_{0},f(s_{1},s_{2})) \leq_{B}^{gd} f(t_{0},t_{1})} & \frac{s_{0} \leq_{B}^{gd} f(t_{0},t_{1}) \wedge s_{1} \leq_{B}^{gd} t_{2}}{f(s_{0},s_{1}) \leq_{B}^{gd} f(t_{0},t_{1})} \\ \\ \mathbf{Coupling}_{AC} & \frac{f(s_{0},s_{1}) \leq_{B}^{gd} t_{1} \wedge s_{2} \leq_{B}^{gd} t_{0}}{f(s_{0},f(s_{1},s_{2})) \leq_{B}^{gd} f(t_{0},t_{1})} & \frac{s_{1} \leq_{B}^{gd} f(t_{0},t_{1}) \wedge s_{0} \leq_{B}^{gd} t_{2}}{f(s_{0},s_{1}) \leq_{B}^{gd} f(t_{0},t_{1},t_{2})} \end{aligned}$$

Fig.	2.	Extra	coupling	rules	for	Α,	С,	$\mathbf{AC}$	symb	ools	3
			· · · · · · · · · · · · · · · · · · ·				- /		·····		

**Proposition 2.** Given  $\Sigma$  and B, for terms t and t' in  $\mathscr{T}_{\Sigma}(\mathscr{X})$ ,  $t \leq_B t'$  iff  $t \leq_B^{gd} t'$ .

*Example 6.* Consider the binary symbol + obeying associativity and commutativity axioms, and the terms t = +(1, 2) and t' = +(2, +(3, 1)) of Example 5. We can prove  $t \leq_B^{gd} t'$  by

$$\frac{\frac{1 \leq_B^{gd} 1}{1 \leq_B^{gd} + (3,1)}}{2 \leq_B^{gd} + (2,+(3,1))} 2 \leq_B^{gd} 2$$

We can also prove a more complex embedding goal by first using the right inference rule for AC of Fig. 2 and then the generic Coupling and Diving inference rules.

$$\frac{\frac{2 \leq_B^{gd} 2}{2 \leq_B^{gd} + (4,2)} \quad 3 \leq_B^{gd} 3}{+(2,3) \leq_B^{gd} + (+(4,2),3)} \quad 1 \leq_B^{gd} 1 \\ +(1,+(2,3)) \leq_B^{gd} + (+(4,2),+(3,1))$$

It is immediate to see that, when the size of the involved terms t and t' grows, the improvement in performance of  $\trianglelefteq_B^{gd}$  w.r.t.  $\trianglelefteq_B$  can be significant (just compare these two embedding proofs with the corresponding search trees for  $\trianglelefteq_B$ ).

#### 4.2 Reachability-Based, Goal-Driven Homeomorphic Embedding Formulation

Let us provide a more operational goal-driven characterization of the homeomorphic embedding modulo B. We formalize it in the reachability style of Definition 5. The main challenge here is how to generate a suitable rewrite theory  $R^{rogd}(\Sigma, B)$  that can decide embedding modulo B by running a reachability goal.

**Definition 7 (Goal-driven homeomorphic embedding rewrite rules** modulo B). Given  $\Sigma$  and B, we define the TRS  $R^{rogd}(\Sigma, B)$  as follows.

- 1. We include in  $\mathbb{R}^{rogd}(\Sigma, B)$  a rewrite rule of the form  $u \leq_B^{rogd} v \to true$  for each (particular instance of the) inference rules of the form  $u \leq_B^{qd} v$  given Definition 6 (e.g., the Variable Inference Rule from Definition 3 or the Coupling Inference Rule from Definition 3, for the case of a constant symbol c).
- 2. We include in  $R^{rogd}(\Sigma, B)$  a rewrite rule of the form  $u \trianglelefteq_B^{rogd} v \to u_1 \trianglelefteq_B^{rogd}$  $v_1 \land \dots \land u_k \trianglelefteq_B^{rogd} v_k$  for each (particular instance of the) inference rules of the form  $\frac{u_1 \trianglelefteq_B^{gd} v_1 \land \dots \land u_k \trianglelefteq_B^{gd} v_k}{u \bowtie_B^{gd} v}$  given in Definition 6.

**Proposition 3.** Given  $\Sigma$ , B, and terms  $t, t', t \leq \subseteq_B^{gd} t'$  iff  $(t \leq_B^{rogd} t') \rightarrow_{R^{rogd}(\Sigma,B),B}^* true.$ 

*Example 7.* Consider the binary symbol + of Example 1. According to Definition 6, there are twelve inference rules for  $\leq_B^{qd}$ :

Variable	Diving	Coupling
$\overline{x {\trianglelefteq}_B^{gd} y}$	$\frac{x \trianglelefteq_B^{gd} t_1}{x \trianglelefteq_B^{gd} suc(t_1)}$	$\overline{0 \trianglelefteq_B^{gd} 0}$
	$\frac{x \trianglelefteq_B^{gd} t_1}{x \trianglelefteq_B^{gd} + (t_1, t_2)}$	$\frac{t_1 \leq_B^{\bar{gd}} t_1'}{suc(t_1) \leq_B^{gd} suc(t_1')}$
	$\frac{x \trianglelefteq_B^{gd} t_2}{x \trianglelefteq_B^{gd} + (t_1, t_2)}$	$\frac{t_1 \trianglelefteq_B^{gd} t_1' \land t_2 \trianglelefteq_B^{gd} t_2'}{+(t_1, t_2) \trianglelefteq_B^{gd} + (t_1', t_2')}$
Coupling <sub>C</sub>	$\operatorname{Coupling}_{A}$	$\operatorname{Coupling}_{AC}$
$\frac{t_1 \trianglelefteq_B^{gd} t_2' \land t_2 \trianglelefteq_B^{gd} t_1'}{+(t_1, t_2) \trianglelefteq_B^{gd} + (t_1', t_2')}$	$\frac{+(t_0,t_1) \trianglelefteq_B^{gd} t_1' \land t_2 \trianglelefteq_B^{gd} t_2'}{+(t_0,+(t_1,t_2)) \trianglelefteq_B^{gd} + (t_1',t_2')}$	$\frac{+(t_0,t_1) \trianglelefteq_B^{gd} t_2' \land t_2 \trianglelefteq_B^{gd} t_1'}{+(t_0,+(t_1,t_2)) \oiint_B^{gd} + (t_1',t_2')}$
× /	$\frac{t_1 \trianglelefteq_B^{gd} + (t'_0, t'_1) \land t_2 \trianglelefteq_B^{gd} t'_2}{+(t_1, t_2) \trianglelefteq_B^{gd} + (t'_0, +(t'_1, t'_2))}$	$\frac{t_2 \trianglelefteq_B^{gd} + (t'_0, t'_1) \land t_1 \trianglelefteq_B^{gd} t'_2}{+(t_1, t_2) \trianglelefteq_B^{gd} + (t'_0, +(t'_1, t'_2))}$

However, the corresponding TRS  $R^{rogd}(\Sigma, B)$  only contains six rewrite rules because, due to pattern matching modulo associativity and commutativity in rewriting logic, the other rules are redundant:

For example, the rewrite sequence proving  $+(1, +(2, 3)) \leq_B^{rogd} +(+(4, 2), +(3, 1))$  is:

$$\begin{split} +(1,+(2,3)) \trianglelefteq_{B}^{rogd} + (+(4,2),+(3,1)) \\ \to_{R^{rogd}(\Sigma,B),B} +(2,3)) \trianglelefteq_{B}^{rogd} + (+(4,2),3) \wedge 1 \trianglelefteq_{B}^{rogd} 1 \\ \to_{R^{rogd}(\Sigma,B),B} 2 \trianglelefteq_{B}^{rogd} + (4,2) \wedge 3 \trianglelefteq_{B}^{rogd} 3 \\ \to_{R^{rogd}(\Sigma,B),B} 2 \trianglelefteq_{B}^{rogd} 2 \\ \to_{R^{rogd}(\Sigma,B),B} true \end{split}$$

Although the improvement in performance achieved by using the rewriting relation  $\rightarrow_{R^{rogd}(\Sigma,B),B}$  versus the rewriting relation  $\rightarrow^*_{Emb(\Sigma),B}$  is important, the search space is still huge since the expression  $+(1,+(2,3)) \leq^{gd}_{B}$ +(+(4,2),+(3,1)) matches the left-hand side  $+(T_1,T_2) \leq^{gd}_{B} +(T'_1,T'_2)$  in many different ways (e.g.,  $\{T_1 \mapsto 1, T_2 \mapsto +(2,3), \ldots\}, \{T_1 \mapsto 2, T_2 \mapsto +(1,3), \ldots\}, \{T_1 \mapsto 3, T_2 \mapsto +(1,2), \ldots\}$ ).

In the following section, we further optimize the calculus of homeomorphic embedding modulo axioms by considering equational (deterministic) normalization (thus avoiding search) and by exploiting the meta-level features of Maude (thus avoiding any theory generation).

# 5 Meta-Level Deterministic Goal-Driven Homeomorphic Embedding Modulo *B*

The meta-level representation of terms in Maude [5, Chapter 14] works with flattened versions of the terms that are rooted by poly-variadic versions of the associative (or associative-commutative) symbols. For instance, given an associative (or associative-commutative) symbol f with n arguments and  $n \geq 2$ , flattened terms rooted by f are canonical forms w.r.t. the set of rules given by the following rule schema

$$f(x_1,\ldots,f(t_1,\ldots,t_n),\ldots,x_m) \to f(x_1,\ldots,t_1,\ldots,t_n,\ldots,x_m) \quad n,m \ge 2$$

Given an associative (or associative-commutative) symbol f and a term  $f(t_1, \ldots, t_n)$ , we call *f*-alien terms (or simply alien terms) those terms among the  $t_1, \ldots, t_n$  that are not rooted by f. In the following, we implicitly consider that all terms are in *B*-canonical form.

In the sequel, a variable x of sort s is meta-represented as  $\bar{x} = 'x$ :s and a non-variable term  $t = f(t_1, \ldots, t_n)$ , with  $n \ge 0$ , is meta-represented as  $\bar{t} = 'f[\bar{t_1}, \ldots, \bar{t_n}]$ .

**Definition 8 (Meta-level homeomorphic embedding modulo** *B*). The meta-level homeomorphic embedding modulo B,  $\leq_B^{ml}$ , is defined for term meta-representations by means of the equational theory  $E^{ml}$  of Fig. 3, where the auxiliary meta-level functions **any** and **all** implement the existential and universal

$$\begin{array}{l} \sharp \ \exists_{B}^{ml} \ \sharp \ = true \\ F[TermList] \ \exists_{B}^{ml} \ \sharp \ = false \\ T \ \exists_{B}^{ml} \ F[TermList] \ = any(T, TermList) & \text{if } root(T) \neq F \\ F[TermList] \ \exists_{B}^{ml} \ F[TermList] \ = any(F[TermList], TermList2) \\ \text{or all}(TermList], TermList2) & \text{if } F \text{ is } C \\ \text{or } (U \ \exists_{B}^{ml} \ X \text{ and } V \ \exists_{B}^{ml} \ X) \\ F[U,V] \ \exists_{B}^{ml} \ F[X,Y] \ = any(F[TermList], TermList2) & \text{if } F \text{ is } C \\ \text{or } (U \ \exists_{B}^{ml} \ Y \text{ and } V \ \exists_{B}^{ml} \ X) \\ F[TermList] \ \exists_{B}^{ml} \ F[TermList2] \ = any(F[TermList], TermList2) & \text{if } F \text{ is } A \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } A \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } A \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(TermList], TermList2) & \text{if } F \text{ is } AC \\ \text{or all} \ A(U,V,V:L) = U \ \exists_{B}^{ml} \ V \text{ or any}(U,L) \\ & \text{all}(nil, nil) = \text{true} \\ \text{all}(nil, nil) = \text{true} \\ \text{all}(nil, nil) = \text{false} \\ \text{all}(U:L1,V:L2) = U \ \exists_{B}^{ml} \ V \text{ and all}(L1,L2) \\ \text{all} \ A(U:L1,V:L2) = (U \ \exists_{B}^{ml} \ V \text{ and all}(L1,L2)) \\ \text{all} \ A(U:L1,V:L2) = (U \ \exists_{B}^{ml} \ V \text{ and all}(L1,L2)) \\ \text{all} \ A(U:L1,L2,L3) = all \ AC \ Aux(U:L1,L2,L3) \\ \text{all} \ AC(nil,L) = \text{true} \\ \text{all} \ AC(u:L1,L2) = \text{all} \ AC \ Aux(U:L1,L2,L3) \\ \text{all} \ AC \ Aux(U:L1,N1,L3) = false \\ \text{all} \ AC \ Aux(U:L1,N1,L3) = false \\ \text{all} \ AC \ Aux(U:L1,V:L2,L3) = (U \ \ \ aml} \ AC \ Aux(U:L1,L2,L3)) \\ \text{remove}(U,nil) = \text{nil} \\ \text{remove}(U,nil) = \text{nil} \\ \text{remove}(U,nil) = \text{nil} \\ \text{remove}(U,nil) = \text{nil} \\ \text{remove}(U,N1,L) = \text{nil} \\ \text{remove}(U,N1,L) = \text{nil} \ AC \ Aux(U:L1,L2,L3) \\ Aux(U:L1,U1,U2,L3) = \text{nil} \\ Aux(U:L1,U2,$$

Fig. 3. Meta-level homeomorphic embedding modulo axioms

tests in the Diving and Coupling inference rules of Fig. 1, and we introduce two new meta-level functions **all\_A** and **all\_AC** that implement existential tests that are specific to A and AC symbols. For the sake of readability, these new existential tests are also formulated (for ordinary terms instead of meta-level terms) as the inference rules Coupling<sub>A</sub> and Coupling<sub>AC</sub> of Fig. 4.

$$\begin{aligned} \mathbf{Coupling}_{A} & \frac{\exists j \in \{1, \dots, m-n+1\} : s_{1} \leq \underline{\exists}_{B}^{ml} t_{j} \land f(s_{2}, \dots, s_{n}) \leq \underline{\exists}_{B}^{ml} f(t_{j+1}, \dots, t_{m}) \land \forall k < j : s_{1} \leq \underline{\exists}_{B}^{ml} t_{k}}{f(s_{1}, \dots, s_{n}) \leq \underline{\exists}_{B}^{ml} f(t_{1}, \dots, t_{m})} \\ & \mathbf{Coupling}_{AC} \frac{\exists j \in \{1, \dots, m\} : s_{1} \leq \underline{\exists}_{B}^{ml} t_{j} \land f(s_{2}, \dots, s_{n}) \leq \underline{\exists}_{B}^{ml} f(t_{1}, \dots, t_{j-1}, t_{j+1}, \dots, t_{m})}{f(s_{1}, \dots, s_{n}) \leq \underline{\exists}_{B}^{ml} f(t_{1}, \dots, t_{m})} \end{aligned}$$

Fig. 4. Coupling rule for associativity-commutativity functions

*Example 8.* Given the embedding problem for +(1, +(2, 3)) and +(+(4, 2), +(3, 1)), the corresponding call to the meta-level homeomorphic embedding  $\leq_B^{ml}$  of Definition 8 is '+['1, '2, '3]  $\leq_B^{ml}$  '+['4, '2, '3, '1].

**Proposition 4.** Given  $\Sigma$ , B, and terms t and t',  $t \leq_B^{gd} t'$  iff  $(t \leq_B^{ml} t')!_{E^{ml},B} = true.$ 

Finally, a further optimized version of Definition 8 can be easily defined by replacing the Boolean conjunction (*and*) and disjunction (*or*) operators with the computationally more efficient Maude Boolean operators and-then and or-else that avoid evaluating the second argument when the result of evaluating the first one suffices to compute the result.

**Definition 9.** (Strategic meta-level deterministic embedding modulo *B*). We define  $\trianglelefteq_B^{sml}$  as the strategic version of relation  $\trianglelefteq_B^{ml}$  that is obtained by replacing the Boolean operators and and or with the and-then operator for short-circuit version of conjunction and the or-else operator for short-circuit disjunction [5, Chapter 9.1], respectively.

# 6 Experiments

We have implemented in Maude all four equational homeomorphic embedding formulations  $\trianglelefteq_B$ ,  $\trianglelefteq_B^{rogd}$ ,  $\trianglelefteq_B^{ml}$ , and  $\trianglelefteq_B^{sml}$  of previous sections. The implementation consists of approximately 250 function definitions (2.2K lines of Maude source code) and is publicly available online<sup>3</sup>. In this section, we provide an experimental comparison of the four equational homeomorphic embedding implementations by running a significant number of equational embedding goals. In order to compare the performance of the different implementations in the worst possible scenario, all benchmarked goals return false, which ensures that the whole search space for each goal has been completely explored, while the execution times for succeeding goals whimsically depend on the particular node of the search tree where success is found.

We tested our implementations on a 3.3 GHz Intel Xeon E5-1660 with 64 GB of RAM running Maude v2.7.1, and we considered the average of ten executions for each test. We have chosen four representative programs: (i) KMP, the classical KMP string pattern matcher [3]; (ii) NatList, a Maude implementation of lists of natural numbers; (iii) Maze, a non-deterministic Maude specification that defines a maze game in which multiple players must reach a given exit point by walking or jumping, where colliding players are eliminated from the game [1]; and (iv) Dekker, a Maude specification that models a faulty version of Dekker's protocol, one of the earliest solutions to the mutual exclusion problem that appeared in [5]. As testing benchmarks we considered a set of representative embeddability problems for the four programs that are generated during the execution of the partial evaluator Victoria [2].

<sup>&</sup>lt;sup>3</sup> At http://safe-tools.dsic.upv.es/embedding/.

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Benchmark	# Axioms				$\leq_B$			$\trianglelefteq_B^{rogd}$			$\trianglelefteq^{ml}_B, \trianglelefteq^{sml}_B$		
	Ø	Α	С	AC	C $\sharp E \sharp R$ GT (ms)		‡Ε	₿R	GT (ms)	‡Ε	₿R	GT (ms)	
Kmp	9	0	0	0	0	15	1	0	57	2	21	0	0
NatList	5	1	1	2	0	10	1	0	26	1	21	0	0
Maze	5	1	0	1	0	36	7	0	787	15	21	0	0
Dekker	16	1	0	2	0	59	8	0	823	18	21	0	0

 Table 1. Size of generated theories for naïve and goal-driven definitions vs. meta-level definitions

Benchmark	# 5	Symbols	Size	9	$\leq_B$	$\exists B \\ B $	$\trianglelefteq^{sml}_B$	
	Α	AC	T1	T2	Time (ms)	Time (ms)	Time (ms)	Time (ms)
Kmp	0	0	5	5	10	6	1	1
				10	150	125	4	1
				100	ТО	ТО	5     1     1       125     4     1       FO     280     95       FO     714     460       2892     1     1       540540     1     1       FO     88     2       FO     60     5       25     1     1       20790     4     1	
				500	ТО	ТО	714	460
NatList	1	2	5	5	2508	2892	1	1
				10	840310	640540	1	1
				100	ТО	ТО	8	2
				500	ТО	ТО	60	5
Maze	1	1	5	5	40	25	1	1
				10	ТО	20790	4	1
				100	ТО	ТО	256	2
				500	ТО	ТО	19808	10
Dekker	1	1	5	5	50	40	1	1
				10	111468	110517	2	1
				100	ТО	ТО	5	3
				500	ТО	ТО	20	13

Tables 1, 2, and 3 analyze different aspects of the implementation. In Table 1, we compare the size of the generated rewrite theories for the naïve and the goaldriven definitions versus the meta-level definitions. For both,  $\leq_B^{ml}$  and  $\leq_B^{sml}$ , there are the same number (21) of generated equations ( $\sharp E$ ), whereas the number of generated rules ( $\sharp R$ ) is zero because both definitions are purely equational (deterministic) and just differ in the version of the boolean operators being used. As for the generated rewrite theories for computing  $\leq_B$  and  $\leq_B^{rogd}$ , they contain no equations, while the number of generated rules increases with the complexity of the program (that heavily depends on the equational axioms that the function symbols obey). The number of generated rules is much bigger for  $\leq_B^{rogd}$ 

T1						T2					$\trianglelefteq_B$	$\trianglelefteq_B^{rogd}$	$\trianglelefteq^{ml}_B$	$\trianglelefteq^{sml}_B$	
Size		# S	Sym	bol	8	Size	Size # Symbols				Time (ms)	Time (ms)	Time (ms)	Time (ms)	
OT	$\mathbf{FT}$	Ø	$\mathbf{C}$	Α	AC	OT	$\mathbf{FT}$	Ø	С	А	AC				
5	5	5	0	0	0	100	100	100	0	0	0	165	70	1	1
5	5	3	2	0	0	100	100	50	50	0	0	ТО	38	60	35
5	2	4	0	1	0	100	2	50	0	50	0	ТО	ТО	108035	3
5	2	4	0	0	1	100	2	50	0	0	50	ТО	ТО	42800	4
5	3	8	0	1	2	100	3	50	0	25	25	ТО	ТО	22796	5
5	5	5	0	0	0	500	500	500	0	0	0	48339	34000	12	4
5	5	3	2	0	0	500	500	250	250	0	0	ТО	2183	6350	2005
5	2	4	0	1	0	500	2	250	0	250	0	ТО	ТО	ТО	30
5	2	4	0	0	1	500	2	250	0	0	250	ТО	ТО	ТО	27
5	3	8	0	1	2	500	3	250	0	125	125	ТО	ТО	ТО	50

**Table 3.** Performance of equational homeomorphic embedding implementations w.r.t.

 axiom entanglement for the NatList example

than for  $\leq_B$  (for instance,  $\leq_B^{rogd}$  is encoded by 823 rules for the Dekker program versus the 59 rules of  $\leq_B$ ). Columns  $\emptyset$ , A, C, and AC summarize the number of free, associative, commutative, and associative-commutative symbols, respectively, for each benchmark program. The generation times (GT) are negligible for all rewrite theories.

For all benchmarks  $T1 \leq_B^{\alpha} T2$  in Table 2, we have fixed to five the size of T1 that is measured in the depth of (the non-flattened version of) the term. As for T2, we have considered terms with increasing depths: five, ten, one hundred, and five hundred. The  $\sharp$  Symbols column records the number of A (resp. AC) symbols occurring in the benchmarked goals.

The figures in Table 2 confirm our expectations regarding  $\leq_B$  and  $\leq_B^{rogd}$  that the search space is huge and increases exponentially with the size of T2 (discussed for  $\leq_B$  in Example 5 and for  $\leq_B^{rogd}$  in Example 6). Actually, when the size of T2 is 100 (and beyond) a given timeout (represented by TO in the tables) is reached that is set for 3.6e+6 ms (1 h). The reader can also check that the more A, C, and AC symbols occur in the original program signature, the bigger the execution times. An odd exception is the Maze example, where the timeout is already reached for the size 10 of T2 even if the number of equational axioms is comparable to the other programs. This is because the AC-normalized, flattened version of the terms is much smaller than the original term size for the NatList and Dekker benchmarks but not for Maze, where the flattened and original terms have similar size. On the other hand, our experiments demonstrate that both  $\leq_B^{ml}$  and  $\leq_B^{sml}$  bring impressive speedups, with  $\leq_B^{sml}$  working outstandingly well in practice even for really complex terms.

The reader may wonder how big the impact is having A, C, or AC operators. In order to compare the relevance of these symbols, in Table 3 we fix one single benchmark program (NatList) that contains all three kinds of operators: two associative operators (list concatenation ; and natural division /), a commutative (natural pairing) operator (11), and two associative-commutative arithmetic operators (+,\*). With regard to the size of the considered terms, we confront the size of the original term (OT) versus the size of its flattened version (FT); e.g., 500 versus 2 for the size of T2 in the last row.

We have included the execution times of  $\leq_B$  and  $\leq_B^{rogd}$  for completeness, but they do not reveal a dramatic improvement of  $\leq_B^{rogd}$  with respect to  $\leq_B$  for the benchmarked (false) goals, contrary to what we initially expected. This means that  $\leq_B^{rogd}$  cannot be generally used in real applications due to the risk of intolerable embedding test times, even if  $\leq_B^{rogd}$  may be far less wasteful than  $\leq_B$  for succeeding goals, as discussed in Sect. 4. For  $\leq_B^{ml}$  and  $\leq_B^{sml}$ , the figures show that the more A and AC operators comparatively occur in the problem, the bigger the improvement achieved. This is due to the following: (i) these two embedding definitions manipulate flattened meta-level terms; (ii) they are equationally defined, which has a much better performance in Maude than doing search; and (iii) our definitions are highly optimized for lists (that obey associativity) and sets (that obey both associativity and commutativity).

Homeomorphic embedding has been extensively used in Prolog for different purposes, such as termination analysis and partial deduction.

In Fig. 5, we have compared on a logarithmic scale our best embedding definition,  $\leq_B^{sml}$ , with a standard meta-level Prolog<sup>4</sup> implementation of the (syntactic) pure homeomorphic embedding  $\leq$  of Definition 3.

We chose the NatList example and terms T1 and T2 that do not contain symbols obeying equational axioms as this is the only case that can be handled by the syntactic Prolog implementation. Our experiments show that our refined deterministic formulation  $\trianglelefteq_B^{sml}$  (i.e. without search) outperforms the Prolog version



**Fig. 5.** Comparison of  $\trianglelefteq$  vs.  $\trianglelefteq_{\emptyset}^{sml}$  for NatList

so no penalty is incurred when syntactic embeddability tests are run in our equational implementation.

<sup>&</sup>lt;sup>4</sup> To avoid any bias, we took the Prolog code for the homeomorphic embedding of the ECCE system [13] that is available at https://github.com/leuschel/ecce, and we run it in SWI-Prolog 7.6.3.

# 7 Concluding Remarks

Homeomorphic embedding has been extensively used in Prolog but it has never been investigated in the context of expressive rule-based languages like Maude, CafeOBJ, OBJ, ASF+SDF, and ELAN that support symbolic reasoning methods modulo equational axioms. We have introduced a new equational definition of homeomorphic embedding with a remarkably good performance for theories with symbols having any combination of associativity and commutativity. We have also compared different definitions of embedding identifying some key conclusions: (i) definitions of equational homeomorphic embedding based on (nondeterministic) search in Maude perform dramatically worse than their equational counterparts and are not feasible in practice, (ii) definitions of equational homeomorphic embedding based on generated theories perform dramatically worse than meta-level definitions; and (iii) the flattened meta-representation of terms is crucial for homeomorphic embedding definitions dealing with A and AC operators to pay off in practice. As future work, we plan to extend our results to the case when the equational theory B may contain the identity axiom, which is non-trivial since B is not class-finite.

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# Logic-Based Distributed/Concurrent Programming


# Multiparty Classical Choreographies

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Abstract. We present Multiparty Classical Choreographies (MCC), a language model where global descriptions of communicating systems (choreographies) implement typed multiparty sessions. Typing is achieved by generalising classical linear logic to judgements that explicitly record parallelism by means of hypersequents. Our approach unifies different lines of work on choreographies and processes with multiparty sessions, as well as their connection to linear logic. Thus, results developed in one context are carried over to the others. Key novelties of MCC include support for server invocation in choreographies, as well as logicdriven compilation of choreographies with replicated processes.

# 1 Introduction

Choreographic Programming [17] is a programming paradigm where programs, called *choreographies*, define the intended communication behaviour of a system based on message passing, using an "Alice and Bob" notation, rather than the behaviour of each endpoint. Choreographies are useful for several reasons: they give a succinct description, or *blueprint*, of the intended behaviour of a whole system, making the implementation less error-prone. Then, correct-by-construction distributed implementations can be synthesised automatically by means of *projection*, a compilation algorithm that generates the code for each endpoint described in the choreography [6,8]. Reversely, it is often possible to obtain a choreography from an endpoint implementation by means of *extraction*, providing a precise blueprint of a distributed system.

Choreographic programming has a deep relationship with the proof theory of linear logic [9]. Specifically, choreographic programs can be seen as terms describing the reduction steps of cut elimination in linear logic (choreographies as cut reductions). The key advantage of this result is that it provides a logical reconstruction of two useful translations, one from choreographies to processes (*projection*, or synthesis) and another from processes to choreographies (*extraction*) – this is obtained by exploiting the correspondence between intuitionistic linear logic and a variant of the  $\pi$ -calculus [4]. These translations can be used to keep process implementations aligned with the desired communication flows given as choreographies, whenever code changes are applied to any of the two.

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This kind of alignment is a desirable property in practice, e.g., it is the basis of the Testable Architecture development lifecycle for web services [14].

Unfortunately, the logical reconstruction of choreographies in [9] covers only the multiplicative-additive fragment of intuitionistic linear logic, limiting its practical applicability to simple scenarios. The aim of this paper is to push the boundaries of this approach towards more realistic scenarios with sophisticated features. In this article, we define a model, strictly related to classical linear logic, that allows for *replicated services*, and *multiparty sessions*.

Reaching our aim is challenging for both design and technical reasons. In the multiplicative-additive fragment of linear logic considered in [9], all reductions intuitively match choreographic terms explored in previous works on choreographies, i.e., communication of a channel and branch selection [8]. This is not the case for the exponential fragment, which yields *reductions never considered before in choreographies*, e.g., explicit garbage collection of services and server cloning (see kill and **clone** operations). To bridge this gap, we exploit the fact that these operations occur naturally in the process language and, through the logic, can be reflected to choreographic primitives for management of services as explicit resources that can be duplicated, used, or destroyed. We show that the reductions for these terms correspond to the principal cut reductions for exponentials in classical linear logic. Typing guarantees that resource management is safe, e.g., no destroyed resource is ever used again.

In [9], all sessions (protocols) have exactly two participants. This works well in intuitionistic linear logic, where sequents are two-sided: two processes can be connected if one "provides" a behaviour and the other "needs" it. This is verified by checking identity of types, respectively between a type on the right-hand side of the sequent of the first process and a type of the left-hand side of the sequent for the second. To date, it is still unclear how identity for two-sided sequents can be generalised to multiparty sessions, where a session can have multiple participants and thus we need to check compatibility of multiple types. Instead, this topic has been investigated in the setting of classical linear logic, where multiparty compatibility is captured by coherence, a generalisation of duality [10]. Therefore, our formulation of Multiparty Classical Choreographies (MCC) is based on classical linear logic. In order to bridge choreographies to multiparty sessions, we introduce a new session environment, which records the types of multiparty communications performed by a choreography as *global types* [13]. The manipulation of the session environment reveals that typing a choreography with multiparty sessions corresponds to building the coherence proofs for typing its sessions. Since a proof of coherence is the type compatibility check required by the multiparty version of cut in classical linear logic, our result generalises the choreographies as cut reductions approach to the multiparty case as one would expect, providing further evidence of the robustness of this idea. The final result of our efforts is an expressive calculus for programming choreographies with multiparty sessions and services, which supports both projection and extraction operations for all typable programs.

# 2 Preview

We start by introducing MCC informally, focusing on modelling a protocol inspired by OpenID [20], where a client authenticates through a third-party identity provider. MCC offers a way of specifying protocols in terms of global types. For example, our variant of OpenID can be specified by the global type G:

 $u \to rp(\mathsf{String}); u \to ip(\mathsf{String}); u \to ip(\mathsf{PWD}); ip \to rp.\mathsf{case}(\ u \to rp(\mathsf{String}); G_1,\ G_2)$ 

This protocol concerns three endpoints (often called roles in literature) denoted by u (user), rp (relaying party) and ip (identity provider). The user starts by sending its login string to both rp and ip. Then, it sends its password to ip which will either confirm or reject u's authentication to rp. If the authentication is successful then the user will send an evaluation of the authentication service to rp, and then complete as the unspecified protocol  $G_1$ . Otherwise, if the password is wrong, then the protocol continues as  $G_2$ . The specification given by the global type G can be used by a programmer during an implementation. In MCC, we could give an implementation in terms of the choreography:

u starts $rp, ip;$	// $u$ sta	arts protocol with rp and i	p
$u(user_u) \to rp(user_{rp});$	// $u  { m sen}$	nds its login to $rp$	
$u(login_u) \rightarrow ip(login_{ip});$	// $u  { m sen}$	nds its login to $ip$	
$u(pwd_u) \rightarrow ip(pwd_{ip});$	// $u$ aut	thenticates with $ip$	
(inl : $u'$ starts $s;$		// $u^\prime$ starts protocol wit	hs)
$u'(rep_{u'}) \to s($	$(rep_s);$	// $u^\prime$ sends report to $s$	
$ip \rightarrow rp. \left\{ s(ack_s) \rightarrow u'(ack_s) \right\}$	$ack_{u'});$	// $s$ acknowledges to $u^\prime$	
$u(rep_u) \to rp($	$(rep_{rp}); P,$	// $u$ sends report to $rp$	
(inr: Q		<pre>// authentication fails</pre>	J

Each line is commented with an explanation of the performed action. We observe that two different protocols are started. The first line starts the OpenID protocol between u, rp and ip described above. Moreover, after branching, the choreography starts another session between the user (named u') and a server s that is used for reviewing the authentication service given by ip. In this case, the protocol used is  $G' = u' \rightarrow s(\text{String}); s \rightarrow u'(\text{String}); G_3$ , for some unspecified  $G_3$ . We leave undefined the case in which the identity provider receives a wrong password (term Q).

In this work, we show how a choreography that follows a protocol such as G can be expressed as a proof in a proof theory strictly related to classical linear logic. Moreover, thanks to proof transformations, the choreography above can be projected into a parallel composition of endpoint processes, each running a different endpoint. As an example, the endpoint process for the user would correspond to the process  $P_u$ , defined as

use  $u; \overline{u}(user_u); \overline{u}(login_u); \overline{u}(pwd_u);$  use  $u'; \overline{u'}(rep_{u'}); u'(ack_{u'}); \overline{u}(rep_u); R$ 

which mimics the behaviour of u and u' specified in the choreography. Operator use is used to start a session, while the other two operators utilised above are for in-session communication. Similarly, we can have the endpoint processes for rp, ip and s:

$$\begin{split} P_{rp} = \mathsf{srv}\, rp; rp(user_{rp}); rp.\mathsf{case}(rp(rep_{rp}); R_1, Q_1) \\ P_{ip} = \mathsf{srv}\, ip; ip(login_{ip}); ip(pwd_{ip}); R_2 \qquad P_s = \mathsf{srv}\, s; s(rep_s); \overline{s}(ack_s); R_3 \end{split}$$

# 3 GCP with Hypersequents

In this section, we present the *action fragment* of MCC, where we only consider local actions, e.g., inputs or outputs. The action fragment is a variant of Globallygoverned Classical Processes (GCP) [7] whose typing rules use hypersequents. In the remainder, we denote a vector of endpoints  $x_1, \ldots, x_n$  as  $\tilde{x}$  or  $(x_i)_i$ .

Syntax. The action fragment is a generalisation of Classical Processes [22] that supports multiparty session types. As hinted in Sect. 2, when writing a program in our language, we do not identify sessions via channel names, but rather we name sessions' *endpoints*. Each process owns a single endpoint of a session it participates in. The complete syntax is given by the following grammar:

P :: =	$x^A \to y$	link	use $x; P$	client
	$P \mid Q$	parallel	srvy;P	server
	$(\boldsymbol{\nu}\tilde{\boldsymbol{x}}:\boldsymbol{G})\boldsymbol{P}$	restriction	kill x   P	server kill
	$\overline{x}(x'); (P \mid Q)$	send	$  \operatorname{clone} x(x'); P$	server clone
	y(y'); P	receive		
	close[x]	close session		
	wait[y]; P	receive close	y.case(P,Q)	branching
	x.inl; P	left selection	$\mid x.(inl:P,inr:Q)$	general selection
	x.inr; Q	right selection	$x^{\tilde{u}}.case()$	empty choice

With a few exceptions, the terms above are identical to those of GCP. For space restriction reasons, we only discuss the key differences. Parallel and restriction constructs form a single term  $(\nu \tilde{x} : G)(P \mid Q)$  in the original GCP. The link process  $x^A \to y$  is a forwarder from x to y. We further allow the general selection x.(inl : P, inr : Q), denoting a process that non-deterministically selects a left or a right branch. For services, an endpoint x may kill all servers by executing the action kill  $x \mid P$ , or duplicate them by means of clone x(x'); P – these operations were silent in the original GCP. In cloning, the new server copies are replicated at fresh endpoints, ready to engage in a session with new endpoint x'. More generally, we follow the convention of [22], denoting the result of refreshing names in Q by Q' (changing each  $x \in \mathsf{fv}(Q)$  into a fresh x').

A :: =	$A\otimes B$	output	$ A \otimes B$	input	G :: =	$\tilde{x} \to y(G); H$	$(\otimes \&)$
	$A\oplus B$	selection	A&B	choice		$x \to \tilde{y}.case(G,H)$	$(\oplus \&)$
	!A	server	?A	client		$!x \to \tilde{y}(G)$	(!?)
	1	close		wait		$\tilde{x} \rightarrow y$	$(1 \bot)$
	0	false	T	empty		$x \to \tilde{y}.case()$	$(0\top)$
	X	variable	$ X^{\perp}$	dual variable		$x^A \to y$	(AXIOM)

Types. Types, used to ensure proper behaviour of endpoints, are defined as:

In the multiparty setting, types can be split into *local types A*, which specify behaviours of a single process, and *alobal types* G, which describe interaction within sessions (and choreography actions). Again, most global types correspond to pairs of local types, the exception being the global axiom type, describing a linking session (restricted by typing to type variables and their duals). Local type operators are based on connectives from classical linear logic – thus,  $A \otimes B$ is the type of a process that outputs an endpoint of type A and continues with type B, whereas  $A \otimes B$  is the type of a process that receives endpoints of type A and is itself ready to continue as B. The corresponding global type  $\tilde{x} \to y(G)$ ; H types the interaction where each of the processes owning an endpoint  $x_i$  sends their new endpoint to y. Type 0 is justified by the necessity of having a type dual to  $\top$ , while the rule  $0\top$  is essential for the definition of coherence. Type variables are used to represent concrete datatypes. It is worth noting that the logic formulas in our type system enjoy the usual notion of duality, where a formula's dual is obtained by recursively replacing each connective by the other one in the same row in the table above. For example, the dual of  $!(A \otimes 0)$  is  $?(A^{\perp} \otimes \top)$ , where  $A^{\perp}$  is the dual of formula A.

Typing. We type our terms in judgements of the form  $\Sigma \Vdash P \ \circ \Psi$ , where: (i)  $\Sigma$  is a set of session typings of the form  $(x_i)_i:G$ ; (ii) P is a process; and, (iii)  $\Psi$  is a hypersequent, a set of classical linear logic sequents. Intuitively,  $\Sigma \Vdash P \ \circ \Psi$  reads as " $\Psi$  types P under the session protocols described in  $\Sigma$ ."

Given a judgment  $\Sigma \Vdash P \circ \Psi \models \Gamma, x : A$ , checking whether x is available – not engaged in a session – is implicitly done by verifying that x does not occur in the domain of  $\Sigma$ . Note that names cannot occur more than once in  $\Sigma$ : each endpoint x may only belong to (at most) one session G. Hypersequents  $\Psi_1, \Psi_2$ and sets of sessions  $\Sigma_1, \Sigma_2$  can only be joined if their domains do not intersect. Moreover, we use indexing in different ways:  $(\Sigma_i \Vdash P_i \circ \Psi_i)_i$  denotes several judgements  $\Sigma_1 \Vdash P_1 \circ \Psi_1, \ldots, \Sigma_n \Vdash P_n \circ \Psi_n$ ; indexed pairs  $(x_i : A_i)_i$  are a set of pairs  $x_1 : A_1, \ldots, x_n : A_n$ ; and, finally,  $(\vdash \Gamma_i)_i$  denotes the hypersequent  $\vdash \Gamma_1 \mid \ldots \mid \vdash \Gamma_n$ .

In order to separate restriction and parallel (reasons for this separation will be explained in Sect. 4), we split the classical linear logic Cut rule into two:

$$\frac{(\Sigma_i \Vdash P_i \ \circ \ \Psi_i \mid \vdash \Gamma_i, x_i : A_i)_i \ G \vDash (x_i : A_i)_i}{(\Sigma_i)_i, (x_i)_i : G \Vdash (P_i)_i \ \circ \ (\Psi_i \mid \vdash \Gamma_i, x_i : A_i)_i} \operatorname{Conn} \quad \frac{\Sigma_i, (x_i)_i : G \Vdash P \ \circ \ \Psi \mid (\vdash \Gamma_i, x_i : A_i)_i}{\Sigma \Vdash (\nu \tilde{x} : G) P \ \circ \ \Psi \mid \vdash (\Gamma_i)_i} \operatorname{Scope}_{i \in \mathcal{N}} \operatorname{Scope}_{i \in \mathcal{N}} (\mu \tilde{x} : G) = \frac{(\lambda_i \cap \mathcal{N}_i)_i : G \vdash (\Psi \tilde{x} : G)_i \cap (\Psi \tilde{x} : G)_i \cap (\Psi \tilde{x} : G)_i)_i}{(\Sigma \vdash (\Psi \tilde{x} : G) \cap (\Psi \tilde{x} : G)_i)_i \cap (\Psi \tilde{x} : G)_i \cap (\Psi \tilde{x} : G)_i)_i} \operatorname{Scope}_{i \in \mathcal{N}} (\mu \tilde{x} : G)_i \cap (\Psi \tilde{x} : G)_i \cap (\Psi \tilde{x} : G)_i)_i$$

Rule Conn is used for merging proofs that provide *coherent* types (we address coherence below), but without removing them from the environment. Since such types need to remain in the conclusion of the rule, we need to use hypersequents. The sequents involved in a session get merged once a Scope rule is applied. This hypersequent presentation is similar to a classical linear logic variant of [9] with sessions explicitly remembered in a separate context  $\Sigma$ .

Coherence is a generalisation of duality [7] to more than two parties: when describing a multiparty session, simple duality of types does not suffice to talk about their compatibility. In Fig. 1, we report the rules defining the coherence relation  $\vDash$ . We do not describe these here in detail, as they remain unchanged compared to the original GCP presentation, with the exception of the axiom rule which is only applicable to atomic types in our system.

$$\begin{aligned} \frac{G \models (x_i:A_i)_i, \ y:C \quad H \models \Gamma, \ (x_i:B_i)_i, \ y:D}{\tilde{x} \to y(G); H \models \Gamma, \ (x_i:A_i \otimes B_i)_i, \ y:C \ \Diamond D} & \otimes \ \Im & \xrightarrow{\tilde{x} \to y \models (x_i:1)_i, y:\bot} 1 \bot \\ \\ \frac{G_1 \models \Gamma, x:A, (y_i:C_i)_i \quad G_2 \models \Gamma, x:B, (y_i:D_i)_i}{x \to \tilde{y}.\mathsf{case}(G_1, G_2) \models \Gamma, x:A \oplus B, (y_i:C_i \ \& D_i)_i} & \oplus \ \& & \frac{G \models x:A, (y_i:B_i)_i}{!x \to \tilde{y}(G) \models x:?A, (y_i:!B_i)_i} & !? \\ \\ \\ \frac{x \to \tilde{y}.\mathsf{case}() \models \Gamma, x:0, (y_i:\top)_i}{x \to \tilde{y}.\mathsf{case}() \models \Gamma, x:0, (y_i:\top)_i} & 0 \top & \frac{A^\top = X \text{ or } A = X^\bot}{x^A \to y \models x:A, y:A^\bot} \end{aligned}$$



$$\begin{array}{c|c} A = X \text{ or } A = X^{\perp} \\ \hline & \Vdash x^{A} \rightarrow y \ \circ \ \vdash x:A, y:A^{\perp} \\ \hline & H \ x^{A} \rightarrow y \ \circ \ \vdash x:A, y:A^{\perp} \\ \hline & X \ y:A \ y:A^{\perp} \\ \hline & X \ y:A \ y:A^{\perp} \\ \hline & X \ y:A \ y:A \ y:A \ y:A \ y:B \\ \hline & X \ y:A \ y:B \\ \hline & X \ y:A \ y:B \\ \hline & Y \ y:B \ y:A \ y:B \\ \hline & Y \ y:B \ y$$

Fig. 2. Rules for the action fragment.

The remaining typing rules for the action fragment, presented in Fig. 2 are identical to those of GCP with the exception that a context in GCP may be

distributed among several sequents here. For example, rule  $\otimes$  takes two sequents  $\vdash \Gamma_1, x' : A$  and  $\vdash \Gamma_2, x : B$  from two different hypersequents, and merges them into  $\vdash \Gamma_1, \Gamma_2, x : A \otimes B$ , as in classical linear logic. However, elements of  $\Gamma_1$  and  $\Gamma_2$  may be connected through  $\Sigma_1$  and  $\Sigma_2$  to other parts of  $\Psi_1$  and  $\Psi_2$  respectively (as a result of previously applied Conn). Note that the rules of this fragment work only with processes not engaged in any session, since the endpoints explicitly mentioned in proof terms cannot occur in the domain of  $\Sigma$ : this is an implicit check in all rules of Fig. 2. Rule  $\top$  introduces a single sequent  $\Gamma, x : \top$ , allowing for any  $\Gamma$ . The proof term  $x^{\tilde{u}}.case()$  keeps track of the endpoints introduced in  $\Gamma$ : it ensures that all endpoints in the typing are mentioned in the proof term, which is useful when defining semantics. In this article, we restrict the axiom to only type variables (see Sect. 6).

Semantics. The semantics of the action fragment is almost identical to that of standard GCP. It is obtained from cases of the proof of cut elimination: the principal cases describe reductions  $(\longrightarrow)$ , while the permutations of rule applications give rise to the rules for structural equivalence  $(\equiv)$ , reported in Fig. 3. Note that as we are interested only in commuting conversions of typable programs, there are certain cases where the correct equivalence can be found only by looking at the typing derivation which contains information that is not part of the process term. Under  $\equiv$ , parallel distributes safely over **case** (because only the actions of one branch are going to be executed). A similar mechanism can be found in the original presentation of Classical Processes [22], and was

 $(\boldsymbol{\nu}\tilde{x}:G)(P \mid \tilde{Q}) \equiv (\boldsymbol{\nu}\tilde{x}:G)P \mid \tilde{Q}$  $(\tilde{P} \mid Q) \mid \tilde{S} \equiv \tilde{P} \mid (Q \mid \tilde{S})$  $(\boldsymbol{\nu}\tilde{x}:G)(\boldsymbol{\nu}\tilde{y}:H)P \equiv (\boldsymbol{\nu}\tilde{y}:H)(\boldsymbol{\nu}\tilde{x}:G)P$  $(\overline{x}(x'); (P \mid Q)) \mid \tilde{S} \equiv \overline{x}(x'); ((P \mid \tilde{S}) \mid Q)$  $(\boldsymbol{\nu}\tilde{w}:G)\left(\overline{x}(x');(P \mid Q)\right) \equiv$  $(\overline{x}(x'); (P \mid Q)) \mid \tilde{S} \equiv \overline{x}(x'); (P \mid (Q \mid \tilde{S}))$  $\overline{x}(x'); ((\boldsymbol{\nu}\tilde{w}:G) P \mid Q) \quad (\exists i.w_i \in \mathsf{fv}(P))$  $(\boldsymbol{\nu}\tilde{w}:G)(\overline{x}(x');(P \mid Q)) \equiv$  $y(y'); P \mid \tilde{Q} \equiv y(y'); (P \mid \tilde{Q})$  $\overline{x}(x'); (P \mid (\boldsymbol{\nu}\tilde{w}:G)Q) \quad (\exists i.w_i \in \mathsf{fv}(Q))$ wait[y];  $P \mid \tilde{Q} \equiv wait[y]; (P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G)(y(y');P) \equiv y(y'); (\boldsymbol{\nu}\tilde{w}:G)P$  $x.inl; P \mid \tilde{Q} \equiv x.inl; (P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G)(x.inl;P) \equiv x.inl; (\boldsymbol{\nu}\tilde{w}:G)P$  $x.inr; Q \mid \tilde{Q} \equiv x.inr; (P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G)(x.\mathsf{inr};Q) \equiv x.\mathsf{inr}; (\boldsymbol{\nu}\tilde{w}:G)Q$  $x.(\mathsf{inl}: P, \mathsf{inr}: Q) \mid \tilde{S} \equiv$  $(\boldsymbol{\nu}\tilde{w}:G)(x.(\mathsf{inl}:P,\mathsf{inr}:Q)) \equiv$  $x.(\mathsf{inl}: P \mid \tilde{S}, \mathsf{inr}: Q \mid \tilde{S})$  $x.(\mathsf{inl}:(\boldsymbol{\nu}\tilde{w}:G)P,\mathsf{inr}:(\boldsymbol{\nu}\tilde{w}:G)Q)$  $y.\mathsf{case}(P,Q) \mid \tilde{S} \equiv y.\mathsf{case}(P \mid \tilde{S},Q \mid \tilde{S})$  $(\boldsymbol{\nu}\tilde{w}:G)(y.\mathsf{case}(P,Q)) \equiv$ use  $x; P \mid \tilde{Q} \equiv$  use  $x; (P \mid \tilde{Q})$  $y.case((\boldsymbol{\nu}\tilde{w}:G) P, (\boldsymbol{\nu}\tilde{w}:G) Q)$  $\operatorname{kill} x \mid P \mid \tilde{Q} \equiv \operatorname{kill} x \mid (P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G)$  (use x; P)  $\equiv$  use  $x; (\boldsymbol{\nu}\tilde{w}:G) P$  $(\boldsymbol{\nu}\tilde{w}:G)$  (kill  $x \mid P) \equiv$  kill  $x \mid (\boldsymbol{\nu}\tilde{w}:G) P$ clone x(x');  $P \mid \tilde{Q} \equiv$  clone x(x');  $(P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G)$  (clone  $x(x'); P) \equiv$  clone  $x(x'); (\boldsymbol{\nu}\tilde{w}:G) P$  $(\boldsymbol{\nu}\tilde{x}:G)\,(\mathsf{srv}\,y;P\mid\tilde{Q})\,\equiv\,\mathsf{srv}\,y;(\boldsymbol{\nu}\tilde{x}:G)\,(P\mid\tilde{Q})$  $(\boldsymbol{\nu}\tilde{z}z:G)(x^{\tilde{u},z}.\mathsf{case}() \mid \tilde{Q}) \equiv x^{\tilde{u},\tilde{v}}.\mathsf{case}()$ where  $\tilde{v} = \mathsf{vars}(\tilde{Q}) \setminus \tilde{z}$ 

Fig. 3. Equivalences for commuting the action fragment with Conn and Scope. All rules assume that both sides of the equation are typable in the same context.

 $(\boldsymbol{\nu}\tilde{x}, y, \tilde{z} : \tilde{x} \to y(G); H) \left( (\overline{x_i}(x'_i); (P_i \mid Q_i))_i \mid y(y'); R \mid \tilde{S} \right)$  $(\boldsymbol{\nu}\tilde{x}', y': G\{\tilde{x}'/\tilde{x}, y'/y\}) \left(\tilde{P} \mid (\boldsymbol{\nu}\tilde{x}, y, \tilde{z}: H) \left(\tilde{Q} \mid R \mid \tilde{S}\right)\right)$  $(\boldsymbol{\nu}\tilde{x}, y: \tilde{x} \to y) \left( (\mathsf{close}[x_i])_i \mid \mathsf{wait}[y]; P \right)$  $\longrightarrow P$  $(\boldsymbol{\nu}x, \tilde{y}, \tilde{z}: x \to \tilde{y}.\mathsf{case}(G, H))$   $(x.\mathsf{inl}; P \mid (y_i.\mathsf{case}(Q_i, R_i))_i \mid \tilde{S})$  $\longrightarrow (\boldsymbol{\nu} x, \tilde{y}, \tilde{z} : G) \left( P \mid \tilde{Q} \mid \tilde{S} \right)$  $(\boldsymbol{\nu}x, \tilde{y}, \tilde{z} : x \to \tilde{y}.\mathsf{case}(G, H)) (x.\mathsf{inr}; P \mid (y_i.\mathsf{case}(Q_i, R_i))_i \mid \tilde{S})$  $\longrightarrow (\boldsymbol{\nu} x, \tilde{y}, \tilde{z} : H) \left( P \mid \tilde{R} \mid \tilde{S} \right)$  $(\boldsymbol{\nu}x, \tilde{y}, \tilde{z} : x \to \tilde{y}.\mathsf{case}(G, H)) (x.(\mathsf{inl} : P, \mathsf{inr} : Q) \mid (y_i.\mathsf{case}(R_i, S_i))_i \mid \tilde{T})$  $\longrightarrow (\boldsymbol{\nu} x, \tilde{y}, \tilde{z} : G) \left( P \mid \tilde{R} \mid \tilde{T} \right)$  $(\boldsymbol{\nu}x, \tilde{y}, \tilde{z} : x \to \tilde{y}.\mathsf{case}(G, H)) (x.(\mathsf{inl} : P, \mathsf{inr} : Q) \mid (y_i.\mathsf{case}(R_i, S_i))_i \mid \tilde{T})$  $\longrightarrow (\boldsymbol{\nu} x, \tilde{y}, \tilde{z} : H) \left( Q \mid \tilde{S} \mid \tilde{T} \right)$  $\longrightarrow (\boldsymbol{\nu} x, \tilde{y} : G) \left( P \mid \tilde{Q} \right)$  $(\boldsymbol{\nu} x, \tilde{y} : !x \to \tilde{y}(G))$  (use  $x; P \mid (\operatorname{srv} y_i; Q_i)_i$ )  $(\boldsymbol{\nu} x, \tilde{y} : !x \to \tilde{y}(G)) (\text{kill } x \mid P \mid (\text{srv } y_i; Q_i)_i)$  $\longrightarrow$  (kill  $u_j$ )<sub>i</sub> | P where  $\forall i. \forall v_i \in \mathsf{fv}(Q_i). v_i \neq y_i \Rightarrow \exists j. v_i = u_i$  $(\boldsymbol{\nu} x, \tilde{y} : !x \to \tilde{y}(G)) (\operatorname{clone} x(x'); P \mid (\operatorname{srv} y_i; Q_i)_i) \longrightarrow$  $\left(\operatorname{clone} u_j(u'_j)\right)_{:} ; (\boldsymbol{\nu} x, \tilde{y} : !x \to \tilde{y}(G)) \ (\boldsymbol{\nu} x', \tilde{y}' : !x' \to \tilde{y}'(G\{x'/x, \tilde{y}'/\tilde{y}\})) \ \left(P \mid (\operatorname{srv} y_i; Q_i)_i \mid (\operatorname{srv} y'_i; Q'_i)_i\right) \right) = 0$ where  $\forall i. \forall v_i \in \mathsf{fv}(Q_i). v_i \neq y_i \Rightarrow \exists j. v_i = u_j$  $(\boldsymbol{\nu} x, y: x^X \to y) \left( x^X \to w \mid P \right)$  $\longrightarrow P\{w/y\}$  $(\boldsymbol{\nu}x, y: x^{X^{\perp}} \to y) (w^X \to x \mid P)$  $\longrightarrow P\{w/y\}$ 

Fig. 4. Semantics for the action fragment.

later demonstrated to correspond to a bisimulation law in [1]. The semantics of the action fragment of our calculus is presented in Fig. 4. Notice that the  $\beta$ -reductions are coordinated by a global type, as they correspond to multiple parties communicating.<sup>1</sup> The reduction rules for server killing and cloning may look strange because both kill and **clone** remain in the proof term after reduction. This is because of the corresponding reduction in classical linear logic, where it is necessary to use weakening and contraction (corresponding to kill and clone respectively) also after reduction. As a consequence, we get them as proof terms.

# 4 Extending GCP with Choreographies

In order to obtain full MCC, we extend the action fragment presented in the previous section with choreography terms (interactions).

*Syntax.* Unlike a process in the action fragment, a choreography, which describes a global view of the communications of a process, will own all of the endpoints of

<sup>&</sup>lt;sup>1</sup> It may be surprising that some of the rules also include a restriction to a vector  $\tilde{z}$ , and a session using a vector of processes  $\tilde{S}$ , whose shape we do not inspect. This follows from the shape of coherence rules: rules such as  $\otimes \otimes$ ,  $\oplus \&$  and  $0\top$  contain an additional context  $\Gamma$ , captured here by  $\tilde{z}$ .

the sessions it describes. We call the fragment of MCC with choreography terms the *interaction fragment*. Formally, MCC syntax is extended as follows:

P::= as in the action fragm	ent	$x$ starts $\tilde{y}; P$	server accept/request
$  z \leftarrow y^B \rightarrow x; P$	link	$x \text{ kills } \widetilde{y(Q)}; P$	server kill
$\tilde{x}(\tilde{x}') \rightarrow y(y'); P$	communication	$  x \operatorname{clones} \tilde{y}(x', \tilde{y}'); P$	server clone
$\tilde{x}$ closes $y; P$	session close		
$  x \to \tilde{y}.inl(P;Q_1,\ldots,Q_n)$	left selection	$\mid x \to \tilde{y}.(inl: P, inr: Q)$	general selection
$  x \rightarrow \tilde{y}.inr(P_1,\ldots,P_n;Q)$	right selection		

The link term  $z \leftarrow y^B \to x; P$  gives the choreographic view of an axiom connected to some other process P through endpoints x and y. A linear interaction  $\tilde{x}(\tilde{x}') \to y(y'); P$  denotes a communication from endpoints  $\tilde{x}$  to the endpoint y, where a new session with endpoints  $\tilde{x}', y'$  is created. The choreography  $\tilde{x}$  closes y; P closes a session between endpoints  $\tilde{x}, y$ . When it comes to branching, we have two choreographic terms denoting left and right selection:  $x \to \tilde{y}.inl(P; Q_1, \ldots, Q_n)$  and  $x \to \tilde{y}.inr(P_1, \ldots, P_n; Q)$ . A third term,  $x \to$  $\tilde{y}.(inl : P, inr : Q)$ , is used for non-deterministic choice. In MCC, we can model non-linear behaviour: this is done with the terms x starts  $\tilde{y}; P, x$  kills  $\widetilde{y(Q)}; P$  and x clones  $\tilde{y}(x', \tilde{y}'); P$ . The first term features a client x starting a new session with servers  $\tilde{y}$ , while the second term is used by endpoint x to shut down servers  $\tilde{y}$ . Finally, we have a term for cloning servers so that they can be used by different clients in different sessions.

Typing. Figure 5 details the rules for typing choreography terms. Each of these rules combines two rules from the action fragment simulating their reduction, where the conclusion of a rule corresponds to the redex and the premise to the reductum. Unlike process rules, the choreography rules now also look at  $\Sigma$  to check that the interactions described conform to the types of the ongoing sessions. In rule  $C_{1\perp}$ , we close a session (removed from  $\Sigma$ ) and terminate all processes involved in it. Rule  $C_{\otimes \otimes}$  types the creation of a new session with protocol G, created among endpoints  $\tilde{z}$  and w; this session is stored in  $\Sigma$ , while the process types are updated as in rules  $\otimes$  and  $\otimes$  above. The remaining rules in the linear fragment are similarly understood. Exponentials give rise to three rules, all of them combining ! with another rule. In rule C!?, process x invokes the services provided by  $\tilde{y}$ , creating a new session among these processes with type G. Rule  $C_{lw}$  combines ! with Weaken: here the processes providing the service are simply removed from the context. Finally, rule  $C_{lC}$  combines ! with Contract, allowing a service to be duplicated.

Reduction Semantics. Figure 6 gives the reductions for the interaction fragment. From a proof-theoretical perspective, these reductions correspond to proof transformations of C rules from Fig. 5 followed by a structural Scope rule; the transformation removes the C rule and pushes Scope higher up in the proof tree.

Linear Fragment:



Fig. 5. Rules for the interaction fragment.

Remark 1 (Server Cloning). The reduction rule for a server cloning choreography must clone all of the doubled endpoints. Looking at the typing rule  $C_{!C}$  on Fig. 5, cloned variables  $u_j$  are all of the endpoints mentioned in  $(?\Gamma_i)_i$ , and  $u'_j$  are corresponding endpoints from  $(?\Gamma'_i)_i$ . To make the search for these variables syntactic, one could do an endpoint projection, as described in the next section, and look at the appropriate subterm of the Conn rule which connects  $y_i$  and x. The  $u_j$  are then the free variables of this subterm, excluding  $y_i$ .

Structural Equivalence. The reductions given earlier require that programs are written in the very specific form given in their left-hand side. Formally, this is achieved by closing  $\longrightarrow$  under structural equivalence: if  $P \equiv P', P' \longrightarrow Q'$  and  $Q' \equiv Q$ , then  $P \longrightarrow Q$ . The equivalences for the interaction part are given in Fig. 7. As in the action fragment, we are only interested in commuting

conversions of typable programs, and therefore rely on typing derivations for finding the correct equivalence. Besides the commuting conversions, we also have the usual structural equivalence rules where parallel composition under restriction,

$$\begin{aligned} (\nu x, y: x^X \to y) (w \leftarrow y^{X^{\perp}} \to x; P) &\longrightarrow P\{w/x\} \\ (\nu x, y: x^{X^{\perp}} \to y) (w \leftarrow y^X \to x; P) &\longrightarrow P\{w/x\} \\ (\nu \bar{x}, y; \bar{z}: \bar{x} \to y(G); H) (\bar{x}(\bar{x}') \to y(y'); P) &\longrightarrow P\{w/x\} \\ (\nu \bar{x}, y; \bar{z}: \bar{x} \to y(G); H) (\bar{x}(\bar{x}') \to y(y'); P) &\longrightarrow P\{w/x\} \\ (\nu \bar{x}, y; \bar{z}: \bar{x} \to y(G); H) (\bar{x}(\bar{x}') \to y(y'); P) &\longrightarrow P \\ (\nu x, \bar{y}, \bar{z}: x \to \bar{y}. case(G, H)) (x \to \bar{y}. inl(P; Q_1, \dots, Q_n)) &\longrightarrow (\nu x, \bar{y}, \bar{z}: G) P \\ (\nu x, \bar{y}, \bar{z}: x \to \bar{y}. case(G, H)) (x \to \bar{y}. inl(P; Q_1, \dots, P_n; Q)) &\longrightarrow (\nu x, \bar{y}, \bar{z}: H) Q \\ (\nu x, \bar{y}, \bar{z}: x \to \bar{y}. case(G, H)) (x \to \bar{y}. (inl: P, inr: Q)) &\longrightarrow (\nu x, \bar{y}, \bar{z}: G) P \\ (\nu x, \bar{y}, \bar{z}: x \to \bar{y}. case(G, H)) (x \to \bar{y}. (inl: P, inr: Q)) &\longrightarrow (\nu x, \bar{y}, \bar{z}: H) Q \\ (\nu x, \bar{y}, \bar{z}: x \to \bar{y}. case(G, H)) (x \to \bar{y}. (inl: P, inr: Q)) &\longrightarrow (\nu x, \bar{y}, \bar{z}: H) Q \\ (\nu x, \bar{y}: \bar{x}: x \to \bar{y}. G)) (x \text{ starts } \bar{y}; P) &\longrightarrow (\nu x, \bar{y}: G) P \\ (\nu x, \bar{y}: !x \to \bar{y}(G)) (x \text{ clones } \bar{y}(x', \bar{y}'); P) &\longrightarrow (kill u_j)_j \mid P \quad (\forall v_i \in fv(Q_i). v_i \neq y_i \Rightarrow \exists j. v_i = u_j) \\ (\nu x, \bar{y}: !x \to \bar{y}(G)) (x \text{ clones } \bar{y}(x', \bar{y}'; !x' \to \bar{y}'(G\{x'/x, \bar{y}'/\bar{y}\})) P \quad (\text{see Remark } 1) \end{aligned}$$

#### Fig. 6. Semantics for the interaction fragment.

 $w \leftarrow y^B \to x; P \mid \tilde{Q} \equiv w \leftarrow y^B \to x; (P \mid \tilde{Q})$  $\tilde{x}(\tilde{x}') \to y(y'); P \mid \tilde{Q} \equiv \tilde{x}(\tilde{x}') \to y(y'); (P \mid \tilde{Q})$  $\tilde{x}$  closes  $y; P \mid \tilde{Q} \equiv \tilde{x}$  closes  $y; (P \mid \tilde{Q})$  $x \to \tilde{y}.inl(P; Q_1, \dots, Q_n) \mid \tilde{S} \equiv x \to \tilde{y}.inl((P \mid \tilde{S}); Q_1, \dots, Q_n)$  $x \to \tilde{y}.\mathsf{inl}(P; Q_1, \dots, Q_n) \mid \tilde{S} \equiv x \to \tilde{y}.\mathsf{inl}((P \mid \tilde{S}); (Q_1, \dots, (Q_i \mid \tilde{S}), \dots, Q_n))$  $x \to \tilde{y}.\mathsf{inr}(P_1, \ldots, P_n; Q) \mid \tilde{S} \equiv x \to \tilde{y}.\mathsf{inr}(P_1, \ldots, P_n; (Q \mid \tilde{S}))$  $x \to \tilde{y}.\mathsf{inr}(P_1, \ldots, P_n; Q) \mid \tilde{S} \equiv x \to \tilde{y}.\mathsf{inr}((P_1, \ldots, (P_i \mid \tilde{S}), \ldots, P_n); (Q \mid \tilde{S}))$ x starts  $\tilde{y}; P \mid \tilde{Q} \equiv x$  starts  $\tilde{y}; (P \mid \tilde{Q})$  $x \text{ kills } \widetilde{y(Q)}; P \mid \widetilde{S} \equiv x \text{ kills } \widetilde{y(Q)}; (P \mid \widetilde{S})$  $x \operatorname{clones} \tilde{y}(x', \tilde{y}'); P \mid \tilde{Q} \equiv x \operatorname{clones} \tilde{y}(x', \tilde{y}'); (P \mid \tilde{Q})$  $(\boldsymbol{\nu}\tilde{w}:G) (\tilde{x}(\tilde{x}') \to y(y'); P) \equiv \tilde{x}(\tilde{x}') \to y(y'); (\boldsymbol{\nu}\tilde{w}:G) P$  $(\boldsymbol{\nu}\tilde{w}:G)$   $(\tilde{x}$  closes y;P)  $\equiv$   $\tilde{x}$  closes  $y;(\boldsymbol{\nu}\tilde{w}:G)$  P $(\boldsymbol{\nu}\tilde{w}:G) (x \to \tilde{y}.\mathsf{inl}(P;Q_1,\ldots,Q_n)) \equiv x \to \tilde{y}.\mathsf{inl}((\boldsymbol{\nu}\tilde{w}:G)P;Q_1,\ldots,Q_n)$  $(\boldsymbol{\nu}\tilde{w}:G)(x \to \tilde{y}.\mathsf{inl}(P;Q_1,\ldots,Q_n)) \equiv x \to \tilde{y}.\mathsf{inl}((\boldsymbol{\nu}\tilde{w}:G)P;Q_1,\ldots,(\boldsymbol{\nu}\tilde{w}:G)Q_i,\ldots,Q_n)$  $(\boldsymbol{\nu}\tilde{w}:G) (x \to \tilde{y}.\mathsf{inr}(P_1, \ldots, P_n; Q)) \equiv x \to \tilde{y}.\mathsf{inr}(P_1, \ldots, P_n; (\boldsymbol{\nu}\tilde{w}:G) Q)$  $(\boldsymbol{\nu}\tilde{w}:G)(x \to \tilde{y}.\mathsf{inr}(P_1,\ldots,P_n;Q)) \equiv x \to \tilde{y}.\mathsf{inr}(P_1,\ldots,(\boldsymbol{\nu}\tilde{w}:G)P_i,\ldots,P_n;(\boldsymbol{\nu}\tilde{w}:G)Q)$  $(\boldsymbol{\nu}\tilde{w}:G)(x.(\mathsf{inl}:P,\mathsf{inr}:Q)) \equiv x.(\mathsf{inl}:(\boldsymbol{\nu}\tilde{w}:G)P,\mathsf{inr}:(\boldsymbol{\nu}\tilde{w}:G)Q)$  $(\boldsymbol{\nu}\tilde{w}:G)(x \text{ starts } \tilde{y};P) \equiv x \text{ starts } \tilde{y};(\boldsymbol{\nu}\tilde{w}:G)P$  $(\boldsymbol{\nu}\tilde{w}:G)(x \text{ kills } \widetilde{y(Q)};P) \equiv x \text{ kills } \widetilde{y_i(Q_i)}; (\boldsymbol{\nu}\tilde{w}:G)P$  $(\boldsymbol{\nu}\tilde{w}:G)$   $(x \text{ clones } \tilde{y}(x', \tilde{y}'); P) \equiv x \text{ clones } \tilde{y}(x', \tilde{y}'); (\boldsymbol{\nu}\tilde{w}:G) P$  $(\boldsymbol{\nu}\tilde{z}:G)(x \operatorname{clones} \tilde{y}(x', \tilde{y}'); P \mid \tilde{Q}) \equiv x \operatorname{clones} \tilde{y}(x', \tilde{y}'); (\boldsymbol{\nu}\tilde{z}:G)(P \mid \tilde{Q})$ 

Fig. 7. Equivalences for commuting C-rules with Conn and Scope. All rules assume that both sides are typable in the same context.

linking process and global type for linking sessions are all symmetric. Furthermore, the order of restrictions can be swapped.

$$\begin{aligned} x^{A} \to y &\equiv y^{A^{\perp}} \to x \\ (\boldsymbol{\nu}\tilde{w}, y, x, \tilde{z}:G) \,\tilde{P} \mid R \mid Q \mid \tilde{S} &\equiv (\boldsymbol{\nu}\tilde{w}, x, y, \tilde{z}:G) \,\tilde{P} \mid Q \mid R \mid \tilde{S} \\ (\boldsymbol{\nu}z, \tilde{w}:H) \, (\boldsymbol{\nu}x, \tilde{y}:G) \,P \mid \tilde{R} \mid \tilde{Q} &\equiv (\boldsymbol{\nu}x, \tilde{y}:G) \, (\boldsymbol{\nu}z, \tilde{w}:H) \,P \mid \tilde{Q} \mid \tilde{R} \end{aligned}$$

*Properties.* We finish the presentation of MCC by establishing the expected meta-theoretic properties of the system. As structural congruence is typing-based, subject congruence is a property holding by construction:

**Theorem 1 (Subject Congruence).**  $\Sigma \Vdash P \ : \Psi \text{ and } P \equiv Q \text{ implies that } \Sigma \Vdash Q \ : \Psi.$ 

*Proof.* By induction on the proof that  $P \equiv Q$ . In [5], it is explained how the rules for structural equivalence were derived, making this proof straightforward.

Moreover, our reductions preserve typing since they are proof transformations.

**Theorem 2 (Subject Reduction).**  $\Sigma \Vdash P \ : \Psi \text{ and } P \longrightarrow Q \text{ implies}$  $\Sigma \Vdash Q \ : \Psi.$ 

*Proof.* By induction on the proof that  $P \longrightarrow Q$ . In [5], it is explained how the semantics of MCC were designed in order to make this proof straightforward.

Finally, we can show that MCC is deadlock-free, since the top-level Scope application can be pushed up the derivation. In case the top-level Scope application is next to an application of Conn, either the choreography can reduce directly or both rules can be pushed up. Proof-theoretically, this procedure can be viewed as MCC's equivalent of the Principal Lemma of Cut Elimination.

**Theorem 3 (Deadlock-freedom).** If P begins with a restriction and  $\Sigma \Vdash P \ : \Psi$ , then there exists Q such that  $P \longrightarrow Q$ .

*Proof (Sketch).* Our proof idea is similar to that of Theorem 3 in [9]. We apply induction on the size of the proof of  $\Sigma \Vdash P \ ^{\circ}_{\circ} \Psi$ . If a rule from Fig. 4 or Fig. 6 is applicable (corresponding to a proof where an application of **Conn** and an application of **Scope** meet), then the thesis immediately holds.

Otherwise, we apply commuting conversions from Fig. 3 or Fig. 7, "pushing" the top-level Scope application up in the derivation (and, if it is preceded by an application of Conn, "pushing" also that application). This results in a smaller proof of  $\Sigma \Vdash P \stackrel{\circ}{\circ} \Psi$ , to which the induction hypothesis can be applied.

### 5 Projection and Extraction

As suggested by the previous sections, interactions can be implemented in two ways: as a single choreography term, or as multiple process terms appearing in different behaviours composed in parallel. In this section, we formally show that choreography interactions can be projected to process implementations, and symmetrically, process implementations can be extracted to choreographies. We do this by transforming proofs (derivations in the typing system), similarly to the way we defined equivalences and reductions for MCC.

We start by defining the principal transformations for projection and extraction, a set of equivalences that require proof terms to have a special shape. We report such transformations in Fig. 8: they perform extraction if read from left to right, while they perform projection if read from right to left. The extraction relation requires access to the list of open sessions  $\Sigma$  to ensure that we have all the endpoints participating in the session to extract a choreography from. The first two rules deal with axioms: the parallel composition (rule Conn) of an axiom with a process P can be expressed by rule  $C_{Ax}$  and vice-versa. On the third line, we show how to transform the parallel composition of an output  $(\otimes)$ and an input ( $\otimes$ ) into a  $C_{\otimes \otimes}$ . Similarly,  $\tilde{x}$  closes y; P is the choreographic representation of the term  $(close[x_i])_i$  | wait[y]; P. Each branching operation (left, right, non-deterministic) has a representative in both fragments with straightforward transformations. A server sry y; Q can either be used by a client, killed or cloned. In the first two cases, such interactions trivially correspond to the choreographic terms x starts  $\tilde{y}$ ;  $(P \mid \tilde{Q})$  and x kills y(Q); P. In the case of cloning, we create the interaction term  $x \operatorname{clones} \tilde{y}(x', \tilde{y}'); (P \mid (\operatorname{srv} y_i; Q_i)_i \mid (\operatorname{srv} y'_i; Q'_i)_i),$ which shows how the choreographic cloning  $x \operatorname{clones} \tilde{y}(x', \tilde{y}')$ ; must be followed by two instances of the server that is cloned. Note that these transformations are derived by applying similar techniques as those of cut elimination. Concrete derivations, here omitted, are straightforward: an example can be found in [5].

$$\begin{array}{rcl} P \mid x^X \rightarrow y &\rightleftharpoons x \leftarrow y^{X^{\perp}} \rightarrow w; P & (w, y) \in \operatorname{dom}(\varSigma) \\ P \mid y^X \rightarrow x &\rightleftharpoons x \leftarrow y^X \rightarrow w; P & (w, y) \in \operatorname{dom}(\varSigma) \\ \hline (\overline{x_i}(x_i'); (P_i \mid Q_i))_i \mid y(y'); R \mid \tilde{S} &\rightleftharpoons \tilde{x}(\tilde{x}') \rightarrow y(y'); \left(\tilde{P} \mid \tilde{Q} \mid R \mid \tilde{S}\right) \\ & (\operatorname{close}[x_i])_i \mid \operatorname{wait}[y]; P &\rightleftharpoons \tilde{x} \operatorname{closes} y; P \\ \hline x.\operatorname{inl}; P \mid (y_i.\operatorname{case}(Q_i, R_i))_i \mid \tilde{S} &\rightleftharpoons x \rightarrow \tilde{y}.\operatorname{inl}(P \mid \tilde{Q} \mid \tilde{S}; R_1, \dots, R_n) \\ & x.\operatorname{inr}; P \mid (y_i.\operatorname{case}(Q_i, R_i))_i \mid \tilde{S} &\rightleftharpoons x \rightarrow \tilde{y}.\operatorname{inl}(Q_1, \dots, Q_n; P \mid \tilde{R} \mid \tilde{S}) \\ \hline x.(\operatorname{inl}: P, \operatorname{inr}: Q) \mid (y_i.\operatorname{case}(R_i, S_i))_i \mid \tilde{T} &\rightleftharpoons x \rightarrow \tilde{y}.(\operatorname{inl}: P \mid \tilde{R} \mid \tilde{T}, \operatorname{inr}: Q \mid \tilde{S} \mid \tilde{T}) \\ & \operatorname{use} x; P \mid (\operatorname{srv} y_i; Q_i)_i &\rightleftharpoons x \operatorname{stars} \tilde{y}; (P \mid \tilde{Q}) \\ & \operatorname{kill} x \mid P \mid (\operatorname{srv} y_i; Q_i)_i &\rightleftharpoons x \operatorname{clones} \tilde{y}(x', \tilde{y}'); (P \mid (\operatorname{srv} y_i; Q_i)_i \mid (\operatorname{srv} y_i'; Q_i')_i) \\ \end{array}$$



*Remark 2.* In order to project/extract an arbitrary well-typed term, given the strict format required by the transformations in Fig. 8, we will sometimes have to perform rewriting of terms in accordance with the commuting conversions to reach an expected shape. In particular, we note that when projecting, we must first project the subterms (we start from the leaves of a proof), step by step moving down to the main term. In contrast, when extracting, we must proceed from the root of the proof towards the leaves.

Note that our example in Sect. 2 does not provide an exact projection: in order to improve readability, we have removed all parallels that follow output operations, which would be introduced by the translation presented above. This is not problematic, since the outputs in the example are just basic types.

Properties. In the sequel, we write  $P \xrightarrow{\tilde{x}}_{extr} P'$  whenever it is possible to apply one of the transformations in Fig. 8 to (a term equivalent to) term P from left to right, where  $\tilde{x}$  are the endpoints involved in the transformation. Similarly, we write  $P \xrightarrow{\tilde{x}}_{proj} P'$  whenever it is possible to apply a transformation from Fig. 8 to (a term equivalent to) term P from right to left. We also write  $P \Longrightarrow_{extr} P'$  $(P \Longrightarrow_{proj} P')$  if there is a finite sequence of applications of  $\longrightarrow_{extr} (\longrightarrow_{proj})$  and P' cannot be further transformed. We then have the following results:

**Theorem 4 (Type Preservation).** If  $P \xrightarrow{\tilde{x}}_{\mathsf{extr}} Q$  and  $\Sigma \Vdash P \ \circ \Psi$ , then  $\Sigma \Vdash Q \ \circ \Psi$ , and if  $Q \xrightarrow{\tilde{x}}_{\mathsf{proj}} P$  and  $\Sigma \Vdash Q \ \circ \Psi$ , then  $\Sigma \Vdash P \ \circ \Psi$ .

*Proof.* By induction on the proof that  $P \xrightarrow{\tilde{x}}_{extr} Q$  or  $Q \xrightarrow{\tilde{x}}_{proj} P$ . In [5], we explain how the rules for projection and extraction were derived from the typing rules to ensure that the proof of this result is straightforward.

**Theorem 5 (Admissibility of Conn and C-rules).** Let P be a proof term such that  $\Vdash P \ := \vdash \Gamma$ . Then,

- there exists P' such that  $P \Longrightarrow_{\mathsf{extr}} P'$  and P' is Conn-free;
- there exists P' such that  $P \Longrightarrow_{\text{proj}} P'$  and P' is free from C-rules.

*Proof (Sketch).* The idea is similar to the proof of Theorem 4.4.1 in [17]: by applying commuting conversions we can always rewrite P such that one of the rules in Fig. 8 is applicable, thus eliminating the outermost application of Conn (in the case of extraction) or the innermost application of a C-rule (in the case of projection). See also Remarks 2 and 3.

*Remark 3.* The theorem above is only applicable to judgments of the form  $\Vdash P \ ^{\circ} \vdash \Gamma$ . This is because of the commuting conversion of the server rule

$$(\boldsymbol{\nu}\tilde{x}x:G)(\operatorname{srv} y;P \mid \tilde{Q}) \equiv \operatorname{srv} y; (\boldsymbol{\nu}\tilde{x}x:G)(P \mid \tilde{Q})$$

where we can only permute **Conn** and **Scope** together. This conversion is needed to rearrange certain proofs into the format required by the transformations in Fig. 8. Note that any judgement  $\Sigma \Vdash P \ \circ \Psi$  can always be transformed into this format, by repeatedly applying rule **Scope** to all elements in  $\Sigma$ .

As a consequence of the admissibility of Conn, every program can be rewritten into a (non-unique) process containing only process terms by applying the rules in Fig. 8 from right to left until no longer possible. Conversely, because of admissibility of C-rules, every program can be rewritten into a maximal choreographic form by applying the same rules from left to right until no longer possible.

We conclude this section with our main theorem that shows the correspondence between the two fragments with respect to their semantics. In order to do that, we annotate our semantics with the endpoints where the reduction takes place. This is denoted by  $P \longrightarrow^{\tilde{x}} Q$  and  $P \longrightarrow^{\bullet \tilde{x}} Q$  where the first relation is a reduction in the action fragment, while the second is a reduction in the interaction fragment. The sequence  $\operatorname{rev}(\tilde{x})$  is obtained by reversing  $\tilde{x}$ .

**Theorem 6** (Correspondence). Let P be a proof term such that  $\Sigma \Vdash P \degree \Psi$ . Then,

 $\begin{array}{l} -P \longrightarrow^{\tilde{x}} Q \text{ implies that there exists } P' \text{ s.t. } P \xrightarrow{\tilde{x}}_{\mathsf{extr}} P' \text{ and } P' \longrightarrow^{\bullet \tilde{x}} Q; \\ -P \longrightarrow^{\bullet \tilde{x}} Q \text{ implies that there exists } P' \text{ s.t. } P \xrightarrow{\mathsf{rev}(\tilde{x})}_{\mathsf{proj}} P' \text{ and } P' \longrightarrow^{\tilde{x}} Q. \end{array}$ 

*Proof.* This proof follows the same strategy as that of Theorem 6 in [9].

### 6 Related Work and Discussion

*Related Work.* The principle of choreographies as cut reductions was introduced in [9]. As discussed in Sect. 1, that system cannot capture services or multiparty sessions. Another difference is that it is based on intuitionistic linear logic, whereas ours on classical linear logic – in particular, on Classical Processes [22].

Switching to classical linear logic is not a mere change of appearance. It is what allows us to reuse the logical understanding of multiparty sessions in linear logic as *coherence proofs*, introduced in [10] and later extended to polymorphism in [7]. These works did not consider choreographic programs, and thus do not offer a global view on how different sessions are composed, as we do in this paper.

Extracting choreographies from compositions of process code is well-known to be a hard problem. In [15], choreographies that abstract from the exchanged values and computation are extracted from communicating finite-state machines. The authors of [11] present an efficient algorithm for extracting concrete choreographic programs with asynchronous messaging. These works do not consider the composition of multiple sessions, multiparty sessions, and services, as in MCC. However, they can both deal with infinite behaviour (through loops or recursion), which we do not address. An interesting direction for this feature would be to integrate structural recursion for classical linear logic [16].

Our approach can be seen as a principled reconstruction of previous works on choreographic programming. The first work that typed choreographies using multiparty session types is [8]. The idea of mixing choreographies with processes using multiparty session types is from [19]. None of these consider extraction. *Discussion.* For the sake of clarity, our presentation of MCC adopts simplifications that may limit the model expressivity. Below, we discuss some key points as well as possible extensions based on certain developments in this research line.

**Non-determinism.** We introduced non-determinism in a straightforward way, i.e., our non-deterministic rules in both action and interaction fragments require for each branch to have the same type, as done for standard session typing. However, this solution breaks the property of confluence that we commonly have in logics. In order to preserve confluence, we would have to extend MCC with the non-deterministic linear types from [3].

 $\eta$ -expansion. GCP in [7] allows for the axiom to be of any type A. This requires heavily using  $\eta$ -expansions for transforming axioms into processes with communication actions. It is straightforward to do this in the action fragment of MCC. However, given the way choreographies work, we can only define an axiom for binary sessions in the interaction fragment. As a consequence, in order to apply extraction to a process where an axiom is engaged in a multiparty session with several endpoints, we would need to first use  $\eta$ -expansions to transform such axiom into an ordinary process. In the opposite direction, we would never be able to project a process containing an axiom from a choreography, unless it is part of a binary session. We leave further investigation of this as future work.

Annotated Types. The original version of GCP [7] comes with an extension called MCP, where an endpoint type A is annotated with names of endpoints which it will be in a session with. In this way, endpoint types become more expressive, since it is possible to specify with whom each endpoint has to communicate, without having to use a global type (coherence proof) during execution. We claim that this extension is straightforward for our presentation of MCC.

**Polymorphism.** As in GCP [7], we can easily add polymorphic types to MCC. However, for simplifying the presentation of this work, we have decided to leave it out, even though adding the GCP rules to the action fragment is straightforward. In the case of the interaction fragment, we obtain the following rule:

$$\begin{split} X \not\in \mathsf{fv}(\Psi, \Gamma, (\Gamma_i)_i) \\ \\ \frac{\Sigma, \boxed{(x, \tilde{y}, \tilde{u}) : G\{A/X\}} \Vdash P\{A/X\} \ \ : \Psi \mid \vdash \Gamma, \underline{x : B\{A/X\}} \mid (\vdash \Gamma_i, y_i : B_i\{A/X\})_i}{\Sigma, \boxed{(x, \tilde{y}, \tilde{u}) : x \to \tilde{y}.(X)G} \mid \vdash x[A] \to \tilde{y}(X); P \ \ : \Psi \mid \vdash \Gamma, \underline{x : \exists X.B} \mid (\vdash \Gamma_i, y_i : \forall X.B_i)_i} C_{\exists \forall} \end{split}$$

Above we have added to the syntax of global types the term  $x \to \tilde{y}.(X)G$ , denoting a session where an endpoint x is supposed to send a type to endpoints  $\tilde{y}$ . At choreography level, endpoint x realises the abstraction of the global type sending the actual type A. When it comes to extraction and projection, we would have to add the following transformation:

$$x[A]; P \mid (y_i(X); Q_i)_i \mid \tilde{S} \quad \rightleftharpoons \quad x[A] \to \tilde{y}(X); \left(P \mid \tilde{Q} \mid \tilde{S}\right)$$

where x[A]; P and  $y_i(X); Q_i$  are action fragment terms (as those of GCP).

**Other Extensions.** By importing the functional stratification from [21], we could obtain a monadic integration of choreographies with functions. The calculus of classical higher-order processes [18] could be of inspiration for adding code mobility to MCC, by adding higher-order types. Types for manifest sharing in [2] may lead us to global specifications of sharing in choreographies. And the asynchronous interpretation of cut reductions in [12] might give us an asynchronous implementation of choreographies in MCC. We leave an exploration of these extensions to future work. Hopefully, the shared foundations of linear logic will make it possible to build on these pre-existing technical developments following the same idea of choreographies as cut reductions.

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# A Pragmatic, Scalable Approach to Correct-by-Construction Process Composition Using Classical Linear Logic Inference

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Abstract. The need for rigorous process composition is encountered in many situations pertaining to the development and analysis of complex systems. We discuss the use of Classical Linear Logic (CLL) for correct-by-construction resource-based process composition, with guaranteed deadlock freedom, systematic resource accounting, and concurrent execution. We introduce algorithms to automate the necessary inference steps for binary compositions of processes in parallel, conditionally, and in sequence. We combine decision procedures and heuristics to achieve intuitive and practically useful compositions in an applied setting.

**Keywords:** Process modelling  $\cdot$  Composition  $\cdot$  Correct by construction  $\cdot$  Workflow  $\cdot$  Linear logic

# 1 Introduction

The ideas behind process modelling and composition are common across a variety of domains, including program synthesis, software architecture, multi-agent systems, web services, and business processes. Although the concept of a "process" takes a variety of names – such as agent, role, action, activity, and service – across these domains, in essence, it always captures the idea of an abstract, functional unit. Process composition then involves the combination and connection of these units to create systems that can perform more complex tasks. We typically call the resulting model a (*process*) workflow. Viewed from this standpoint, resource-based process composition then captures a structured model of the resource flow across the components, focusing on the resources that are created, consumed, or passed from one process to another within the system.

Workflows have proven useful tools for the design and implementation of complex systems by providing a balance between an intuitive **abstract model**, typically in diagrammatic form, and a **concrete implementation** through process automation. Evidence can be found, for example, in the modelling of clinical

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F. Mesnard and P. J. Stuckey (Eds.): LOPSTR 2018, LNCS 11408, pp. 77–93, 2019. https://doi.org/10.1007/978-3-030-13838-7\_5 care pathways where workflows can be both understandable by healthcare stakeholders and yet remain amenable to formal analysis [10, 15].

A scalable approach towards establishing trust in the correctness of the modelled system is that of *correct-by-construction* engineering [12,26]. In general, this refers to the construction of systems in a way that guarantees correctness properties about them at design time. In this spirit, we have developed the WorkflowFM system for correct-by-construction process composition [21]. It relies on Classical Linear Logic (see Sect. 2.1) to rigorously compose abstract process specifications in a way that:

- 1. systematically accounts for resources and exceptions;
- 2. prevents deadlocks;
- 3. results in a concrete workflow where processes are executed concurrently.

From the specific point of view of program synthesis, these benefits can be interpreted as (1) no memory leaks or missing data, (2) no deadlocks, hanging threads, or loops, and (3) parallel, asynchronous (non-blocking) execution.

The inference is performed within the proof assistant HOL Light, which offers systematic guarantees of correctness for every inference step [11]. The logical model can be translated through a process calculus to a concrete workflow implementation in a host programming language.

There are numerous aspects to and components in the WorkflowFM system, including, for instance, the diagrammatic interface (as shown in Fig. 1), the code translator, the execution engine, the process calculus correspondence, and the architecture that brings it all together [21]. In this particular paper we focus on the proof procedures that make such resource-based process compositions feasible and accessible. These are essential for creating meaningful workflow models with the correctness-by-construction properties highlighted above, but without the need for tedious manual CLL reasoning. Instead, the user can use high level composition actions triggered by simple, intuitive mouse gestures and without the need to understand the underlying proof, which is guaranteed to be correct thanks to the rigorous environment of HOL Light.

It is worth emphasizing that our work largely aims at tackling pragmatic challenges in real applications as opposed to establishing theoretical facts. We rely on existing formalisms, such as the proofs-as-processes theory described below, in our attempt to exploit its benefits in real world scenarios. As a result, the vast majority of our design decisions are driven by practical experience and the different cases we have encountered in our projects.

Table 1 is a list of some of our case studies in the healthcare and manufacturing domain that have driven the development of WorkflowFM. It includes an indication of the size of each case study based on (1) the number of (atomic) component processes, (2) the number of different types of resources involved in the inputs and outputs of the various processes (see Sect. 3), (3) the number of binary composition actions performed to construct the workflows (see Sect. 4), and (4) the total number of composed workflows.

All of these case studies are models of actual workflows, built based on data from real-world scenarios and input from domain experts such as clinical teams

Case study theme	Processes	Resource types	Actions	Workflows
Patient handovers	9	16	13	2
Tracheostomy care pathway	33	47	32	3
HIV care pathways	128	129	121	13
Pen manufacturing (ongoing)	42	45	60	20
Total	212	237	226	38

Table 1. Sample case studies and an indication of their size.

and managers of manufacturing facilities. The results have been useful towards process improvement in their respective organisations, including a better qualitative understanding based on the abstract model and quantitative analytics obtained from the concrete implementation. As a result, we are confident that the evidence and experience we accumulated from these case studies are representative of the requirements and needs of real applications and that the approach and algorithms presented in this paper can offer significant value.

We note that the presentation accompanying this paper is available online<sup>1</sup>.

# 2 Background

The systematic accounting of resources in our approach can be demonstrated through a hypothetical example from the healthcare domain [21]. Assume a process **DeliverDrug** that corresponds to the delivery of a drug to a patient by a nurse. Such a process requires information about the *Patient*, the *Dosage* of the drug, and some reserved *NurseTime* for the nurse to deliver the drug. The possible outcomes are that either the patient is *Treated* or that the drug *Failed*. In the latter case, we would like to apply the **Reassess** process, which, given some allocated clinician time (*ClinTime*) results in the patient being *Reassessed*. A graphical representation of these 2 processes, where dashed edges denote the optional outcomes of **DeliverDrug**, is shown at the top of Fig. 1.

If we were to compose the 2 processes in a workflow where the drug failure is always handled by **Reassess**, what would be the specification (or specifically the output) of the composite process?

Given the workflow representation in Fig. 1, one may be inclined to simply connect the *Failed* edge of DeliverDrug to the corresponding edge of Reassess, leading to an overall output of either *Treated* or *Reassessed*. However, this would be erroneous, as the input *ClinTime*, is consumed in the composite process even if Reassess is never used. Using our CLL-based approach, the workflow output is either *Reassessed* which occurs if the drug failed, or *Treated* coupled with the unused *ClinTime*, as shown at the bottom of Fig. 1 [21].

Systematically accounting for such unused resources is non-trivial, especially considering larger workflows with tens or hundreds of processes and many different outcomes. The CLL inference rules enforce this by default and the proof

<sup>&</sup>lt;sup>1</sup> https://github.com/PetrosPapapa/Presentations/raw/master/LOPSTR2018.pdf.



Fig. 1. The visualisation of the DeliverDrug and Reassess processes (top) and their sequential composition. The auxiliary triangle helps properly display the output.

reflects the level of reasoning required to achieve this. In addition, the process code generated from this synthesis is fully asynchronous and deadlock-free, and relies on the existence of concrete implementations of DeliverDrug and Reassess.

#### 2.1 Classical Linear Logic

Linear Logic, as proposed by Girard [9], is a refinement to classical logic where the rules of contraction and weakening are limited to the modalities ! and ?. Propositions thus resemble resources that cannot be ignored or copied arbitrarily.

In this work, we use a one-sided sequent calculus version of the multiplicative additive fragment of propositional CLL without units (MALL). Although there exist process translations of full CLL and even first-order CLL, the MALL fragment allows enough expressiveness while keeping the reasoning complexity at a manageable level (MALL is PSPACE-complete whereas full CLL is undecidable [14]). The inference rules for MALL are presented in Fig. 2.

Fig. 2. One-sided sequent calculus versions of the CLL inference rules.

In this version of MALL, linear negation  $(\cdot^{\perp})$  is defined as a syntactic operator with no inference rules, so that both A and  $A^{\perp}$  are considered atomic formulas. The de Morgan style equations in Fig. 3 provide a *syntactic* equivalence of formulas involving negation [27]. This allows us to use syntactically equivalent formulas, such as  $A^{\perp} \Im B^{\perp}$  and  $(A \otimes B)^{\perp}$  interchangeably. In fact,

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in the proofs presented in this paper we choose to present formulas containing  $\otimes$  and  $\oplus$  over their counterparts  $\Re$  and & due to the polarity restrictions we introduce in Sect. 3.

 $(A^{\perp})^{\perp} \equiv A \qquad (A \otimes B)^{\perp} \equiv A^{\perp} \Im B^{\perp} \qquad (A \oplus B)^{\perp} \equiv A^{\perp} \& B^{\perp} \\ (A \Im B)^{\perp} \equiv A^{\perp} \otimes B^{\perp} \qquad (A \& B)^{\perp} \equiv A^{\perp} \oplus B^{\perp}$ 

Fig. 3. The equations used to define linear negation for MALL.

In the 90s, Abramsky, Bellin and Scott developed the so-called proofs-asprocesses paradigm [2,4]. It involved a correspondence between CLL inference and concurrent processes in the  $\pi$ -calculus [18]. They proved that cut-elimination in a CLL proof corresponds to reductions in the  $\pi$ -calculus translation, which in turn correspond to communication between concurrent processes. As a result,  $\pi$ -calculus terms constructed via CLL proofs are inherently free of deadlocks.

The implications of the proofs-as-processes correspondence have been the subject of recent research in concurrent programming by Wadler [28], Pfenning et al. [3,5,25], Dardha [7,8] and others. Essentially, each CLL inference step can be translated to an executable workflow, with automatically generated code to appropriately connect the component processes. As a result, the CLL proofs have a direct correspondence to the "piping", so to speak, that realises the appropriate resource flow between the available processes, such that it does not introduce deadlocks, accounts for all resources explicitly, and maximizes runtime concurrency. The current paper examines CLL inference and we take the correspondence to deadlock-free processes for granted.

#### 2.2 Related Work

Diagrammatic languages such as BPMN [20] are commonly used for the description of workflows in different organisations. However, they typically lack rigour and have limited potential for formal verification [23]. Execution languages such as BPEL [19] and process calculi such as Petri Nets [1] are often used for workflow management in a formal way and our CLL approach could potentially be adapted to work with these. Linear logic has been used in the context of web service composition [22], but in a way that diverges significantly from the original theory and compromises the validity of the results. Finally, the way the resource flow is managed through our CLL-based processes is reminiscent of monad-like structures such as Haskell's arrows<sup>2</sup>. One of the key differences is the lack of support for optional resources, which is non-trivial as we show in this paper.

<sup>&</sup>lt;sup>2</sup> https://www.haskell.org/arrows.

### 3 Process Specification

Since CLL propositions can naturally represent resources, CLL sequents can be used to represent processes, with each literal representing a type of resource that is involved in that process. These abstract types can have a concrete realisation in the host programming language, from primitive to complicated objects.

Our approach to resource-based composition is to construct CLL specifications of abstract processes based on their inputs (and preconditions) and outputs (and effects), also referred to as IOPEs. This is standard practice in various process formalisms, including WSDL for web services [6], OWL-S for Semantic Web services [16], PDDL for actions in automated planning [17], etc.

The symmetry of linear negation as shown in Fig. 3 can be used to assign a *polarity* to each CLL connective in order to distinctly specify input and output resources. We choose to treat negated literals,  $\mathfrak{N}$ , and & as **inputs**, and positive literals,  $\otimes$ , and  $\oplus$  as **outputs**, with the following intuitive interpretation:

- Multiplicative conjunction  $(tensor \otimes)$  indicates a pair of parallel outputs.
- Additive disjunction  $(plus \oplus)$  indicates exclusively optional outputs (alternative outputs or exceptions).
- Multiplicative disjunction  $(par \ \Im)$  indicates a pair of simultaneous inputs.
- Additive conjunction (with &) indicates exclusively optional input.

Based on this, a process can be specified as a CLL sequent consisting of a list of input formulas and a **single** output formula. In this, the order of the literals does not matter, so long as they obey the polarity restrictions (all but exactly one are negative). In practice, we treat sequents as multisets of literals and manage them using particular multiset reasoning techniques in HOL Light. The description of these techniques is beyond the scope of this paper.

The polarity restrictions imposed on our process specifications match the specification of Laurent's Polarized Linear Logic (LLP) [13], and has a proven logical equivalence to the full MALL. Moreover, these restrictions match the programming language paradigm of a *function* that can have multiple input arguments and returns a single (possibly composite) result.

# 4 Process Composition

Using CLL process specifications as assumptions, we can produce a composite process specification using forward inference. Each of the CLL inference rules represent a logically legal way to manipulate and compose such specifications.

The axiom  $\vdash A, A^{\perp}$  represents the so-called *axiom buffer*, a process that receives a resource of type A and outputs the same resource unaffected.

Unary inference rules, such as the  $\oplus_L$  rule, correspond to manipulations of a single process specification. For example, the  $\oplus_L$  rule (see Fig. 2) takes a process P specified by  $\vdash \Gamma, A$ , i.e. a process with some inputs  $\Gamma$  and an output A, and produces a process  $\vdash \Gamma, A \oplus B$ , i.e. a process with the same inputs  $\Gamma$  and

output either A or B. Note that, in practice, the produced composite process is a transformation of P and thus will always produce A and never B.

Binary inference rules, such as the  $\otimes$  rule, correspond to binary process composition. The  $\otimes$  rule in particular (see Fig. 2) takes a process P specified by  $\vdash \Gamma, A$  and another process Q specified by  $\vdash \Delta, B$  and composes them, so that the resulting process  $\vdash \Gamma, \Delta, A \otimes B$  has all their inputs  $\Gamma$  and  $\Delta$  and a simultaneous output  $A \otimes B$ . Notably, the *Cut* rule corresponds to the composition of 2 processes in sequence, where one consumes a resource A given by the other.

Naturally, these manipulations and compositions are primitive and restricted. Constructing meaningful compositions requires several rule applications and, therefore, doing this manually would be a very tedious and impractical task. Our work focuses on creating high level actions that use CLL inference to automatically produce binary process compositions that are correct-by-construction based on the guarantees described above. More specifically, we introduce actions for parallel (TENSOR), conditional (WITH), and sequential composition (JOIN).

Since we are using forward inference, there are infinitely many ways to apply the CLL rules and therefore infinite possible compositions. We are interested in producing compositions that are intuitive for the user. It is practically impossible to produce a formal definition of what these compositions should be. Instead, as explained earlier, we rely on practical experience and user feedback from the various case studies for workflow modelling (see Table 1).

Based on this, we have introduced a set of what can be viewed as *unit tests* for our composition actions, which describe the expected and logically valid results of example compositions. As we explore increasingly complex examples in practice, we augment our test set and ensure our algorithms satisfy them. *Selected* unit tests for the WITH and JOIN actions are shown in Tables 2 and 3 respectively. Moreover, as a general principle, our algorithms try to *maximize* resource usage, i.e. involve as many resources as possible, and *minimize* the number of rule applications to keep the corresponding process code more compact.

For example, row 3 of Table 3 indicates that a process with output  $A \oplus B$ when composed with a process specified by  $\vdash A^{\perp}, B$  should produce a process with output B. As we discuss in Sect. 8.3, a different CLL derivation for the same scenario could lead to a process with output  $B \oplus B$ . This result is unnecessarily more complicated, and its complexity will propagate to all subsequent compositions which will have to deal with 2 options of a type B output. The unit test therefore ensures that the algorithm always leads to a minimal result.

All our algorithms are implemented within the Higher Order Logic proof tactic system of HOL Light. As a result, the names of some methods have the \_TAC suffix, which is conventionally used when naming HOL Light tactics.

### 5 Auxiliary Processes

During composition, we often need to construct auxiliary processes that manipulate the structure of a CLL type in particular ways. We have identified 2 types of such processes: *buffers* and *filters*.

P	Q	Result
$\vdash X^{\perp}, Z$	$\vdash Y^{\perp}, Z$	$\vdash (X \oplus Y)^{\perp}, Z$
$\vdash X^{\perp}, Z$	$\vdash Y^{\perp}, W$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, B^{\perp}, Z \oplus W$
$\vdash X^{\perp}, A^{\perp}, B^{\perp}, Z$	$\vdash Y^{\perp}, Z$	$\vdash (X \oplus Y)^{\perp}, Z \oplus (Z \otimes A \otimes B)$
$\vdash X^{\perp}, A^{\perp}, Z$	$\vdash Y^{\perp}, B^{\perp}, W$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, B^{\perp}, (Z \otimes B) \oplus (W \otimes A)$
$\vdash X^{\perp}, A^{\perp}, C^{\perp}, Z$	$\vdash Y^{\perp}, B^{\perp}, W$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, B^{\perp}, C^{\perp}, (Z \otimes B) \oplus (W \otimes A \otimes C)$
$\vdash X^{\perp}, A^{\perp}, C^{\perp}, Z$	$\vdash Y^{\perp}, B^{\perp}, C^{\perp}, W$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, B^{\perp}, C^{\perp}, (Z \otimes B) \oplus (W \otimes A)$
$\vdash X^{\perp}, A^{\perp}, C^{\perp}, C^{\perp}, Z$	$\vdash Y^{\perp}, B^{\perp}, C^{\perp}, W$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, B^{\perp}, C^{\perp}, C^{\perp}, (Z \otimes B) \oplus (W \otimes A \otimes C)$
$\vdash X^{\perp}, A \otimes B$	$\vdash Y^{\perp}, B \otimes A$	$\vdash (X \oplus Y)^{\perp}, A \otimes B$
$\vdash X^{\perp}, A^{\perp}, Z \otimes A$	$\vdash Y^{\perp}, Z$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, Z \otimes A$
$\vdash X^{\perp}, A^{\perp}, A \otimes Z$	$\vdash Y^{\perp}, Z$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, A \otimes Z$
$\vdash X^{\perp}, A^{\perp}, Z \oplus (Z \otimes A)$	$\vdash Y^{\perp}, Z$	$\vdash (X \oplus Y)^{\perp}, A^{\perp}, Z \oplus (Z \otimes A)$

**Table 2.** Examples of the expected result of the WITH action between  $X^{\perp}$  of a process **P** and  $Y^{\perp}$  of a process **Q**.

Buffers: Similarly to the axiom buffer introduced in the previous section, composite buffers (or simply buffers) can carry any composite resource without affecting it. This is useful when a process is unable to handle the entire type on its own, and some resources need to be simply buffered through. For example, if a process needs to handle a resource of type  $A \otimes B$ , but only has an input of type  $A^{\perp}$ , then B will be handled by a buffer.

More formally, buffers are processes specified by  $\vdash A^{\perp}, A$ , where A is arbitrarily complex. Such lemmas are always provable in CLL for any formula A. We have introduced an automatic procedure BUFFER\_TAC that can accomplish this, but omit the implementation details in the interest of space and in favour of the more interesting composition procedures that follow.

We also introduce the concept of a *parallel buffer*, defined as a process  $\vdash A_1^{\perp}, A_2^{\perp}, ..., A_n^{\perp}, A_1 \otimes A_2 \otimes ... \otimes A_n$ . Such buffers are useful when composing processes with an optional output (see Sect. 8.3). Their construction can also be easily automated with a decision procedure we call PARBUF\_TAC.

Filters: Often during process composition by proof, resources need to match exactly for the proof to proceed. In some cases, composite resources may not match exactly, but may be manipulated using the CLL inference rules so that they end up matching. For example, the term  $A \otimes B$  does not directly match  $B \otimes A$ . However, both terms intuitively represent resources A and B in parallel. This intuition is reflected formally to the commutativity property of  $\otimes$ , which is easily provable in CLL:  $\vdash (A \otimes B)^{\perp}, B \otimes A$ . We can then use the *Cut* rule with this property to convert an output of type  $A \otimes B$  to  $B \otimes A$  (similarly for inputs).

We call such lemmas that are useful for converting CLL types to logically equivalent ones, *filters*. In essence, a filter is any provable CLL lemma that preserves our polarity restrictions. We prove such lemmas automatically using the proof strategies developed by Tammet [24]. We call such lemmas that are useful for converting CLL types to logically equivalent ones, *filters*. In essence, a filter is any provable CLL lemma that preserves our polarity restrictions. We prove such lemmas automatically using the proof strategies developed by Tammet [24].

We give some examples of how filters are used to match terms as we go through them below. However, as a general rule the reader may assume that, for the remainder of this paper, by "equal" or "matching" terms we refer to terms that are equal modulo the use of filters.

A main consequence of this is that our algorithms often attempt to match literals that do not match. For example, the attempt to compose  $\vdash A^{\perp}, B$  in sequence with  $\vdash C^{\perp}, D^{\perp}, E$  would generate and try to prove 2 false conjectures  $\vdash B^{\perp}, C$  and  $\vdash B^{\perp}, D$  in an effort to match the output B with any of the 2 inputs  $C^{\perp}$  and  $D^{\perp}$  before failing<sup>3</sup>. This highlights the need for an efficient proof procedure for filters, with an emphasis on early failure.

**Table 3.** Examples of the expected result of the JOIN action between a process P and a process Q. Column *Pr.* gives the *priority* parameter (see Sect. 8.4).

Р	Pr.	Q	Selected input	Result
$\vdash X^{\perp}, A$		$\vdash A^{\perp}, Y$	$A^{\perp}$	$\vdash X^{\perp}, Y$
$\vdash X^{\perp}, A \otimes B$	L	$\vdash A^{\perp}, Y$	$A^{\perp}$	$\vdash X^{\perp}, Y \otimes B$
$\vdash X^{\perp}, A \oplus B$	L	$\vdash A^{\perp}, B$	$A^{\perp}$	$\vdash X^{\perp}, B$
$\vdash X^{\perp}, A \otimes B \otimes C$	L	$\vdash A^{\perp}, Y$	$A^{\perp}$	$\vdash X^{\perp}, Y \otimes B \otimes C$
$\vdash X^{\perp}, A \oplus B$	L	$\vdash A^{\perp}, C^{\perp}, Y$	$A^{\perp}$	$\vdash X^{\perp}, C^{\perp}, Y \oplus (C \otimes B)$
$\vdash X^{\perp}, A \oplus B$	R	$\vdash B^{\perp}, C^{\perp}, Y$	$B^{\perp}$	$\vdash X^{\perp}, C^{\perp}, (C \otimes A) \oplus Y$
$\vdash X^{\perp}, A \oplus B$	L	$\vdash (B \oplus A)^{\perp}, Y$	$(B\oplus A)^{\perp}$	$\vdash X^{\perp}, Y$
$\vdash X^{\perp}, A \oplus (B \otimes C)$	L	$\vdash (B \oplus A)^{\perp}, Y$	$(B\oplus A)^{\perp}$	$\vdash X^{\perp}, Y \oplus (B \otimes C)$
$\vdash X^{\perp}, A \oplus (B \otimes C)$	RL	$\vdash (B \oplus A)^{\perp}, Y$	$(B\oplus A)^{\perp}$	$\vdash X^{\perp}, A \oplus (Y \otimes C)$
$\vdash X^{\perp}, A \oplus B$	L	$\vdash (C \oplus A \oplus D)^{\perp}, Y$	$(C \oplus A \oplus D)^{\perp}$	$\vdash X^{\perp}, Y \oplus B$
$\vdash X^{\perp}, C \oplus (A \otimes B)$	L	$\vdash C^{\perp}, A \otimes B$	$C^{\perp}$	$\vdash X^{\perp}, A \otimes B$
$\vdash X^{\perp}, C \oplus (A \otimes B)$	L	$\vdash C^{\perp}, B \otimes A$	$C^{\perp}$	$\vdash X^{\perp}, B \otimes A$
$\vdash X^{\perp}, C \oplus (A \otimes (B \oplus D))$	L	$\vdash C^{\perp}, (B \oplus D) \otimes A$	$C^{\perp}$	$\vdash X^{\perp}, (B \oplus D) \otimes A$
$\vdash X^{\perp}, C \oplus (A \otimes B)$	L	$\vdash C^{\perp}, Y \oplus (B \otimes A)$	$C^{\perp}$	$\vdash X^{\perp}, Y \oplus (B \otimes A)$
$\vdash X^{\perp}, C \oplus (A \otimes B)$	L	$\vdash C^{\perp}, (B \otimes A) \oplus Y$	$C^{\perp}$	$\vdash X^{\perp}, (B \otimes A) \oplus Y$
$\vdash X^{\perp}, (A \otimes B) \oplus C$	R	$\vdash C^{\perp}, Y \oplus (B \otimes A)$	$C^{\perp}$	$\vdash X^{\perp}, Y \oplus (B \otimes A)$
$\vdash X^{\perp}, (A \otimes B) \oplus C$	R	$\vdash C^{\perp}, (B \otimes A) \oplus Y$	$C^{\perp}$	$\vdash X^{\perp}, (B \otimes A) \oplus Y$
$\vdash X^{\perp}, C \oplus (A \otimes B)$	L	$\vdash C^{\perp}, Y \oplus (B \otimes A)$	$C^{\perp}$	$\vdash X^{\perp}, Y \oplus (B \otimes A)$

# 6 Parallel Composition - The TENSOR Action

The TENSOR action corresponds to the parallel composition of two processes so that their outputs are provided in parallel. It trivially relies on the tensor ( $\otimes$ )

<sup>&</sup>lt;sup>3</sup> In practice, the user will have to select a matching input to attempt such a composition (see Sect. 8).

inference rule. Assuming 2 processes,  $\vdash A^{\perp}, C^{\perp}, D$  and  $\vdash B^{\perp}, E$ , the TENSOR action will perform the following composition:

$$\frac{\vdash A^{\perp}, C^{\perp}, D \quad \vdash B^{\perp}, E}{\vdash A^{\perp}, B^{\perp}, C^{\perp}, D \otimes E} \otimes$$

# 7 Conditional Composition - The WITH Action

The WITH action corresponds to the *conditional* composition of two processes. This type of composition is useful in cases where each of the components of an optional output of a process needs to be handled by a different receiving process.

For example, assume a process S has an optional output  $A \oplus C$  where C is an exception. We want A to be handled by some process P, for example specified by  $\vdash A^{\perp}, B^{\perp}, X$ , while another process Q specified by  $\vdash C^{\perp}, Y$  plays the role of the exception handler for exception C. For this to happen, we need to compose P and Q together using the WITH action so that we can construct an input that matches the output type  $A \oplus C$  from S. This composition can be viewed as the construction of an *if-then* statement where if A is provided then P will be executed (assuming B is also provided), and if C is provided then Q will be executed in a mutually exclusive choice. The generated proof tree for this particular example is the following:

$$\frac{\overline{\vdash A^{\perp}, B^{\perp}, X} \ \mathsf{P}}{\vdash A^{\perp}, B^{\perp}, X \oplus (Y \otimes B)} \oplus L \quad \frac{\overline{\vdash C^{\perp}, Y} \ \mathbb{Q} \quad \overline{\vdash B^{\perp}, B} \ \mathsf{Id}}{\vdash C^{\perp}, B^{\perp}, Y \otimes B} \otimes \\ \frac{\vdash C^{\perp}, B^{\perp}, X \oplus (Y \otimes B)}{\vdash C^{\perp}, B^{\perp}, X \oplus (Y \otimes B)} \oplus R \qquad (1)$$

The WITH action fundamentally relies on the & rule of CLL. The following derivation allows us to compose 2 processes that also have different outputs X and Y:

$$\frac{\vdash \Gamma, A^{\perp}, X}{\vdash \Gamma, A^{\perp}, X \oplus Y} \oplus L \quad \frac{\vdash \Gamma, C^{\perp}, Y}{\vdash \Gamma, C^{\perp}, X \oplus Y} \oplus R \\ \vdash \Gamma, (A \oplus C)^{\perp}, X \oplus Y \qquad \& \qquad (2)$$

The particularity of the & rule is that the context  $\Gamma$ , i.e. all the inputs except the ones involved in the WITH action, must be the same for both the involved processes. In practice, this means we need to account for unused inputs. In the example above, P apart from input  $A^{\perp}$  has another input  $B^{\perp}$  which is missing from Q. In the conditional composition of P and Q, if exception C occurs, the provided B will not be consumed since P will not be invoked. In this case, we use a buffer to let B pass through together with the output Y of Q.

More generally, in order to apply the & rule to 2 processes P and Q, we need to minimally adjust their contexts  $\Gamma_P$  and  $\Gamma_Q$  (i.e. their respective multisets of inputs excluding the ones that will be used in the rule) so that they end up being the same  $\Gamma = \Gamma_P \cup \Gamma_Q$ . By "minimal" adjustment we mean that we only add the inputs that are "missing" from either side, i.e. the multiset  $\Delta_P = \Gamma_Q \setminus \Gamma_P$ for P and  $\Delta_Q = \Gamma_P \setminus \Gamma_Q$  for Q, and no more.

In the previous example in (1), exculding the inputs  $A^{\perp}$  and  $C^{\perp}$  used in the rule, we obtain  $\Delta_Q = \Gamma_P \setminus \Gamma_Q = \{B^{\perp}\} \setminus \{\} = \{B^{\perp}\}$ . We then construct a parallel buffer (see Sect. 5) of type  $\otimes \Delta_Q^{\perp 4}$  (converting all inputs in  $\Delta_Q$  to an output; in this example only one input) using PARBUF\_TAC. In the example, this is an atomic *B* buffer. The parallel composition between this buffer and Q results in the process  $\vdash \Gamma_Q, \Delta_Q, C^{\perp}, Y \otimes (\otimes \Delta_Q^{\perp})$ . The same calculation for P yields  $\Delta_P = \emptyset$ so no change is required for P.

Since  $\Gamma_P \uplus \Delta_P = \Gamma_Q \uplus \Delta_Q = \Gamma$  (where  $\uplus$  denotes multiset union), the & rule is now applicable and derivation (2) yields the following process:

$$\vdash \Gamma, (A \oplus C)^{\perp}, \left( X \otimes (\otimes \Delta_P^{\perp}) \right) \oplus \left( Y \otimes (\otimes \Delta_Q^{\perp}) \right)$$
(3)

The output Y of Q has now been paired with the buffered resources  $\Delta_Q$ . Finally, we consider the special case where the following holds:

$$\left(X \otimes (\otimes \Delta_P^{\perp})\right) = \left(Y \otimes (\otimes \Delta_Q^{\perp})\right) = G \tag{4}$$

In this case, the output of the composition in (3) will be  $G \oplus G$ . Instead we can apply the & directly without derivation (2), yielding the simpler output G.

Note that, as discussed in Sect. 5, (4) above does not strictly require equality. The special case can also be applied if we can prove and use the filter  $\vdash (X \otimes (\otimes \Delta_P^{\perp}))^{\perp}, (Y \otimes (\otimes \Delta_P^{\perp}))$ .

These results and the complexity underlying their construction demonstrate the non-trivial effort needed to adhere to CLL's systematic management of resources and, more specifically, its systematic accounting of unused resources. These properties, however, are essential guarantees of correct resource management offered by construction in our process compositions.

### 8 Sequential Composition - The JOIN Action

The JOIN action reflects the connection of two processes in sequence, i.e. where (some of) the outputs of a process are connected to (some of) the corresponding inputs of another. More generally, we want to compose a process P with specification  $\vdash \Gamma, X$ , i.e. with some (multiset of) inputs  $\Gamma$  and output X in sequence with a process Q with specification  $\vdash \Delta, C^{\perp}, Y$ , i.e. with an input  $C^{\perp}$ , output Y, and (possibly) more inputs in context  $\Delta$ . We also assume the user selects a subterm A of X in P and a matching subterm A of the input  $C^{\perp}$  in Q.

The strategy of the algorithm behind the JOIN action is to construct a new input for Q based on the chosen  $C^{\perp}$  such that it directly matches the output X of P (and prioritizing the output selection A). This will enable the application of the *Cut* rule, which requires the cut literal to match exactly. In what follows, we present how different cases for X are handled.

$${}^{4} \otimes \{a_{1},...,a_{n}\}^{\perp} = a_{1}^{\perp} \otimes ... \otimes a_{n}^{\perp}.$$

#### 8.1 Atomic or Matching Output

If X is atomic, a straighforward use of the *Cut* rule is sufficient to connect the two processes. For example, the JOIN action between  $\vdash A^{\perp}, B^{\perp}, X$  and  $\vdash X^{\perp}, Z$  results in the following proof:

$$\frac{\overline{\vdash A^{\perp}, B^{\perp}, X} \stackrel{\mathsf{P}}{\vdash X^{\perp}, Z} \stackrel{\mathsf{Q}}{\vdash A^{\perp}, B^{\perp}, Z} \stackrel{\mathsf{Q}}{Cut}$$

The same approach can be applied more generally for any non-atomic X as long as a matching input of type  $X^{\perp}$  (including via filtering) is selected in Q.

#### 8.2 Parallel Output

If X is a parallel output, such as  $B \otimes C$ , we need to manipulate process Q so that it can receive an input of type  $(B \otimes C)^{\perp}$ .

If **Q** has both inputs  $B^{\perp}$  and  $C^{\perp}$ , then we can use the  $\mathfrak{P}$  rule to combine them. For example, the generated proof tree of the JOIN action between  $\vdash A^{\perp}, D^{\perp}, B \otimes C$  and  $\vdash B^{\perp}, C^{\perp}, E^{\perp}, Y$  is the following:

$$\frac{}{\frac{\vdash A^{\perp}, D^{\perp}, B \otimes C}{\vdash A^{\perp}, D^{\perp}, E^{\perp}, Y}} \stackrel{\mathsf{P}}{\xrightarrow{\vdash B^{\perp}, C^{\perp}, E^{\perp}, Y}}{\frac{\vdash B^{\perp}, D^{\perp}, E^{\perp}, Y}{\vdash (B \otimes C)^{\perp}, E^{\perp}, Y}} \stackrel{\mathfrak{P}}{Cut}$$

As previously mentioned, the JOIN action attempts to connect the output of P to Q maximally, i.e. both B and C, regardless of the user choice. The user may, however, want to only connect one of the two resources. We have currently implemented this approach as it is the most commonly used in practice, but are investigating ways to enable better control by the user.

If Q has only one of the two inputs, for example  $B^{\perp}$ , i.e. Q is of the form  $\vdash \Delta, B^{\perp}, Y$  and  $C^{\perp} \notin \Delta$ , then C must be buffered. In this case, we use the following derivation:

$$\frac{\overbrace{\vdash \Delta, B^{\perp}, Y}^{\square \cup \Box, Y} \stackrel{\mathbb{Q}}{\vdash C^{\perp}, C}}{\underset{\vdash \Delta, (B \otimes C)^{\perp}, Y \otimes C}{\vdash \Delta, (B \otimes C)^{\perp}, Y \otimes C}} \stackrel{\otimes}{\gamma} \qquad (5)$$

We use  $BUFFER_TAC$  from Sect. 5 to prove the buffer of C.

Depending on the use of the  $\otimes$  rule in (5), the resulting output could be either  $Y \otimes C$  or  $C \otimes Y$ . We generally try to match the form of P's output, so in this case we would choose  $Y \otimes C$  to match  $B \otimes C$ . Our algorithm keeps track of this orientation through the **orient** parameter (see Sect. 8.4).

#### 8.3 Optional Output

If X is an optional output, such as  $B \oplus C$ , then we need to manipulate process **Q** to synthesize an input  $(B \oplus C)^{\perp}$ . Assume **Q** can handle B (symmetrically for C) and thus has specification  $\vdash \Delta, B^{\perp}, Y$ . We construct a parallel buffer (using PARBUF\_TAC, see Sect. 5) of type  $(\otimes \Delta^{\perp}) \otimes C$  (converting all inputs in  $\Delta$  to outputs). We then apply derivation (2) as follows:

$$\frac{\overset{\mathsf{PARBUF}_{-}\mathsf{TAC}}{\vdots}}{\vdash \Delta, B^{\perp}, Y \oplus ((\otimes \Delta^{\perp}) \otimes C)} \oplus L \xrightarrow{\vdash \Delta, C^{\perp}, (\otimes \Delta^{\perp}) \otimes C}{\vdash \Delta, C^{\perp}, Y \oplus ((\otimes \Delta^{\perp}) \otimes C)} \oplus R \\ \xrightarrow{\vdash \Delta, (B \oplus C)^{\perp}, Y \oplus ((\otimes \Delta^{\perp}) \otimes C)} \& \qquad (6)$$

Similarly to the WITH action, the particular structure of the & rule ensures the systematic management of unused resources. In the example above, if C is received then  $\mathbb{Q}$  will never be executed. As a result, any resources in  $\Delta$  will remain unused and need to be buffered together with C. This is the reason behind the type  $(\otimes \Delta^{\perp}) \otimes C$  of the constructed buffer (as opposed to plainly using type C).

The proof tree of an example of the JOIN action between process P specified by  $\vdash A^{\perp}, D^{\perp}, B \oplus C$  and process Q specified by  $\vdash B^{\perp}, E^{\perp}, Y$  is shown below:

$$\frac{\overbrace{\vdash A^{\perp}, D^{\perp}, B \oplus C}}{\vdash A^{\perp}, D^{\perp}, E^{\perp}, D^{\perp}, E^{\perp}, Y \oplus (C \otimes E)} \stackrel{\mathbb{Q}}{\oplus L} \xrightarrow[\vdash C^{\perp}, C]{Id} \xrightarrow[\vdash E^{\perp}, E]{Id}}_{\vdash E^{\perp}, E} \stackrel{\mathbb{M}}{\otimes} \\ \frac{\overbrace{\vdash A^{\perp}, D^{\perp}, B \oplus C}}{\vdash (B \oplus C)^{\perp}, E^{\perp}, Y \oplus (C \otimes E)} \stackrel{\mathbb{Q}}{\oplus L} \xrightarrow[\vdash C^{\perp}, E^{\perp}, Y \oplus (C \otimes E)]}_{\vdash C^{\perp}, E^{\perp}, Y \oplus (C \otimes E)} \underbrace{\mathbb{Q}}_{\mathbb{K}} \stackrel{\mathbb{Q}}{\otimes} \\ \mathbb{Q}_{\mathbb{K}}$$

It is interesting to consider a couple of special cases. Case 1: If  $\vdash \Delta, C^{\perp}, Y$  is a parallel buffer, (6) can be simplified as follows:

$$\frac{\begin{array}{ccc}
 & \text{PARBUF_TAC} \\
\vdots \\
 & \vdash \Delta, B^{\perp}, Y \\
 & \vdash \Delta, C^{\perp}, Y \\
 & \vdash \Delta, (B \oplus C)^{\perp}, Y \\
\end{array} \&$$
(7)

This may occur, for example, if  $\Delta = \emptyset$  and Y = C. Such cases arise in processes used to recover from an exception. For instance, a recovery process  $\vdash Exception^{\perp}$ , Resource can convert an output Resource  $\oplus Exception$  to simply Resource (which either was there in the first place, or was produced through the recovery process).

Case 2: If  $Y = D \oplus E$  for some D and E such that  $\vdash \Delta, C^{\perp}, D$  (or symmetrically  $\vdash \Delta, C^{\perp}, E$ ) is a parallel buffer, then we can apply the following derivation:

$$\frac{\mathsf{PARBUF}_{-}\mathsf{TAC}}{\stackrel{:}{\vdash} \Delta, B^{\perp}, D \oplus E} \mathbb{Q} \xrightarrow{\vdash} \Delta, C^{\perp}, D \oplus E} \stackrel{\oplus L}{\vdash} \Delta, (B \oplus C)^{\perp}, D \oplus E} \overset{\oplus L}{\&} \tag{8}$$

This may occur, for example, if  $\Delta = \emptyset$  and  $Y = C \oplus E$ . The recovery process above may itself throw an exception:  $\vdash Exception^{\perp}$ ,  $Resource \oplus Failed$ . This will convert output  $Resource \oplus Exception$  to  $Resource \oplus Failed$  (either we had the *Resource* from the beginning, or we recovered and still got a *Resource*, or the recovery process failed) instead of (*Resource*  $\oplus$  *Failed*)  $\oplus$  *Resource*.

Table 4. Examples of how the priority parameter can affect the behaviour of  $INPUT_TAC$ . The selected subterms and the output of Q are highlighted in bold.

Target	Priority	Q	Result of $INPUT_-TAC$
$X = \mathbf{A} \otimes (A \oplus B)$	Left	$\vdash A^{\perp}, \mathbf{Y}$	$\vdash X^{\perp}, \mathbf{Y} \otimes (A \oplus B)$
$X = A \otimes (\mathbf{A} \oplus B)$	Right; Left	$\vdash A^{\perp}, \mathbf{Y}$	$\vdash X^{\perp}, A \otimes (\mathbf{Y} \oplus B)$
$X = \mathbf{A} \oplus (B \otimes C)$	Left	$\vdash (B \oplus A)^{\perp}, \mathbf{Y}$	$\vdash X^{\perp}, \mathbf{Y} \oplus (B \otimes C)$
$X = A \oplus (\mathbf{B} \otimes C)$	Right; Left	$\vdash (B \oplus A)^{\perp}, \mathbf{Y}$	$\vdash X^{\perp}, A \oplus (\mathbf{Y} \otimes C)$

#### 8.4 Putting It All Together

In the general case, the output X of P can be a complex combination of multiple parallel and optional outputs. For that reason, we apply the above proof strategies in a recursive, bottom-up way, prioritizing the user selections. We call the algorithm that produces the appropriate input  $X^{\perp}$  (or equivalent) from Q "INPUT\_TAC" and it has the following arguments (see Algorithm 1):

- sel: optional term corresponding to the user selected input  $C^{\perp}$  of Q.
- **priority**: a list representing the path of the user selected subterm A in the syntax tree of the output X of P. For example, if the user selects B in the output  $(A \otimes B) \oplus C$ , the priority is [Left; Right].
- orient: our latest path (left or right) in the syntax tree of X so that we add the corresponding buffers on the same side (see Sect. 8.2).
- ${\bf inputs}:$  a list of inputs of  ${\tt Q}.$  We remove used inputs from this to avoid reuse.
- **target**: the input term we are trying to construct. This is initially set to X, but may take values that are subterms of X in recursive calls.
- proc: the CLL specification of Q as it evolves.

The priority parameter is useful when more than one subterms of the output either (a) are the same or (b) have the same matching input in Q. Table 4 shows examples of how different priorities change the result of INPUT\_TAC.

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**Algorithm 1.** Derives a new process specification from the given "proc" such that it includes an input of type "target".

```
1: function INPUT_TAC(sel, priority, orient, inputs, target, proc)
     Try to match target with sel (if provided) or one of the inputs
 2.
     if it matches then return proc
3:
     else if target is atomic then
 4:
       if priority \neq None then fail
                                       \triangleright we couldn't match the user selected output
 5:
       else Create a target buffer using (5) depending on orient
 6:
       end if
 7:
     else if target is L \otimes R then
 8.
       if priority = Left then
9:
         proc' = INPUT_TAC(sel, tail(priority), orient, inputs, L, proc)
10:
         proc = INPUT_TAC(None, None, Right, inputs - \{L\}, R, proc')
11:
       else
12.
         proc' = INPUT_TAC(sel, tail(priority), orient, inputs, R, proc)
13:
         proc = INPUT_TAC(None, None, Left, inputs - {R}, L, proc')
14:
       end if
15:
       Use the \mathfrak{P} rule to create the (L \otimes R)^{\perp} input
16:
     else if target is L \oplus R then
17:
       if priority = Left then
18.
         proc = INPUT_TAC(sel, tail(priority), orient, inputs, L, proc)
19 \cdot
         Try derivation (7) orElse Try derivation (8) orElse Use derivation (6)
20:
21:
       else if priority = Right then
         proc = INPUT_TAC(sel, tail(priority), orient, inputs, R, proc)
22.
         Try derivation (7) orElse Try derivation (8) orElse Use derivation (6)
23:
24:
       else
25:
         Try as if priority = Left orElse Try as if priority = Right
         else Create a target buffer using (5) depending on orient
26:
       end if
27 \cdot
     end if
28:
     return proc
29:
30: end function
```

# 9 Conclusion

CLL's inherent properties make it an ideal language to reason about resources. CLL sequents (under polarity restrictions) can be viewed as resource-based specifications of processes. The CLL inference rules then describe the logically legal, but primitive ways to manipulate and compose such processes.

We presented algorithms that allow intuitive composition in parallel, conditionally, and in sequence. We call these composition actions TENSOR, WITH, and JOIN respectively, and they are implemented in HOL Light. We analysed how each action functions in different cases and examples.

As a result of the rigorous usage of CLL inference rules, the constructed compositions have guaranteed resource accounting, so that no resources disappear or are created out of nowhere. The proofs-as-processes paradigm and its recent evolutions allow the extraction of process calculus terms from these proofs, for concurrent and guaranteed deadlock-free execution.

In the future, we intend to work towards relaxing identified limitations along 2 main lines: (a) functionality, by incorporating and dealing with increasingly more complex specifications including those requiring formulation of more complex filters, and (b) expressiveness, by extending the fragment of CLL we are using while keeping a balance in terms of efficiency.

Through this work, it is made obvious that intuitive process compositions in CLL require complex applications of a large number of inference rules. Our algorithms automate the appropriate deductions and alleviate this burden from the user. We have tied these with the diagrammatic interface of WorkflowFM [21], so that the user is not required to know or understand CLL or theorem proving, but merely sees inputs and outputs represented graphically. They can then obtain intuitive process compositions with the aforementioned correctness guarantees with a few simple clicks.

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# Confluence of CHR Revisited: Invariants and Modulo Equivalence

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Abstract. Abstract simulation of one transition system by another is introduced as a means to simulate a potentially infinite class of similar transition sequences within a single transition sequence. This is useful for proving confluence under invariants of a given system, as it may reduce the number of proof cases to consider from infinity to a finite number. The classical confluence results for Constraint HThe invariant is formalized and ling Rules (CHR) can be explained in this way, using CHR as a simulation of itself. Using an abstract simulation based on a ground representation, we extend these results to include confluence under invariant and modulo equivalence, which have not been done in a satisfactory way before.

Keywords: Constraint Handling Rules  $\cdot$  Confluence  $\cdot$  Confluence modulo equivalence  $\cdot$  Invariants  $\cdot$  Observable confluence

# 1 Introduction

Confluence of a transition system means that any two alternative transition sequences from a given state can be extended to reach a common state. Proving confluence of nondeterministic systems may be important for correctness proofs and it anticipates parallel implementations and application order optimizations. Confluence modulo equivalence generalizes this so that these "common states" need not be identical, but only equivalent according to an equivalence relation. This allows for redundant data representations (e.g., sets as lists) and procedures that search for an optimal solution to a problem, when any of two equally good solutions can be accepted (e.g., the Viterbi algorithm analyzed for confluence modulo equivalence in [8]).

We introduce a notion of abstract simulation of one system, the object system, by another, the meta level system, and show how proofs of confluence (under invariant, modulo equivalence) for an object system may be expressed within a meta level system. This may reduce the number of proof cases to be considered,

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often from infinity to a finite number. We apply this to the programming language of Constraint Handling Rules, CHR [14–16], giving a clearer exposition of existing results and extending them for invariants and modulo equivalence.

By nature, invariants and state equivalences are meta level properties that in general cannot be expressed in its own system: the state itself is implicit and properties such as groundness (or certain arguments restricted to be uninstantiated variables) cannot be expressed in a logic-based semantics for CHR. Using abstract simulation we can add the necessary enhanced expressibility to the meta level, and the ground representation of logic programs, that was studied in-depth in the late 1980s and -90s in the context of meta-programming in logic (e.g., [5,18,19]), comes in readily as a well-suited and natural choice for this. The following minimalist example motivates both invariant and state equivalence for CHR.

*Example 1* ([7,8]). The following CHR program, consisting of a single rule, collects a number of separate items into a set represented as a list of items.

```
set(L), item(A) <=> set([A|L]).
```

This rule will apply repeatedly, replacing constraints matched by the left hand side by the one indicated to the right. The query

```
?- item(a), item(b), set([]).
```

may lead to two different final states, {set([a,b])} and {set([b,a])}, both representing the same set. Thus, the program is not confluent, but it may be confluent modulo an equivalence that disregards the order of the list-elements. Confluence modulo equivalence still requires an invariant that excludes more than one set/1 constraint, as otherwise, an element may go to an arbitrary of those.

#### 1.1 Related Work

Some applications of our abstract simulations may be seen as special cases of abstract interpretation [10]. This goes for the re-formulation of the classical confluence results for CHR, but when invariants are introduced, this is not obvious; a detailed argument is given in Sect. 5. It is related to symbolic execution and constraint logic programming [22], where reasoning takes place on compact abstract representations parameterized in suitable ways, rather than checking multitudes of concrete instances. Bisimulation [26], which has been applied in many contexts, indicates a tighter relationship between states and transitions of two systems than the abstract simulation: when a state  $s_0$  is simulated by an abstract state  $s'_0$  and there is a transition  $s'_0 \rightarrow s_1$ , bisimulation would require the existence of an abstract transition  $s'_0 \rightarrow s'_1$ , which may not be case as demonstrated by Example 6.

Previous results on confluence of CHR programs, e.g., [1-3], mainly refer to a logic-based semantics, which is well-suited for showing program properties, but it does not comply with typical implementations [20, 28] and applies only for a small subset of CHR programs. Other works [7,8] suggest an alternative operational semantics that lifts these limitations, including the ability to handle Prolog-style built-in predicates such as var/1, etc. To compare with earlier work and for simplicity, the present paper refers to the logic-based semantics.

As long as invariants and modulo equivalence are not considered, the logicbased semantics allows for elegant confluence proofs based on Newman's Lemma (Lemma 1, below). A finite set of critical pairs can be defined, whose joinability ensures confluence for terminating programs. Duck et al. [13] proposed a generalization of this approach to confluence under invariant, called observable confluence; no practically relevant methods were suggested, and (as the authors point out) even a simple invariant such as groundness explodes into infinitely many cases.

Confluence modulo equivalence was introduced and motivated for CHR by [7], also arguing that invariants are important for specifying meaningful equivalences. An in-depth theoretical analysis, including the use of a ground representation, is given by [8] in relation the alternative semantics mentioned above. However, it has not been related to abstract simulations, and the proposal for a detailed language of meta level constraints in the present paper is new. Repeating the motivations of [7,8] in the context of the logic-based semantics, [17] suggested to handle confluence modulo equivalence along the lines of [13], thus inheriting the problems of infinitely many proof case pointed out above.

An approach to show confluence of a transition system, by producing a mapping into another confluent system, is described by [11] and extended to confluence modulo equivalence by [23]; the relationship between such two systems is different from the abstract simulations introduced in the present paper. Confluence, including modulo equivalence, has been studied since the first half of the 20th century in a variety of contexts; see, e.g., [8,21] for overview.

#### 1.2 Contributions

We introduce abstract simulation as a setting for proofs of confluence for general transitions systems and demonstrate this specifically for CHR. We recast classical results (without invariant and equivalence), showing that they are essentially based on a simulation of CHR's logic-based semantics by itself, and we can pinpoint, why it does not generalize for invariants (see Example 4, p. 9).

These results are extended for invariants and modulo equivalence, using an abstract simulation; it is based on a ground meta level representation and suitable meta level constraints to reason about it.

#### 1.3 Overview

Sections 2 and 3 introduce basic concepts of confluence plus our notion of abstract simulation. Section 4 gives syntax and semantics of CHR along with a discussion of how much nondeterminism to include in a semantics used when considering confluence. Section 5 re-explains the classical results in terms of abstract simulation. Section 6 extends these results for invariants and modulo equivalence;

proofs can be found in an extended report [6]. The concluding Sect. 7 gives a summary and explains briefly how standard mechanisms, used to prevent loops by CHR's propagation rules, can be added.

# 2 Basic Concepts, Confluence, Invariants and Equivalences

A transition system  $D = \langle S, \mapsto \rangle$  consists of a set of states S, and a transition is an element of  $\mapsto : S \times S$ , written  $s_0 \mapsto s_1$  or, alternatively,  $s_1 \leftrightarrow s_0$ . A transition sequence or path is a chain of transitions  $s_0 \mapsto s_1 \mapsto \cdots \mapsto s_n$  where  $n \ge 0$ ; if such a path exists, we write  $s_0 \stackrel{*}{\mapsto} s_n$ . A state  $s_0$  is final (or normal form) whenever  $\nexists s_1 \ s_0 \mapsto s_1$ , and D is terminating whenever every path is finite. To anticipate the application for logic programming systems, a given transition system may have a special final state called *failure*.

An invariant I for  $D = \langle S, \mapsto \rangle$  is a subset  $I \subseteq S$  such that

$$s_0 \in I \land s_0 \mapsto s_1 \quad \Rightarrow \quad s_1 \in I.$$

We write a fact  $s \in I$  as I(s) and refer to s as an I state. The restriction of D to I is the transition system  $\langle I, \stackrel{I}{\mapsto} \rangle$  where  $\stackrel{I}{\mapsto}$  is the restriction of  $\mapsto$  to I. A set of allowed initial states  $S' \subseteq S$  defines an invariant of those states reachable from some  $s \in S'$ , i.e., reachable $(S') = \{s' \mid s \in S' \land s \stackrel{*}{\mapsto} s'\}$ . A (state) equivalence is an equivalence relation over S, typically denoted  $\approx$ . In the context of an invariant I, the relations  $\approx$  and  $\mapsto$  are understood to be restricted to I.

The following  $\alpha$  and  $\beta$  corners<sup>1</sup> were introduced in [7,8], being implicit in [21]. An  $\alpha$  corner is a structure  $s_1 \leftrightarrow s_0 \mapsto s_2$ , where  $s_0, s_1, s_2 \in S$  and the indicated relationships hold;  $s_0$  is called a common ancestor and  $s_1, s_2$  wing states. A  $\beta$  corner is a structure  $s_1 \approx s_0 \mapsto s_2$ , where  $s_0, s_1, s_2 \in S$  and the indicated relationships hold. In the context of an invariant I, the different types of corners are defined only for I states.

Two states  $s_1, s_2$  are *joinable (modulo*  $\approx$ ) whenever there exist paths  $s_1 \stackrel{*}{\mapsto} s'_1$ and  $s_2 \stackrel{*}{\mapsto} s'_2$  with  $s'_1 = s'_2$  ( $s'_1 \approx s'_2$ ). A corner  $s_1 \operatorname{Rel} s_0 \mapsto s_2$  is *joinable (modulo*  $\approx$ ) when  $s_1, s_2$  are joinable (modulo  $\approx$ );  $\operatorname{Rel} \in \{ \leftarrow, \approx \}$ .

A transition system  $D = \langle S, \mapsto \rangle$  is confluent (modulo  $\approx$ ) whenever

 $s_1 \stackrel{*}{\leftarrow} s_0 \stackrel{*}{\mapsto} s_2 \Rightarrow s_1 \text{ and } s_2 \text{ are joinable (modulo $\approx$)}.$ 

It is *locally confluent* (modulo equivalence  $\approx$ ) whenever all its  $\alpha$  ( $\alpha$  and  $\beta$ ) corners are joinable. The following properties are fundamental.

**Lemma 1 (Newman** [25]). A terminating transition system (under invariant I) is confluent if and only if it is locally confluent.

 $<sup>^1</sup>$  In recent literature within term rewriting, the terms peaks and cliffs have been used for  $\alpha$  and  $\beta$  corners, respectively.

**Lemma 2** (Huet [21]). A terminating transition system (under invariant I) is confluent modulo  $\approx$  if and only if it is locally confluent modulo  $\approx$ .

These properties reduce proofs of confluence (mod. equiv.) for terminating systems to proofs of the simpler property of local confluence (mod. equiv.), but still, this may leave an infinite number of corners to be examined.

#### 3 Abstract Simulation

Consider two transition systems,  $D_O = \langle S_O, \mapsto_O \rangle$  and  $D^M = \langle S^M, \mapsto^M \rangle$ , referred to as *object* and *meta level* systems. A *replacement* is a (perhaps partial) function  $\rho: S^M \to S_O$ ; the application of  $\rho$  to some  $s \in S^M$  is written  $s\rho$ . For any structure  $f(s_1, \ldots s_n)$  with states  $s_1, \ldots s_n$  of  $D^M$  (a transition, a tuple, etc.), replacements apply in a compositional way,  $f(s_1, \ldots s_n)\rho = f(s_1\rho, \ldots s_n\rho)$ . For a family of replacements  $P = \{\rho_i\}_{i \in Inx}$ , the *covering* (or *concretization*) of a structure  $f(s_1, \ldots s_n)$  is defined as

$$\llbracket f(s_1, \dots s_n) \rrbracket_O^M = \{ f(s_1, \dots s_n) \rho \mid \rho \in P \}.$$

Notice that P is left implicit in this notation, as in the context of given object and meta level systems, there will be one and only one replacement family.

**Definition 1.** An abstract simulation of  $D_O$  by  $D^M$  with possible invariants  $I_O$ , resp.,  $I^M$ , and equivalences  $\approx_O$ , resp.,  $\approx^M$ , is defined by a family of replacements  $P = \{\rho_i\}_{i \in Inx}$  which satisfies the following conditions.

$$\begin{split} s_0 &\mapsto^M s_1 &\Rightarrow &\forall \rho \in P \colon s_0 \rho \mapsto_O s_1 \rho \,\lor \, s_0 \rho = s_1 \rho \\ I^M(s) &\Rightarrow &\forall \rho \in P \colon I_O(s\rho) \\ s_0 &\approx^M s_1 &\Rightarrow &\forall \rho \in P \colon s_0 \rho \approx_O s_1 \rho \end{split}$$

Notice that an abstract simulation does not necessarily cover all object level states, transitions, etc.

*Example 2.* Let  $A = \{a_1, a_2, \ldots\}$ ,  $B = \{b_1, b_2, \ldots\}$  and  $C = \{c_1, c_2, \ldots\}$  be sets of states, and O and M the following transition systems.

$$O = \langle A \cup B \cup C, \{a_i \mapsto_O b_i \mid i = 1, 2, \ldots\} \cup \{a_i \mapsto_O c_i \mid i = 1, 2, \ldots\} \rangle$$
  
$$M = \langle \{a, b, c\}, \{a \mapsto_M b, a \mapsto_M c\} \rangle$$

Assume equivalences  $b \approx^M c$  and  $b_i \approx_O c_i$ , for all *i*. Then the family of replacements  $P = \{\rho_i\}_{i=1,2,...}$ , where  $a\rho_i = a_i$ ,  $b\rho_i = b_i$  and  $c\rho_i = c_i$ , defines a simulation of O by M. It appears that O and M are not confluent, cf. the non-joinable corners  $b_1 \leftarrow_O a_1 \mapsto_O c_1$  and  $b \leftarrow_M a \mapsto_M c$ , but both are confluent modulo  $\approx_O (\approx^M)$ . A meta level structure m covers an object structure k whenever  $k \in [\![m]\!]_O^M$ . When  $[\![m]\!]_O^M = \emptyset$ , m is inconsistent. When  $[\![m']\!]_O^M \subseteq [\![m]\!]_O^M$ , m' is a substate/subcorner, etc. of m, depending on the inherent type of m. When  $D_O$  and  $D^M$  both include failure, it is required that  $[\![failure]\!]_O^M = \{failure\}$ . A given meta level state S is mixed whenever  $[\![S]\!]_O^M$  includes both failure and non-failure states. Transitions are only allowed from consistent and neither failed nor mixed states.

The following is a consequence of the definitions.

**Lemma 3.** An object level corner, which is covered by a joinable (mod. equiv.) meta level corner, is joinable (mod. equiv.).

When doing confluence proofs, we may search for a small set of *critical* meta level corners,<sup>2</sup> whose joinability guarantees joinability of any object level corner, i.e., any other object level corner not covered by one of these is seen to be joinable in other ways. For term rewriting systems, e.g., [4], and previous work on CHR, such critical sets have been defined by explicit constructions.

We introduce a mechanism for splitting a meta level corner  $\Lambda$  into a set of corners, which together covers the same set of object corners as  $\Lambda$ . This is useful when  $\Lambda$  in itself is not joinable, but each of the new corners are. In some cases, splitting is necessary for proving confluence under an invariant as shown in Sect. 5 and exemplified in Examples 4 and 6.

**Definition 2.** Let s be a meta level state (or corner). A set of states (or corners)  $\{s_i\}_{i\in Inx}$  is a splitting of s whenever  $\bigcup_{i\in Inx} [\![s_i]\!]_O^M = [\![s]\!]_O^M$ . A corner (set of corners) is split joinable (mod. equiv.) if it (each of its corners) is joinable (mod. equiv.), inconsistent, or has a splitting into a set of split joinable (mod. equiv.) corners.

**Corollary 1.** An object level corner, which is covered by a split joinable (mod. equiv.) meta level corner, is joinable (mod. equiv.).

# 4 Constraint Handling Rules

Most actual implementations of CHR are fully deterministic, i.e., for a given query, there is at most one answer state (alternatively, the program is nonterminating). In this light, it may be discussed whether confluence is an interesting property, and if so, to what extent the applied semantics should be nondeterministic. Our thesis is the following: choice of next constraints to be tried and which rule to be used should be nondeterministic. Thus a confluent program can be understood by the programmer without considering the detailed control mechanisms in the used implementation; this also anticipates parallel implementations. We see only little interest in considering confluence for the

 $<sup>^2</sup>$  In the literature, the term *critical pair* is used for the pair of wing states of our critical corners.

so-called refined CHR semantics [12] in which only very little nondeterminism is retained.

Similarly to [7,8], we remove w.l.o.g. two redundancies from the logic-based semantics [1,16]: global variables and the two-component constraint store.

- Global variables are those in the original query. Traditionally they are kept as a separate state-component, such that values bound to them can be reported to the user at the end. The same effect can be obtained by a constraint global/2 that does not appear in any rule, but may be used in the original query: writing ?- p(X) as ?- p(X), global('X',X), means that the value of the variable named 'X' can be read out as the second argument of this constraint in a final state.
- We avoid separating the constraint store into query and active parts, as the transition sequences with or without this separation are essentially the same.

#### 4.1 Syntax

Standard first-order notions of variables, terms, predicates atoms, etc. are assumed. Two disjoint sets of *constraint predicates* are assumed, *user constraints* and *built-in constraints*; the actual set of built-ins may vary depending on the application. We use the generalized simpagation form [16] to capture all rules of CHR. A *rule* is a structure of the form

$$H_1 \setminus H_2 \iff G \mid C$$

where  $H_1 \setminus H_2$  is the *head* of the rule,  $H_1$  and  $H_2$  being sequences, not both empty, of user constraints; G is the *guard* which is a conjunction of built-in constraints; and C is the *body* which is a sequence of constraints of either sort. When  $H_2$  is empty, the rule is a *simplification*, which may be written  $H_1 \iff G \mid C$ ; when  $H_2$  is empty, it is a *propagation*, which may be written  $H_2 \implies G \mid C$ ; any other rule is a *simplagation*; when G = true,  $(G \mid)$  may be left out. The *head variables* of a rule are those appearing in the head, any other variable is *local*. The following notion is convenient when defining the CHR semantics and its meta level simulation.

**Definition 3.** A pre-application of a rule  $r = (H_1 \setminus H_2 \triangleleft G \mid C)$  is of the form  $(H'_1 \setminus H'_2 \triangleleft G' \mid C')\sigma$  where  $r' = (H'_1 \setminus H'_2 \triangleleft G' \mid C')$  is a variant of r with fresh variables and  $\sigma$  is a substitution to the head variables of r', where, for no variable  $x, x\sigma$  contains a local variable of r'.

The operator  $\uplus$  refers to union of multisets, so that, e.g.,  $\{a, a\} \uplus \{a\} = \{a, a, a\}$ ; for difference of multisets, we use standard notation for set difference, assuming it takes into account the number of copies, e.g.,  $\{a, a\} \setminus \{a\} = \{a\}$ .

#### 4.2 The Logic-Based Operational Semantics for CHR

The semantics presented here is essentially identical to the one used by [1] and the so-called abstract operational semantics  $\omega_t$  of [16], taking into account the simplifications explained above. Following [27], we define a state as an equivalence class, abstracting away the specific variables used and the different ways the same logical meaning can be expressed by different conjunctions of built-ins.<sup>3</sup> A logical theory  $\mathcal{B}$  is assumed for the built-in predicates.

A state representation (s.repr.) is a pair  $\langle S, B \rangle$ , where the constraint store S is a multiset of constraint atoms and the *built-in store* B is a conjunction of built-ins; any s.repr. with an unsatisfiable built-in store is considered identical to failure. Two s.repr.s  $\langle S, B \rangle$  and  $\langle S', B' \rangle$  are variants whenever, either<sup>4</sup>

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- they are both failure, or
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- there is a renaming substitution  $\rho$  such that  $\mathcal{B} \models \forall (B\rho \rightarrow \exists (S\rho = S' \land B')) \land \mathcal{B} \models \forall (B' \rightarrow \exists (S\rho = S' \land B\rho))$ 

A *state* is an equivalence class of s.repr.s under the variant relationship. For simplicity of notation, we typically indicate a state by one of its s.repr.s.

A rule application w.r.t. to a non-failure state  $\langle S, B \rangle$  is a pre-application  $H_1 \backslash H_2 \iff G | C$  for which  $\mathcal{B} \models B \rightarrow \exists_L G$ , where L is the list of its local variables. There are two sorts of transitions, by rule application and by built-in.

# 5 Confluence Under the Logic-Based Semantics Re-Explained, and Why Invariants Are Difficult

Here we explain the results of [1,2], also summarized in [16], using abstract simulation. Object and meta level systems coincide and are given by a CHR program under the logic-based semantics. Two rules give rise to a critical corner if a state can be constructed in which one rule consumes constraints that the other one needs to be applied; in that case, rule applications do not commute and a specific proof of joinability must be considered. We anticipate the re-use of the construction, when invariants are introduced: in a *pre*-corner, the guards are not necessarily satisfied (but may be so in the context of an invariant).

<sup>&</sup>lt;sup>3</sup> Raiser et al. [27] defined "state" similarly to what we call state representation, and they defined an operational semantics over equivalence classes of such states. We have taken the natural step of promoting such equivalence classes to be our states.

<sup>&</sup>lt;sup>4</sup> An equation between multisets should be understood as an equation between suitable permutations of their elements.

**Definition 4.** Consider two rules  $r: H_1 \setminus H_2 \iff G \mid C$  and  $r': H'_1 \setminus H'_2 \iff G' \mid C'$ renamed apart, and let A and A' be non-empty sets of constraints such that  $A \subseteq H_2, A' \subseteq H'_1 \uplus H'_2$  and  $\mathcal{B} \models \exists (A=A')$ . In that case, let

$$\begin{split} \bar{H} &= (H_1 \uplus H_2 \uplus H'_1 \uplus H'_2) \setminus A \\ s_0 &= \langle \bar{H}, (G \land G' \land A = A') \rangle \\ s &= \langle \bar{H} \backslash H_2 \uplus C, (G \land G' \land A = A') \rangle \\ s' &= \langle \bar{H} \backslash H'_2 \uplus C', (G \land G' \land A = A') \rangle \end{split}$$

When  $s \neq s'$ ,  $s_0$  is a critical, common ancestor state, and  $s \leftarrow_{logic} s_0 \mapsto_{logic} s'$ is a critical  $\alpha$  pre-corner; the constraints A (or A') is called the overlap of r and r'. When, furthermore,  $\mathcal{B} \models \exists (G \land G' \land A = A')$ , it is a critical  $\alpha$  corner.

The simulation is given by the following cover function.

$$[\![\langle S, B \rangle]\!]_{logic}^{logic} = \{\langle S \uplus S^+, B \land B^+ \rangle$$

 $S^+$  is a multiset of user and built-in constraints,

 $B^+$  is a conjunction of built-ins }

$$\begin{split} \llbracket \langle S, B \rangle \mapsto_{logic} \langle S', B' \rangle \rrbracket_{logic}^{logic} &= \{ (\langle S \uplus S^+, B \land B^+ \rangle \mapsto_{logic} \langle S' \uplus S^+, B' \land B^+ \rangle) \mid \\ S^+ \text{ is a multiset of user and built-in constraints,} \\ B^+ \text{ is a conjunction of built-ins, } \exists (B \land B^+) \text{ holds} \} \end{split}$$

It is easy to check that this definition satisfies the conditions for being an abstract simulation given in Sect. 3, relying on *monotonicity*:  $\mathcal{B} \models B \land B^+ \to \exists_L G$ .

It can be shown that any corner not covered by a critical corner (Definition 4) is trivially joinable, see the extended report [6]. Thus, according to Lemmas 1 and 3, the program under investigation is confluent whenever it is terminating and this set of critical corners is joinable. The set of critical corners is finite and that allows for automatic confluence proofs by checking the critical corners, one by one, e.g., [24].

*Example 3.* Consider the one-rule **set**-program of Example 1, ignoring invariant and state equivalence. There are two critical corners, given by the two ways, the rule can overlap with itself:

$$\begin{array}{ccc} \langle \{\texttt{set}([\texttt{X1}|\texttt{L}]),\texttt{item}(\texttt{X2})\},\textit{true} \rangle & \langle \{\texttt{set}([\texttt{X}|\texttt{L1}]),\texttt{set}(\texttt{L2})\},\textit{true} \rangle \\ & & \uparrow_{logic} & & \uparrow_{logic} \\ \langle \{\texttt{item}(\texttt{X1}),\texttt{set}(\texttt{L}),\texttt{item}(\texttt{X2})\},\textit{true} \rangle & & \langle \{\texttt{set}(\texttt{L1}),\texttt{item}(\texttt{X}),\texttt{set}(\texttt{L2})\},\textit{true} \rangle \\ & & & \downarrow_{logic} & & \downarrow_{logic} \\ \langle \{\texttt{item}(\texttt{X1}),\texttt{set}([\texttt{X2}|\texttt{L}])\},\textit{true} \rangle & & \langle \{\texttt{set}(\texttt{L1}),\texttt{set}([\texttt{X}|\texttt{L2}])\},\textit{true} \rangle \end{array}$$

None of these corners are joinable, so the program is not confluent.

The simulation defined above, relying on monotonicity, do not generalize well for confluence under invariant, referred to as "observable confluence" in [13].

Example 4. Consider the CHR program consisting of the following four rules.

$$\begin{array}{ll} r_1: & p(X) <=> q(X) & r_3: & q(X) <=> X>0 \mid r(X) \\ r_2: & p(X) <=> r(X) & r_4: & r(X) <=> X \leq 0 \mid q(X) \end{array}$$

It is not confluent as its single critical corner  $q(X) \leftrightarrow p(X) \mapsto r(X)$  is not joinable (the built-in stores are *true* and thus omitted). However, adding the invariant "reachable from an initial state p(n) where n is an integer" makes it confluent. We indicate the set of all non-trivial object level corners as follows, with the dashed transitions proving each of them joinable.

These object corners and their proofs of joinability obviously fall in two groups of similar shapes, but there is no way to construct a finite set (of, say, one or two elements) that covers all object corners. In other words, the smallest set of meta level corners that covers this set is the set itself. This was also noticed in [13] that used a construction that essentially reduces to the abstract simulation shown above.

The abstract simulation given by  $[-]_{logic}^{logic}$  of Definition 4 above defines an abstract interpretation, whose abstract domain is the complete lattice of CHR states ordered by the substate relationship (Sect. 3). Referring to Example 4, for instance the join of the infinite set of states  $\{\langle \mathbf{p}(t), b \rangle \mid t \text{ is a term, } b \text{ is a conjunction of built-ins } \}$  is  $\langle \mathbf{p}(\mathbf{X}), true \rangle$ . When the grounding invariant is introduced, the join operator is not complete; an attempt to join, say,  $\langle \mathbf{p}(\mathbf{0}), true \rangle$  and  $\langle \mathbf{p}(\mathbf{1}), true \rangle$  would not satisfy the invariant.<sup>5</sup>

#### 6 Invariants and Modulo Equivalence

A program is typically developed with an intended set of queries in mind, giving rise to a state invariant, which may make an otherwise non-confluent program observably confluent (mod. equiv.). We can indicate a few general patterns of invariants and their possible effect on confluence.

- Elimination of non-joinable critical corners that do not cover any object corner satisfying the invariant. This was shown in Example 4 above, and is also demonstrated in the continuation of Example 3 (Example 7, below): "only one set constraint allowed".

<sup>&</sup>lt;sup>5</sup> Such an attempt might be  $(p(X), (X=0 \lor X=1))$ ; notice that X is a variable, thus breaking the invariant.

- Making it possible to apply a given rule, which otherwise could not apply, e.g., providing a "missing" head constraint or enforcing guard satisfaction:
  - 1. "if a state contains p(something), it also contains q(the-samesomething)",
  - 2. "if a state contains p(something), this something is a constant > 1".

An invariant of type 1 ensures confluence mod. equiv. of a version of the Viterbi algorithm [8]; an invariant of type 2 is indicated in Example 4 and formalized in Example 6, below.

As shown in Example 4 above, invariants block for a direct re-use CHR's logical semantics as its own meta-level and, accordingly, existing methods and confluence checkers. In some cases, it is possible to eliminate invariants by program transformations, so that rules apply exactly when the invariant and the original rule guards are satisfied; this means that the transformed program is confluent if and only if the original one is confluent under the invariant.

*Example 5.* Reconsidering the program of Example 4, the following is an example of such a transformed program; the constants **a** and **b** are introduced as representations of positive, resp., non-positive integers.

Such program transformations become more complex when the guards describe more involved dependencies between the head variables. More importantly, invariants that exclude certain constraints in a state cannot be expressed in this way, for example "only one **set** constraint allowed" (Examples 3 and 7). Thus we refrain from pursuing a transformational approach. To obtain a maximum degree of generality, we introduce a meta level formalization of CHR's operational semantics that include representations as explicit data objects of states and their components, possibly parameterized by constrained meta variables.

#### 6.1 The Choice of a Ground Representation

Invariants and state equivalences are inherently meta level statements, as they are *about* states, and may refer to notions inexpressible at the object level, e.g., that some part being ground or a variable. Earlier work on meta-interpreters for logic programs, e.g., [5,18,19], offers the desired expressibility in terms of a *ground representation*. Any object term, formula, etc. is named by a ground meta level term. Variables are named by special constants, say X by 'X', and any other symbol by a function symbol written the same way; e.g., the non-ground object level atom  $\mathbf{p}(\mathbf{A})$  is named by the ground meta level term  $\mathbf{p}('\mathbf{A}')$ . For any such ground meta level term mt, we indicate the object it names as  $[mt]^{Gr}$ . For example,  $[\mathbf{p}('\mathbf{A}')]^{Gr} = \mathbf{p}(\mathbf{A})$  and  $[\mathbf{p}('\mathbf{A}') \wedge '\mathbf{A}' > 2]^{Gr} = (\mathbf{p}(\mathbf{A}) \wedge \mathbf{A} > 2)$ .

For a given object entity e, we define its *lifting* to the meta level by (1) selecting a meta level term that names e, and (2) replacing variable names in it consistently by fresh meta level variables. For example,  $p(X) \land X>2$  is lifted to

 $p(x) \land x>2$ , where X and x are object, resp., meta variables. By virtue of this overloaded syntax, we may read such an entity e (implicitly) as its lifting.

A collection of meta level constraints is assumed whose meanings are given by a theory  $\mathcal{M}$ . We start defining meta level states without detailed assumptions about  $\mathcal{M}$ , that are postponed to Definition 6 below. We assume object level built-in theory  $\mathcal{B}$ , invariant  $I_{logic}$  and state equivalence  $\approx_{logic}$ .

**Definition 5.** A constrained meta level term is a structure of the form (mt WHERE M), where mt is a meta level term and M a conjunction of  $\mathcal{M}$  constraints. We define

 $[M] = \{ \sigma \mid \mathcal{M} \models M\sigma \}, \\ [[mt WHERE M]]_{logic}^{meta} = \{ [[mt \sigma]]^{Gr} \mid \sigma \in [M] \}.$ 

A meta level state representation (s.repr.) is a constrained meta level term st WHERE M for which [st WHERE M]  $l_{logic}^{meta}$  is a set of object level states. Two meta level s.repr.s  $SR_1$ ,  $SR_2$  are variants whenever each object level s.repr. in  $[SR_1]_{logic}^{meta}$  is a variant of some object level s.repr. in  $[SR_2]_{logic}^{meta}$  and vice versa. A meta level state is an equivalence class of meta level s.repr.s under the variant relationship. For structures of meta level states (transitions, corners, etc.), we apply the following convention, where f may represent any such structure.

 $[[f(mt_1 \text{ WHERE } M_1, \dots, mt_n \text{ WHERE } M_n)]]_{logic}^{meta}$   $= [[f(mt_1, \dots, mt_n) \text{ WHERE } M_1 \wedge \dots \wedge M_n]]_{logic}^{meta}$ 

Meta level invariant  $I_{logic}^{meta}$  and equivalence  $\approx_{logic}^{meta}$  are defined as follows.

- $I_{logic}^{meta}(S)$  whenever  $I_{logic}(s)$  for all  $s \in [S]_{logic}^{meta}$ .
- $-S_1 \approx_{logic}^{meta} S_2$  whenever  $s_1 \approx_{logic} s_2$  for all  $(s_1, s_2) \in [(S_1, S_2)]_{logic}^{meta}$ .

As before, we may indicate a meta level state by a representation of it.

**Definition 6.** The theory  $\mathcal{M}$  includes at least the following constraints.

- = /2 with its usual meaning of syntactic identity,
- Type constraints type/2. For example type(var, x) is true in  $\mathcal{M}$  whenever x is the name of an object level variable; var is an example of a type, and we introduce more types below when we need them.
- Modal constraints  $\Box F$  and  $\Box F$  defined to be true in  $\mathcal{M}$  whenever  $\mathcal{B} \models \llbracket F \rrbracket^{Gr}$ , resp.,  $\mathcal{B} \models \llbracket \neg F \rrbracket^{Gr}$ .
- We define two constraints inv and equiv such that  $inv(\Sigma)$  is true in  $\mathcal{M}$ whenever  $\llbracket \Sigma \rrbracket^{Gr}$  is an  $I_{logic}$  state (representation) of the logical semantics, and equiv $(\Sigma_1, \Sigma_2)$  whenever  $\llbracket (\Sigma_1, \Sigma_2) \rrbracket^{Gr}$  is a pair of states (representations)  $(s_1, s_2)$  of the logical semantics such that  $s_1 \approx_{logic} s_2$ .
- freshVars(L,T) is true in M whenever L is a list of all different variables names, none of which occur in the term T; freshVars(L<sub>1</sub>,L<sub>2</sub>,T) abbreviates freshVars(L<sub>12</sub>,T)) where L<sub>12</sub> is the concat. of L<sub>1</sub> and L<sub>2</sub>.

Definitions 5 and 6 comprise the first steps towards a simulation of the logicbased semantics, and we continue with the last part, the transition relation.

**Definition 7.** Consider a (lifted version of a) pre-application  $H_1 \setminus H_2 \iff G \mid C$ with local variables L and a consistent meta level state (S WHERE M) with  $S = \langle H_1 \uplus H_2 \uplus S^+, B^+ \rangle$  and

$$\mathcal{M} \models M \rightarrow (\operatorname{inv}(S) \land \boxdot B^+ \land \boxdot (B^+ \rightarrow \exists_L G) \land \operatorname{freshVars}(L,S)).$$

Then the following is a meta level transition by rule application.

S WHERE  $M \longmapsto_{logic}^{meta} \langle H_1 \uplus C \uplus S^+, G \land B^+ \rangle$  WHERE M

Consider a (lifted version of a) built-in b of  $\mathcal{B}$  and a consistent meta level state (S WHERE M) with  $S = \langle \{b\} \uplus S^+, B^+ \rangle$  and

$$\mathcal{M} \models M \rightarrow (\operatorname{inv}(S) \land \boxdot B^+).$$

Then the following is a meta level transition by built-in.

 $\langle \{b\} \uplus S^+, B^+ \rangle \text{ where } M \quad \longmapsto_{logic}^{meta} \quad \langle S^+, b \land B^+ \rangle \text{ where } M$ 

Notice that for both sorts of transitions, the implication of  $\square B^+$  excludes transitions from failed and mixed states. For built-in transitions, the resulting states may be non-failed, failed or mixed.

**Lemma 4.** For a given CHR program with  $I_{logic}$  and  $\approx_{logic}$ , the definitions of meta level states and transitions  $\mapsto_{logic}^{meta}$ ,  $I_{logic}^{meta}$  and  $\approx_{logic}^{meta}$ , together with  $[-]_{logic}^{meta}$  comprise an abstract simulation of the logic-based semantics.

Transitions are not possible from a mixed or failed meta level state, but modal constraints are useful for restricting to the relevant substate, such that transitions are known to exists. This is expressed by the following propositions that are immediate consequences of the definitions.

**Proposition 1.** Let  $r: H_1 \setminus H_2 \iff G \mid C$  be a (lifted version of a) preapplication with local variables L and  $\Sigma = (\langle S, B \rangle \text{ WHERE } M)$  a meta level state with  $H_1 \uplus H_2 \subseteq S$ . Whenever the meta level state  $\Sigma^{\boxdot} = (\langle S, B \rangle \text{ WHERE } M \land \widehat{M})$ is consistent, with  $\widehat{M} = \operatorname{inv}(\langle S, B \rangle) \land \boxdot B \land \boxdot(B \to \exists_L G) \land \operatorname{freshVars}(L, \Sigma)$ , there exists a meta level rule application by r,

$$\Sigma^{\fbox{}} \longmapsto_{logic}^{meta} \langle S \backslash H_2 \uplus C, B \land G \rangle \text{ WHERE } M \land \widehat{M}.$$

Furthermore,  $\Sigma^{\square}$  is the greatest substate of  $\Sigma$  to which r can apply.

**Proposition 2.** Let b be a (lifted version of a) built-in and  $\Sigma = (\langle S, B \rangle$  WHERE M) a meta level state with  $b \in S$ . When  $\Sigma^{\square} = (\langle S, B \rangle$  WHERE  $M \land \widehat{M})$  is consistent, with  $\widehat{M^{\square}} = \operatorname{inv}(\langle S, B \rangle) \land \square B \land \square (B \to b)$ , there is a meta level trans.,

$$\Sigma^{\fbox{}} \longmapsto_{logic}^{meta} \langle S \setminus \{b\}, B \land b \rangle \text{ WHERE } M \land M^{\fbox{}}$$

$$\Sigma^{\boxtimes} \longmapsto_{logic}^{meta} \langle S \setminus \{b\}, B \wedge b \rangle \text{ WHERE } M \wedge \widehat{M^{\boxtimes}}.$$

The state  $\Sigma^{\boxdot}$  (resp.  $\Sigma^{\boxdot}$ ) is the greatest substate of  $\Sigma$  for which the meta level transition by b leads to a non-failure and non-mixed (resp. failed) state.

With Propositions 1 and 2 in mind, we define meta level critical corners from the critical corners of Definition 4.

**Definition 8.** Let  $\langle S_1, B_1 \rangle \leftarrow_{logic} \langle S_0, B_0 \rangle \mapsto_{logic} \langle S_2, B_2 \rangle$  be a (lifted version of a) critical  $\alpha$  pre-corner given by Definition 4, in which the leftmost (rightmost) rule application has local variables  $L_1$  ( $L_2$ ) and guard  $G_1$  ( $G_2$ ). Assume  $S^+$  and  $B^+$  are fresh meta level variables and let, for i = 0, 1, 2,

$$\begin{array}{lll} \Sigma_i &=& \langle S_i \uplus S^+, B_i \wedge B^+ \rangle \\ M &=& \operatorname{inv}(\Sigma_0) \wedge \boxdot B_0 \wedge \boxdot (B_0 \wedge B^+ \to \exists_{L_1} G_1) \wedge \boxdot (B_0 \wedge B^+ \to \exists_{L_2} G_2) \wedge \\ && \operatorname{freshVars}(L_1, L_2, \Sigma) \end{array}$$

When  $(\Sigma_0 \text{ WHERE } M)$  is consistent, the following is a critical meta level  $\alpha$  corner.

$$(\Sigma_1 \text{ WHERE } M) \xleftarrow{meta}_{logic} (\Sigma_0 \text{ WHERE } M) \xleftarrow{meta}_{logic} (\Sigma_2 \text{ WHERE } M)$$

Example 6. (Continuing Example 4) The invariant is formalized at the meta level as states of the form  $\langle \{pred(n)\}, true \rangle$  WHERE type(int, n) where pred is one of p, q and r. Below is shown the non-joinable critical meta level corner generated by Definition 8. It is split-joinable as demonstrated by its splitting into two corners; each shown joinable by the indicated dotted transition. Let M stand for the meta-level constraint type(int, n),  $M_1$  for  $M \land \boxdot n \le 0$  and  $M_2$  for  $M \land \boxdot n \ge 0$ .



According to Lemma 5 shown below, the program is confluent.

When, furthermore, a state equivalence  $\approx_{logic}$  is assumed, we need also show joinability of  $\beta$  corners, i.e., those composed by an equivalence and a transition.

**Definition 9.** Let  $H \setminus H' \leq G \mid C$  be a (lifted version of a) variant of a rule with local variables L. Assume  $S^+$ ,  $B^+$  and  $\Sigma_1$  are fresh meta-variables, and let

 $\Sigma_0 = \langle H \uplus H' \uplus S^+, B^+ \rangle \qquad \qquad \Sigma_2 = \langle H \uplus C \uplus S^+, G \land B^+ \rangle$ 

$$M = \operatorname{inv}(\Sigma_0) \wedge \boxdot B \wedge \boxdot (B \to \exists LG) \wedge \operatorname{freshVars}(L, \Sigma_0) \wedge \operatorname{equiv}(\Sigma_0, \Sigma_1)$$

When  $(\Sigma_0 \text{ WHERE } M)$  is consistent, the following is a critical meta level  $\beta$  corner by rule application.

 $(\Sigma_1 \text{ WHERE } M) \approx_{logic}^{meta} (\Sigma_0 \text{ WHERE } M) \longmapsto_{logic}^{meta} (\Sigma_2 \text{ WHERE } M)$ 

Let b be a (lifted version of a) built-in atom whose arguments are fresh variables. Assume  $S^+$ ,  $B^+$  and  $\Sigma_1$  are fresh meta-variables, and let

When  $(\Sigma_0 \text{ WHERE } M)$  is consistent, the following is a critical meta level  $\beta$  corner by built-in.

$$(\Sigma_1 \text{ WHERE } M) \approx_{logic}^{meta} (\Sigma_0 \text{ WHERE } M) \longmapsto_{logic}^{meta} (\Sigma_2 \text{ WHERE } M)$$

**Lemma 5.** Let a terminating CHR program  $\Pi$  with invariant  $I_{logic}$  (and state quivalence  $\approx_{logic}$ ) be given. Then  $\Pi$  is confluent (modulo  $\approx_{logic}$ ) if and only if its set of critical corners (Definitions 8–9) is split-joinable w.r.t.  $I_{logic}^{meta}$  (modulo  $\approx_{logic}^{meta}$ ).

Example 7. (Cont. Example 3; adapted from [8]). The invariant is formalized at the meta level as states of the form

 $\langle \{ \mathtt{set}(L) \} \uplus S, true \rangle$  WHERE type(constList,L)  $\land$  type(constItems,S);

we assume types const for all constants, constList for all lists of such, and constItems for sets of constraints of the form item(c) where c is a constant.

The state equivalence is formalized at the meta level as the relationships of states of the following form, where  $perm(L_1, L_2)$  means that  $L_1$  and  $L_2$  are lists being permutations of each other; and  $M^{\approx}$  stands for type(constList,  $L_1$ )  $\land$ type(constList,  $L_1$ )  $\land$  perm( $L_1, L_2$ )  $\land$  type(constLitems, S),

 $\langle \{ \mathtt{set}(L_1) \} \uplus S, true \rangle$  where  $M^{\approx} \approx \overset{meta}{logic} \langle \{ \mathtt{set}(L_2) \} \uplus S, true \rangle$  where  $M^{\approx}$ 

The critical object level corner with two set constraints in the states does not give rise to a critical meta level corner as the invariant is not satisfied. The other one is shown here, including (with dotted arrows) its proof of joinability modulo equivalence;  $M^{\alpha}$  stands for type(const, $x_1$ )  $\land$  type(constList,L)  $\land$  type(const, $x_2$ )  $\land$ type(constItems,S).

We consider the following critical meta level  $\beta$  corner.  $M^{\beta}$  stands for type(const, x)  $\land$  type(constList,  $L_1$ )  $\land$  type(constList,  $L_2$ )  $\land$  perm( $L_1, L_2$ )  $\land$  type(constItems, S).

$$\begin{array}{c} \langle \{\texttt{item}(x), \texttt{set}(L_1)\} \Downarrow S, true \rangle \text{ WHERE } M^{\beta} \\ \swarrow & \swarrow & \checkmark \\ \langle \{\texttt{item}(x), \texttt{set}(L_2)\} \nexists S, true \rangle \text{ WHERE } M^{\beta} \\ & \langle \{\texttt{set}([x|L_1])\} \nexists S, true \rangle \text{ WHERE } M^{\beta} \\ & & (\texttt{set}([x|L_2])\} \Downarrow S, true \rangle \text{ WHERE } M^{\beta} \end{array}$$

All critical corners are joinable modulo equivalence, and since the program is obviously terminating, Lemma 5 gives that the program is confluent mod. equiv.

#### 7 Conclusion

We generalized the critical pair approach using a meta level simulation to prove confluence under invariant and modulo equivalence for Constraint Handling Rules. We have demonstrated how this principle makes it possible to express natural invariants and equivalences, that cannot be expressed in CHR itself, in a formal way at the meta level, anticipating machine supported proofs using a meta level constraint solver, based on a ground representation. A constraint solver is currently under development, partly inspired by [5]. Depending on the complexity of the invariants and equivalences – and of the CHR programs under investigation – it may be difficult to obtain a complete solver.

For simplicity of notation, we did not include mechanisms to prevent loops caused by propagation rules; [8] has included this in a meta level representation for the Prolog based semantics, and is easily adapted for the logic based semantics exposed in the present paper. For comparison with earlier work on confluence for CHR, we used here a logic-based CHR semantics, which has nice theoretical properties, but is incompatible with standard implementations of CHR and applies only for a limited set of programs. In [9], we have defined meta level constraints and a simulation for an alternative CHR semantics [7,8] that reflects CHR's Prolog based implementation, including a correct handling of Prolog's non-logical devices (e.g., var/1, nonvar/2, is/2) and runtime errors.

The abstract simulations used for the classical CHR confluence results are special cases of abstract interpretations, but when invariants are introduced – or when considering full CHR including Prolog-style non-logical devices, cf. [9] – this correspondence does not hold. The concept of abstract simulations and their use for proving confluence (mod. equiv.) seem obvious to investigate for a large variety of rewrite based systems, e.g., constrained term rewriting, conditional term rewriting, interactive theorem provers, and rule-based specifications of abstract algorithms.

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# **Analysis of Logic Programming**



# Compiling Control as Offline Partial Deduction

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Abstract. We present a new approach to a technique known as compiling control, whose aim is to compile away special mechanisms for non-standard atom selection in logic programs. It has previously been conjectured that compiling control could be implemented as an instance of the first Futamura projection, in which an interpreter is specialized for an input program. However, the exact nature of such an interpreter and of the required technique for specialization were never specified. In this work, we propose a Prolog meta-interpreter which applies the desired non-standard selection rule and which is amenable to specialization using offline partial deduction. After the initial analysis phase of compiling control, we collect annotations to specialize the interpreter using the Logen system for offline partial deduction. We also show that the result of the specialization is equivalent to the program obtained using the traditional approach to compiling control. In this way, we simplify the synthesis step.

**Keywords:** Compiling control  $\cdot$  Offline partial deduction  $\cdot$  Coroutines  $\cdot$  First Futamura projection

# 1 Introduction

Compiling control is a program transformation technique which aims to compile the runtime behavior of pure logic programs executed under a non-standard selection rule to logic programs which are totally equivalent under the standard, left-to-right selection rule of Prolog. It was originally presented in [1,2]. The technique is designed to work in two phases. In a first phase, the computation flow of the program, executed under the non-standard rule, is analyzed, resulting in a symbolic evaluation tree that captures the entire flow. In a second phase, from the symbolic evaluation tree, a new logic program is synthesized. The technique was formalized and proven correct under certain technical conditions, but it possessed certain drawbacks. Most importantly, it was an ad hoc solution. Because of this, showing that the analysis phase of the transformation was complete for a specific program required a manual proof by induction. However, since the original presentation of compiling control, several frameworks have been developed which provide a more formal perspective on the two phases of compiling control and whose general correctness results can be reused. Most notably, abstract interpretation [3] and partial deduction [4,5]. In [6], we showed that the technique could

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in some cases be reformulated and formalized using abstract conjunctive partial deduction, a framework proposed in [7] which integrates the aforementioned frameworks. In addition, we proposed a new abstraction, *multi*, to analyze computations with unboundedly growing goals. This allowed us to analyze a diverse set of well-known programs and to compile these into programs executed under the standard selection rule. Unfortunately, the *multi* abstraction also broke an explicit assumption of the abstract conjunctive partial deduction framework. In the current work, we propose a different perspective. We show that the synthesis obtained using the previously published approach can also be obtained by applying the first Futamura projection, in which program specialization is applied to an interpreter and an input program. Such an approach has been speculated upon in the past, but the current work is the first that demonstrates that this is indeed feasible. It is not an instance of the abstract conjunctive partial deduction framework, but rather a standard offline partial deduction, which implies that no changes to the abstract conjunctive partial deduction framework are necessary to relax the aforementioned assumption which is not met. Based on our experiments, there are no programs which can be compiled using the classical approach but not the approach presented here.

The idea of applying the first Futamura projection to obtain a more structured representation of control flow can also be found in [8], where Java bytecode is decompiled to Prolog. The notion of modelling and analyzing the execution of a (PIC) program as a logic program and partially evaluating that can be found in [9]. This is akin to what we do, though we abstractly analyze the program to be executed itself, which is already a logic program, and partially evaluate the interpreter. Other examples of specialization of logic program interpreters, also using the Logen system, are provided in [10].

We will first give a brief introduction to the first Futamura projection and to offline partial deduction in Sect. 2. Then, we will provide a motivating example. We will explain the notation and operations shown in the motivating example by introducing our abstract domain in Sect. 4. We will use the abstract domain to express the scope of the technique in Sect. 5. Next, in Sect. 6, we will round out the analysis for the first example program. We will also show a simple metainterpreter which, using information obtained from the analysis, can be configured to implement the desired non-standard selection rule. Once the basic idea has been illustrated, we will show the most interesting parts of the analysis of a more complex program, as well as extensions to the meta-interpreter required to run this program in a satisfactory way in Sect. 8. In Sect. 9, we will show how the meta-interpreter can be specialized using the Logen system for offline partial deduction. Then, in Sect. 10, we will show that the obtained specialization is indeed equivalent to the "classical" synthesis. We wrap up with a discussion and with avenues for future work. A set of example programs along with corresponding analyses and syntheses using both techniques is available as an electronic appendix at https://perswww.kuleuven.be/~u0055408/cc-as-opd.html.

### 2 Preliminaries

In [11], Futamura showed that partially evaluating an interpreter for a language  $l_1$  (written in language  $l_2$ ) for a "source program" (in  $l_1$ ) yields an "object program" (in  $l_2$ ) with the semantics of the source program, as run by the interpreter. That is, partially evaluating an interpreter for a source program is an act of compiling.

This is expressed by the following equation:  $int(s,r) = \alpha(int,s)(r)$ . Here, int is the function encoded by the interpreter. Its arguments s and r are the "static" and "runtime" inputs, i.e. inputs which remain constant and which may vary between executions, respectively. In the setting of partial evaluation of an interpreter, the "static" input is the program to be interpreted, whereas the "runtime" input is the input to the interpreted program. The function  $\alpha$  is the partial evaluation function: it specializes its first argument for the static input. In this setting, this produces a program which takes the runtime inputs, so r, and behaves as the interpreter would when also given s. Therefore  $\alpha(int, s)$  is the compiled version of the source program which Futamura calls the "object program". The observation that a compiled program can be obtained through specialization of an interpreter is known as the first Futamura projection [12].

Partial deduction is a technique for logic program specialization originally introduced in [4] and formalized in [13]. The idea behind partial deduction is to compute a set of derivation trees for some top level goal A such that all expected queries instantiate A. The computations represented by the branches of these trees can then be collected into logical implications, referred to as "resultants". These are then encoded as logic program clauses, so that a program is obtained which is equivalent to the original program for all queries which instantiate A, but not necessarily for other queries.

An important notion is "closedness". Under closedness, all atoms in a partial deduction are instances of an atom in the root of a derivation tree. This implies that a goal is never reduced to a goal that has not been specialized and that completeness is ensured. Because the set of all roots of trees is denoted  $\mathcal{A}$ , this is also referred to as  $\mathcal{A}$ -closedness.

There are two broad approaches for dealing with the issue of control [14]. In "online partial deduction", control is tackled during the specialization phase itself. That is, the construction of SLDNF-trees is monitored and unfolding continues as long as there is evidence that interesting computations are performed.

Offline partial deduction is different in that control decisions are taken before the specialization phase. These are then cast in the form of program annotations for the specializer. During the specialization phase, unfolding proceeds in a leftto-right fashion. Broadly speaking, depending on the annotation of a call, it may be unfolded, it may be treated as an instance of a specialized atom or it may be kept residual. Given the annotations, specialization is straightforward. Annotations can be written manually, but can also be derived automatically in a separate phase. This phase is referred to as a "binding-time analysis" and is performed before the static input is available. The above concepts and techniques can be generalized to "conjunctive partial deduction". In conjunctive partial deduction, the roots and leaves of derivation trees need not be atoms, but can also be conjunctions. This adds a layer of complexity: While partial deduction splits up conjunctions into atoms before starting new SLDNF-trees, conjunctive partial deduction has more options for splitting them into subconjunctions.

# 3 Running Example: Permutation Sort

To introduce the essential components of compiling control, we will use a motivating example. The following is a Prolog implementation of permutation sort, which sorts a list by permuting it and then checking if the permutation is ordered correctly.

permsort(X,Y) :- perm(X,Y), ord(Y).
perm([],[]).
perm([X|Y],[U|V]) :- select(U,[X|Y],W), perm(W,V).
ord([]).
ord([X]).
ord([X]).
ord([X,Y|Z]) :- X =< Y, ord([Y|Z]).</pre>

While this sorting program clearly expresses the declarative perspective on sorting as creating an ordered permutation, its naive implementation is problematic. Its efficiency can be improved by using a different selection rule. Informally, such a selection rule interleaves calls which build the permutation with calls which check for the correct ordering of elements. Specifically, as soon as the first two elements of the permutation have been generated, their ordering can be checked. Figures 1 and 2 can be considered as symbolic derivation trees representing a computation under this more efficient selection rule. In these figures, a value  $a_i$ , where i is a natural number, stands for any term, whereas a value  $g_j$ , where j is also a natural number, stands for a ground term. When an atom is underlined once, we consider the effects of resolving said atom. If it is underlined twice, we treat it as a built-in. The reader is not intended to understand every

$$\begin{array}{c|c}1: \underline{permsort(g_1, a_1)}\\2: \underline{perm(g_1, a_1)}, ord(a_1)\\a_1 = [] & g_1 = [g_2|g_3]\\g_1 = [] & a_1 = [a_2|a_3]\\3: \underline{ord([])} & 4: \underline{select(a_2, [g_2|g_3], a_4)},\\ & perm(a_4, a_3), ord([a_2|a_3])\\ & | & a_2 = g_4\\a_4 = g_5\\\Box & 5: perm(g_5, a_3), ord([g_4|a_3])\end{array}$$

Fig. 1. First analysis tree for permutation sort

aspect of the two trees at this point. They are only intended to give an idea of the type of computation the synthesized program should execute. In the following sections, we will provide formal underpinnings for the data and operations in these symbolic trees and will explain how they can be used to synthesize a program which simulates the program with the desired selection rule.

#### 4 Abstract Domain

The trees shown in Sect. 3 constitute the analysis phase of compiling control, which is a form of abstract interpretation. This abstract interpretation remains entirely the same as in [6]. We reproduce the key points here.

As any abstract interpretation, the analysis phase is based on an abstract domain, whose elements represent sets of concrete values with specific properties. The fundamental building blocks of the abstract domain are two types of abstract variables,  $a_i$  and  $g_j$ . An abstract variable  $a_i$  represents the set of all concrete values, whereas a variable  $g_j$  represents all ground concrete values. The union of these two sets is denoted  $AVar_P$ .



Fig. 2. Second analysis tree for permutation sort

Abstract counterparts to concrete program constants are included in the abstract domain. These represent the singleton sets consisting of the corresponding concrete constants. This is why, in the example in Sect. 3, the empty list [] occurs as an argument. From these abstract variables and abstract constants, abstract terms, abstract atoms and abstract conjunctions are constructed, yielding the sets  $ATerm_P$ ,  $AAtom_P$  and  $AConAtom_P$ . Example members of these sets are  $[g_2|g_3]$ ,  $permsort(g_1, a_1)$  and  $perm(g_1, a_1), ord(a_1)$ , respectively. If an abstract term, atom or conjunction contains some  $a_i$  or  $g_j$  several times (i.e. the occurrences have the same subscript), then the represented concrete term, atom

or conjunction contains the *same* subterm at every position corresponding to the positions of  $a_i$  or  $g_j$ . For instance, in the second node in the running example, the result of the permutation operation,  $a_1$ , must be ordered. Note that two abstract variables  $a_i$  and  $g_j$ , when i = j, are not assumed to be aliased.

Let  $ATerm_{P/\approx}$ ,  $AAtom_{P/\approx}$  and  $AConAtom_{P/\approx}$  denote the sets of equivalence classes of abstract terms, abstract atoms and abstract conjunctions, respectively. Equivalence of abstract terms (or atoms or conjunctions) is based on abstract substitutions, which are finite sets of ordered pairs in  $AVar_P \times ATerm_P$ and whose application instantiates abstract variables in a way that is analogous to how applying substitutions in the concrete domain instantiate concrete variables. Two abstract terms (or atoms or conjunctions) A and B are equivalent, denoted  $A \approx B$ , if and only if there are abstract substitutions  $\theta_1$  and  $\theta_2$  such that  $A\theta_1 = B$  and  $B\theta_2 = A$ .

The abstract domain,  $ADom_P$ , is the union of  $AVar_P$ ,  $ATerm_{P/\approx}$  $AAtom_{P/\approx}$  and  $AConAtom_{P/\approx}$  (leaving aside the *multi* abstraction until Sect. 8). Note that we will refer to an equivalence class by taking a representative. For instance, we write  $permsort(g_1, a_1)$  when we mean "all abstract atoms equivalent to  $permsort(g_1, a_1)$ " and assume that the intended meaning is clear from the context. Finally, let  $Dom_P$  be the concrete domain and let  $\gamma : ADom_P \to 2^{Dom_P}$  be the concretization function, which maps elements of the abstract domain to their concrete denotation. For example, the denotation of  $permsort(g_1, a_1)$  is an infinite set of concrete atoms with predicate symbol permsort, with a ground first argument and any kind of second argument.

# 5 Instantiation

In general, abstract interpretation requires a "widening" operator to achieve termination. A widening operator replaces one abstract value with another, more general abstract value, which can come at the cost of accuracy of the analysis. For our abstract domain, depth-k abstraction is such a type of widening. Depth-k abstraction entails that any abstract values whose term depth exceeds a certain limit k are replaced with more general terms. For instance, if only one level of term nesting is allowed and the atom  $ord([g_1, g_2|a_1])$  is computed, this term must be generalized. The most specific term which does not exceed the nesting limit is  $ord([g_1|a_2])$ . If such a widening were applied in the running example, the resulting synthesis would not simulate the desired selection rule. In general, applying depth-k abstraction may affect the correctness of the technique, depending on the program and the level of nesting which is allowed. In what follows, we will assume that depth-k abstraction is not required for termination.

The abstract domain is also tied to the selection rule. We assume that the selection rule is an instantiation based selection rule, which is defined as follows:

**Definition** (instantiation-based selection rule). An instantiation-based selection rule for P is a strict partial order < on  $AAtom_{P/\approx}$ , such that  $\gamma(s_1) \subset \gamma(s_2)$ implies  $s_2 \leq s_1$ , where  $\subset$  denotes strict set inclusion. An instantiation-based selection rule expresses which atom is selected from an abstract conjunction. Our technique requires that an instantiation-based selection rule can completely specify the desired control flow. This is formalized as follows.

**Definition** (complete instantiation-based selection rule). An instantiationbased selection rule for a program P is complete if, for each  $A \in ACon_{P/\approx}$ , there exists an abstract atom b in A, such that  $\forall c \in A : c \not\approx b \Rightarrow b < c$ .

**Definition** (selection by a complete instantiation-based selection rule). Let  $A \in AConAtom_{P/\approx}$ . Then, the abstract atom selected from A by < is the leftmost abstract atom b, such that  $\forall c \in A : c \not\approx b \Rightarrow b < c$ .

In the running example, a complete instantiation-based selection rule contains a pair  $(ord([g_1, g_2|a_1]), perm(g_1, a_1))$ . There is no aliasing between the elements. That is, this ordered pair has the same meaning as  $(ord([g_1, g_2|a_1]), perm(g_3, a_2))$ .

We assume that fully evaluated abstract atoms are dealt with in left-toright order. We also assume that fully evaluated abstract atoms which can be fully evaluated are selected before those which can be unfolded.<sup>1</sup> Under the assumption that more instantiated atoms are always ordered before less instantiated ones, we will represent < by its generating set, *Preprior*. The selection rule < itself is inferred from *Preprior* using the assumptions about fully evaluated abstract atoms and the fact that < is transitive. For our running example, formally, *Preprior* contains the pairs  $perm(g_1, a_1) < ord([g_1|a_1])$  and  $ord([g_1, g_2|a_1]) < perm(g_1, a_1)$ .

# 6 The Analysis Phase

The analysis consists of a number of abstract derivation trees whose roots are all in a finite set  $\mathcal{A}$  of abstract conjunctions. The first of these conjunctions is the abstract conjunction representing the intended call pattern, e.g.  $permsort(g_1, a_1)$ . The leaves of these trees are also in  $\mathcal{A}$ , or they are empty. Atoms underlined once are selected by the non-standard selection rule and an abstraction of resolution is applied to them. Atoms underlined twice represent atoms which, in a concrete computation, are selected, but an abstraction of resolution is not applied to them. Instead, these atoms are considered to be completely executed and the result of this execution is collected in a set of output bindings. That is, the evaluation of such atoms is not interleaved with that of other atoms and we are therefore only interested in the effects they have on the remaining atoms.

<sup>&</sup>lt;sup>1</sup> Both assumptions pertaining to fully evaluated atoms are strictly for notational convenience.

# 7 A Suitable Meta-interpreter

If each abstract conjunction in Figs. 1 and 2 is assigned the number in front of the conjunction as an identifier and the empty goal is assigned the atom "empty", a simple meta-interpreter can run permutation sort under the desired selection rule.

```
compute(Gs) :- mi(Gs,1).
mi([],_).
mi([G|Gs],State) :-
  selected_index(State,Idx),
  divide_goals([G|Gs],Idx,Before,Selected,After),
  mi_clause(Selected,Body,RuleIdx),
  state_transition(State,NewState,RuleIdx),
  append(Before,Body,NewGsA),
  append(NewGsA,After,NewGs),
  mi(NewGs,NewState).
mi([G|Gs],State) :-
  selected_index(State,Idx),
  divide_goals([G|Gs],Idx,Before,Selected,After),
  mi_full_eval(Selected,FullAIIdx),
  call(Selected),
  state_transition(State,NewState,FullAIIdx),
  append(Before,After,NewGs),
  mi(NewGs,NewState).
divide_goals(Goals,Idx,Before,Selected,After) :-
  length(Before,Idx),
```

```
append(Before,[Selected|After],Goals).
```

```
Here, mi/2 is the meta-interpretation predicate. It takes a concrete conjunction and a state. The initial call is compute([permsort(G, A)]) where G is instantiated to a ground value and A can be any value. The mi\_clause/3 predicate encodes clauses as a head, a list of body atoms and a unique identifier for the clause. The mi\_full\_eval/2 predicate identifies fully evaluated goals, e.g. mi\_full\_eval(select(X,Y,Z),fullai1) to remove an element X from a list Y, which yields Z. The selected\_index/2 predicate supplies the index of the atom to be selected in a particular state. The meta-interpreter itself does not inspect groundness or aliasing characteristics of conjunctions. Such characteristics are derived during the analysis phase. The state\_transition/3 predicate expresses which states are directly reachable from which other states and which rules cause the transitions. The full code is in the electronic appendix.
```

# 7.1 Instantiation of the First Futamura Projection

Using Logen, the interpreter can be specialized for mi([permsort(X, Y)], 1). We cannot express to Logen that X will be instantiated to a ground value, but the control flow is already encoded in  $selected_index/2$  and  $state\_transition/3$ .

Therefore, the result of the specialization will still be a compiled program with the desired control flow.

With regard to the equation  $int(s, r) = \alpha(int, s)(r)$ , the vector s is described in [11] as "a source program and information needed for syntax analysis and semantic analysis". As such, the selection rule is a component of s in addition to the source program, P. Because the program should work for multiple input queries, a program's top-level goal is considered a runtime input. However, a top-level goal instantiated by all runtime top-level goals is a static input. Hence, such a goal is a component of s, even if P is not intended to be run with this goal directly. This is what allows logic programs to be specialized for certain top-level calls. Groundness characteristics of the query pattern can also be considered as static information. This is not done in partial deduction of pure logic programs, but we have to take it into account for our approach.

To this end, the compiling control analysis can be encoded as a component of s. Because it provides all required information about the static inputs to the meta-interpreter, there is no need to provide those as direct inputs to the evaluator as well. However, it does *not* necessarily contain enough information about predicates not involved in the coroutining control flow. That is, certain predicates may be fully evaluated during the analysis phase even if their definition is part of the source program, so the source program is also a component of s. The analysis completely specifies the local and global control for a conjunctive partial deduction of P under the semantics implemented by *int*. In light of the first Futamura projection, an offline partial evaluation of the interpreter must then produce a program equivalent to that produced by the synthesis step [6].<sup>2</sup> In other words, the "classical" synthesis step can be seen as a problem-specific shortcut to the outcome of the first Futamura projection.

#### 8 Programs Requiring the *multi* Abstraction: Primes

The application of compiling control can be recast as an abstract conjunctive partial deduction [7] in the case of permutation sort. However, this does not hold for every program with an instantiation-based selection rule. This is due to the generation of abstract conjunctions of arbitrary length in some programs. Conjunctive partial deduction can deal with conjunctions of arbitrary length by splitting goals to obtain an  $\mathcal{A}$ -closed set. In our context, however, this would cause a loss of important information regarding aliasing between subconjunctions. The finite analysis of some coroutining programs therefore requires an addition to the abstract domain, known as the *multi* abstraction.

The formal details of this abstraction are quite elaborate. In brief, the concretization of an abstract conjunction containing a *multi* abstraction contains an infinite number of concrete conjunctions of arbitrary length, but with a structure

 $<sup>^2</sup>$  We use "partial evaluation" rather than "partial deduction" for impure logic programs.

which follows a constrained pattern, expressed as a conjunction.<sup>3</sup> It is easier to illustrate the *multi* through an example first and to indicate the role each component plays rather than to define it beforehand. Here, we will use the following primes generator as an example:

```
primes(N,Primes) :-
    integers(2,I),sift(I,Primes),length(Primes,N)
integers(N,[]).
integers(N,[N|I]) :- plus(N,1,M),integers(M,I).
sift([N|Ints],[N|Primes]) :-
    filter(N,Ints,F),sift(F,Primes).
sift([],[]).
filter(N,[M|I],F) :- divides(N,M), filter(N,I,F).
filter(N,[M|I],[M|F]) :-
    does_not_divide(N,M), filter(N,I,F).
filter(N,[],[]).
length([],0).
length([H|T],N) :- minus(N,1,M),length(T,M).
```

This program is run with a top-level call primes(N, P), where  $N \in \mathbb{N}$ .

Rather than list *Preprior* here in full, we will describe it in terms of a set  $KA = \{integers(g_1, a_1), sift(a_1, a_2), filter(g_1, a_1, a_2), length(a_1, g_1)\}$ . Two rules are sufficient to perform all the comparisons needed to complete the analysis phase:  $\forall x \in KA : x \notin integers(g_1, a_1)$  and  $\forall x \in AAtom_{P/\approx} : (\exists y \in KA : \gamma(x) \subset \gamma(y)) \Rightarrow \forall y \in KA : x < y$  (where  $\subset$  denotes the strict subset relation).

With the resulting order < and without further abstraction, the abstract analysis leads to the introduction of abstract conjunctions with an arbitrary number of filter/3 abstract atoms which are aliased in a consistent manner. Given that  $\mathcal{A}$  must be finite and that the first argument of primes/2 can be an arbitrarily large number, adding a conjunction to  $\mathcal{A}$  for every possible number of filters is not an option. Instead, we abstract away the precise number of filters by replacing the filter/3 abstract atoms with a multi abstraction, which represents any strictly positive natural number of filters.  $multi(filter(g_{id,i,1}, a_{id,i,1}, a_{id,i,2}), \{a_{id,1,1} = a_1\}, \{a_{id,i+1,1} = a_{id,i,2}\}, \{a_{id,\mathcal{L},2} = a_{id,i,2}\}$  $a_2$ ), for example, denotes the following set of abstract conjunctions:  $\{filter(g_{f_1}, a_1, a_2), filter(g_{f_1}, a_1, a_{f_1}) \land filter(g_{f_2}, a_{f_1}, a_2), filter(g_{f_1}, a_1, a_{f_1}) \land filter(g_{f_1}, a_1, a_{f_1}) \land filter(g_{f_1}, a_1, a_{f_1}) \land filter(g_{f_1}, a_{f_1}, a_{f_1}) \land filter(g_{f_1}, a_{f_$  $filter(g_{f_2}, a_{f_1}, a_{f_2}) \wedge filter(g_{f_3}, a_{f_2}, a_2), \ldots$  where every  $f_N$  is a unique index which does not occur for that variable type in the conjunction that the set member is part of. The first element of the *multi* abstraction is the conjunctive pattern. In the pattern, *id* represents an identifier unique to this *multi*. This is required as a single abstract conjunction can contain several *multi* abstractions, which need to be distinguished from one another, but whose variables can also be aliased in some cases. The i is symbolic and is not used in the pattern itself, but in the remaining arguments to *multi*, which are sets of constaints. The numeric

 $<sup>^{3}</sup>$  In the most recent work on the analysis [6], we have assumed the pattern of a *multi* to be a conjunction. Known examples of other patterns are artificial. One is provided in the appendix.

index plays the same part as a regular abstract variable subscript, i.e. to indicate aliasing within an occurrence of the pattern. The second argument to *multi*, which is called *Init*, is a set of constraints which expresses the aliasing applied to certain variables in the first represented instance of the conjunctive pattern. Here, the symbolic *i* is replaced with 1 to indicate this. The third argument, *Consecutive*, expresses aliasing between consecutive occurrences of the pattern. The fourth, *Final*, expresses aliasing applied to variables in the last represented instance of the pattern. Here, the symbolic *i* is replaced with  $\mathcal{L}$  (which stands for "last") to indicate this. When an instance of the pattern becomes eligible for abstract resolution, a case split is applied to the *multi* abstraction. The underlying intuition is that it represents either one or multiple occurrences of its pattern. For a more formal and detailed account of the *multi* abstraction, we refer to [6].

A specification of the control flow using  $selected\_index/2$  is no longer possible if the *multi* abstraction is required for analysis: in two different concrete instances of an abstract conjunction containing *multi*, the concrete atoms selected for resolution can be at different index positions. The solution, then, is to transform the program so that this is no longer the case. Transforming the interpreted program P into a different purely logical program which can be dealt with—at least through a simple transformation—is not an option. It is, however, possible to generalize the SLD-based execution mechanism implemented by the meta-interpreter in a way which still allows the results of partial deduction to be applied. The idea behind this is to introduce an additional operation, *grouping*, which transforms atoms into arguments of an atom with a special predicate symbol, *cmulti*. This operation does not affect the results of the program in any way, but strengthens the correspondence between the abstract analysis and a concrete execution. In this way, the length of *concrete* conjunctions also becomes bounded.

#### 8.1 An Extended Meta-interpreter

The key modification to the meta-interpreter is the addition of the following clauses:

```
building_block(Patt2)|BBs]),
After),
state_transition(State,NewState,many),
append(Before,Patt1,NewGsA),
append(NewGsA,[cmulti([building_block(Patt2)|BBs])],NewGsB),
append(NewGsB,After,NewGs),
mi(NewGs,NewState).
mi([G|Gs],State) :-
grouping(State,NextState,Groupings),
apply_groupings([G|Gs],Groupings,NewGs),
mi(NewGs,NextState).
```

The first two of these clauses extract atoms from a concrete counterpart to a *multi* abstraction. The third one is the one which introduces the concrete counterpart to the *multi* abstraction during interpretation. It uses *apply\_groupings*/3 to group certain concrete conjunctions in a concrete *cmulti* structure. The code for this operation is quite long, but it is sufficient to know that the second argument to  $apply\_groupings/3$  specifies, for a given state, which subconjunctions of the overall goal should be considered instantiations of a *multi* abstraction. This information is available from the abstract interpretation and is encoded using the qrouping/3 predicate, which states which atom indices are grouped during the transition from one state to the next. For example, grouping(54, 55, [[(2, 3), (3, 4)]]) expresses that, in a concrete instance of the transition from state 54 to state 55, the atoms at index positions 2 (that is, from index position 2 to right before index 3) and 3 (that is, from index position 3 to right before index position 4) instantiate a *multi* and can be grouped together. Each instance of the pattern of the *multi* is further wrapped inside a *building\_block* structure to easily extract a single instance of the pattern from the *cmulti* at a later point in the program. Code for the full meta-interpreter and the encoded primes generator, along with instructions on how to generate the analysis, is in the electronic appendix. In the following sections, we show how a specialization of the meta-interpreter can be obtained using Logen.

# 9 Specialization Using Logen

Logen is driven by annotations. Specifically, filter declarations and clause annotations. Filter declarations associate arguments of predicates with so-called "binding types". A binding type can be a binding *time*, e.g. "static" or "dynamic", but it can also restrict the structure of the argument. Clause annotations indicate how every call in a clause body should be treated during unfolding. For more detail, we refer to [15, 16].

Our filter declarations require the following binding types: "static", "dynamic", "nonvar", "struct" and "list". The first two simply express whether or not an argument will be known at specialization time. The third expresses that an argument has some outermost structure during specialization. The fourth can specify an argument's functor and the binding types of its arguments. The last binding type is for *closed* lists, i.e. lists whose length is bounded. A binding type can also be a disjunction, in which case an argument is considered to be an instance of the first applicable disjunct. The purpose of binding types is this: before a call to a predicate with certain binding types is specialized, the call is generalized and is used as the root of a partial derivation tree. The binding types determine to which extent the call is generalized. A static argument is not generalized, whereas a dynamic one is replaced with a variable.

All clause annotations for the current work can be written using the following strategies: "unfold", "call", "rescall" and "memo". The first means that an atom should be resolved. The second means that a call should be executed at specialization time, while the third means that it should be executed at runtime. The last one means that it should not be unfolded further, but that it should be used as the root for a new derivation tree.

#### 9.1 Simple Meta-interpreter

For the simple meta-interpreter, we start from the naive program annotation performed by Logen. This annotates every predicate call as "unfold" and every builtin call as "call". We change the recursive call to mi/2 from "unfold" to "memo", so that it may be specialized separately. This can be seen as starting a new tree in partial deduction and adding the root of this tree to the set of analyzed conjunctions  $\mathcal{A}$ . If the default option, "unfold", is used, new derivation trees are not started and the set of analyzed conjunctions  $\mathcal{A}$  is not closed. Furthermore, we annotate the call to call/1 as "rescall". Calls to call/1 apply to atoms which, in the abstract analysis, are underlined twice and which we consider residual. The "rescall" annotation expresses precisely the idea that a call should simply be executed when the program is run. The "call" annotation should not be used, as this executes the call during specialization time instead of during program execution. The most important filters are as follows:

- compute(struct(permsort,[dynamic,dynamic]))

We specify as much information about the top-level call as possible. We cannot specify that the arguments to the call will have certain groundness characteristics, but this is not necessary as the  $selected_index/2$  and  $state_transition/3$  predicates are based on this information.

- mi(type(list(nonvar)), static) Neither the goal, which is represented as a list, nor its elements should be abstracted to variables in the root of a derivation tree. We also keep track of the state index and do not generalize it.

The arguments of other filters can be left dynamic: some values *will* be known at specialization time, but if the calls to an annotated predicate are never used as the root of a derivation tree (and they are not, because they never have the "memo" annotation), annotating them as "static" has no impact on the partial deduction.

#### 9.2 Extended Meta-interpreter

For the extended meta-interpreter, the reasoning behind the clause annotations remains mostly the same. However, a crucial change must be applied to deal with the case split on the *multi* abstraction during specialization. During partial evaluation, some information regarding the structure of the *multi* abstraction is necessarily generalized away. To the best of our knowledge, we cannot use filter declarations to preserve info from the abstract analysis about the internal structure of a concrete *multi* and generalize calls so that  $\mathcal{A}$  is closed. The problem is that the specialization procedure must generalize over concrete lists of arbitrary length. This can only be done using a variable, which means that information regarding the contents of the list is lost. Specifically, it becomes impossible to use append operations to build a conjunction at specialization time if some of the appended elements are free variables. However, we can re-encode the required information into the call annotations. We modify and annotate the interpreter as follows.

```
logen(mi/2,mi([B|C],A)) :-
        logen(unfold,selected_index(A,D)),
        logen(unfold,extracted_patt_one(A,Patt1)),
        logen(unfold,
          divide_goal(
            [B|C], D, E,
            cmulti([building_block(Patt1)]),F)),
        logen(unfold,state_transition(A,J,one)),
        . . .
        logen(memo,mi(L,J)).
logen(mi/2,mi([B|C],A)) :-
        logen(unfold,selected_index(A,D)),
        logen(unfold,
          extracted_patts_many(A,Patt1,
                                [building_block(Patt2)|BBs])),
        logen(unfold,
          divide_goal(
            [B|C],D,E,
            cmulti([building_block(Patt1),
                    building_block(Patt2)[BBs]),F)),
        logen(unfold,state_transition(A,M,many)),
        . . .
        logen(memo,mi(P,M)).
```

Here, *extracted\_patt\_one(State, Patt)* defines the structure of the pattern extracted from a *multi* abstraction when a single subconjunction is unfolded. Its counterpart, *extracted\_patts\_many(State, Patt, Patts)*, defines the structure of the extracted pattern and the remaining, non-extracted patterns. Neither predicate affects the correctness of the meta-interpreter. We have included an annotated version of the meta-interpreter without these extra steps in the electronic appendix for comparison.

The filter declarations remain mostly the same. However, goals now consist of program-specific atoms and universal cmulti/1 atoms. To reflect this, we use the following filter declaration for mi/2:

This preserves as much information about each conjunct as possible, as the disjunction (indicated by ;) is not commutative. While this solution is somewhat ad hoc, it can easily be applied to all examples that we are aware of. Furthermore, theoretical work on Logen [16] mentions the possibility of using custom binding types. It is possible that a custom binding type for the multi abstraction could help us avoid the above workaround.

# 10 Equivalence with the Classical Approach

The programs produced by Logen appear quite different from the syntheses obtained using the classical approach. To bring them together, we applied the ECCE online specializer to both resulting programs, because it can smooth away some differences through several post-processing steps. The resulting programs produce identical answer sets and display identical finite failure behavior.<sup>4</sup> Furthermore, while they retain some surface differences, they produce their answers after a nearly identical number of inferences. This demonstrates that both versions implement the same control flow and have nearly identical runtime performance. For instance, the sum total of the number of inferences required to find all solutions to the 10-queens problem with one approach deviates less than 3.5% from the number of inferences required with the other approach. More exhaustive benchmarks can be found in the electronic appendix. Therefore empirical evidence strongly points to both approaches being equivalent.

The compile-time performance for both approaches can safely be said to be nearly identical. The reason for this is that the analysis phase, which is the most expensive phase, is common to both approaches.

# 11 Discussion

We have shown that compiling control can be regarded as a specialization of a specific meta-interpreter for logic programs. This is an application of the first

<sup>&</sup>lt;sup>4</sup> With the exception of the postprocessed classical synthesis of the prime sieve. This suggests a bug in ECCE, but the numbers before postprocessing are still within 16.5%.

Futamura projection, which answers a question which has long remained open. We have also provided a software pipeline which applies this idea in practice. We have restricted ourselves here to programs for which depth-k abstraction is not required, as there is currently no hard-and-fast rule to ascertain whether depth-k abstraction affects the eventual program flow. It would be useful to develop such a rule to further extend the set of programs which can be analyzed correctly in an automated way. It is also possible to apply a counterpart to the *multi* abstraction for terms which contain an arbitrary amount of nesting, but which have a predictable, repeating structure. Finally, we plan to investigate a variant of the *multi* abstraction whose pattern is not a conjunction, but a disjunction of conjunctions.

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# Predicate Specialization for Definitional Higher-Order Logic Programs

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**Abstract.** Higher-order logic programming is an interesting extension of traditional logic programming that allows predicates to appear as arguments and variables to be used where predicates typically occur. Higher-order characteristics are indeed desirable but on the other hand they are also usually more expensive to support. In this paper we propose a program specialization technique based on partial evaluation that can be applied to a modest but useful class of higher-order logic programs and can transform them into first-order programs without introducing additional data structures. The resulting first-order programs can be executed by conventional logic programming interpreters and benefit from other optimizations that might be available. We provide an implementation and experimental results that suggest the efficiency of the transformation.

# 1 Introduction

Higher-order logic programming has been long studied as an interesting extension of traditional first-order logic programming and various approaches exist with different features and semantics [2,4,12]. Typically, higher-order logic programs are allowed to define predicates that accept other predicates as arguments and variables can appear in places where predicate constants typically occur. Higher-order logic programs enjoy similar merits as their functional counterparts. The support of higher-order features however, usually comes with a price, and the efficient implementation in either logic or functional programming is a non-straightforward task.

The use of higher-order constructs is a standard feature in every functional language in contrast to the logic programming languages. As a result, there exists a plethora of optimizations that target specifically the efficient implementation of such features. A popular direction is to remove higher-order structures altogether by transforming higher-order programs into equivalent first-order ones, with the hope that the execution of the latter will be much more efficient. Reynolds, in his seminal paper [17], proposed a defunctionalization algorithm that is complete,

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i.e. it succeeds to remove all higher-order parameters from an arbitrary functional program. There is however a tradeoff; his algorithm requires the introduction of data structures in order to compensate for the inherent loss of expressivity [7]. Other approaches [5,13,14] have been proposed that do not use data structures, but share the limitation that are not complete.

In the logic programming context there exist many transformation algorithms with the purpose of creating more efficient programs. Partial evaluation algorithms [6,9,11], for example, can be used to obtain a more efficient program by iteratively unfolding logic clauses. Most of the proposals, however, focus on firstorder logic programs. Proposals that can be applied to higher-order programs are limited. The prominent technique that targets higher-order logic programs proposed in [4,21] and adopted from Hilog. It employs the Reynolds' defunctionalization adapted for logic programs. As a consequence it naturally suffers from the same shortcomings of the original technique: the resulting programs are not natural and the conventional logic programming interpreters fail to identify potential optimizations without specialized tuning [18].

In this paper, we propose a partial evaluation technique that can be applied to higher-order logic programs. The technique propagates only higher-order arguments and avoids to change the structure of the original program. Moreover, it differs from Reynolds' style defunctionalization approaches as it does not rely on any type of data structures. As a result, the technique will only guarantee to remove the higher-order arguments in a well-defined subset of higher-order logic programs. The main contributions of the present paper are the following:

- 1. We propose a technique based on the abstract framework of partial evaluation that targets higher-order arguments. We have identified a well-defined fragment of higher-order logic programming that the technique terminates and produces a logic program without higher-order arguments.
- 2. We provide an implementation of the proposed technique and we experimentally assess its performance. We also compare it with the Reynolds' defunctionalization implemented in Hilog. Moreover, we experiment with the ability of combining this technique with the well-known tabling optimization.

The rest of the paper is organized as follows. In Sect. 2 we give an intuitive overview of our method using a simple example. In Sect. 3 we formally define the fragment of the higher-order logic programs we will use. Section 4 describes the abstract framework of partial evaluation and Sect. 5 introduces the details of our method. Section 6 discusses some implementation issues and Sect. 7 discusses the performance of our transformation on various experiments. Lastly, we compare our method with related approaches in Sect. 8 and we conclude the paper with possible future work.

#### 2 A Simple Example

We start with an introductory example so as to give an informal description of our technique. We borrow an example from the area of knowledge representation which deals with the expression of user preferences [3]. The following program selects the most preferred tuples T out of a given unary relation R, based on a binary preference predicate P. The preference predicate given two tuples it succeeds if the first tuple is more preferred than the second.

```
winnow(P,R,T) := R(T), not bypassed(P,R,T).
bypassed(P,R,T) := R(Z), P(Z,T).
```

The program contains *predicate variables* (for example, P and R), that is variables that can occur in places where predicates typically occur.

Assume that we have a unary predicate movie which corresponds to a relation of movies and a binary predicate pref which given two movies succeeds if the first argument has a higher ranking than the second one. Now, suppose that we issue the query:

```
?- winnow(pref,movie,T).
```

We expect as answers the most "preferred" movies, that is all movies with the highest ranking.

In the following, we will show how we can create a first-order version of the original program specialized for this specific query. Notice that the atom winnow(pref,movie,T), that makes up our given query, does not contain any free predicate variables, but on the contrary, all of its predicate variables are substituted with predicate names. Therefore, we can specialize every program clause that defines winnow by substituting its predicate variables with the corresponding predicate names. By doing so, we get a program where our query yields the same results as to those in the original program:

```
winnow(pref,movie,T) :- movie(T), not bypassed(pref,movie,T).
bypassed(P,R,T) :- R(Z), P(Z,T).
```

We can continue this specialization process by observing that in the body of this newly constructed clause there exists the atom bypassed(pref,movie,T), in which all predicate variables are again substituted with predicate names. Therefore, we can specialize the second clause of the program accordingly:

```
winnow(pref,movie,T) :- movie(T), not bypassed(pref,movie,T).
bypassed(pref,movie,T) :- movie(Z), pref(Z,T).
```

There are no more predicate specializations to be performed and the transformation stops. Notice that the resulting program does not contain any predicate variables, but it is not a valid first-order one. Therefore, we have to perform a simple rewriting in order to remove all unnecessary predicate names that appear as arguments.

```
winnow1(T) :- movie(T), not bypassed2(T).
bypassed2(T) :- movie(Z), pref(Z,T).
```

Due to this renaming process, instead of the initial query, the user now has to issue the query ?- winnow1(T). Comparing the final first-order program with the original one it is easy to observe that no additional data structures were

introduced during the first-order transformation, a characteristic that leads to performance improvement (ref. Sect. 7).

This technique, however, cannot be applied in every higher-order logic program. Notice that the resulting program of the previous example does not contain any predicate variables. This holds due to the fact that in the original program, every predicate variable that appears in the body of a clause it also appears in the head of this clause. By restricting ourselves to programs that have this property we ensure that the transformation outputs a first-order program. Moreover, the transformation in this example terminates because the set of the special*ization atoms* (i.e. winnow(pref,movie,T) and bypassed(pref,movie,T)) is finite, which is not the case in every higher-order logic program. To solve this, we need to keep the set specialization atoms finite. This is achieved in two ways. Firstly, we ignore all first-order arguments in every specialization atom, meaning that in a query of the form ?- winnow(pref,movie,m\_001), we will specialize the program with respect to the atom winnow(pref,movie,T). Secondly, we impose one more program restriction; we focus in programs where the higherorder arguments are either variables or predicate names. Since the set of all predicate names is finite and since we ignore all first-order values, the set of specialization atoms is also finite and as a result the algorithm is ensured to terminate.

#### 3 Higher-Order Logic Programs

In this section we define the higher-order language of our interest. We begin with the syntax of the language  $\mathcal{H}$  we use throughout the paper.  $\mathcal{H}$  is based on a simple type system with two base types: o, the boolean domain, and  $\iota$ , the domain of data objects. The composite types are partitioned into three classes: *functional* (assigned to function symbols), *predicate* (assigned to predicate symbols) and *argument* (assigned to parameters of predicates).

**Definition 1.** A type can either be functional, argument, or predicate, denoted by  $\sigma$ ,  $\rho$  and  $\pi$  respectively and defined as:

$$\sigma := \iota \mid (\iota \to \sigma) \pi := o \mid (\rho \to \pi) \rho := \iota \mid \pi$$

**Definition 2.** The alphabet of the language  $\mathcal{H}$  consists of the following:

- 1. Predicate variables of every predicate type  $\pi$  (denoted by capital letters such as P, Q, R, ...).
- 2. Individual variables of type  $\iota$  (denoted by capital letters such as  $X, Y, Z, \ldots$ ).
- 3. Predicate constants of every predicate type  $\pi$  (denoted by lowercase letters such as  $p, q, r, \ldots$ ).
- 4. Individual constants of type  $\iota$  (denoted by lowercase letters such as  $a, b, c, \ldots$ ).
- 5. Function symbols of every functional type  $\sigma \neq \iota$  (denoted by lowercase letters such as f, g, h, ...).

6. The inverse implication constant  $\leftarrow$ , the negation constant  $\sim$ , the comma, the left and right parentheses, and the equality constant  $\approx$  for comparing terms of type  $\iota$ .

The set consisting of the predicate variables and the individual variables of  $\mathcal{H}$  will be called the set of *argument variables* of  $\mathcal{H}$ . Argument variables will be usually denoted by V and its subscripted versions.

**Definition 3.** The set of expressions of  $\mathcal{H}$  is defined as follows:

- Every predicate variable (resp. predicate constant) of type  $\pi$  is an expression of type  $\pi$ ; every individual variable (resp. individual constant) of type  $\iota$  is an expression of type  $\iota$ ;
- if f is an n-ary function symbol and E<sub>1</sub>,..., E<sub>n</sub> are expressions of type ι then
   (f E<sub>1</sub>...E<sub>n</sub>) is an expression of type ι;
- if  $\mathsf{E}$  is an expression of type  $\rho_1 \to \cdots \rho_n \to o$  and  $\mathsf{E}_i$  an expression of type  $\rho_i$ for  $i \in \{1, \ldots, n\}$  then  $(\mathsf{E} \mathsf{E}_1 \cdots \mathsf{E}_n)$  is an expression of type o.
- if  $E_1, E_2$  are expressions of type  $\iota$ , then  $(E_1 \approx E_2)$  is an expression of type o.

We will omit parentheses when no confusion arises. Expressions of type o will often be referred to as *atoms*. We write vars(E) to denote the set of all variables in E. We say that  $E_i$  is the *i*-th argument of an atom  $E E_1 \cdots E_n$ . A ground expression E is an expression where vars(E) is the empty set.

**Definition 4.** A clause is a formula

$$\mathsf{p} \, \mathsf{V}_1 \cdots \mathsf{V}_n \leftarrow \mathsf{L}_1, \dots, \mathsf{L}_m, \sim \mathsf{L}_{m+1}, \dots, \sim \mathsf{L}_{m+k}$$

where **p** is a predicate constant of type  $\rho_1 \to \cdots \to \rho_n \to o, V_1, \ldots, V_n$  are distinct variables of types  $\rho_1, \ldots, \rho_n$  respectively, and  $L_1, \ldots, L_{m+k}$  are expressions of type o, such that every predicate argument of  $L_i$  is either variable or ground. A program P of the higher-order language  $\mathcal{H}$  is a finite set of program clauses.

The syntax of programs given in Definition 4 differs slightly from the usual Prolog-like syntax that we have used in Sect. 2. However, one can easily verify that we can rewrite every program from the former syntax to the latter. For instance, we could use the constant  $\approx$  in order to eliminate individual constants that appear in the head of a clause that uses the Prolog-like syntax.

*Example 1.* Consider the following program in Prolog-like syntax, in which we have three predicate definitions, namely  $\mathbf{p}: \iota \to \mathbf{o}$ ,  $\mathbf{q}: \iota \to \iota \to \mathbf{o}$ , and  $\mathbf{r}: (\iota \to o) \to (\iota \to \iota) \to o$ .

p(a). q(X,X). r(P,Q,f(X)) :- P(X),Q(Y).

In our more formal notation, these clauses can be rewritten as:

p X 
$$\leftarrow$$
 (X  $\approx$  a).  
q X Y  $\leftarrow$  (X  $\approx$  Y).  
r P Q Z  $\leftarrow$  (Z  $\approx$  f(X)), (P X), (Q Y).

Notice that all clauses are now valid  $\mathcal{H}$  clauses.

Notice that in a  $\mathcal{H}$  program, all arguments of predicate type are either variables or predicate names, which as discussed in Sect. 2 leads to the termination of our technique. However, in a  $\mathcal{H}$  program all head predicate variables to be distinct. That implies that checking for equality between predicates (higher-order unification) is forbidden. In other words, the higher-order parameters can be used in ways similar to functional programming, namely either invoked or passed as arguments. We decided to impose this restriction because equality between predicates is treated differently in various higher-order languages [2,4,12]. Moreover, in Sect. 2, we briefly discussed that the reason why our technique can produce a first-order program is due to the following property:

**Definition 5.** A clause will be called definitional *iff* every predicate variable that appears in the body appears also as a formal parameter of the clause. A definitional program is a finite set of definitional clauses.

Example 2. Consider the following program in Prolog-like syntax:

p(Q,Q) := Q(a).q(X) := R(a,X).

This program does not belong to our fragment, because the first clause is a non- $\mathcal{H}$  clause and the second clause is a non-definitional clause. Regarding the first clause, the predicate variable Q appears twice in the head, therefore the formal parameters are not distinct. Regarding the second clause, the predicate variable R that appears in the body, does not appear in the head of the clause.

We extend the well-known notion of substitution to apply to  $\mathcal{H}$  programs.

**Definition 6.** A substitution  $\theta$  is a finite set  $\{V_1/E_1, \ldots, V_n/E_n\}$  where the  $V_i$ 's are different argument variables and each  $E_i$  is a term having the same type as  $V_i$ . We write  $dom(\theta) = \{V_1, \ldots, V_n\}$  to denote the domain of  $\theta$ .

**Definition 7.** Let  $\theta$  be a substitution and  $\mathsf{E}$  be an expression. Then,  $\mathsf{E}\theta$  is an expression obtained from  $\mathsf{E}$  as follows:

 $\begin{array}{l} - \ \mathsf{E}\theta = \mathsf{E} \ if \ \mathsf{E} \ is \ a \ predicate \ constant \ or \ individual \ constant; \\ - \ \mathsf{V}\theta = \theta(\mathsf{V}) \ if \ \mathsf{V} \in dom(\theta); \ otherwise, \ \mathsf{V}\theta = \mathsf{V}; \\ - \ (\mathsf{f} \ \mathsf{E}_1 \cdots \mathsf{E}_n)\theta = (\mathsf{f} \ \mathsf{E}_1\theta \cdots \mathsf{E}_n\theta); \\ - \ (\mathsf{E} \ \mathsf{E}_1 \ \cdots \ \mathsf{E}_n)\theta = (\mathsf{E}\theta \ \mathsf{E}_1\theta \ \cdots \ \mathsf{E}_n\theta); \\ - \ (\mathsf{E}_1 \approx \mathsf{E}_2)\theta = (\mathsf{E}_1\theta \approx \mathsf{E}_2\theta); \\ - \ (\mathsf{L}_1, \dots, \mathsf{L}_m, \sim \mathsf{L}_{m+1}, \dots, \sim \mathsf{L}_n)\theta = (\mathsf{L}_1\theta, \dots, \mathsf{L}_m\theta, \sim (\mathsf{L}_{m+1}\theta), \dots, \sim (\mathsf{L}_n\theta)). \end{array}$ 

Let  $\theta$  be a substitution and E an expression. Then,  $\mathsf{E}\theta$  is called an *instance* of E.

### 4 Partial Evaluation of Logic Programs

*Partial evaluation* [8] is a program optimization that specializes a given program according to a specific set of input data, such that the new program is more efficient than the original and both programs behave in the same way according to

the given data. In the context of logic programming [6,9,11], a partial evaluation algorithm takes a program P and a goal G and produces a new program P' such that  $P \cup \{G\}$  and  $P' \cup \{G\}$  are semantically equivalent. In Fig. 1 we illustrate a basic scheme that aims to describe every partial evaluation algorithm in logic programming, which is based in similar ones in the literature [6,9]. Notice that this general algorithm depends on two operations, namely UNFOLD and ABSTRACT, which can be implemented differently in several partial evaluation systems.

1: Input: a program P and a goal G 2: Output: a specialized program P' 3:  $S := \{A : A \text{ is an atom of } G\}$ 4: repeat 5: S' := S6: P' := UNFOLD(P, S)7:  $S := S \cup \{A : A \text{ is an atom that appears in a body of a clause in } P'\}$ 8: S := ABSTRACT(S)9: until S' = S (modulo variable renaming) 10: return P'

Fig. 1. Basic algorithm for partial evaluation.

Firstly, the algorithm uses an *unfolding* rule [19] in order to construct a finite and possibly incomplete proof tree for every atom in the set S and then creates a program P' such that every clause of it is constructed from all root-to-leaf derivations of these proof trees. This part of the process is referred as the *local control* of partial evaluation. There are many possible unfolding rules, some of which being more useful for a particular application than others. Examples include determinate, leftmost non-determinate, loop-preventing or depth-bound unfolding strategies [6,9]. In some cases though, taking a simple approach which performs no unfolding at all, or in other words by using *one-step unfolding strategy*, may result in useful program optimizations. In such a case, UNFOLD exports a program that is constructed by finding the clauses that unify with each atom in S and then by specializing these clauses accordingly, using simple variable substitutions.

Secondly, the algorithm uses an ABSTRACT operation, which calculates a finite *abstraction* of the set S. We say that S' is an *abstraction* of S if every atom of S is an instance of some atom in S', and there does not exist two atoms in S' that have a common instance in S'. This operation is used to keep the size of the set of atoms S finite, which will ensure the termination of the algorithm. This part of the process is referred as the *global control* of partial evaluation. Examples of abstraction operators include the use of a most specific generalizer and a finite bound in the size of S [9], or by exploiting a distinction between static and dynamic arguments for every atom in S [10].

A partial evaluation algorithm should ensure termination in both levels of control. Firstly, we have the *local termination problem*, which is the problem of the non-termination of the unfolding rule, and the *global termination problem* which is the problem of the non-termination of the iteration process (i.e. the repeat loop in the algorithm). As we stated earlier, the global termination problem is solved by keeping the set S finite through a finite abstraction operation. Regarding the local termination problem, one possible solution is ensuring that all constructed proof trees are finite. The one-step unfolding rule is by definition a strategy that can ensure local termination.

#### 5 Predicate Specialization

In the following, we define our technique using the standard framework of partial evaluation (ref. Sect. 4), by specifying its local and global control strategies (namely UNFOLD and ABSTRACT operations). In particular, we will use a onestep unfolding rule and an abstraction operation which generalizes all individual (i.e. non-predicate) arguments from all atoms of the partial evaluation.

**Definition 8.** Let P be a program and S be a set of atoms. Then,

$$\mathrm{Unfold}(\mathsf{P},\mathsf{S}) = \left\{ p \; \mathsf{E}_1 \cdots \mathsf{E}_n \leftarrow \mathsf{B}\theta : \begin{array}{l} (p \; \mathsf{E}_1 \cdots \mathsf{E}_n) \in \mathsf{S}, \\ (p \; \mathsf{V}_1 \cdots \mathsf{V}_n \leftarrow \mathsf{B}) \in \mathsf{P}, \\ \theta = \{\mathsf{V}_1/\mathsf{E}_1, \dots, \mathsf{V}_n/\mathsf{E}_n\} \end{array} \right\}$$

Definition 9. Let S be a set of atoms. Then,

$$ABSTRACT(S) = \{ p \ \mathsf{E}'_1 \cdots \mathsf{E}'_n : (p \ \mathsf{E}_1 \cdots \mathsf{E}_n) \in \mathsf{S} \}$$

where  $\mathsf{E}'_i = \mathsf{E}_i$  if  $\mathsf{E}_i$  is of predicate type, otherwise  $\mathsf{E}'_i = \mathsf{V}_i$ , where  $\mathsf{V}_i$  is a fresh variable of the same type as of  $\mathsf{E}_i$ .

In the following, we will show some properties of our transformation. Firstly we will need the following lemma:

Lemma 1. Let P be a program, S be a (possibly infinite) set of atoms. Then:

- 1. If S is finite, then UNFOLD(P, S) is finite.
- 2. ABSTRACT(S) is a finite abstraction of S.
- 3. If every element of S does not contain any free predicate variables, then every atom of UNFOLD(P, S) does not contain any free predicate variables.

Proof. 1. Obvious from the construction of UNFOLD(P, S).

- 2. Every predicate argument of every atom that appears in P is either a variable or a predicate name, therefore ABSTRACT(S) is finite.
- 3. Suppose that UNFOLD(P,S) contains an atom A that contains a free predicate variable V. If A appears in the head of a clause, then from the construction of UNFOLD(P,S), S must contain A. If A appears in the body of a clause, then since P is definitional, V also appears in the head of this clause. In any case, S must contain an atom that contains the free predicate variable V.

The first part of the lemma ensures local termination and the second part of the lemma ensures global termination. The third part identifies that the transformation to first-order succeeds, provided that the program belongs to our fragment and the initial goal does not contain free higher-order variables. In the following corollaries, by  $\Phi$  we denote the algorithm of Fig. 1 combined with the operations in Definitions 8 and 9.

**Corollary 1.** Let P be a H program and G an goal. Then, the computation of  $\Phi(P,G)$  terminates in a finite number of steps.

**Corollary 2.** Let P be a definitional program and G an goal that does not contain any free predicate variables. Then, the output of  $\Phi(P,G)$  does not contain any free predicate variables.

The result of  $\Phi$  is neither a valid  $\mathcal{H}$  program since it contains predicate names as arguments in the heads, nor a valid first-order program since some symbols appear both as arguments and as predicate symbols. Therefore, we must apply a simple *renaming* [6, Sect. 3] in order to construct a valid first-order output. In our case, at the end of the partial evaluation algorithm, every atom  $\mathsf{p} \mathsf{E}_1 \cdots \mathsf{E}_n$ of  $\mathsf{S}$  is renamed into  $\mathsf{p}' \mathsf{V}_1 \cdots \mathsf{V}_m$ , where  $\mathsf{p}'$  is a fresh predicate symbol and  $\{\mathsf{V}_1 \cdots \mathsf{V}_m\} = vars(\mathsf{p} \mathsf{E}_1 \cdots \mathsf{E}_n)$ . Moreover, all instances of every atom of  $\mathsf{S}$  in the resulting program are renamed accordingly.

#### 6 Implementation

We have developed a prototype implementation<sup>1</sup> of our predicate specialization technique. Instead of developing a tailor-made higher-order language only for the purpose of demonstrating the benefits of the transformation, we build upon an existing higher-order logic programming language. The source programs in have to be written in the higher-order language Hilog [4], a mature and well-known language with a stable implementation within the XSB system [20].

A feature that we need and is not supported in Hilog though, is the use of types. Our algorithm needs types not only for deciding whether the input program belongs to our fragment, but also for the abstraction operation in Definition 9. Since the process of extending Hilog with types is outside of the scope of this paper, we assume that the input programs are well-typed and accompanied with type annotations for all predicates that contain predicate arguments.

The fragment that we discussed in Sect. 3 consists of programs that the only elements that can appear as predicate arguments are variables and predicate constants. However, most higher-order languages (and Hilog among them) allow more complex expressions to appear as predicate arguments. One such example is the use of *partial applications*, i.e., the ability to apply a predicate to only some of its arguments. Consider the following simple program.

<sup>&</sup>lt;sup>1</sup> The implementation of the transformation is open source and can be accessed at http://bitbucket.org/antru/firstify.

conj2(P,Q,X) :- P(X),Q(X). conj3(P,Q,R,X) :- conj2(P,conj2(Q,R),X).

In the second clause the expression conj2(Q,R) is a partial application where only the first two arguments are defined. A partial application effectively produces a new relation and therefore typically occur in higher-order arguments.

In the implementation we are able to handle programs that make a limited use of complex predicate expressions, as a syntactic sugar for our initial fragment. In particular, we allow non-variable and non-constant predicate arguments in an expression of the form  $\mathbf{p} \in \mathbf{E}_1 \cdots \in \mathbf{E}_n$  that appears in the body of a clause  $\mathbf{q} \, \mathbf{V}_1 \cdots \mathbf{V}_m \leftarrow \mathbf{B}$  only if  $\mathbf{p}$  and  $\mathbf{q}$  do not belong in the same cycle in the *predicate dependency graph*.<sup>2</sup> The transformation in this case is also ensured to terminate (because due to the form of the program all predicate variables of a predicate that depends on itself have to be specialized only with predicate names and therefore the set of all possible specialization atoms will remain finite). As we mentioned earlier, this class of programs has the same expressive power as our initial fragment. For example, the aforementioned logic program is equivalent to the following program that does not use any partial applications.

conj2(P,Q,X) :- P(X),Q(X). conj31(P,Q,R,X) :- conj22(P,Q,R,X). conj22(P,Q,R,X) :- P(X),conj2(Q,R,X).

Interestingly, we can use our algorithm to convert a program of the extended fragment into its equivalent  $\mathcal{H}$  program. This can be done by initializing the transformation process with the top predicate (here conj3(P,Q,R,X)).

#### 7 Experiments

In this section we present some experiments to illustrate that our technique can lead to the improvement of the execution runtime of higher-order logic programs.

We have tested our method with a set of benchmarks that include the computation of the transitive closure of a chain of elements, a k-ary disjunction and k-ary conjunction of k relations (for k = 5, 10), the computation of the shortest path programs of a directed acyclic graph and a set of programs that deal with preference representation [3]. The higher-order program is expressed in Hilog and executed using the Hilog module of XSB. XSB essentially transforms Hilog programs into first-order programs using the techniques and optimizations described in [18], and it also uses an optimized WAM instruction set to efficiently execute Hilog. The measurements obtained include these optimizations. We compare them with the execution of the Prolog programs produced by the our predicate specialization technique. Apart from XSB<sup>3</sup>, we also consider for the execution

 $<sup>^2</sup>$  An edge from the predicate p to predicate q in the predicate dependency graph means that there exists a clause that p appears in the head and p appears in the body of the same clause.

<sup>&</sup>lt;sup>3</sup> version 3.7, cf. http://xsb.sourceforge.net/.

of the specialized program in other Prolog engines. The Prolog engines that we use are SWI-Prolog<sup>4</sup>, and YAP<sup>5</sup>. Every program is executed several times, each time with a predefined set of facts. All data has been artificially generated.

In addition to the standard execution for Hilog and Prolog code, we also perform a *tabled execution* of both the higher-order and the first-order programs in XSB. Tabling is a standard optimization technique that is widely used in Prolog systems. In this optimization, a re-evaluation of a *tabled predicate* is avoided by memorizing (i.e. remembering) its answers. The XSB system is known for its elaborate and efficient implementation of tabling for first-order logic programs. For higher-order Hilog programs however, XSB's tabling mechanism may not be as effective as it is for first-order ones. The reason is that in order to table any Hilog predicate one has to table all Hilog code. This may lead to high memory consumption, and can be problematic for large-scale program development. We decided to table all predicates of the first-order programs as well, despite the fact that it might have been possible to make a more efficient use of tabling in this case. The idea behind this decision is to enable us to draw a fair comparison between tabled Hilog and tabled Prolog.

Program	Hilog	Prolog			Hilog	Prolog	Program size		
	xsb	xsb	swi	yap	xsb+tab.	xsb+tab.	h.o.	f.o.	Facts
closure	1744.829	17.426	15.813	8.782	16.980	17.067	3	3	1000-8000
closure_1000	12.132	0.801	0.609	0.372	0.872	0.672	3	3	1000
closure_2000	91.284	2.884	2.644	1.332	2.944	3.004	3	3	2000
closure_4000	709.356	11.336	10.918	5.464	10.812	11.076	3	3	4000
closure_6000	2365.728	25.536	23.459	13.532	25.236	25.548	3	3	6000
closure_8000	5545.644	46.576	41.433	23.208	45.036	45.036	3	3	8000
conj5	9.887	1.090	0.026	0.010	2.918	0.571	3	6	1000-8000
genconj(5)	9.921	1.101	0.028	0.011	2.031	0.573	4	4	1000-8000
conj10	21.676	2.414	0.023	0.015	11.741	1.276	3	11	1000 - 8000
genconj(10)	21.580	2.415	0.039	0.013	9.618	1.275	4	4	1000 - 8000
union5	0.035	0.028	0.030	0.023	0.037	0.038	4	10	1000-8000
genunion(5)	0.034	0.030	0.025	0.021	0.050	0.042	5	5	1000-8000
union10	0.063	0.062	0.046	0.036	0.075	0.065	4	20	1000-8000
genunion(10)	0.062	0.079	0.054	0.035	0.091	0.104	5	5	1000-8000
path_dag	971.326	679.557	975.027	54.156	0.001	0.001	6	6	10-80
path_naive	5.725	4.248	6.661	0.407	0.021	0.016	6	6	10-80
winnow	0.147	0.130	0.117	0.039	1.107	1.115	3	3	1000 - 10000
w(2)	3.920	3.257	3.844	0.527	0.168	0.213	10	12	100 - 2000
w(3)	129.457	107.183	122.556	21.103	0.119	0.123	10	12	100-2000
wt(2)	4.146	3.288	3.857	0.530	0.144	0.219	11	13	100 - 2000
wt(3)	130.540	108.048	126.876	21.360	0.100	0.119	11	13	100-2000

Table 1. Experiment results. All execution times are in seconds.

<sup>&</sup>lt;sup>4</sup> version 7.2.3, cf. http://www.swi-prolog.org/.

<sup>&</sup>lt;sup>5</sup> version 6.2.2, cf. http://www.dcc.fc.up.pt/~vsc/Yap/.

Table 1 summarizes the experimental results. The average execution time is depicted in seconds for each program and for each engine. The execution time is measured using the standard time/1 predicate. Apart from the execution time, the table also contains the number of the (non-fact) clauses of the original higher-order program, the number of the (non-fact) clauses of the resulting first-order program after the transformation, and the ranges of the number of the corresponding facts. We do not show the runtime of each transformation from the higher-order to first-order since the execution of process was negligible (e.g. less than 0.01 s in all cases).

Firstly, we observe that the first-order programs are in general much faster than the higher-order ones. Even in the context of XSB which offers a native support of Hilog, the Prolog code is in almost all cases faster than the Hilog code. Especially in the transitive closure and the k-ary conjunction, we have an improvement by one or more of orders of magnitude. In most programs in our experiment, we noticed that the ratio between the execution time of Prolog code and the execution time of Hilog code does not change much if we increase the number of facts, with the exception of the transitive closure benchmark, in which the more we increase the number of facts, the more this ratio decreases. The most important advantage of executing standard Prolog though, is that it allows us to choose from a wide range of available Prolog engines. From the three Prolog engines that we used, YAP is the most performant one. Therefore, we can get a further decrease in execution times by simply choosing a different Prolog engine, a fact that is not possible if we want to execute Hilog code directly.

As we stated earlier, tabling is another standard optimization technique that is widely used in Prolog systems. Tabling was very effective in many cases in the experiment, especially in the preference operations (winnow, w and wt) and in the path programs (notice the dramatic decrease in the execution times for the path\_dag benchmark). It seems that the performance of this optimization offers the same performance gain for both Hilog and Prolog code, since the execution times are in most cases similar. A notable exception is that of the k-ary conjunction benchmark, in which the tabled Prolog code is 5 to 10 times faster than that of the tabled Hilog code. Also, the fact that we table all Hilog and Prolog code did not have much negative effect in our experiment after all, because (with the sole exception of the winnow benchmark) the tabled executions are not slower than their non-tabled counterparts.

Finally, consider the programs that deal with the k-ary conjunction and disjunction, i.e. the pairs conj5 – genconj(5), conj10 – genconj(10), union5 – genunion(5) and union10 – genunion(10). Both programs of each of these pairs are making the same computation, with the former expressed in a non-recursive way and the latter in a recursive way. These programs differ also in the size of their first order counterparts. The first-order form of the non-recursive version has more clauses than the first-order form of the recursive version. We observe that both the higher-order and the first-order versions of the same computation have similar execution times, even though the first-order versions have different numbers of clauses. As a result, an increase on the size of the first-order program did not produce any overhead in the overall program execution time.

#### 8 Related Work

The proposed predicate specialization is closely connected with related work on *partial evaluation* of logic programs [6,9,11]. More specifically, the proposed technique is a special form of partial evaluation which targets higher-order arguments and uses a simple one-step unfolding rule to propagate the constant higher-order arguments without changing the structure of the original program. Consequently, first-order programs remain unchanged. To the extend of our knowledge, partial evaluation techniques have not been previously applied directly to higher-order logic programming with the purpose to produce a simpler first-order program.

Other techniques, however, have been proposed that focus on the removal of higher-order parameters in logic programs. Warren, in one of the early papers that tackle similar issues [21], proposed that simple higher-order structures are non-essential and can be easily encoded as first-order logic programs. The key idea is that every higher-order argument in the program can be encoded as a symbol utilizing its name and a special apply predicate should be introduced to distinguish between different higher-order calls. A very similar approach has been employed in Hilog [4]; a language that offers a higher-order syntax with first-order semantics. A Hilog program is transformed into an equivalent firstorder one using a transformation similar to Warren's technique [21]. Actually, these techniques are closely related to Reynolds' *defunctionalization* [17] that has been originally proposed to remove higher-order arguments in functional programs. These techniques are designed to be applied in arbitrary programs in comparison to our approach. In order to achieve this they require data structures in the resulting program. However, on a theoretical view this imposes the requirement that the target language should support data structures even if the source language does not support that. This is apparent when considering Datalog; transforming a higher-order Datalog program will result into a first-order Prolog program. On a more practical point, the generic data structures introduced during the defunctionalization render the efficient implementation of these programs challenging. The wrapping of the higher-order calls with the generic apply predicate makes it cumbersome to utilize the optimizations in first-order programs such as indexing and tabling. In comparison, our technique produce more natural programs that do not suffer for this phenomenon. Moreover, it does not introduce any data structures and as a result a higher-order Datalog program will be transformed into a first-order one amenable to more efficient implementation.

In order to remedy the shortcomings of defunctionalization there have been proposed some techniques to improve the performance of the transformed programs. [18] proposed a compile-time optimization of the classical Hilog encoding that eliminates some partial applications using a family of apply predicates thus increasing the number of the predicates in the encoded program, which leads to a more efficient execution. The original first-order encoding of Hilog as well as this optimization are included in the XSB system [20]. In the context of functionallogic programming, there exist some mixed approaches that consider defunctionalization together with partial evaluation for functional-logic programs [1,16], where a partial evaluation process is applied in a defunctionalized functionallogic program. Even though these approaches can usually offer a substantial performance improvement, the resulting programs still use a Reynolds' style encoding; for instance, the performance gain of the optimizations offered by XSB is not sufficient when compared to the technique presented in this paper, as presented in Sect. 7.

The process of eliminating higher-order functions is being studied extensively in the functional programming domain. Apart from defunctionalization, there exist some approaches that do not introduce additional data structures while removing higher-order functions. These techniques include the *higher-order* removal method of [5], the firstification technique of [14] and the firstify algorithm of [13]. The removal of higher-order values here is achieved without introducing additional data structures, so the practical outcome is that the resulting programs can be executed in a more efficient way than the original ones. The basic operation of these transformation methods is *function specialization*, which involves generating a new function in which the function-type arguments of the original definition are eliminated. A predicate specialization operation is also the core operation in our approach, so in this point these approaches are similar to ours. The remaining operations that can be found in those approaches (e.g. simplification rules, inlining, eta-abstractions etc.), are either inapplicable to our domain or not needed for our program transformation. Contrary to Reynolds' defunctionalization, these higher-order removal techniques [5, 13, 14] are not complete, meaning that they do not remove all higher-order values from a functional program, and therefore the resulting programs are not always first order. This phenomenon would happen in our case as well if we considered the full power of higher-order programming. However, because of the fact that we focus on a smaller but still useful class of higher-order logic programs, we are sure that the output of our transformation technique will produce a valid first-order program for every program that belongs to our fragment.

#### 9 Conclusions and Future Work

In this paper we presented a program transformation technique that reduces higher-order programs into first-order ones through argument specialization. The transformation does not introduce additional data structures and therefore the resulting programs can be executed efficiently in any standard Prolog system. We do not consider the full power of higher-order logic programming, but we focus on a modest but useful class of programs; in these programs we do not allow partial applications or existential predicate variables in the body of a clause.

In our actual implementation we considered a slightly broader class than the fragment discussed before; we allowed a limited use of partial applications in the case of predicates that do not belong to the same cycle in the predicate dependency graph. This extension however does not increase the expressive power of the language. An interesting open question that arises is whether this technique can be used as a first-order reduction method only for programs that belong to our fragment (or a fragment that have the same expressive power as ours) or if it can be used for a wider class of programs that are more expressive than our fragment. Moreover, any expansion of the supported class would be desirable, even if it has the same expressivity as our current fragment.

Until now, we have used and evaluated our transformation technique only as an optimization method for performance improvement. However, in the functional programming domain, such techniques have been used in additional applications, such as program analysis [13] and implementation of debuggers [15]. Therefore, an interesting aspect for future investigation would be the search of similar or completely new applications of our higher-order removal technique in the logic programming domain.

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# An Assertion Language for Slicing Constraint Logic Languages

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Abstract. Constraint Logic Programming (CLP) is a language scheme for combining two declarative paradigms: constraint solving and logic programming. Concurrent Constraint Programming (CCP) is a declarative model for concurrency where agents interact by telling and asking constraints in a shared store. In a previous paper, we developed a framework for dynamic slicing of CCP where the user first identifies that a (partial) computation is wrong. Then, she marks (selects) some parts of the final state corresponding to the data (constraints) and processes that she wants to study more deeply. An automatic process of slicing begins, and the partial computation is "depurated" by removing irrelevant information. In this paper we give two major contributions. First, we extend the framework to CLP, thus generalizing the previous work. Second, we provide an assertion language suitable for both, CCP and CLP, which allows the user to specify some properties of the computations in her program. If a state in a computation does not satisfy an assertion then some "wrong" information is identified and an automatic slicing process can start. We thus make one step further towards automatizing the slicing process. We show that our framework can be integrated with the previous semi-automatic one, giving the user more choices and flexibility. We show by means of examples and experiments the usefulness of our approach.

**Keywords:** Concurrent Constraint Programming · Constraint Logic Programming · Dynamic slicing · Debugging · Assertion language

# 1 Introduction

Constraint Logic Programming (CLP) is a language scheme [20] for combining two declarative paradigms: constraint solving and logic programming (see an overview in [19]). Concurrent Constraint Programming (CCP) [28] (see a survey in [25]) combines concurrency primitives with the ability to deal with constraints, and hence, with partial information. The notion of concurrency is based upon the

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F. Mesnard and P. J. Stuckey (Eds.): LOPSTR 2018, LNCS 11408, pp. 148–165, 2019. https://doi.org/10.1007/978-3-030-13838-7\_9 shared-variables communication model. CCP is intended for reasoning, modeling and programming concurrent agents (or processes) that interact with each other and their environment by posting and asking information in a medium, a socalled *store*. CCP is a very flexible model and has been applied to an increasing number of different fields such as probabilistic and stochastic, timed and mobile systems [9,26], and more recently to social networks with spatial and epistemic behaviors [25], as well as modeling of biological systems [6,10,11,24].

One crucial problem with constraint logic languages is to define appropriate debugging tools. Various techniques and several frameworks have been proposed for debugging these languages. Abstract interpretation techniques have been considered (e.g. in [12, 13, 16, 17]) as well as (abstract) declarative debuggers following the seminal work of Shapiro [30]. However, these techniques are approximated (case of abstract interpretation) or it can be difficult to apply them when dealing with complex programs (case of declarative debugging) as the user should answer to too many questions.

In this paper we follow a technique inspired by slicing. Slicing was introduced in some pioneer works by Weiser [33]. It was originally defined as a static technique, independent of any particular input of the program. Then, the technique was extended by introducing the so called dynamic program slicing [22]. This technique is useful for simplifying the debugging process, by selecting a portion of the program containing the faulty code. Dynamic program slicing has been applied to several programming paradigms (see [21] for a survey). In the context of constraint logic languages, we defined a tool [15] able to interact with the user and filter, in a given computation, the information which is relevant to a particular observation or result. In other words, the programmer could mark (select) the information (constraints, agents or atoms) that she is interested to check in a particular computation that she suspects to be wrong. Then, a corresponding depurated partial computation is obtained automatically, where only the information relevant to the marked parts is present.

In a previous paper [15] we presented the first formal framework for debugging CCP via dynamic slicing. In this paper we give two major contributions. First, we extend our framework to CLP. Second, we introduce an assertion language which is integrated within the slicing process for automatizing it further. The extension to CLP is not immediate, as while for CCP programs non-deterministic choices give rise to one single computation, in CLP all computations corresponding to different non-deterministic choices can be followed and can lead to different solutions. Hence, some rethinking of the framework is necessary. We show that it is possible to define a transformation from CLP programs to CCP programs, which allows us to show that the set of observables of a CLP program and of its translation to a CCP program correspond. This result also shows that the computations in the two languages are pretty similar and the framework for CCP can be extended to deal with CLP programs.

Our framework [15] consists of three main steps. First the standard operational semantics of the sliced language is extended to an enriched semantics that adds to the standard semantics the needed meta-information for the slicer. Second, we consider several analyses of the faulty situation based on the program wrong behavior, including causality, variable dependencies, unexpected behaviors and store inconsistencies. This second step was left to the user's responsibility: the user had to examine the final state of the faulty computation and manually mark/select a subset of constraints that she wants to study further. The third step is an automatic marking algorithm that removes the information not relevant to derive the constraints selected in the second step. This algorithm is flexible and applicable to timed extensions of CCP [27]. Here, for CLP programs we introduce also the possibility to mark atoms, besides constraints.

We believe that the second step above, namely identifying the right state and the relevant information to be marked, can be difficult for the user and we believe that it is possible to improve automatization of this step. Hence, one major contribution of this paper is to introduce a specialized assertion language which allows the user to state properties of the computations in her program. If a state in a computation does not satisfy an assertion then some "wrong" information is identified and an automatic slicing process can start. We show that assertions can be integrated in our previous semi-automatic framework [15], giving the user more choices and flexibility. The assertion language is a good companion to the already implemented tool for the slicing of CCP programs to automatically detect (possibly) wrong behaviors and stop the computation when needed. The framework can also be applied to timed variants of CCP.

Organization and Contributions. Section 2 describes CCP and CLP and their operational semantics. We introduce a translation from CLP to CCP programs and prove a correspondence theorem between successful computations. In Sect. 3 we recall the slicing technique for CCP [15] and extend it to CLP. The extension of our framework to CLP is our first contribution. As a second major contribution, in Sect. 4 we present our specialized assertion language and describe its main operators and functionalities. In Sect. 4.2 we show some examples to illustrate the expressiveness of our extension, and the integration into the former tool. Within our examples we show how to automatically debug a biochemical system specified in timed CCP and one classical search problem in CLP. Finally, Sect. 5 discusses some related work and concludes.

#### 2 Constraint Logic Languages

In this section we define an operational semantics suitable for both, CLP [19] and CCP programs [28]. We start by defining CCP programs and then we obtain CLP by restricting the set of CCP operators.

Processes in CCP interact with each other by telling and asking constraints (pieces of information) in a common store of partial information. The type of constraints is not fixed but parametric in a constraint system (CS), a central notion for both CCP and CLP. Intuitively, a CS provides a signature from which constraints can be built from basic tokens (e.g., predicate symbols), and two basic operations: conjunction  $\sqcup$  (e.g.,  $x \neq y \sqcup x > 5$ ) and variable hiding  $\exists$  (e.g.,  $\exists x.y = f(x)$ ). As usual,  $\exists x.c$  binds x in c. The CS defines also an entailment

relation ( $\models$ ) specifying inter-dependencies between constraints:  $c \models d$  means that the information d can be deduced from the information c (e.g.,  $x > 42 \models x > 37$ ). We shall use C to denote the set of constraints with typical elements c, c', d, d'.... We assume that there exist  $t, f \in C$ , such that for any  $c \in C$ ,  $c \models t$  and  $f \models C$ . The reader may refer to [25] for different formalizations and examples of constraint systems.

The Language of CCP Processes. In process calculi, the language of processes in CCP is given by a small number of primitive operators or combinators. Processes are built from constraints in the underlying constraint and:  $P, Q ::= \mathbf{skip} | \mathbf{tell}(c) | \sum_{i \in I} \mathbf{ask} (c_i) \mathbf{then} P_i | P || Q | (\mathbf{local} x) P | p(\overline{x})$ 

The process **skip** represents inaction. The process **tell**(c) adds c to the current store d producing the new store  $c \sqcup d$ . Given a non-empty finite set of indexes I, the process  $\sum_{i \in I} \mathbf{ask} (c_i)$  then  $P_i$  non-deterministically chooses  $P_k$  for execution if the store entails  $c_k$ . The chosen alternative, if any, precludes the others. This provides a powerful synchronization mechanism based on constraint entailment. When I is a singleton, we shall omit the " $\Sigma$ " and we simply write

ask (c) then P.

The process  $P \parallel Q$  represents the parallel (interleaved) execution of P and Q and  $(\mathbf{local} x) P$  behaves as P and binds the variable x to be local to it.

Given a process definition  $p(\overline{y}) \stackrel{\Delta}{=} P$ , where all free variables of P are in the set of pairwise distinct variables  $\overline{y}$ , the process  $p(\overline{x})$  evolves into  $P[\overline{x}/\overline{y}]$ . A CCP program takes the form  $\mathcal{D}.P$  where  $\mathcal{D}$  is a set of process definitions and P is a process.

The Structural Operational Semantics (SOS) of CCP is given by the transition relation  $\gamma \longrightarrow \gamma'$  satisfying the rules in Fig. 1. Here we follow the formulation in [14] where the local variables created by the program appear explicitly in the transition system and parallel composition of agents is identified by a multiset of agents. More precisely, a *configuration*  $\gamma$  is a triple of the form  $(X; \Gamma; c)$ , where cis a constraint representing the store,  $\Gamma$  is a multiset of processes, and X is a set of hidden (local) variables of c and  $\Gamma$ . The multiset  $\Gamma = P_1, P_2, \ldots, P_n$  represents the process  $P_1 \parallel P_2 \parallel \cdots \parallel P_n$ . We shall indistinguishably use both notations to denote parallel composition. Moreover, processes are quotiented by a structural congruence relation  $\cong$  satisfying: (STR1)  $P \cong Q$  if P and Q differ only by a renaming of bound variables (alpha conversion); (STR2)  $P \parallel Q \cong Q \parallel P$ ; (STR3)  $P \parallel (Q \parallel R) \cong (P \parallel Q) \parallel R$ ; (STR4)  $P \parallel$  **skip**  $\cong P$ . We denote by  $\longrightarrow^*$ the reflexive and transitive closure of a binary relation  $\longrightarrow$ .

**Definition 1** (Observables and traces). A trace  $\gamma_1\gamma_2\gamma_3\cdots$  is a sequence of configurations s.t.  $\gamma_1 \longrightarrow \gamma_2 \longrightarrow \gamma_3\cdots$ . We shall use  $\pi, \pi'$  to denote traces and  $\pi(i)$  to denote the *i*-th element in  $\pi$ . If  $(X; \Gamma; d) \longrightarrow^* (X'; \Gamma'; d')$  and  $\exists X'.d' \models c$  we write  $(X; \Gamma; d) \Downarrow_c$ . If  $X = \emptyset$  and d = t we simply write  $\Gamma \Downarrow_c$ .

Intuitively, if P is a process then  $P \Downarrow_c$  says that P can reach a store d strong enough to entail c, *i.e.*, c is an output of P. Note that the variables in X' above are hidden from d' since the information about them is not observable.

$$\frac{d \models c_k \quad k \in I}{(X; \mathbf{tell}(c), \Gamma; d) \longrightarrow (X; \mathbf{skip}, \Gamma; c \sqcup d)} \xrightarrow{\mathbf{R}_{\text{TELL}}} \frac{d \models c_k \quad k \in I}{(X; \sum_{i \in I} \mathbf{ask} \ (c_i) \ \mathbf{then} \ P_i, \Gamma; d) \longrightarrow (X; P_k, \Gamma; d)} \xrightarrow{\mathbf{R}_{\text{SUM}}} \frac{x \notin X \cup fv(d) \cup fv(\Gamma)}{(X; (\mathbf{local} \ x) \ P, \Gamma; d) \longrightarrow (X \cup \{x\}; P, \Gamma; d)} \xrightarrow{\mathbf{R}_{\text{LOC}}} \frac{p(\overline{y}) \stackrel{\Delta}{=} P \in \mathcal{D}}{(X; p(\overline{x}), \Gamma; d) \longrightarrow (X; P[\overline{x}/\overline{y}], \Gamma; d)} \xrightarrow{\mathbf{R}_{\text{CALL}}} \frac{(X; \Gamma; c) \cong (X'; \Gamma'; c') \longrightarrow (Y'; \Delta'; d') \cong (Y; \Delta; d)}{(X; \Gamma; c) \longrightarrow (Y; \Delta; d)} \xrightarrow{\mathbf{R}_{\text{EQUIV}}}$$

Fig. 1. Operational semantics for CCP calculi

#### 2.1 The Language of CLP

A CLP program [20] is a finite set of rules of the form

$$p(\overline{x}) \leftarrow A_1, \dots, A_n$$

where  $A_1, \ldots, A_n$ , with  $n \ge 0$ , are literals, i.e. either atoms or constraints in the underlying constraint system C, and  $p(\overline{x})$  is an atom. An atom has the form  $p(t_1, \ldots, t_m)$ , where p is a user defined predicate symbol and the  $t_i$  are terms from the constraint domain.

The top-down operational semantics is given by derivations from goals [20]. A configuration takes the form  $(\Gamma; c)$  where  $\Gamma$  (a goal) is a multiset of literals and c is a constraint (the current store). The reduction relation is as follows.

**Definition 2 (Semantics of CLP** [20]). Let  $\mathcal{H}$  be a CLP program. A configuration  $\gamma = (L_1, ..., L_i, ..., L_n; c)$  reduces to  $\psi$ , notation  $\gamma \longrightarrow_{CLP(\mathcal{H})} \psi$ , by selecting and removing a literal  $L_i$  and then:

- 1. If  $L_i$  is a constraint d and  $d \sqcup c \neq f$ , then  $\gamma \longrightarrow_{CLP(\mathcal{H})} (L_1, ..., L_n; c \sqcup d)$ .
- 2. If  $L_i$  is a constraint d and  $d \sqcup c = f$  (i.e., the conjunction of c and d is inconsistent), then  $\gamma \longrightarrow_{CLP(\mathcal{H})} (\Box; f)$  where  $\Box$  represents the empty multiset.
- 3. If  $L_i$  is an atom  $p(t_1, ..., t_k)$ , then  $\gamma \longrightarrow_{CLP(\mathcal{H})} (L_1, ..., L_{i-1}, \Delta, L_{i+1}, ..., L_n; c)$ where one of the definitions for  $p, p(s_1, ..., s_k) \leftarrow A_1, ..., A_n$ , is selected and  $\Delta = A_1, ..., A_n, s_1 = t_1, ..., s_k = t_k$ .

A computation from a goal G is a (possibly infinite) sequence  $\gamma_1 = (G; t) \longrightarrow_{CLP(\mathcal{H})} \gamma_2 \longrightarrow_{CLP(\mathcal{H})} \cdots$ . We say that a computation finishes if the last configuration  $\gamma_n$  cannot be reduced, i.e.,  $\gamma_n = (\Box; c)$ . In this case, if c = f then the derivation fails otherwise we say that it succeeds.

Given a goal with free variables  $\overline{x} = var(G)$ , we shall also use the notation  $G \Downarrow_c^{\mathcal{H}}$  to denote that there is a successful computation  $(G; t) \longrightarrow_{CLP(\mathcal{H})}^* (\Box; d)$  s.t.  $\exists \overline{x}.d \models c$ . We note that the free variables of a goal are progressively "instantiated" during computations by adding new constraints. Finally, the answers of a goal G, notation  $G \Downarrow^{\mathcal{H}}$  is the set  $\{\exists_{var(c)\setminus var(G)}(c) \mid (G; t) \longrightarrow_{CLP(\mathcal{H})}^* (\Box; c), c \neq f\}$  where " $\setminus$ " denotes set difference.

From CLP to CCP. CCP is a very general paradigm that extends both Concurrent Logic Programming and Constraint Logic Programming [23]. However, in CLP, we have to consider non-determinism of the type "don't know" [29], which means that each predicate call can be reduced by using each rule which defines such a predicate. This is different from the kind of non-determinism in CCP, where the choice operator selects randomly one of the choices whose ask guard is entailed by the constraints in the current store (see  $R_{SUM}$  in Fig. 1).

It turns out that by restricting the syntax of CCP and giving an alternative interpretation to non-deterministic choices, we can have an encoding of CLP programs as CCP agents. More precisely, we shall remove the synchronization operator and we shall consider only blind choices of the form  $Q = \sum_{i \in I} \mathbf{ask}(t)$  then  $P_i$ .

Note that  $c \models t$  for any c and then, the choices in Q are not guarded/constrained. Hence, any of the  $P_i$  can be executed regardless of the current store. This mimics the behavior of CLP predicates (see (3) in Definition 2), but with a different kind of non-determinism. The next definition formalizes this idea.

**Definition 3 (Translation).** Let C be a const. system,  $\mathcal{H}$  be a CLP program and G be a goal. We define the set of CCP process definitions  $\llbracket \mathcal{H} \rrbracket = \mathcal{D}$  as follows. For each user defined predicate symbol p of arity j and 1...m defined rules of the form  $p(t_1^i, ..., t_j^i) \leftarrow A_1^i, ..., A_{n_i}^i$ , we add to  $\mathcal{D}$  the following process definition  $p(x_1, ..., x_j) \stackrel{\Delta}{=} \mathbf{ask}$  (t) then ((local  $\overline{z_1}$ )  $\prod D_1 \parallel \llbracket A_1^1 \rrbracket \parallel \cdots \parallel \llbracket A_{n_1}^1 \rrbracket$ )  $+ ... + \mathbf{ask}$  (t) then ((local  $\overline{z_m}$ )  $\prod D_m \parallel \llbracket A_1^m \rrbracket \parallel \cdots \parallel \llbracket A_{n_m}^m \rrbracket$ )

where  $\overline{z_i} = var(t_1^i, ..., t_j^i) \cup var(A_1^i, ..., A_{n_i}^i)$ ,  $D_i$  is the set of constraints  $\{x_1 = t_1^i, ..., x_j = t_j^i\}$ ,  $\prod D_i$  means  $\mathbf{tell}(x_1 = t_1^i) \parallel \cdots \parallel \mathbf{tell}(x_j = t_j^i)$  and literals are translated as  $\llbracket A(\overline{t}) \rrbracket = A(\overline{t})$  (case of atoms) and  $\llbracket c \rrbracket = \mathbf{tell}(c)$  (case of constraints). Moreover, we translate the goal  $\llbracket A_1, ..., A_n \rrbracket$  as  $\llbracket A_1 \rrbracket \parallel \cdots \parallel \llbracket A_n \rrbracket$ .

The head  $p(\overline{x})$  of a definition  $p(\overline{x}) \stackrel{\Delta}{=} P$  in CCP can only have variables while a head of a CLP rule  $p(\overline{t}) \leftarrow B$  may have arbitrary terms with (free) variables. Moreover, in CLP, each call to a predicate returns a variant with distinct new variables (renaming the parameters of the predicate) [20]. These two features of CLP can be encoded in CCP by first introducing local variables ((**local**  $z_i$ ) in the above definition) and then, using constraints ( $D_i$ ) to establish the connection between the formal and the actual parameters of the process definition.

Consider for instance this simple CLP program dealing with lists:

p([] , []) . p([H1 | L1] , [H2 | L2]) :- c(H1,H2), p(L1,L2) .

and its translation

$$p(x,y) \stackrel{\Delta}{=} \mathbf{ask} \ (\mathtt{t}) \ \mathbf{then} \ (\mathbf{tell}(x = []) \parallel \mathbf{tell}(y = [])) \\ + \mathbf{ask} \ (\mathtt{t}) \ \mathbf{then} \ (\mathbf{local} \ X) \ (\prod D \parallel c(H1, H2) \parallel p(L1, L2))$$

where  $D = \{x = [H1|L1], y = [H2|L2]\}$  and  $X = \{H1, H2, L1, L2\}$ . Note that the CCP process  $p(l_a, l_b)$  can lead to 2 possible outcomes:

- Using the first branch, the store becomes  $l_a = [] \sqcup l_b = []$ .

- In the second branch, due to rule  $R_{LOC}$ , four local distinct variables are created (say h1, h2, l1, l2), the store becomes  $l_a = [h1|l1] \sqcup l_b = [h2|l2] \sqcup c(h1, h2)$  and the process p(l1, l2) is executed on this new store.

These CCP executions match exactly the behavior of the CLP goal p(LA, LB). We emphasize that one execution of a CCP program will give rise to a single computation (due to the kind of non-determinism in CCP) while the CLP computation model characterizes the set of all possible successful derivations and corresponding answers. In other terms, for a given initial goal G, the CLP model defines the full set of answer constraints for G, while the CCP translation will compute only one of them, as only one possible derivation will be followed.

**Theorem 1 (Adequacy).** Let C be a constraint system,  $c \in C$ ,  $\mathcal{H}$  be a CLP program and G be a goal. Then,  $G \Downarrow_c^{\mathcal{H}}$  iff  $\llbracket G \rrbracket \Downarrow_c$ .

#### 3 Slicing CCP and CLP Programs

Dynamic slicing is a technique that helps the user to debug her program by simplifying a partial execution trace, thus depurating it from parts which are irrelevant to find the bug. It can also help to highlight parts of the programs which have been wrongly ignored by the execution of a wrong piece of code. In [15] we defined a slicing technique for CCP programs that consisted of three main steps:

- **S1** Generating a (finite) trace of the program. For that, a new semantics is needed in order to generate the (meta) information needed for the slicer.
- **S2** Marking the final store, to select some of the constraints that, according to the wrong behavior detected, should or should not be in the final store.
- **S3** Computing the trace slice, to select the processes and constraints that were relevant to produce the (marked) final store.

We shall briefly recall the step S1 in [15] which remains the same here. Steps S2 and S3 need further adjustments to deal with CLP programs. In particular, we shall allow the user to select processes (literals in the CLP terminology) in order to start the slicing. Moreover, in Sect. 4, we provide further tools to automatize the slicing process.

Enriched Semantics (Step S1). The slicing process requires some extra information from the execution of the processes. More precisely, (1) in each operational step  $\gamma \to \gamma'$ , we need to highlight the process that was reduced; and (2) the constraints accumulated in the store must reflect, exactly, the contribution of each process to the store. In order to solve (1) and (2), we introduced in [15] the enriched semantics that extracts the needed meta information for the slicer. Roughly, we identify the parallel composition  $Q = P_1 \parallel \cdots \parallel P_n$  with the sequence  $\Gamma_Q = P_1:i_1, \cdots, P_n:i_n$  where  $i_j \in \mathbb{N}$  is a unique identifier for  $P_j$ . The use of indexes allow us to distinguish, e.g., the three different occurrences of P in " $\Gamma_1, P:i, \Gamma_2, P:j$ , (ask (c) then P): k". The enriched semantics uses transitions with labels of the form  $\xrightarrow{[i]_k}$  where *i* is the identifier of the reduced process and k can be either  $\perp$  (undefined) or a natural number indicating the branch chosen in a non-deterministic choice (Rule  $R'_{SUM}$ ). This allows us to identify, unequivocally, the selected alternative in an execution. Finally, the store in the enriched semantics is not a constraint (as in Fig. 1) but a set of (atomic) constraints where  $\{d_1, \dots, d_n\}$  represents the store  $d_1 \sqcup \cdots \sqcup d_n$ . For that, the rule of **tell**(*c*) first decomposes *c* in its atomic components before adding them to the store.

Marking the Store (Step S2). In [15] we identified several alternatives for marking the final store in order to indicate the information that is relevant to the slice that the programmer wants to recompute. Let us suppose that the final configuration in a partial computation is  $(X; \Gamma; S)$ . The user has to select a subset  $S_{sliced}$  of the final store S that may explain the (wrong) behavior of the program.  $S_{sliced}$  can be chosen based on the following criteria:

- 1. Causality: the user identifies, according to her knowledge, a subset  $S' \subseteq S$  that needs to be explained (i.e., we need to identify the processes that produced S').
- 2. Variable Dependencies: The user may identify a set of relevant variables  $V \subseteq freeVars(S)$  and then, we mark  $S_{sliced} = \{c \in S \mid vars(c) \cap V \neq \emptyset\}$ .
- 3. Unexpected behaviors: there is a constraint c entailed from the final store that is not expected from the intended behavior of the program. Then, one would be interested in the following marking  $S_{sliced} = \bigcup \{S' \subseteq S \mid \bigcup S' \models c \text{ and } S' \text{ is set minimal}\}$ , where "S' is set minimal" means that for any  $S'' \subseteq S', S'' \not\models c$ .
- 4. Inconsistent output: The final store should be consistent with respect to a given specification (constraint) c, i.e., S in conjunction with c must not be inconsistent. In this case, we have  $S_{sliced} = \bigcup \{S' \subseteq S \mid \bigsqcup S' \sqcup c \models f \text{ and } S' \text{ is set minimal} \}.$

For the analysis of CLP programs, it is important also to mark literals (i.e., calls to procedures in CCP). In particular, the programmer may find that a particular goal p(x) is not correct if the parameter x does not satisfy certain conditions/constraints. Hence, we shall consider also markings on the set of processes, i.e., the marking can be also a subset  $\Gamma_{sliced} \subseteq \Gamma$ .

**Trace Slice (Step S3).** Starting from the pair  $\gamma_{sliced} = (S_{sliced}, \Gamma_{sliced})$  denoting the user's marking, we define a backward slicing step. Roughly, this step allows us to eliminate from the execution trace all the information not related to  $\gamma_{sliced}$ . For that, the fresh constant symbol • is used to denote an "irrelevant" constraint or process. Then, for instance, " $c \sqcup \bullet$ " results from a constraint  $c \sqcup d$  where d is irrelevant. Similarly in processes as, e.g., **ask** (c) **then**  $(P \parallel \bullet) + \bullet$ . A replacement is either a pair of the shape [T/i] or [T/c]. In the first (resp. second) case, the process with identifier i (resp. constraint c) is replaced with T. We shall use  $\theta$  to denote a set of replacements and we call these sets as "replacing substitutions". The composition of replacing substitutions  $\theta_1$  and  $\theta_2$  is given by the set union of  $\theta_1$  and  $\theta_2$ , and is denoted as  $\theta_1 \circ \theta_2$ .

```
Input: - a trace \gamma_0 \xrightarrow{[i_1]_{k_1}} \cdots \xrightarrow{[i_n]_{k_n}} \gamma_n where \gamma_i = (X_i; \Gamma_i; S_i)
                   - a marking (S_{sliced}, \Gamma_{sliced}) s.t. S_{sliced} \subseteq S_n and \Gamma_{sliced} \subseteq \Gamma_n
      Output: a sliced trace \gamma'_0 \longrightarrow \cdots \longrightarrow \gamma'_n
 1 begin
              let \theta = \{ [\bullet/i] \mid P : i \in \Gamma_n \setminus \Gamma_s \} in
 2
 3
               \gamma'_n \leftarrow (X_n \cap vars(S_{sliced}, \Gamma_{sliced}); \Gamma_n \theta; S_{sliced});
               for l = n - 1 to 0 do
  4
                     \operatorname{let}\langle\theta',c\rangle = sliceProcess(\gamma_l,\gamma_{l+1},i_{l+1},k_{l+1},\theta,S_l) in
 5
 6
                       S_{sliced} \leftarrow S_{sliced} \cup S_{minimal}(S_l, c)
 7
                       \theta \leftarrow \theta' \circ \theta
                      \gamma'_{l} \leftarrow (X_{l} \cap vars(S_{sliced}, \Gamma_{sliced}) ; \Gamma_{l}\theta ; S_{l} \cap S_{sliced})
 8
              \mathbf{end}
  9
10 end
```

```
Algorithm 1. Trace Slicer. S_{minimal}(S,c) = \emptyset if c = t; otherwise, S_{minimal}(S,c) = \bigcup \{ S' \subseteq S \mid \bigsqcup S' \models c \text{ and } S' \text{ is set minimal} \}.
```

Algorithm 1 extends the one in [15] to deal with the marking on processes  $(\Gamma_{sliced})$ . The last configuration  $(\gamma'_n \text{ in line 3})$  means that we only observe the local variables of interest, i.e., those in  $vars(S_{sliced}, \Gamma_{sliced})$  as well as the relevant processes  $(\Gamma_{sliced})$  and constraints  $(S_{sliced})$ . The algorithm backwardly computes the slicing by accumulating replacing pairs in  $\theta$  (line 7). The new replacing substitutions are computed by the function *sliceProcess* that returns both, a replacement substitution and a constraint needed in the case of ask agents as explained below.

```
1 Function sliceProcess(\gamma, \psi, i, k, \theta, S)
               let \gamma = (X_{\gamma}; \Gamma, P: i, \Gamma'; S_{\gamma}) and \psi = (X_{\psi}; \Gamma, \Gamma_Q, \Gamma'; S_{\psi}) in
 2
               match P with
 з
                      case tell(c) do
  4
  5
                              let c' = sliceConstraints(X_{\gamma}, X_{\psi}, S_{\gamma}, S_{\psi}, S) in
                              if c' = \bullet or c' = \exists \overline{x} \bullet then return \langle [\bullet/i], t \rangle else return \langle [tell(c')/i], t \rangle;
  6
  7
                      case \sum ask (c_l) then Q_l do
  8
                              if \Gamma_Q \theta = \bullet then return \langle [\bullet/i], t \rangle else return
                                 \langle [\mathbf{ask} \ (c_k) \ \mathbf{then} \ (\Gamma_Q \theta) + \bullet \ / \ i], c_k \rangle;
                      case (\operatorname{local} x) Q do
 9
                              let \{x'\} = X_{\psi} \setminus X_{\gamma} in
10
                              if \Gamma_Q[x'/x]\theta = \bullet then return \langle [\bullet/i], t \rangle else return
11
                                 \langle [(\mathbf{local} x') \Gamma_Q[x'/x]\theta/i], t \rangle;
                      case p(\overline{y}) do
12
13
                         if \Gamma_Q \theta = \bullet then return \langle [\bullet/i], t \rangle else return \langle \emptyset, t \rangle;
14
              end
15 end
16 Function sliceConstraints(X_{\gamma}, X_{\psi}, S_{\gamma}, S_{\psi}, S)
              let S_c = S_{\psi} \setminus S_{\gamma} and \theta = \emptyset in
foreach c_a \in S_c \setminus S do \theta \leftarrow \theta \circ [\bullet/c_a];
17
18
              return \exists_{X_{\psi} \setminus X_{\gamma}} . \bigsqcup S_c \theta
19
20 end
```

```
Algorithm 2. Slicing processes and constraints
```

Marking Algorithms. Let us explain how the function *sliceProcess* works. Consider for instance the process  $Q = (\mathbf{ask} (c') \mathbf{then} P) + (\mathbf{ask} (c) \mathbf{then} \mathbf{tell}(d \sqcup e))$  and assume that we are backwardly slicing the trace  $\cdots \gamma \xrightarrow{[i]_2} \cdots \psi \xrightarrow{[j]} \rho \cdots$  where Q (identified with i) is reduced in  $\gamma$  by choosing the second branch and, in  $\psi$ , the tell agent  $\operatorname{tell}(d \sqcup e)$  (identified by j) is executed. Assume that the configuration  $\rho$  has already been sliced and d was considered irrelevant and removed (see  $S_l \cap S_{sliced}$  in line 8 of Algorithm 1). The procedure sliceProcessis applied to  $\psi$  and it determines that only e is relevant in  $\operatorname{tell}(d \sqcup e)$ . Hence, the replacement  $[\operatorname{tell}(\bullet \sqcup e)/j]$  is returned (see line 7 in Algorithm 1). The procedure is then applied to  $\gamma$ . We already know that the ask agent Q is (partially) relevant since  $\operatorname{tell}(d \sqcup e)\theta \neq \bullet$  (i.e., the selected branch does contribute to the final result). Thus, the replacement  $[\bullet + \operatorname{ask}(c) \operatorname{then} \operatorname{tell}(\bullet \sqcup e)/i]$  is accumulated in order to show that the first branch is irrelevant. Moreover, since the entailment of cwas necessary for the reduction, the procedure returns also the constraint c (line 5 of Algorithm 1) and the constraints needed to entail c are added to the set of relevant constraints (line 6 of Algorithm 1).

Example 1. Consider the following (wrong) CLP program:

length([],0).
length([A | L],M) :- M = N, length(L, N).

The translation to CCP is similar to the example in Sect. 2.1. An excerpt of a possible trace for the execution of the goal length([10,20], Ans). is

```
[0 ; length([10,20],Ans) ; t] -->
[0 ; leagth([10,20],Ans) ; t] ->
[0 ; leagth(... ; t] ->
[0 ; leagth(... ; t] ->
[1 LL NI M1 ; [10,20]= [H1|L1] || Ans=N1 || N1=M1 || length(L1, M1) ; t] ->
...
[... H2 L2 N2 M2 ; [20]=[H2 |L2] || M1=M2 || N2=M2 || length(L2, M2) ; [10,20]= [H1|L1], Ans=N1, N1=M1] ->
[... H2 L2 N2 M2 ; M1=N2 || N2=M2 || length(L2, M2) ; [10,20]= [H1|L1], Ans=N1, N1=M1] ->
...
[... H2 L2 N2 M2 ; M1=N2 || N2=M2 || length(L2, M2) ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2]] ->
...
[... H2 L2 N2 M2 ; M2=0 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; M2=0 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, M2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 M2 ; [10,20]= [H1|L1], Ans=N1, N1=M1, [20]=[H2 |L2], M1=N2, N2=M2, L2=[]] ->
[... H2 L2 N2 H2 [N2=[]] ->
[... H2 L2 N2 H2 [N2=[]] ->
[... H2 L2 N2 [N2
```

Here, we can see how the calls to the process definition length are unfolded and, in each state, new constraints are added. Those constraint relate, e.g., the variable Ans and the local variables created in each invocation (e.g., M1 and M2).

In the last configuration, it is possible to mark only the equalities dealing with numerical expressions (i.e., Ans=N1,N1=M1,M1=N2,N2=M2,M2=0) and the resulting trace will abstract away from all the constraints and processes dealing with equalities on lists:

```
[0 ; length([10,20],Ans) ; t] -->
[0 ; * + ask() ... ; t] ->
[0 ; local... ; t] ->
[N1 M1 ; * || Ans=N1 || N1=M1 || length(L1, M1) ; t] ->
[N1 M1 ; Ans=N1 || N1=M1 || length(L1, M1) ; ] ->
[N1 M1 ; N1=M1 || length(L1, M1) ; Ans=N1] ->
[N1 M1 ; length(L1, M1) ; Ans=N1, N1=M1] ->
...
```

The fourth line should be useful to discover that Ans cannot be equal to M1 (the parameter used in the second invocation to length).

# 4 An Assertion Language for Logic Programs

The declarative flavor of programming with constraints in CCP and CLP allows the user to reason about (partial) invariants that must hold during the execution of her programs. In this section we give a simple yet powerful language of assertion to state such invariants. Then, we give a step further in automatizing the process of debugging.

**Definition 4 (Assertion Language).** Assertions are built from:  $F ::= pos(c) \mid neg(c) \mid cons(c) \mid icons(c) \mid F \oplus F \mid p(\overline{x})[F] \mid p(\overline{x})\langle F \rangle$ where c is a constraint  $(c \in C)$ ,  $p(\cdot)$  is a process name and  $\oplus \in \{\land, \lor, \rightarrow\}$ .

The first four constructs deal with partial assertions about the current store. These constructs check, respectively, whether the constraint c: (1) is entailed, (2) is not entailed, (3) is consistent wrt the current store or (4) leads to an inconsistency when added to the current store. Assertions of the form  $F \oplus F$ have the usual meaning. The assertions  $p(\bar{x})[F]$  states that all instances of the form  $p(\bar{t})$  in the current configuration must satisfy the assertion F. The assertions  $p(\bar{x})\langle F \rangle$  is similar to the previous one but it checks for the existence of an instance  $p(\bar{t})$  that satisfies the assertion F.

Let  $\pi(i) = (X_i; \Gamma_i; S_i)$ . We shall use  $store(\pi(i))$  to denote the constraint  $\exists X_i \sqcup S_i$  and  $procs(\pi(i))$  to denote the sequence of processes  $\Gamma_i$ . The semantics for assertions is formalized next.

**Definition 5 (Semantics).** Let  $\pi$  be a sequence of configurations and F be an assertion. We inductively define  $\pi, i \models_{\mathcal{F}} F$  (read as  $\pi$  satisfies the formula F at position i) as:

- $\pi, i \models_{\mathcal{F}} pos(c)$  if  $store(\pi(i)) \models c$ .
- $-\pi, i \models_{\mathcal{F}} neg(c) \text{ if } store(\pi(i)) \not\models c.$
- $-\pi, i \models_{\mathcal{F}} cons(c) \text{ if } store(\pi(i)) \sqcup c \not\models f.$
- $-\pi, i \models_{\mathcal{F}} icons(c) \text{ if } store(\pi(i)) \sqcup c \models f.$
- $-\pi, i \models_{\mathcal{F}} F \land G \text{ if } \pi, i \models_{\mathcal{F}} F \text{ and } \pi, i \models_{\mathcal{F}} G.$
- $-\pi, i \models_{\mathcal{F}} F \lor G \text{ if } \pi, i \models_{\mathcal{F}} F \text{ or } \pi, i \models_{\mathcal{F}} G.$
- $-\pi, i \models_{\mathcal{F}} F \to G \text{ if } \pi, i \models_{\mathcal{F}} F \text{ implies } \pi, i \models_{\mathcal{F}} G.$
- $-\pi, i \models_{\mathcal{F}} p(\overline{x})[F] \text{ if for all } p(\overline{t}) \in procs(\pi(i)), \ \pi, i \models_{\mathcal{F}} F[\overline{t}/\overline{x}].$
- $-\pi, i \models_{\mathcal{F}} p(\overline{x}) \langle F \rangle \text{ if there exists } p(\overline{t}) \in procs(\pi(i)), \ \pi, i \models_{\mathcal{F}} F[\overline{t}/\overline{x}].$

If it is not the case that  $\pi, i \models_{\mathcal{F}} F$ , then we say that F does not hold at  $\pi(i)$  and we write  $\pi(i) \not\models_{\mathcal{F}} F$ .

The above definition is quite standard and reflects the intuitions given above. Moreover, let us define  $\sim F$  as  $\sim \text{pos}(c) = \text{neg}(c)$  (and vice-versa),  $\sim \text{cons}(c) = \text{icons}(c)$  (and vice-versa),  $\sim (F \oplus F)$  as usual and  $\sim p(\overline{x})[F(\overline{x})] = p(\overline{x})\langle \sim F(\overline{x}) \rangle$  (and vice-versa). Note that,  $\pi(i) \models_{\mathcal{F}} F$  iff  $\pi(i) \not\models_{\mathcal{F}} \sim F$ .

*Example 2.* Assume that the store in  $\pi(1)$  is  $S = x \in 0..10$ . Then,

- $-\pi, 1 \models_{\mathcal{F}} \operatorname{cons}(x=5)$ , i.e., the store is consistent wrt the specification x=5.
- $-\pi, 1 \not\models_{\mathcal{F}} \mathbf{icons}(x=5)$ , i.e., the store is not inconsistent wrt the specification x=5.
- $-\pi, 1 \not\models_{\mathcal{F}} \mathsf{pos}(x=5)$ , i.e., the store is not "strong enough" in order to satisfy the specification x=5.

 $-\pi, 1 \models_{\mathcal{F}} \operatorname{neg}(x=5)$ , i.e., store is "consistent enough" to guarantee that it is not the case that x = 5.

Note that  $\pi, i \models_{\mathcal{F}} \mathsf{pos}(c)$  implies  $\pi, i \models_{\mathcal{F}} \mathsf{cons}(c)$ . However, the other direction is in general not true (as shown above). We note that CCP and CLP are monotonic in the sense that when the store c evolves into d, it must be the case that  $d \models c$ (i.e., information is monotonically accumulated). Hence,  $\pi, i \models pos(c)$  implies  $\pi, i+j \models \mathsf{pos}(c)$ . Finally, if the store becomes inconsistent,  $\mathsf{cons}(c)$  does not hold for any c. Temporal [23] and linear [14] variants of CCP remove such restriction on monotonicity.

Checking assertions amounts, roughly, to testing the entailment relation in the underlying constraint system. Checking entailments is the basic operation CCP agents perform. Hence, from the implementation point of view, verification of assertions does not introduce a significant extra computational cost.

Example 3 (Conditional assertions). Let us introduce some patterns of assertions useful for verification.

- Conditional constraints: The assertion  $pos(c) \rightarrow F$  checks for F only if c can be deduced from the store. For instance, the assertion  $pos(c) \rightarrow neg(d)$  says that d must not be deduced when the store implies c.
- Conditional predicates: Let  $G = p(\overline{x}) \langle \mathsf{cons}(t) \rangle$ . The assertion  $G \to F$  states that F must be verified whenever there is a call/goal of the form  $p(\bar{t})$  in the context. Moreover,  $(\sim G) \rightarrow F$  verifies F when there are no calls of the form  $p(\bar{t})$  in the context.

#### 4.1**Dynamic Slicing with Assertions**

Assertions allow the user to specify conditions that her program must satisfy during execution. If this is not the case, the program should stop and start the debugging process. In fact, the assertions may help to give a suitable marking pair  $(S_{sliced}, \Gamma_{sliced})$  for the step **S2** of our algorithm as follows.

**Definition 6.** Let F be an assertion,  $\pi$  be a partial computation, n > 0 and assume that  $\pi, n \not\models_{\mathcal{F}} F$ , i.e.,  $\pi(n)$  fails to establish the assertion F. Let  $\pi(n) =$  $(X; \Gamma; S)$ . As testing hypotheses, we define  $symp(\pi, F, n) = (S_{sl}, \Gamma_{sl})$  where

- 1. If F = pos(c) then  $S_{sl} = \{d \in S \mid vars(d) \cap vars(c) \neq \emptyset\}, \Gamma_{sl} = \emptyset$ .
- 2. If  $F = \operatorname{neg}(c)$  then  $S_{sl} = \bigcup \{S' \subseteq S \mid \bigsqcup S' \models c \text{ and } S' \text{ is set min.}\}, \Gamma_{sl} = \emptyset$ 3. If  $F = \operatorname{cons}(c)$  then  $S_{sl} = \bigcup \{S' \subseteq S \mid \bigsqcup S' \sqcup c \models f \text{ and } S' \text{ is set minimal}\},$  $\Gamma_{sl} = \emptyset.$
- 4. If F = icons(c)  $S_{sl} = \{d \in S \mid vars(d) \cap vars(c) \neq \emptyset\}$  and  $\Gamma_{sl} = \emptyset$ .
- 5. If  $F = F_1 \wedge F_2$  then  $symp(\pi, F_1, n) \cup symp(\pi, F_2, n)$ .
- 6. If  $F = F_1 \lor F_2$  then  $symp(\pi, F_1, n) \cap symp(\pi, F_2, n)$ .
- 7. If  $F = F_1 \rightarrow F_2$  then  $symp(\pi, \sim F_1, n) \cup symp(\pi, F_2, n)$ .
- 8. If  $F = p(\overline{x})[F_1]$  then  $S_{sl} = \emptyset$  and  $\Gamma_{sl} = \{p(\overline{t}) \in \Gamma \mid \pi, n \not\models_{\mathcal{F}} F_1[\overline{t}/\overline{x}]\}.$
- 9. If  $F = p(\overline{x})\langle F_1 \rangle$  then  $S_{sl} = \{d \in S \mid vars(d) \cap vars(F_1) \neq \emptyset\}, \Gamma_{sl} =$  $\{p(\bar{t}) \in \Gamma\}$

Let us give some intuitions about the above definition. Consider a (partial) computation  $\pi$  of length n where  $\pi(n) \not\models_{\mathcal{F}} F$ . In the case (1) above, c must be entailed but the current store is not strong enough to do it. A good guess is to start examining the processes that added constraints using the same variables as in c. It may be the case that such processes should have added more information to entail c as expected in the specification F. Similarly for the case (4): c in conjunction with the current store should be inconsistent but it is not. Then, more information on the common variables should have been added. In the case (2), c should not be entailed but the store indeed entails c. In this case, we mark the set of constraints that entails c. The case (3) is similar. In cases (5) to (7) we use  $\cup$  and  $\cap$  respectively for point-wise union and intersection in the pair  $(S_{sl}, \Gamma_{sl})$ . These cases are self-explanatory (e.g., if  $F_1 \wedge F_2$  fails, we collect the failure information of either  $F_1$  or  $F_2$ ). In (8), we mark all the calls that do not satisfy the expected assertion  $F(\overline{x})$ . In (9), if F fails, it means that either (a) there are no calls of the shape  $p(\bar{t})$  in the context or (b) none of the calls  $p(\bar{t})$ satisfy  $F_1$ . For (a), similarly to the case (1), a good guess is to examine the processes that added constraints with common variables to  $F_1$  and see which one should have added more information to entail  $F_1$ . As for (b), we also select all the calls of the form  $p(\bar{t})$  from the context. The reader may compare these definitions with the information selected in Step S2 in Sect. 3, regarding possibly wrong behavior.

*Classification of Assertions.* As we explained in Sect. 2.1, computations in CLP can succeed or fail and the answers to a goal is the set of constraints obtained from successful computations. Hence, according to the kind of assertion, it is important to determine when the assertions in Definition 4 must stop or not the computation to start the debugging process. For that, we introduce the following classification:

- **post-conditions,** post(F) assertions: assertions that are meant to be verified only when an answer is found. This kind of assertions are used to test the "quality" of the answers wrt the specification. In this case, the slicing process begins only when an answer is computed and it does not satisfy one of the assertions. Note that assertions of the form  $p(\bar{x})[F(\bar{x})]$  and  $p(\bar{x})\langle F(\bar{x})\rangle$  are irrelevant as post-conditions since the set of goals in an answer must be empty.
- path invariants, inv(F) assertions: assertions that are meant to hold along the whole computation. Then, not satisfying an invariant must be understood as a symptom of an error and the computation must stop. We note that due to monotonicity, only assertions of the form neg(c) and cons(c) can be used to stop the computation (note that if the current configuration fails to satisfy neg(c), then any successor state will also fail to satisfy that assertion). Constraints of the form pos(c), icons(c) can be only checked when the answer is found since, not satisfying those conditions in the partial computation, does not imply that the final state will not satisfy them.

#### 4.2 Experiments

We conclude this section with a series of examples showing the use of assertions. Examples 4 and 5 deal with CLP programs while Examples 6 and 7 with CCP programs.

*Example 4.* The debugger can automatically start and produce the same marking in Example 1 with the following (invariant) assertion:

length([A | L],M) := M = N, length(L, N), inv(pos(M>0)).

*Example 5.* Consider the following CLP program (written in GNU-Prolog with integer finite domains) for solving the well known problem of posing N queens on a  $N \times N$  chessboard in such a way that they do not attack each other.

The program contains one mistake, which causes the introduction of a few additional and not correct solutions, e.g., [1,5,4,3,2] for the goal queens(5,X). The user now has two possible strategies: either she lets the interpreter compute the solutions, one by one and then, when she sees a wrong solution she uses the slicer for marking manually the final store to get the sliced computation; or she can define an assertion to be verified. In this particular case, any solution must satisfy that the difference between two consecutive positions in the list must be greater than 1. Hence, the user can introduce the following post-condition assertion:

Now the slicer stops as soon as the constraint X # = Q+1 becomes inconsistent with the store in a successful computation (e.g., the assertion fails on the -partial- assignment "5,4") and an automatic slicing is performed.

Example 6. In [15] we presented a compelling example of slicing for a timed CCP program modeling the synchronization of events in musical rhythmic patterns. As shown in Example 2 at http://subsell.logic.at/slicer/, the slicer for CCP was able to sufficiently abstract away from irrelevant processes and constraints to highlight the problem in a faulty program. However, the process of stopping the computation to start the debugging was left to the user. The property that failed in the program can be naturally expressed as an assertion. Namely, in the whole computation, if the constraint **beat** is present (representing a sound in the musical rhythm), the constraint **stop** cannot be present (representing the end of the rhythm). This can be written as the conditional assertion  $pos(beat) \rightarrow neg(stop)$ . Following Definition 6, the constraints marked in the wrong computation are the same we considered in [15], thus automatizing completely the process of identifying the wrong computation.

*Example* 7. Example 3 in the URL above illustrates the use of timed CCP for the specification of biochemical systems (we invite the reader to compare in the website the sliced and non-sliced traces). Roughly, in that model, constraints of the form Mdm2 (resp. Mdm2A) state that the protein Mdm2 is present (resp. absent). The model includes activation (and inhibition) of biological rules modeled as processes (omitting some details) of the form **ask** (Mdm2A) **then next tell**(Mdm2) modeling that "if Mdm2 is absent now, then it must be present in the next time-unit". The interaction of many of these rules makes the model trickier since rules may "compete" for resources and then, we can wrongly observe at the same time-unit that Mdm2 is both present and absent. An assertion of the form (pos(Mdm2A)  $\rightarrow$  neg(Mdm2))  $\land$  (pos(Mdm2)  $\rightarrow$  neg(Mdm2A)) will automatically stop the computation and produce the same marking we used to depurate the program in the website.

### 5 Related Work and Conclusions

**Related Work.** Assertions for automatizing a slicing process have been previously introduced in [4] for the functional logic language Maude. The language they consider as well as the type of assertions are completely different from ours. They do not have constraints, and deal with functional and equational computations. Another previous work [31] introduced static and dynamic slicing for CLP programs. However, [31] essentially aims at identifying the parts of a goal which do not share variables, to divide the program in slices which do not interact. Our approach considers more situations, not only variable dependencies, but also other kinds of wrong behaviors. Moreover we have assertions, and hence an automatic slicing mechanism not considered in [31]. The well known debugging box model of Prolog [32] introduces a tool for observing the evolution of atoms during their reduction in the search tree. We believe that our methodology might be integrated with the box model and may extend some of its features. For instance, the box model makes basic simplifications by asking the user to specify which predicates she wants to observe. In our case, one entire computational path is simplified automatically by considering the marked information and identifying the constraints and the atoms which are relevant for such information.

**Conclusions and Future Work.** In this paper we have first extended a previous framework for dynamic slicing of (timed) CCP programs to the case of CLP programs. We considered a slightly different marking mechanism, extended to atoms besides constraints. Don't know non-determinism in CLP requires a different identification of the computations of interest wrt CCP. We considered different modalities specified by the user for selecting successful computations rather than all possible partial computations. As another contribution of this paper, in order to automatize the slicing process, we have introduced an assertion language. This language is rather flexible and allows one to specify different types of assertions that can be applied to successful computations or to all possible partial computations. When assertions are not satisfied by a state of a selected computation then an automatic slicing of such computation can start.

We implemented a prototype of the slicer in Maude and showed its use in debugging several programs. We are currently extending the tool to deal with CLP don't know non-determinism. Being CLP a generalization of logic programming, our extended implementation could be also eventually used to analyze Prolog programs. Integrating the kind of assertions proposed here with already implemented debugging mechanisms in Prolog is an interesting future direction. We also plan to add more advanced graphical tools to our prototype, as well as to study the integration of our framework with other debugging techniques such as the box model and declarative or approximated debuggers [2, 18]. We also want to investigate the relation of our technique with dynamic testing (e.g. concolic techniques) and extend the assertion language with temporal operators, e.g. the past operator  $(\bigcirc)$  for expressing the relation between two consecutive states. Another future topic of investigation is a static version of our framework in order to try to compare and possibly integrate it with analyses and semi automatic corrections based on different formal techniques, and other programming paradigms [1, 3, 5, 7, 8].

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# **Program Analysis**



# Eliminating Unstable Tests in Floating-Point Programs

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Abstract. Round-off errors arising from the difference between real numbers and their floating-point representation cause the control flow of conditional floating-point statements to deviate from the ideal flow of the real-number computation. This problem, which is called test instability, may result in a significant difference between the computation of a floating-point program and the expected output in real arithmetic. In this paper, a formally proven program transformation is proposed to detect and correct the effects of unstable tests. The output of this transformation is a floating-point program that is guaranteed to return either the result of the original floating-point program when it can be assured that both its real and its floating-point flows agree or a warning when these flows may diverge. The proposed approach is illustrated with the transformation of the core computation of a polygon containment algorithm developed at NASA that is used in a geofencing system for unmanned aircraft systems.

**Keywords:** Floating-point numbers  $\cdot$  Round-off error  $\cdot$ Program transformation  $\cdot$  Test instability  $\cdot$  Formal verification

# 1 Introduction

Floating-point numbers are widely used to represent real numbers in computer programs since they offer a good trade-off between efficiency and precision. The round-off error of a floating-point expression is the difference between the ideal computation in real arithmetic and the actual floating-point computation. These round-off errors accumulate during numerical computations. Besides having a direct effect on the result of mathematical operations, round-off errors may significantly impact the control flow of a program. This happens when the guard of a conditional statement contains a floating-point expression whose round-off

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error makes the actual Boolean value of the guard differ from the value that would be obtained assuming real arithmetic. In this case, the conditional statement is called an *unstable test*. Unstable tests are an inherent feature of floatingpoint programs. In general, it is not possible to completely avoid them. However, it is possible to mitigate their effect by transforming the original program into another program that conservatively (and soundly) detects and corrects unstable tests.

This paper presents a program transformation technique to transform a given program into a new one that returns either the same result of the original program or a warning when the real and floating-point flows may diverge. This transformation is parametric with respect to two Boolean abstractions that take into consideration the round-off error in the expressions occurring in the guard. The transformation replaces the unstable conditions with more restrictive conditions that are guaranteed to preserve the control flow of stable tests. The correctness of the proposed transformation is formally verified in the Prototype Verification System (PVS) [16].

The remainder of the paper is organized as follows. Section 2 provides technical background on floating-point numbers and round-off errors. The proposed program transformation technique is presented in Sect. 3. Section 4 illustrates this technique by transforming the core logic of an algorithm for polygon containment that is part of a geofencing system developed by NASA. Section 5 discusses related work and Sect. 6 concludes the paper.

#### 2 Round-Off Errors and Unstable Tests

A floating-point number can be formalized as a pair of integers  $(m, e) \in \mathbb{Z}^2$ , where *m* is called the *significand* and *e* the *exponent* of the float [1,10]. A floating-point *format f* is defined as a pair of integers  $(p, e_{min})$ , where *p* is called the *precision* and  $e_{min}$  is called the *minimal exponent*. For instance, IEEE single and double precision floating-point numbers are specified by the formats (24, 149) and (53, 1074), respectively. A *canonical* float is a float such that is either a normal or subnormal. A *normal* float is a float such that the significand cannot be multiplied by the radix and still fit in the format. A *subnormal* float is a float having the minimal exponent such that its significand can be multiplied by the radix and still fit in the format. Henceforth,  $\mathbb{F}$  will denote the set of floating-point numbers in canonical form and the expression  $\tilde{v}$  will denote a floating-point number (m, e) in  $\mathbb{F}$ . A conversion function  $R : \mathbb{F} \to \mathbb{R}$  is defined to refer to the real number represented by a given float, i.e.,  $R((m, e)) = m \cdot \beta^e$ .

The expression  $F_f(r)$  denotes the floating-point number in format f closest to r. The format f will be omitted when clear from the context. Let  $\tilde{v}$  be a floating-point number that represents a real number r, the difference  $|R(\tilde{v}) - r|$  is called the *round-off error* (or *rounding error*) of  $\tilde{v}$  with respect to r.
#### 2.1 Unstable Tests

Given a set  $\hat{\Omega}$  of pre-defined floating-point operations, the corresponding set  $\Omega$  of operations over real numbers, a finite set  $\mathbb{V}$  of variables representing real values, and a finite set  $\widetilde{\mathbb{V}}$  of variables representing floating-point values, where  $\mathbb{V}$  and  $\widetilde{\mathbb{V}}$  are disjoint, the sets  $\mathbb{A}$  and  $\widetilde{\mathbb{A}}$  of arithmetic expressions over real numbers and over floating-point numbers, respectively, are defined by the following grammar.

$$A ::= d \mid x \mid op(A, \dots, A), \qquad \qquad \widetilde{A} ::= \widetilde{d} \mid \widetilde{x} \mid \widetilde{op}(\widetilde{A}, \dots, \widetilde{A}),$$

where  $A \in \mathbb{A}$ ,  $d \in \mathbb{R}$ ,  $x \in \mathbb{V}$ ,  $op \in \Omega$ ,  $\widetilde{A} \in \widetilde{\mathbb{A}}$ ,  $\widetilde{d} \in \mathbb{F}$ ,  $\widetilde{x} \in \widetilde{\mathbb{V}}$ ,  $\widetilde{op} \in \widetilde{\Omega}$ . It is assumed that there is a function  $\chi_r : \widetilde{\mathbb{V}} \to \mathbb{V}$  that associates to each floating-point variable  $\widetilde{x}$  a variable  $x \in \mathbb{V}$  representing the real value of  $\widetilde{x}$ . The function  $R_{\mathbb{A}} : \widetilde{\mathbb{A}} \to \mathbb{A}$ converts an arithmetic expression on floating-point numbers to an arithmetic expression on real numbers. It is defined by simply replacing each floating-point operation with the corresponding one on real numbers and by applying R and  $\chi_r$  to floating-point values and variables, respectively.

Boolean expressions are defined by the following grammar.

$$B ::= true \mid false \mid B \land B \mid B \lor B \mid \neg B \mid A \lt A \mid A = A \mid A \lt A \mid A = A$$

where  $A \in \mathbb{A}$  and  $\widetilde{A} \in \widetilde{\mathbb{A}}$ . The conjunction  $\wedge$ , disjunction  $\vee$ , negation  $\neg$ , true, and false have the usual classical logic meaning. The symbols  $\mathbb{B}$  and  $\widetilde{\mathbb{B}}$ denote the domain of Boolean expressions over real and floating-point numbers, respectively. The function  $R_{\mathbb{B}} : \widetilde{\mathbb{B}} \to \mathbb{B}$  converts a Boolean expression on floating-point numbers to a Boolean expression on real numbers. Given a variable assignment  $\sigma : \mathbb{V} \to \mathbb{R}$ ,  $eval_{\mathbb{B}}(\sigma, B) \in \{true, false\}$  denotes the evaluation of the real Boolean expression B. Similarly, given  $\widetilde{B} \in \widetilde{\mathbb{B}}$  and  $\widetilde{\sigma} : \widetilde{\mathbb{V}} \to \mathbb{F}$ ,  $\widetilde{eval}_{\widetilde{\mathbb{B}}}(\widetilde{\sigma}, \widetilde{B}) \in \{true, false\}$  denotes the evaluation of the floating-point Boolean expression  $\widetilde{B}$ .

The expression language considered in this paper contains binary and *n*-ary conditionals, let expressions, arithmetic expressions, and a warning exceptional statement. Given a set  $\Sigma$  of function symbols, the syntax of program expressions in  $\mathbb{S}$  is given by the following grammar.

$$S ::= \widetilde{A} \mid if \ \widetilde{B} \ then \ S \ else \ S \mid if \ \widetilde{B} \ then \ S \ [elsif \ \widetilde{B} \ then \ S]_{i=1}^n \ else \ S \\ \mid let \ \widetilde{x} = \widetilde{A} \ in \ S \mid warning,$$

$$(2.1)$$

where  $\widetilde{A} \in \widetilde{\mathbb{A}}$ ,  $\widetilde{B} \in \widetilde{\mathbb{B}}$ ,  $\widetilde{x} \in \widetilde{\mathbb{V}}$ , and  $n \in \mathbb{N}^{>0}$ . The notation  $[elsif \ \widetilde{B} \ then \ S]_{i=1}^n$  denotes a list of  $n \ elsif$  branches.

A program is a function declaration of the form  $\tilde{f}(\tilde{x}_1, \ldots, \tilde{x}_m) = S$ , where  $\tilde{x}_1, \ldots, \tilde{x}_m$  are pairwise distinct variables in  $\widetilde{\mathbb{V}}$  and all free variables appearing in S are in  $\{\tilde{x}_1, \ldots, \tilde{x}_m\}$ . The natural number m is called the *arity* of  $\tilde{f}$ . The set of programs is denoted as  $\mathbb{P}$ .

When if-then-else guards contain floating-point expressions, the output of the considered program is not only directly influenced by rounding errors, but also by the error of taking the incorrect branch in the case of unstable tests. **Definition 1 (Conditional Instability).** A function declaration  $\tilde{f}(\tilde{x}_1,...,\tilde{x}_n) = S$  is said to have an unstable conditional when its body contains a conditional statement of the form if  $\tilde{\phi}$  then  $S_1$  else  $S_2$  and there exist two assignments  $\tilde{\sigma} : {\tilde{x}_1,...,\tilde{x}_n} \to \mathbb{F}$  and  $\sigma : {\chi_r(\tilde{x}_1),...,\chi_r(\tilde{x}_n)} \to \mathbb{R}$  such that for all  $i \in {1,...,n}, \sigma(\chi_r(\tilde{x}_i)) = R(\tilde{\sigma}(\tilde{x}_i))$  and  $eval_{\mathbb{B}}(\sigma, R_{\mathbb{B}}(\tilde{\phi})) \neq eval_{\mathbb{B}}(\tilde{\sigma}, \tilde{\phi})$ . Otherwise, the conditional expression is said to be stable.

In other words, a conditional statement (or test)  $\tilde{\phi}$  is unstable when there exists an assignment from the free variables  $\tilde{x}_i$  in  $\tilde{\phi}$  to  $\mathbb{F}$  such that  $\tilde{\phi}$  evaluates to a different Boolean value with respect to its real valued counterpart  $R_{\mathbb{B}}(\tilde{\phi})$ . In these cases, the program is said to follow an *unstable path*, otherwise, when the flows coincide, it is said to follow a *stable path*.

### 2.2 Floating-Point Denotational Semantics

This section presents a compositional denotational semantics for the expression language of Formula (2.1) that models both real and floating-point path conditions and outputs. This semantics is a modification of the one introduced in [13,21]. The proposed semantics collects for each combination of real and floating-point program paths: the real and floating-point path conditions, two symbolic expressions representing the value of the output assuming the use of real and floating-point arithmetic, respectively, and a flag indicating if the element refers to either a stable or an unstable path. This information is stored in a *conditional tuple*.

**Definition 2 (Conditional Tuple).** A conditional tuple is an expression of the form  $\langle \eta, \tilde{\eta} \rangle_t \twoheadrightarrow (r, \tilde{r})$ , where  $\eta \in \mathbb{B}$ ,  $\tilde{\eta} \in \widetilde{\mathbb{B}}$ ,  $r \in \mathbb{A} \cup \{ \bot_{\mathbf{u}} \}$ ,  $\tilde{r} \in \widetilde{\mathbb{A}} \cup \{ \bot_{\mathbf{u}} \}$ , and  $t \in \{ \mathbf{s}, \mathbf{u} \}$ .

Intuitively,  $\langle \eta, \tilde{\eta} \rangle_t \twoheadrightarrow (r, \tilde{r})$  indicates that if the condition  $\eta \wedge \tilde{\eta}$  is satisfied, the output of the ideal real-valued implementation of the program is r and the output of the floating-point execution is  $\tilde{r}$ . The sub-index t is used to mark by construction whether a conditional tuple corresponds to an unstable path, when  $t = \mathbf{u}$ , or to a stable path, when  $t = \mathbf{s}$ . The element  $\perp_{\mathbf{u}}$  represents the output of the warning construct. Let  $\mathbf{C}$  be the set of all conditional error bounds, and  $\mathbb{C} := \wp(\mathbf{C})$  be the domain formed by sets of conditional error bounds.

An *environment* is defined as a function mapping a variable to a set of conditional tuples, i.e.,  $Env : \widetilde{\mathbb{V}} \to \mathbb{C}$ . The empty environment is denoted as  $\perp_{Env}$  and maps every variable to the empty set  $\emptyset$ .

Given  $\nu \in Env$ , the semantics of program expressions is defined in Fig. 1 as a function  $\mathcal{E} : \mathbb{S} \times Env \to \mathbb{C}$  that returns the set of conditional tuples representing the possible real and floating-point computations and their corresponding path conditions. The operator  $\sqcup$  denotes the least upper bound of the domain of conditional error bounds.

The semantics of a variable  $\tilde{x} \in \tilde{\mathbb{V}}$  consists of two cases. If  $\tilde{x}$  belongs to the environment, then the variable has been previously bound to a program expression S through a let-expression. In this case, the semantics of  $\tilde{x}$  is exactly the

$$\begin{split} & \mathcal{E}\left[\left[\tilde{d}\right]\right]_{\nu} \coloneqq \left\{\left(true, true\right)_{\mathbf{s}} \twoheadrightarrow \left(R\left(\tilde{d}\right), \tilde{d}\right)\right\} \\ & \mathcal{E}\left[\left[warning\right]\right]_{\nu} \coloneqq \left\{\left(true, true\right)_{\mathbf{s}} \twoheadrightarrow \left(\perp_{\mathbf{u}}, \perp_{\mathbf{u}}\right)\right\} & \text{if } \nu(\tilde{x}) = \varnothing \\ \nu(\tilde{x}) & \text{otherwise} \\ & \mathcal{E}\left[\left[\tilde{x}\right]\right]_{\nu} \coloneqq \left\{\left\{\left(true, true\right)_{\mathbf{s}} \twoheadrightarrow \left(\chi_{r}(\tilde{x}), \tilde{x}\right)\right\} & \text{if } \nu(\tilde{x}) = \varnothing \\ \nu(\tilde{x}) & \text{otherwise} \\ & \mathcal{E}\left[\left[\tilde{op}\left(\tilde{A}_{i}\right)_{i=1}^{n}\right]_{\nu} \coloneqq \left\lfloor\left\{\left\langle\bigwedge_{i=1}^{n}\phi_{i},\bigwedge_{i=1}^{n}\tilde{\phi}_{i}\right\rangle_{\mathbf{s}} \twoheadrightarrow \left(op\left(r_{i}\right)_{i=1}^{n},\widetilde{op}\left(\tilde{r}_{i}\right)_{i=1}^{n}\right)\right| \forall 1 \leq i \leq n: \\ & \left\langle\phi_{i},\tilde{\phi}_{i}\right\rangle_{\mathbf{s}} \twoheadrightarrow \left(r_{i},\tilde{r}_{i}\right) \in \mathcal{E}\left[\left[\tilde{A}_{i}\right]\right]_{\nu},\bigwedge_{i=1}^{n}\phi_{i} \neq false,\bigwedge_{i=1}^{n}\tilde{\phi}_{i} \neq false\right\} \\ & \mathcal{E}\left[\left[let\ \tilde{x} = \tilde{A}\ in\ S\right]_{\nu} \coloneqq \mathcal{E}\left[\left[S\right]_{\nu\left[\tilde{x} \rightarrow \mathcal{E}\left[\tilde{A}\right]\right]_{\nu}\right] \\ & \mathcal{E}\left[\left[if\ \tilde{B}\ then\ S_{1}\ else\ S_{2}\right]_{\nu} \coloneqq \mathcal{E}\left[\left[S_{1}\right]_{\nu} \downarrow \left(r_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \sqcup \mathcal{E}\left[\left[S_{1}\right]\right]_{\nu}, \\ & \left\langle\phi_{2},\tilde{\phi}_{2}\right\rangle_{\mathbf{s}} \twoheadrightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\nu}\right\} \Downarrow \left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \sqcup \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{2}\right)_{\mathbf{u}} \twoheadrightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\nu}\right\} \Downarrow \left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \sqcup \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{2}\right)_{\mathbf{u}} \Rightarrow \left(r_{1},\tilde{r}_{2}\right) \right| \left\langle\phi_{1},\tilde{\phi}_{1}\right\rangle_{\mathbf{s}} \Rightarrow \left(r_{1},\tilde{r}_{1}\right) \in \mathcal{E}\left[S_{1}\right]_{\nu}, \\ & \left\langle\phi_{2},\tilde{\phi}_{2}\right\rangle_{\mathbf{s}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\nu}\right\} \Downarrow \left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \sqcup \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{2}\right)_{\mathbf{u}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\nu}\right\} \Downarrow \left\{\left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{2}\right)_{\mathbf{s}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\nu}\right\} \lor \left\{\left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \right. \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1}\right)_{\mathbf{s}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{\mathbb{S}}\right]_{\mu}\right\} \lor \left\{\left(r_{n}_{\mathbb{R}_{\mathbb{B}}}(\tilde{B}),\tilde{B}\right) \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1}\right)_{\mathbf{s}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\mu}\right\} \sqcup \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1},\tilde{h}\right)_{\mathbf{s}} \Rightarrow \left(r_{2},\tilde{r}_{2}\right) \in \mathcal{E}\left[S_{2}\right]_{\mu}\right\} \right] \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1},\tilde{h}\right)_{\mathbf{s}} \Rightarrow \left(r_{1},\tilde{r}_{2}\right) \right\} \right\} \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1},\tilde{h}\right)_{\mathbf{s}} \right\} = \left\{\left(\sigma_{1},\tilde{h}\right)_{\mathbf{s},\tilde{h}\right\} \right\} \right\} \\ \\ & \left\lfloor\left\{\left(\phi_{1},\tilde{\phi}_{1},\tilde{h}\right)_{\mathbf{s}\right\} \right\} \left\{\left(\phi_{$$

Fig. 1. Semantics of a program expression.

semantics of S. If  $\tilde{x}$  does not belong to the environment, then  $\tilde{x}$  is a parameter of the function. Here, a new conditional error bound is added with a placeholder  $\chi_r(\tilde{x})$  representing the real value of  $\tilde{x}$ . The semantics of a floating-point arithmetic operation  $\tilde{op}$  is computed by composing the semantics of its operands. The real and floating-point values are obtained by applying the corresponding arithmetic operation to the values of the operands. The new conditions are obtained as the combination of the conditions of the operands. The semantics of the expression let  $\tilde{x} = \tilde{A}$  in S updates the current environment by associating with variable  $\tilde{x}$  the semantics of expression  $\tilde{A}$ . The semantics of the conditional if  $\widetilde{B}$  then  $S_1$  else  $S_2$  uses an auxiliary operator  $\Downarrow$ .

**Definition 3 (Condition propagation operator).** Given  $b \in \mathbb{B}$  and  $\tilde{b} \in \mathbb{B}$ ,  $\langle \phi, \tilde{\phi} \rangle_t \twoheadrightarrow (r, \tilde{r}) \Downarrow_{(b,\tilde{b})} = \langle \phi \land b, \tilde{\phi} \land \tilde{b} \rangle_t \twoheadrightarrow (r, \tilde{r}) \text{ if } \phi \land b \land \tilde{\phi} \land \tilde{b} \not\Rightarrow \text{ false, otherwise}$  it is undefined. The definition of  $\Downarrow$  naturally extends to sets of conditional tuples: given  $C \in \mathbb{C}$ ,  $C \Downarrow_{(b,\tilde{b})} = \bigcup_{c \in C} c \Downarrow_{(b,\tilde{b})}$ .

The semantics of  $S_1$  and  $S_2$  are enriched with the information about the fact that real and floating-point control flows match, i.e., both  $\widetilde{B}$  and  $R_{\mathbb{B}}(\widetilde{B})$  have the same value. In addition, new conditional tuples are built to model the unstable cases when real and floating-point control flows do not coincide and, therefore, real and floating-point computations diverge. For example, if  $\widetilde{B}$  is satisfied but  $R_{\mathbb{B}}(\widetilde{B})$ is not, the *then* branch is taken in the floating-point computation, but the *else* would have been taken in the real one. In this case, the real condition and its corresponding output are taken from the semantics of  $S_2$ , while the floatingpoint condition and its corresponding output are taken from the semantics of  $S_1$ . The condition  $(\neg R_{\mathbb{B}}(\widetilde{B}), \widetilde{B})$  is propagated in order to model that  $\widetilde{B}$  holds but  $R_{\mathbb{B}}(\widetilde{B})$  does not. The conditional tuples representing this case are marked with **u**.

Similarly, the semantics of an n-ary conditional is composed of stable and unstable cases. The stable cases are built from the semantics of all the program sub-expressions  $S_i$  by enriching them with the information stating that the correspondent guard and its real counter-part hold and all the previous guards and their real counterparts do not hold. All the unstable combinations are built by combining the real parts of the semantics of a program expression  $S_i$  and the floating-point contributions of a different program expression  $S_j$ . In addition, the operator  $\Downarrow$  is used to propagate the information that the real guard of  $S_i$  and the floating-point guard of  $S_j$  hold, while the guards of the previous branches do not hold.

## 3 Program Transformation

In this section, a program transformation is proposed for detecting when roundoff errors affect the evaluation of floating-point conditionals and for ensuring that when the floating-point control flow diverges from the real one a warning is issued. The proposed transformation takes into account round-off errors by abstracting the Boolean expressions in the guards of the original program. This is done by means of two Boolean abstractions  $\beta^+, \beta^- : \mathbb{B} \to \mathbb{B}$ .

Given  $\tilde{\phi} \in \tilde{B}$ , let  $fv(\tilde{\phi})$  be the set of free variables in  $\tilde{\phi}$ . For all  $\sigma : \{\chi_r(\tilde{x}) \mid \tilde{x} \in fv(\tilde{\phi})\} \to \mathbb{R}, \tilde{\sigma} : fv(\tilde{\phi}) \to \mathbb{F}$ , and  $\tilde{x} \in fv(\tilde{\phi})$  such that  $R(\tilde{\sigma}(\tilde{x})) = \sigma(\chi_r(\tilde{x})), \beta^+$  and  $\beta^-$  satisfy the following properties.

- 1.  $\widetilde{eval}_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \beta^+(\tilde{\phi})) \Rightarrow \widetilde{eval}_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \tilde{\phi}) \wedge eval_{\mathbb{B}}(\sigma, R_{\mathbb{B}}(\tilde{\phi})).$
- 2.  $\widetilde{eval}_{\widetilde{\mathbb{R}}}(\tilde{\sigma}, \beta^{-}(\tilde{\phi})) \Rightarrow \widetilde{eval}_{\widetilde{\mathbb{R}}}(\tilde{\sigma}, \neg \tilde{\phi}) \wedge eval_{\mathbb{B}}(\sigma, \neg R_{\mathbb{B}}(\tilde{\phi})).$

Property 1 states that for all floating-point Boolean expressions  $\tilde{\phi}$ ,  $\beta^+(\tilde{\phi})$  implies both  $\tilde{\phi}$  and its real counterpart. Symmetrically, Property 2 ensures that  $\beta^-(\tilde{\phi})$ implies both the negation of  $\tilde{\phi}$  and the negation of its real counterpart.

Example 1. The Boolean abstractions  $\beta^+$  and  $\beta^-$  can be instantiated as follows for conjunctions and disjunction of sign tests. Properties 1 and 2 are formally proven in PVS to hold for the following definitions of  $\beta^+$  and  $\beta^-$ . Let  $\widetilde{expr} \in \widetilde{\mathbb{A}}$ and  $\epsilon \in \mathbb{F}$  such that  $|\widetilde{expr} - R_{\mathbb{A}}(\widetilde{expr})| \leq \epsilon$ .

 $\begin{array}{ll} \beta^+(\widetilde{expr}\leq 0)=\widetilde{expr}\leq -\epsilon & \beta^-(\widetilde{expr}\leq 0)=\widetilde{expr}>\epsilon \\ \beta^+(\widetilde{expr}\geq 0)=\widetilde{expr}\geq \epsilon & \beta^-(\widetilde{expr}\geq 0)=\widetilde{expr}>\epsilon \\ \beta^+(\widetilde{expr}< 0)=\widetilde{expr}< -\epsilon & \beta^-(\widetilde{expr}< 0)=\widetilde{expr}\geq \epsilon \\ \beta^+(\widetilde{expr}> 0)=\widetilde{expr}>\epsilon & \beta^-(\widetilde{expr}> 0)=\widetilde{expr}\leq -\epsilon \\ \beta^+(\widetilde{\phi}_1\wedge\widetilde{\phi}_2)=\beta^+(\widetilde{\phi}_1)\wedge\beta^+(\widetilde{\phi}_2) & \beta^-(\widetilde{\phi}_1\wedge\widetilde{\phi}_2)=\beta^-(\widetilde{\phi}_1)\vee\beta^-(\widetilde{\phi}_2) \\ \beta^+(\widetilde{\phi}_1\vee\widetilde{\phi}_2)=\beta^+(\widetilde{\phi}_1)\vee\beta^+(\widetilde{\phi}_2) & \beta^-(\widetilde{\phi}_1\vee\widetilde{\phi}_2)=\beta^-(\widetilde{\phi}_1)\wedge\beta^-(\widetilde{\phi}_2) \\ \beta^+(\neg\widetilde{\phi})=\beta^-(\widetilde{\phi}) & \beta^-(\neg\widetilde{\phi})=\beta^+(\widetilde{\phi}) \end{array}$ 

The abstractions performed for sign tests are not correct for generic inequalities of the form  $a \leq b$ . In this case, to compensate for the round-off errors of both expressions, additional floating-point operations must be performed. Thus, the round-off error generated by such operations needs to be considered as well to obtain a sound approximation. The naive application of this strategy leads to a non-terminating transformation. The design of an effective approximation for these generic inequalities is left as future work.

The program transformation is defined as follows.

**Definition 4 (Program Transformation).** Let  $\tilde{f}(\tilde{x}_1, \ldots, \tilde{x}_n) = S \in \mathbb{P}$  be a floating-point program that does not contain any warning statements, the transformed program is defined as  $\tilde{f}(\tilde{x}_1, \ldots, \tilde{x}_n) = \tau(S)$  where  $\tau$  is defined as follows.

$$\begin{split} \tau(\tilde{A}) &= \tilde{A} \\ \tau(if \ \tilde{\phi} \ then \ S_1 \ else \ S_2) &= \\ & if \ \beta^+(\tilde{\phi}) \ then \ \tau(S_1) \ else if \ \beta^-(\tilde{\phi}) \ then \ \tau(S_2) \ else \ warning \\ \tau(if \ \tilde{\phi}_1 \ then \ S_1 \ [elsif \ \tilde{\phi}_i \ then \ S_i]_{i=2}^{n-1} \ else \ S_n) &= \\ & if \ \beta^+(\tilde{\phi}_1) \ then \ \tau(S_1) \ [elsif \ \beta^+(\tilde{\phi}_i) \land \bigwedge{}_{j=1}^{i-1} \ \beta^-(\tilde{\phi}_j) \ then \ \tau(S_i)]_{i=2}^{n-1} \\ & elsif \ \bigwedge{}_{j=1}^{n-1} \ \beta^-(\tilde{\phi}_j) \ then \ \tau(S_n) \\ & else \ warning \\ \tau(let \ \tilde{x} = \tilde{A} \ in \ S) = let \ \tilde{x} = \tilde{A} \ in \ \tau(S) \end{split}$$

In the case of the binary conditional statement, the *then* branch of the transformed program is taken when  $\beta^+(\tilde{\phi})$  is satisfied. By Property 1, this means that in the original program both  $\tilde{\phi}$  and  $R(\tilde{\phi})$  hold and, thus, the *then* branch is taken in both real and floating-point control flows. Similarly, the *else* branch of the transformed program is taken when  $\beta^-(\tilde{\phi})$  holds. This means, by Property 2, that in the original program the else branch is taken in both real and floating-point control flows. In the case real and floating-flows diverge, neither  $\beta^+(\tilde{\phi})$  nor  $\beta^-(\tilde{\phi})$  is satisfied and a warning is returned.

In the case of the n-ary conditional statements, the guard  $\tilde{\phi}_i$  of the *i*-th branch is replaced by the conjunction of  $\beta^+(\tilde{\phi}_i)$  and  $\beta^-(\tilde{\phi}_j)$  for all the previous branches j < i. By properties 1 and 2, it follows that the transformed program takes the *i*-th branch only when the same branch is taken in both real and floating-point control flows of the original program. Additionally, a warning is issued by the transformed program when real and floating-point control flows of the original program.

The following theorem states the correctness of the program transformation  $\tau$ . If the transformed program  $\tau(P)$  returns an output  $\tilde{r}$  different from *warning*, then the original program follows a stable path and returns the floating-point output  $\tilde{r}$ . Furthermore, in the case the original program presents an unstable behavior, the transformed program returns *warning*.

**Theorem 1 (Program Transformation Correctness).** Given  $\tilde{f}(\tilde{x}_1,..., \tilde{x}_n) = S \in \mathbb{P}, \sigma : \{\chi_r(\tilde{x}_1)...\chi_r(\tilde{x}_n)\} \to \mathbb{R}, and \tilde{\sigma} : \{\tilde{x}_1...\tilde{x}_n\} \to \mathbb{F}, such that for all <math>i \in \{1,...,n\}, R(\tilde{\sigma}(\tilde{x}_i)) = \sigma(\chi_r(\tilde{x}_i)):$ 

- 1. for all  $\langle \eta', \tilde{\eta}' \rangle_{t'} \twoheadrightarrow (r', \tilde{r}') \in \mathcal{E}[\![\tau(S)]\!]_{\perp_{Env}}$  such that  $\tilde{r} \neq \perp_{\mathbf{u}}$ , there exists  $\langle \eta, \tilde{\eta} \rangle_{\mathbf{s}} \twoheadrightarrow (r, \tilde{r}) \in \mathcal{E}[\![S]\!]_{\perp_{Env}}$  such that  $\widetilde{eval}_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \tilde{\eta}') \Rightarrow eval_{\mathbb{B}}(\sigma, \eta) \land \widetilde{eval}_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \tilde{\eta})$  and  $\tilde{r} = \tilde{r}';$
- 2. for all  $\langle \eta, \tilde{\eta} \rangle_{\mathbf{u}} \twoheadrightarrow (r, \tilde{r}) \in \mathcal{E}[\![S]\!]_{\perp_{Env}}$ , there exists  $\langle \eta', \tilde{\eta}' \rangle_{t'} \twoheadrightarrow (r', \perp_{\mathbf{u}}) \in \mathcal{E}[\![\tau(S)]\!]_{\perp_{Env}}$ such that  $eval_{\mathbb{B}}(\sigma, \eta) \land eval_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \tilde{\eta}) \Rightarrow eval_{\widetilde{\mathbb{B}}}(\tilde{\sigma}, \tilde{\eta}').$

The program transformation defined in Definition 4 has been formalized and Theorem 1 has been proven correct in  $PVS.^1$ 

It is important to remark that the intended semantics of the floating-point transformed program is the real-valued semantics of the original one, i.e., the real-valued semantics of the transformed program is irrelevant. Therefore, even if the transformed program presents unstable tests, Theorem 1 ensures that its floating-point control flow preserves the control flow of stable tests in the original program.

*Example 2.* Consider the program *eps\_line*, which is part of the ACCoRD conflict detection and resolution algorithm [11]. This function is used to compute an implicitly coordinated horizontal resolution direction for the aircraft involved in a pair-wise conflict.

 $eps\_line(\tilde{v}_x, \tilde{v}_y, \tilde{s}_x, \tilde{s}_y) = if \ \widetilde{expr} > 0 \ then \ 1 \ elsif \ \widetilde{expr} < 0 \ then \ -1 \ else \ 0,$ 

<sup>&</sup>lt;sup>1</sup> This formalization is available at https://shemesh.larc.nasa.gov/fm/PRECiSA.

where  $\widetilde{expr} = (\tilde{s}_x * \tilde{v}_y) - (\tilde{s}_y * \tilde{v}_x)$  and  $\tilde{v}_x, \tilde{v}_y, \tilde{s}_x, \tilde{s}_y$  are floating-point variables. For example, if the values of such variables are assumed to lie in the range [-100, 100], the tool PRECiSA [13,21] can be used to compute the round-off error estimation  $\epsilon = 6.4801497501321145 \times 10^{-12}$  for  $\widetilde{expr}$ . PRECiSA is a tool that over-approximates the round-off error of floating-point programs. It is fully automatic and generates PVS proof certificates that guarantee the correctness of the error estimations with respect to the floating-point IEEE-754 standard. The following program is obtained by using the transformation  $\tau$  with the Boolean approximations of Example 1.

$$\begin{aligned} \tau(eps\_line(\tilde{v}_x, \tilde{v}_y, \tilde{s}_x, \tilde{s}_y)) &= if \ \widetilde{expr} > \epsilon \ then \ 1 \ elsif \ \widetilde{expr} < -\epsilon \ then \ -1 \\ elsif \ \widetilde{expr} \ge \epsilon \ \land \ \widetilde{expr} \le -\epsilon \ then \ 0 \ else \ warning \end{aligned}$$

The condition  $\widetilde{expr} \ge \epsilon \land \widetilde{expr} \le -\epsilon$  never holds since  $\epsilon$  is a positive number. Therefore, the transformed program never returns 0. Indeed, when  $\widetilde{expr}$  is close to 0, the test is unstable. The transformed program detects these unstable cases and returns a warning.

# 4 Case Study: PolyCARP Algorithm

PolyCARP<sup>2</sup> (Algorithms for Computations with Polygons) [14,15] is a suite of algorithms for geo-containment applications. One of the main applications of PolyCARP is to provide geofencing capabilities to unmanned aerial systems (UAS), i.e., detecting whether a UAS is inside or outside a given geographical region, which is modeled using a 2D polygon with a minimum and a maximum altitude. Another application of PolyCARP is the detection of weather cells, modeled as moving polygons, along an aircraft trajectory.

A core piece of logic in PolyCARP is the polygon containment algorithm, i.e., the algorithm that checks whether or not a point lies in the interior of a polygon. Algorithms for polygon containment have to be carefully implemented since numerical errors may lead to wrong answers, even in cases where the point is far from the boundaries of the polygon. PolyCARP uses several techniques to detect if a point is contained in a polygon. One of these techniques relies on the computation of the *winding number*. This number corresponds to the number of times the polygon winds around a point p.

Consider two consecutive vertices v and v' of the polygon in the Cartesian plane with the point p as the origin. The function  $winding\_number\_edge$  checks in which quadrants v and v' are located and counts how many axes are crossed by the edge (v, v'). If v and v' belong to the same quadrant, the contribution of the edge to the winding number is 0 since no axis is crossed. If v and v' lie in adjacent quadrants, the contribution is 1 (respectively -1) if moving from v to v' along the edge is in counterclockwise (respectively clockwise) direction. In the case vand v' are in opposite quadrants, the determinant is computed for checking the direction of the edge. If it is counterclockwise the contribution is 2, otherwise

<sup>&</sup>lt;sup>2</sup> PolyCARP is available at https://github.com/nasa/polycarp.

it is -2. The winding number is obtained as the sum of the contributions of all the edges of the polygon. If the result is 0 or 4, the point is inside the polygon, otherwise, it is outside.

 $\begin{array}{l} \mbox{winding}\_number\_edge(v_x,v_y,v_x',v_y',p_x,p_y) = \\ let \ t_x = v_x - p_x \ in \ let \ t_y = v_y - p_y \ in \ let \ n_x = v_x' - p_x \ in \ let \ n_y = v_y' - p_y \ in \ if \ same\_quad \ then \ 0 \\ elsif \ adj\_quad\_ctrclock \ then \ 1 \\ elsif \ adj\_quad\_clock \ then \ -1 \\ elsif \ det\_pos \ then \ 2 \\ else \ -2 \end{array}$ 

where

 $same\_quad = (t_x \ge 0 \land t_y \ge 0 \land n_x \ge 0 \land n_y \ge 0) \lor (t_x \le 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \ge 0) \lor (t_x \ge 0 \land t_y \ge 0 \land n_x \ge 0 \land n_y \le 0) \lor (t_x \le 0 \land t_y \le 0 \land n_x \le 0 \land n_y \le 0)$   $adj\_quad\_ctrclock = (t_x \ge 0 \land t_y \ge 0 \land n_x \ge 0 \land n_y \ge 0) \lor (t_x \ge 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \ge 0) \lor (t_x \le 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \le 0) \lor (t_x \le 0 \land t_y \le 0 \land n_x \ge 0 \land n_y \le 0),$   $adj\_quad\_clock = (t_x \ge 0 \land t_y \ge 0 \land n_x \ge 0 \land n_y \le 0) \lor (t_x \le 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \ge 0),$   $adj\_quad\_clock = (t_x \ge 0 \land t_y \ge 0 \land n_x \ge 0 \land n_y \le 0) \lor (t_x \le 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \ge 0) \lor (t_x \le 0 \land t_y \ge 0 \land n_x \le 0 \land n_y \ge 0) \lor (t_x \ge 0 \land t_y \le 0 \land n_x \le 0 \land n_y \ge 0),$   $det\_pos = (n_x - t_x) * t_y - (n_y - t_y) * t_x \le 0.$ 

The function winding\_number\_edge has been verified in PVS using real arithmetic. However, due to floating-point errors, taking the incorrect branch for one of the edges in the computation of the winding number may result in an incorrect conclusion about the position of the point with respect to the polygon. In order to overcome this problem, the transformation  $\tau$  of Definition 4 is applied to the function winding\_number\_edge resulting in the following function. Given initial bounds for the input variables, PRECiSA [13,21] can be used to compute the round-off error estimations for  $n_x$ ,  $n_y$ ,  $t_x$ ,  $t_y$  and the determinant, which are denoted  $\epsilon_{t_x}$ ,  $\epsilon_{t_y}$ ,  $\epsilon_{n_x}$ ,  $\epsilon_{n_y}$ , and  $\epsilon_{det}$ , respectively.

$$\begin{aligned} \tau(winding\_number\_edge(v_x, v_y, v'_x, v'_y, p_x, p_y)) &= \\ let \ t_x &= v_x - p_x \ in \ let \ t_y &= v_y - p_y \ in \ let \ n_x &= v'_x - p_x \ in \ let \ n_y &= v'_y - p_y \ in \\ if \ same\_quad^\beta \ then \ 0 \\ elsif \ adj\_quad\_ctrclock^\beta \ then \ 1 \\ elsif \ adj\_quad\_clock^\beta \ then \ -1 \\ elsif \ det\_pos^\beta \ then \ 2 \end{aligned}$$

 $\begin{array}{ll} elsif & original\_else^{\beta} \ else \ -2 \\ else & warning, \end{array}$ 

where

$$same\_quad^{\beta} = \beta^{+}(same\_quad) = (t_{x} \ge \epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \ge \epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor (t_{x} \le -\epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor (t_{x} \ge \epsilon_{t_{x}} \land t_{y} \le -\epsilon_{t_{y}} \land n_{x} \ge \epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}) \lor (t_{x} \le -\epsilon_{t_{x}} \land t_{y} \le -\epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}),$$

 $\begin{aligned} &adj\_quad\_ctrclock^{\beta} = \beta^{+}(adj\_quad\_counterclock) \land \beta^{-}(same\_quad), \\ &adj\_quad\_clock^{\beta} = \beta^{+}(adj\_quad\_clock) \land \beta^{-}(adj\_quad\_ctrclock) \land \\ &\beta^{-}(same\_quad), \end{aligned}$ 

$$det\_pos^{\beta} = (n_x - t_x) * t_y - (n_y - t_y) * t_x \le -\epsilon_{det} \land \beta^-(adj\_quad\_clock) \land \beta^-(adj\_quad\_ctrclock) \land \beta^-(same\_quad),$$

$$\begin{split} original\_else^{\beta} = & (n_x - t_x) * t_y - (n_y - t_y) * t_x > \epsilon_{det} \land \beta^-(adj\_quad\_clock) \land \\ & \beta^-(adj\_quad\_ctrclock) \land \beta^-(same\_quad), \end{split}$$

$$\begin{split} \beta^{-}(same\_quad) &= (t_{x} < -\epsilon_{t_{x}} \lor t_{y} < -\epsilon_{t_{y}} \lor n_{x} < -\epsilon_{n_{x}} \lor n_{y} < -\epsilon_{n_{y}}) \land \\ &(t_{x} > \epsilon_{t_{x}} \lor t_{y} < -\epsilon_{t_{y}} \lor n_{x} > \epsilon_{n_{x}} \lor n_{y} < -\epsilon_{n_{y}}) \land \\ &(t_{x} < -\epsilon_{t_{x}} \lor t_{y} > \epsilon_{t_{y}} \lor n_{x} < -\epsilon_{n_{x}} \lor n_{y} > \epsilon_{n_{y}}) \land \\ &(t_{x} > \epsilon_{t_{x}} \lor t_{y} > \epsilon_{t_{y}} \lor n_{x} > \epsilon_{n_{x}} \lor n_{y} > \epsilon_{n_{y}}), \end{split}$$

$$\beta^{+}(adj\_quad\_ctrclock) = (t_{x} \ge \epsilon_{t_{x}} \land t_{y} \le -\epsilon_{t_{y}} \land n_{x} \ge \epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor (t_{x} \ge \epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor (t_{x} \le -\epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}) \lor (t_{x} \le -\epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \ge \epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}),$$

$$\begin{array}{l} \beta \quad (adj\_quad\_ctrclock) = & (t_x < -\epsilon_{t_x} \lor t_y > \epsilon_{t_y} \lor n_x < -\epsilon_{n_x} \lor n_y < -\epsilon_{n_y}) \land \\ & (t_x < -\epsilon_{t_x} \lor t_y < -\epsilon_{t_y} \lor n_x > \epsilon_{n_x} \lor n_y < -\epsilon_{n_y}) \land \\ & (t_x > \epsilon_{t_x} \lor t_y < -\epsilon_{t_y} \lor n_x > \epsilon_{n_x} \lor n_y > \epsilon_{n_y}) \land \\ & (t_x > \epsilon_{t_x} \lor t_y > \epsilon_{t_y} \lor n_x < -\epsilon_{n_x} \lor n_y > \epsilon_{n_y}), \end{array}$$

$$\beta^{+}(adj\_quad\_clock) = (t_{x} \ge \epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \ge \epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}) \lor$$

$$(t_{x} \le -\epsilon_{t_{x}} \land t_{y} \ge \epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor$$

$$(t_{x} \le -\epsilon_{t_{x}} \land t_{y} \le -\epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \ge \epsilon_{n_{y}}) \lor$$

$$(t_{x} \ge \epsilon_{t_{x}} \land t_{y} \le -\epsilon_{t_{y}} \land n_{x} \le -\epsilon_{n_{x}} \land n_{y} \le -\epsilon_{n_{y}}),$$

$$\beta^{-}(adj\_quad\_clock) = (t_{x} < -\epsilon_{t_{x}} \lor t_{y} < -\epsilon_{t_{y}} \lor n_{x} < -\epsilon_{n_{x}} \lor n_{y} > \epsilon_{n_{y}}) \land$$

$$(t_{x} > \epsilon_{t_{x}} \lor t_{y} < -\epsilon_{t_{y}} \lor n_{x} > \epsilon_{n_{x}} \lor n_{y} < -\epsilon_{n_{y}}) \land$$

$$(t_{x} > \epsilon_{t_{x}} \lor t_{y} > \epsilon_{t_{y}} \lor n_{x} > \epsilon_{n_{x}} \lor n_{y} < -\epsilon_{n_{y}}) \land$$

$$(t_{x} < -\epsilon_{t_{x}} \lor t_{y} > \epsilon_{t_{y}} \lor n_{x} > \epsilon_{n_{x}} \lor n_{y} < \epsilon_{n_{y}}).$$

Consider a polygonal geofence and a set of randomly generated points in the square that circumscribes it. For each edge of the polygon and each generated point, the original function *winding\_number\_edge* is executed by using both exact real arithmetic and double-precision floating-point arithmetic. Additionally, the transformed function  $\tau(winding_number_edge)$  is executed with double-precision floating-point arithmetic. For these randomly generated points, both the original and the transformed program return the same result. However, the closer the generated point is to the border of the polygon, the more likely is for the original program to take an unstable path. By considering a set of randomly generated points very close to the edges of the polygon, the transformed program always returns a warning, showing that these are the cases for which the floating-point computation may diverge from the real one. Since an over-approximation of the round-off error is used, not all the generated warnings reflect an actual problem. In fact, false warnings occur when the compensated error computed by the abstraction is larger than the round-off error that actually occurs in the computation. The amount of false warnings converges to the 50% of the number of total warnings as the distance to the edge decreases.

### 5 Related Work

Recently, several program transformations have been proposed with the aim of improving accuracy and efficiency of floating-point computations. It is possible to distinguish two kinds of approaches: precision allocation tools and program optimization ones. Precision allocation (or tuning) tools aim at selecting the lowest floating-point precision that is necessary to achieve a desired accuracy. This approach avoids using more precision than needed and improves the performance of the program. Rosa [8,9] uses a compilation algorithm that, from an ideal real-valued implementation, produces a finite-precision version (if it exists) that is guaranteed to meet the desired overall precision. Rosa soundly deals with unstable tests and with bounded loops. Similarly, FPTuner [3] implements a rigorous approach to precision allocation of mixed-precision arithmetic expressions. Precimonius [18] is a dynamic tool able to identify parts of a program that can be performed at a lower precision. It generates a transformed program where each floating-point variable is typed to the lowest precision necessary to meet a set of given accuracy and performance constraints. Hence, the transformed program uses variables of lower precision and performs better than the original program.

Program optimization tools aim at improving the accuracy of floating-point programs by rewriting arithmetic expressions in equivalent ones with a lower accumulated round-off error. Herbie [17] is a tool that automatically improves the accuracy of floating-point programs though a heuristic search. Herbie detects the expressions where rounding-errors occur and it applies a series of rewriting and simplification rules. It generates a set of transformed programs that are equivalent to the original one but potentially more accurate. The rewriting and simplification process is then applied recursively to the generated transformed programs until the most accurate program is obtained. CoHD [19] is a source-tosource transformer for C code that automatically compensates for the round-off errors of some basic floating-point operations. SyHD [20] is a C code optimizer that explores a set of programs generated by CoDH and selects the one with the best accuracy and computation-time trade-off. The tool Sardana [12], given a Lustre [2] program, produces a set of equivalent programs with simplified arithmetic expressions. Then, it selects the ones for which a better accuracy bound can be proved. Salsa [4] combines Sardana with techniques for intra-procedure [5] and inter-procedure [6,7] program transformation in order to improve the accuracy of a target variable in larger pieces of code containing assignments and control structures. To the best of the authors' knowledge, the program transformation proposed in this work is the only approach that addresses the problem of conditional instability for floating-point programs.

# 6 Conclusion

This paper presents a formally verified program transformation to detect instability in floating-point programs. The transformed program is guaranteed to return a warning when real and floating-point flows may diverge. Otherwise, it behaves as the original program when real and floating-point control flows coincide. The proposed approach is parametric with respect to two Boolean expression abstractions that return more restrictive Boolean conditions using an over-approximation of the round-off error occurring in the guard. These abstractions cause a loss of precision since the guards occurring in the transformed program are more restrictive and, therefore, some stable original traces may be lost in the transformed program. This leads to the possibility of having false instability warnings. However, it is ensured that all the unstable paths of the original program are detected.

This transformation has been formalized and formally proven correct in the interactive theorem prover PVS. The PVS tool PVSio can be used to execute the program transformation. However, a full integration with PRECiSA is the missing step to compute the round-off error approximations and to make the presented approach fully automatic.

The program transformation presented in this paper is the first step towards the much broader goal of improving the quality and reliability of floating-point programs. Future work includes the extension of the formalization to a more expressive language where conditionals are allowed inside Boolean expressions and function calls and loops are supported. This extension is not straightforward since it involves several changes in the formalization. In fact, in such setting, the evaluation of the expressions in the guards can also present unstable behaviors. Additionally, an extensive experimental evaluation is needed in order to assess the quality of the approach and its applicability to real-world applications. Another interesting future direction is the integration of the proposed approach with tools such as Salsa [4] and Herbie [17]. This integration will improve the accuracy of the mathematical expressions used inside a program and, at the same time, prevent unstable tests that may cause unexpected behaviors.

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# Multivariant Assertion-Based Guidance in Abstract Interpretation

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Abstract. Approximations during program analysis are a necessary evil, as they ensure essential properties, such as soundness and termination of the analysis, but they also imply not always producing useful results. Automatic techniques have been studied to prevent precision loss, typically at the expense of larger resource consumption. In both cases (i.e., when analysis produces inaccurate results and when resource consumption is too high), it is necessary to have some means for users to provide information to guide analysis and thus improve precision and/or performance. We present techniques for supporting within an abstract interpretation framework a rich set of assertions that can deal with multivariance/context-sensitivity, and can handle different run-time semantics for those assertions that cannot be discharged at compile time. We show how the proposed approach can be applied to both improving precision and accelerating analysis. We also provide some formal results on the effects of such assertions on the analysis results.

**Keywords:** Program analysis  $\cdot$  Multivariance  $\cdot$  Context sensitivity  $\cdot$  Abstract interpretation  $\cdot$  Assertions  $\cdot$  Static analysis  $\cdot$  User guidance

## 1 Introduction

Abstract Interpretation [6] is a well-established technique for performing static analyses to determine properties of programs. It allows inferring at compiletime and in finite time information that is guaranteed to hold for all program executions corresponding to all possible sets of inputs to the program. Reasoning about these generally infinite sets of inputs and program paths requires (safe) approximations –computing over abstract domains– to ensure termination and soundness. If such approximations are not carefully designed, the information reported by the analyzer may not be accurate enough to be useful for the

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intended application, such as, e.g., performing optimizations or verifying properties. Similarly, although abstract interpretation-based analyzers are guaranteed to terminate, this does not necessarily imply that they do so in acceptable time or space, i.e., their resource usage may be higher than desirable.

Much work has been done towards improving both the accuracy and efficiency of analyzers through the design of automatic analysis techniques that include clever abstract domains, widening and narrowing techniques [1, 28, 29], and sophisticated fixpoint algorithms [3, 15, 21, 26]. Despite these advances, there are still cases where it is necessary for the user to provide input to the analyzer to guide the process in order to regain accuracy, prevent imprecision from propagating, and improve analyzer performance [5, 8]. Interestingly, there is comparatively little information on these aspects of analyzers, perhaps because they are perceived as internal or analyzer implementation-specific.

In this paper we focus on techniques that provide a means for the programmer to be able to optionally annotate program parts in which precision needs to be recovered. Examples are the *entry* and *trust* declarations of CiaoPP [5,24] and the *known facts* of Astrée [7,8] (see Sect. 6 for more related work). Such user annotations allow dealing with program constructs for which the analysis is not complete or the source is only partially available. However, as mentioned before, there is little information in the literature on these assertions beyond a sentence or two in the user manuals or some examples of use in demo sessions. In particular, no precise descriptions exist on how these assertions affect the analysis process and its results.

We clarify these points by proposing a user-guided multivariant fixpoint algorithm that makes use of information contained in different kinds of assertions, and provide formal results on the influence of such assertions on the analysis. We also extend the semantics of the assertions to control if precision can be relaxed, and also to deal with both the cases in which the program execution will and will not incorporate run-time tests for unverified assertions. Note that almost all current abstract interpretation systems assume in their semantics that the run-time checks will be run. However, due to efficiency considerations, assertion checking in often turned off in production code, specially for complex properties [16]. To the best of our knowledge this is the first precise description of how such annotations are processed within a standard parametric and multivariant fixpoint algorithm, and of their effects on analysis results.

### 2 Preliminaries

**Program Analysis with Abstract Interpretation.** Our approach is based on *abstract interpretation* [6], a technique in which execution of the program is simulated on an *abstract domain*  $(D_{\alpha})$  which is simpler than the actual, *concrete domain* (D). Although not strictly required, we assume that  $D_{\alpha}$  has a lattice structure with meet  $(\Box)$ , join  $(\sqcup)$ , and less than  $(\sqsubseteq)$  operators. Abstract values and sets of concrete values are related via a pair of monotonic mappings  $\langle \alpha, \gamma \rangle$ : *abstraction*  $\alpha : D \to D_{\alpha}$ , and *concretization*  $\gamma : D_{\alpha} \to D$ , which form a Galois connection. A description (or abstract value)  $d \in D_{\alpha}$  approximates a concrete value  $c \in D$  if  $\alpha(c) \sqsubseteq d$  where  $\sqsubseteq$  is the partial ordering on  $D_{\alpha}$ . Concrete operations on D values are (over-)approximated by corresponding abstract operations on  $D_{\alpha}$  values. The key result for abstract interpretation is that it guarantees that the analysis terminates, provided that  $D_{\alpha}$  meets some conditions (such as finite ascending chains) and that the results are safe approximations of the concrete semantics (provided  $D_{\alpha}$  safely approximates the concrete values and operations).

Intermediate Representation. For generality, we formulate our analysis to work on a block-level intermediate representation of the program, encoded using Constrained Horn clauses (CHC). A definite CHC program, or program, is a finite sequence of clauses. A *clause* is of the form  $H:-B_1,\ldots,B_n$  where H, the *head*, is an atom, and  $B_1, \ldots, B_n$  is the *body*, a possibly empty finite conjunction of atoms. Atoms are also called *literals*. We will refer to the head and the body of a clause cl with cl.head and cl.body respectively. An *atom* is of the form  $p(V_1,\ldots,V_n)$ . It is normalized if the  $V_1,\ldots,V_n$  are all distinct variables. Normalized atoms are also called *predicate descriptors*. Each maximal set of clauses in the program with the same descriptor as head (modulo variable renaming) defines a *predicate* (or *procedure*). Body literals can be predicate descriptors, which represent *calls* to the corresponding predicates, or *constraints*. A *con*straint is a finite conjunction of built-in relations for some background theory. We assume that all non-builtin atoms are normalized. This is not restrictive since programs can always be put in this form, and it simplifies the presentation of the algorithm. However, in the examples we use non-normalized programs. The encoding of program semantics in CHC depends on the source language and is beyond the scope of the paper. It is trivial for (C)LP programs, and also well studied for several types of imperative programs and compilation levels (e.g., bytecode, llvm-IR, or ISA - see [2,9,11,12,17,20,23]).

*Concrete Semantics.* The concrete semantics that we abstract is that of Constraint Logic Programs - (C)LP [19]. In particular, we use the constraint extension of top-down, left-to-right SLD-resolution, which, given a query (initial state), returns the answers (*exit states*) computed for it by the program. A *query* is a pair  $G:\theta$  with G a (non-empty) conjunction of atoms and  $\theta$  a constraint. Executing (answering) a query with respect to a CHC program consists on determining whether the query is a logical consequence of the program and for which constraints (answers). However, since we are interested in abstracting the calls and answers (states) that occur at different points in the program, we base our semantics on the well-known notion of generalized AND trees [4]. The concrete semantics of a program P for a given set of queries  $\mathcal{Q}$ ,  $[P]_{\mathcal{Q}}$ , is then the set of generalized AND trees that result from the execution of the queries in  $\mathcal{Q}$  for P. Each node  $\langle G, \theta^c, \theta^s \rangle$  in the generalized AND tree represents a call to a predicate G (an atom), with the constraint (state) for that call,  $\theta^c$ , and the corresponding success constraint  $\theta^s$  (answer). The calling\_context(G, P, Q) of a predicate given by the predicate descriptor G defined in P for a set of queries Q is the set  $\{\theta^c \mid \exists T \in \llbracket P \rrbracket_{\mathcal{Q}} s.t. \exists \langle G', \theta'^c, \theta'^s \rangle in T \land \exists \sigma, \sigma(G') = G, \sigma(\theta'^c) = \theta^c \}, \text{ where } \sigma$ 

is a renaming substitution, i.e., a substitution that replaces each variable in the term it is applied to with distinct, fresh variables. We use  $\sigma(X)$  to denote the application of  $\sigma$  to X. We denote by answers(P, Q) the set of success constraints computed by P for queries Q.

**Goal-Dependent Abstract Interpretation.** We use goal-dependent abstract interpretation, in particular a simplified version (PLAI-simp) of the PLAI algorithm [21,22], which is essentially an efficient abstraction of the generalized AND trees semantics, parametric on the abstract domain. It takes as input a program P, an abstract domain  $D_{\alpha}$ , and a set of abstract initial queries  $\mathcal{Q}_{\alpha} = \{G_i:\lambda_i\}$ , where  $G_i$  is a normalized atom, and  $\lambda_i \in D_{\alpha}$  is abstract constraint. The algorithm computes a set of triples  $A = \{\langle G_1, \lambda_1^c, \lambda_1^s \rangle, \ldots, \langle G_n, \lambda_n^c, \lambda_n^s \rangle\}$ . In each  $\langle G_i, \lambda_i^c, \lambda_i^s \rangle$  triple,  $G_i$  is a normalized atom, and  $\lambda_i^c$  and  $\lambda_i^s$ , elements of  $D_{\alpha}$ , are, respectively, abstract call and success constraints. The set of triples for a predicate cover all the concrete call and success constraints that appear during execution of the initial queries from  $\gamma(Q_{\alpha})$ , see Definition 2.

As usual,  $\perp$  denotes the abstract constraint such that  $\gamma(\perp) = \emptyset$ . A tuple  $\langle G_j, \lambda_j^c, \perp \rangle$  indicates that all calls to predicate  $G_j$  with any constraint  $\theta \in \gamma(\lambda_j^c)$  either fail or loop, i.e., they do not produce any success constraints. A represents the (possibly infinite) set of nodes of the generalized AND trees for the queries represented in  $\mathcal{Q}_{\alpha}$  to P. In addition, A is multivariant on calls, namely, it may contain more than one triple for the same predicate descriptor G with different abstract call constraints. The PLAI algorithm provides guarantees on termination and correctness (see Theorem 1 for a more precise formulation).

Assertions. Assertions allow stating conditions on the state (current constraint store) that hold or must hold at certain points of program execution. We use for concreteness a subset of the syntax of the **pred** assertions of [5, 14, 24], which allow describing sets of *preconditions* and *conditional postconditions* on the state for a given predicate. These assertions are instrumental for many purposes, e.g. expressing the results of analysis, providing specifications, and documenting [13, 14, 25]. A **pred** assertion is of the form:

:- [Status] pred Head [: Pre] [=> Post].

where *Head* is a predicate descriptor (i.e., a normalized atom) that denotes the predicate that the assertion applies to, and *Pre* and *Post* are conjunctions of *property literals*, i.e., literals corresponding to predicates meeting certain conditions which make them amenable to checking, such as being decidable for any input [24]. *Pre* expresses properties that hold when *Head* is called, namely, at least one *Pre* must hold for each call to *Head*. *Post* states properties that hold if *Head* is called in a state compatible with *Pre* and the call succeeds. Both *Pre* and *Post* can be empty conjunctions (meaning true), and in that case they can be omitted. *Status* is a qualifier of the meaning of the assertion. Here we consider: **trust**, the assertion represents an actual behavior of the predicate that the analyzer will assume to be correct; **check**, the assertion expresses properties that must hold at run-time, i.e., that the analyzer should prove or else generate run-time checks for (we will return to this in Sect. 4). **check** is the default status of assertions.

*Example 1.* The following assertions describe different behaviors of the **pow** predicate that computes  $P = X^{\mathbb{N}}$ : (1) is stating that if the exponent of a power is an even number, the result (P) is non-negative, (2) states that if the base is a non-negative number and the exponent is a natural number the result P also is non-negative:

Here, for simplicity we assume that the properties even/1, int/1, nat/1, and  $\geq$  are *built-in properties* handled natively by the abstract domain.

In addition to predicate assertions we also consider *program-point assertions*. They can appear in the places in a program in which a literal (statement) can be added and are expressed using literals corresponding to their *Status*, i.e., trust(Cond) and check(Cond). They imply that whenever the execution reaches a state originated at the program point in which the assertion appears, *Cond* (should) hold. Example 2 illustrates their use. Program-point assertions can be translated to **pred** assertions,<sup>1</sup> so without loss of generality we will limit the discussion to **pred** assertions.

**Definition 1 (Meaning of a Set of Assertions for a Predicate).** Given a predicate represented by a normalized atom Head, and a corresponding set of assertions  $\{a_1 \ldots a_n\}$ , with  $a_i =$  ":- pred Head :  $Pre_i => Post_i$ ." the set of assertion conditions for Head is  $\{C_0, C_1, \ldots, C_n\}$ , with:

$$C_i = \begin{cases} \texttt{calls}(Head, \bigvee_{j=1}^n Pre_j) & i = 0\\ \texttt{success}(Head, Pre_i, Post_i) & i = 1..n \end{cases}$$

where  $calls(Head, Pre)^2$  states conditions on all concrete calls to the predicate described by Head, and  $success(Head, Pre_j, Post_j)$  describes conditions on the success constraints produced by calls to Head if  $Pre_j$  is satisfied.

The assertion conditions for the assertions in Example 1 are:

$$\left. \begin{array}{ll} \texttt{calls}( & pow(X,N,P), \left((int(X), even(N)) \lor (X \ge 0, nat(N))\right)), \\ \texttt{success}( & pow(X,N,P), \left(int(X), even(N)\right), & (P \ge 0)), \\ \texttt{success}( & pow(X,N,P), (X \ge 0, nat(N)), & (P \ge 0)) \end{array} \right\}$$

Uses of Assertions. We show examples of the use assertions to guide analysis.

*Example 2. Regaining precision during analysis.* If we analyze the following program with a simple (non-relational) intervals domain, the information inferred

<sup>&</sup>lt;sup>1</sup> E.g., we can replace line 4 in Example 2, by "assrt\_aux(Z),", and add a predicate to the program, assrt\_aux(\_)., with an assertion ":- pred assrt\_aux(Z) : Z = 2.".

<sup>&</sup>lt;sup>2</sup> We denote the calling conditions with calls (plural) for historic reasons, and to avoid confusion with the higher order predicate in Prolog call/2.

for Z would be "any integer" (line 3), whereas it can be seen that it is Z = 2 for any X and Y. We provide the information to the analyzer with an assertion (line 4). The analyzer will *trust* this information even if it cannot be inferred with this domain (because it cannot represent relations between variables).

*Example 3. Speeding up analysis.* Very precise domains suffer less from loss of precision and are useful for proving complex properties, but can be very costly. In some cases less precise information in enough, e.g., this code extracted from LPdoc, the Ciao documentation generator, html\_escape is a predicate that takes a string of characters and transforms it to html:

```
:- trust pred html_escape(S0, S) => (string(S0), string(S)).
1
"'"||S) :- !, html_escape(S0, S).
  html_escape([39|S0],
5
6
  % . . .
7
  html_escape([X|S0], [X|S])
                              :- !, character_code(X), html_escape(S0,
      S).
8
  html_escape([],[]).
9
  % string(Str) :- list(Str, int).
10
```

Analyses based on regular term languages, as, e.g. **eterms** [28] infer precise regular types with subtyping, which is often costly. In this example it would be equivalent to computing an accurate regular language that over-approximates the HTML text encoding. The **trust** assertion provides a general invariant that the analyzer will take instead of inferring a more complex type.

Example 4. Defining abstract usage or specifications of libraries or dynamic predicates. When sources are not available, or cannot be analyzed, assertions can provide the missing abstract semantics. The following code illustrate the use of an assertion to describe the behavior of predicate **receive** in a **sockets** library that is written in C. The assertion in this case transcribes what is stated in natural language in the documentation of the library. Note that if no annotations were made, the analyzer would have to assume the most general abstraction  $(\top)$  for the library arguments.

```
1 :- module(sockets, []).
2
3 :- export(receive/2).
4 :- pred receive(S, M) : (socket(S), var(M)) => list(M, utf8).
5 % receive is written in C
```

Example 5. (Re)defining the language semantics for abstract domains. trust assertions are also a useful tool for defining the meaning (transfer function) of the basic operations of the language. In this example we define some basic properties of the product predicate in a simple types-style abstract domain:

```
1 :- trust pred '*'(A, B, C) : (int(A), int(B)) => int(C).
2 :- trust pred '*'(A, B, C) : (flt(A), int(B)) => flt(C).
3 :- trust pred '*'(A, B, C) : (int(A), flt(B)) => flt(C).
4 :- trust pred '*'(A, B, C) : (flt(A), flt(B)) => flt(C).
```

The semantics of bytecodes or machine instructions can be specified for each domain after transformation into CHCs. Assertions allow representing behaviors for the same predicate for different call descriptions (multivariance).

# 3 Basic Fixpoint Algorithm

We first present a basic, non-guided algorithm to be used as starting point –see Fig. 1. PLAI-simp is essentially the PLAI algorithm [22], but omitting some optimizations that are independent from the issues related with the guidance. The algorithm is parametric on the abstract domain  $D_{\alpha}$ , given by implementing the domain-dependent operations  $\Box$ ,  $\Box$ ,  $\sqcup$ , abs\_call, abs\_proceed, abs\_generalize, abs\_project, and abs\_extend (which will be described later), and transfer functions for program built-ins, that abstract the meaning of the basic operations of the language. These operations are assumed to be monotonic and to correctly over-approximate their correspondent concrete version. As stated before, the goal of the analyzer is to capture the behavior of each procedure (function or predicate) in the program with a set A of triples  $\langle G, \lambda^c, \lambda^s \rangle$ , where G is a normalized atom and  $\lambda^c$  and  $\lambda^s$  are, respectively, the abstract call and success constraints, elements of  $D_{\alpha}$ . For conciseness, we denote looking up in A with a partial function  $a: Atom * D_{\alpha} \mapsto D_{\alpha}$ , where  $\lambda^s = a[G, \lambda^c]$  iff  $\langle G, \lambda^c, \lambda^s \rangle \in A$ , and modify the value of a for  $(G, \lambda^c)$ , denoted with  $a[G, \lambda^c] \leftarrow \lambda^{s'}$  by removing  $\langle G, \lambda^c, \rangle$  from A and inserting  $\langle G, \lambda^c, \lambda^{s'} \rangle$ . In A there may be more than one triple with the same G, capturing multivariance, but only one for each  $\lambda^c$  during the algorithm's execution or in the final results.

**Operation of the Algorithm.** Analysis proceeds from the initial abstract queries  $Q_{\alpha}$  assuming  $\perp$  as under-approximation of their success constraint. The algorithm iterates over possibly incomplete results (in A), recomputing them with any newly inferred information, until a global fixpoint is reached (controlled by flag *changes*). First, the set of captured call patterns and the clauses whose head applies (i.e., there exists a renaming  $\sigma$  s.t.  $G = \sigma(cl.head)$ ) is stored in W. Then, each clause is solved with the following process. An "abstract unification" (abs\_call) is made, which performs the abstract parameter passing. It includes *renaming* the variables, abstracting the parameter values (via function  $\alpha$ ), and *extending* the abstract constraint to all variables present in the head and the body of the clause. To abstractly execute a clause the function solve\_body abstractly executes each of the literals of the body. This implies, for each literal, *projecting* the abstract constraint onto the variables of the literal (abs\_project)

```
Algorithm Analyze(P, \mathcal{Q}_{\alpha})
input: P, \mathcal{Q}_{\alpha} output global: A \leftarrow \emptyset
  1: a[L_i, \lambda_i] \leftarrow \bot for all L_i : \lambda_i \in \mathcal{Q}_\alpha, changes \leftarrow true
                                                                                                                                     ▷ Initial queries
 2: while changes do
 3:
             changes \leftarrow false
 4:
              W \leftarrow \{(G, \lambda^c, \mathsf{cl}) \mid a[G, \lambda^c] \text{ is defined } \land \mathsf{cl} \in P \land \exists \sigma \text{ s.t. } G = \sigma(\mathsf{cl.head})\}
              for each (G, \lambda^c, cl) \in W do
 5:
                    \lambda^t \leftarrow \mathsf{abs\_call}(G, \lambda^c, \mathsf{cl.head})
 6:
 7:
                    \lambda^t \leftarrow \text{solve\_body}(\text{cl.body}, \lambda^t)
                    \lambda^{s_0} \leftarrow \mathsf{abs\_proceed}(G, \mathsf{cl.head}, \lambda^t)
 8:
                    \lambda^{s'} \leftarrow \texttt{abs\_generalize}(\lambda^{s_0}, \{a[G, \lambda^c]\})
 9:
                    if \lambda^{s'} \neq \lambda^s then
10:
                           a[G, \lambda^c] \leftarrow \lambda^{s'}, changes \leftarrow true
                                                                                                                \triangleright Fixpoint not reached yet
11:
12: function solve_body(B, \lambda^t)
13:
              for each L \in B do
                    \lambda^{c} \leftarrow \mathsf{abs\_project}(L, \lambda^{t})
14:
                     Call = \{\lambda \mid a[H, \lambda'] \text{ is defined } \land \exists \sigma \text{ s.t. } \sigma(H) = L \land \lambda = \sigma(\lambda')\}
15:
                    \lambda^{c'} \leftarrow \mathsf{abs\_generalize}(\lambda^c, Call)
16:
                    \lambda^s \leftarrow \mathsf{solve}(L, \lambda^{c'})
17:
18:
                    \lambda^t \leftarrow \mathsf{abs\_extend}(L, \lambda^s, \lambda^t)
19:
              return \lambda^t
```



#### Global: A

```
1: function solve(L, \lambda)
2:
         if L is a built-in then
3:
              return f^{\alpha}(L,\lambda)
                                                                                         \triangleright apply transfer function
         else if a[G, \lambda^c] is defined and \exists \sigma \text{ s.t. } \sigma(G) = L then
4:
              return \sigma(a[G, \lambda^c])
5:
6:
         else
7:
              a[L,\lambda] \leftarrow \bot
8.
              return \perp
```

Fig. 2. Pseudocode for solving a literal.

and generalizing it if necessary (abs\_generalize) before calling solve. Generalization is necessary to ensure termination since we support multivariance and infinite domains. Lastly, after returning from solve (returning from the literal call), abs\_extend propagates the information given by  $\lambda^s$  (success abstract constraint over the variables of L) to the constraint of the variables of the clause  $\lambda^t$ . The solve function executes abstractly a literal (Fig. 2). Depending on the nature of the literal, different actions will be performed. For *built-in operations*, the corresponding transfer function ( $f^{\alpha}$ ) is applied. For *predicates defined in the program*, the answer is first looked up in A. If there is already a computed tuple that matches the abstract call, the previously inferred result is taken. Else (no stored tuple matches the abstract call), an entry with that call pattern and  $\perp$ as success value is added. This will trigger the analysis of this call in the next

Fig. 3. Factorial program and a possible analysis result.

iteration of the loop. Once a body is processed, the actions of **abs\_call** have to be undone in **abs\_proceed**, which performs the "abstract return" from the clause. It *projects* the temporary abstract constraint (used to solve the body) back to the variables in the head of the clause and *renames* the resulting abstract constraint back to the variables of the analyzed head. The result is then abstractly *generalized* with the previous results (either from other clauses that also unify or from previous results of the processed clause), and it is compared with the previous result to check whether the fixpoint was reached. Termination is ensured even in the case of domains with infinite ascending chains because **abs\_generalize** includes performing a widening if needed, in addition to the join operation  $\sqcup$ . This process is repeated for all the tuples of the analysis until the analysis results are the same in two consecutive iterations.

Figure 3 shows a factorial program and an analysis result A for  $\mathcal{Q}_{\alpha} = \{ \texttt{fact}(X, R) : \top \}$  with an abstract domain that keeps information about signs for each of the program variables with values of the lattice shown. For example, the first tuple in A states that fact(X, R) may be called with any possible input and, if it succeeds, X will be an integer and R will be a positive number.

We define analysis results to be correct if the abstract call constraints cover all the call constraints (and, respectively, the abstract success constraints cover all the success constraints) which appear during the concrete execution of the initial queries in Q. Formally:

**Definition 2 (Correct analysis).** Given a program P and initial queries Q, an analysis result A is correct for P, Q if:

 $\begin{aligned} &-\forall G, \theta^c \in calling\_context(G, P, \mathcal{Q}) \ \exists \langle G, \lambda^c, \lambda^s \rangle \in A \ s.t. \ \theta^c \in \gamma(\lambda^c). \\ &-\forall \langle G, \lambda^c, \lambda^s \rangle \in A, \forall \theta^c \in \gamma(\lambda^c) \ if \ \theta^s \in answers(P, \{G: \theta^c\}) \ then \ \theta^s \in \gamma(\lambda^s). \end{aligned}$ 

We recall the result from [22], adapted to the notation used in this paper.

**Theorem 1. Correctness of PLAI.** Consider a program P and a set of initial abstract queries  $\mathcal{Q}_{\alpha}$ . Let  $\mathcal{Q}$  be the set of concrete queries:  $\mathcal{Q} = \{G: \theta \mid \theta \in \gamma(\lambda) \land G: \lambda \in \mathcal{Q}_{\alpha}\}$ . The analysis result  $A = \{\langle G_1, \lambda_1^c, \lambda_1^s \rangle, \ldots, \langle G_n, \lambda_n^c, \lambda_n^s \rangle\}$  for P with  $\mathcal{Q}_{\alpha}$  is correct for  $P, \mathcal{Q}$ .

# 4 Adding Assertion-Based Guidance to the Algorithm

We now address how to apply the guidance provided by the user in the analysis algorithm. But before that we make some observations related to the run-time behavior of assertions.

**Run-Time Semantics of Assertions.** Most systems make assumptions during analysis with respect to the run-time semantics of assertions: for example, Astrée assumes that they are always run, while CiaoPP assumes conservatively that they may not be (because in general they may in fact be disabled by the user). In order to offer the user the flexibility of expressing these different situations we introduce a new status for assertions, sample-check, as well as a corresponding program-point assertion, **sample-check**(Cond). This sample-check status indicates that the properties in these assertions may or may not be checked during execution, i.e., run-time checking can be turned on or off (or done intermittently) for them. In contrast, for check assertions (provided that they have not been discharged statically) run-time checks must always be performed. Table 1 summarizes this behavior with respect to whether run-time testing will be performed and whether the analysis can "trust" the information in the assertion, depending on its status. The information in **trust** assertions is used by the analyzer but they are never checked at run time. check assertions are also checked at run time and the execution will not pass beyond that point if the conditions are not met.<sup>3</sup> This means that **check** assertions can also be "trusted," in a similar way to trust assertions, because execution only proceeds beyond them if they hold. Finally, **sample-check** assertions may or may not be checked at run-time (e.g., for efficiency reasons) and thus they cannot be used as trusts during analysis.

Status	Use in analyzer	Run-time test (if not discharged at compile-time)
trust	Yes	No
check	Yes	Yes
sample-check	No	Optional

 Table 1. Usage of assertions during analysis.

**Correctly Applying Guidance.** We recall some definitions (adapted from [25]) which are instrumental to correctly approximate the properties of the assertions during the guidance.

**Definition 3 (Set of Calls for which a Property Formula Trivially Succeeds (Trivial Success Set)).** Given a conjunction L of property literals and the definitions for each of these properties in P, we define the trivial success set of L in P as:

 $TS(L, P) = \{\theta | Var(L) \ s.t. \ \exists \theta' \in answers(P, \{L : \theta\}), \theta \models \theta'\}$ 

<sup>&</sup>lt;sup>3</sup> This strict run-time semantics for **check** assertions was used in [27].

where  $\theta | Var(L)$  above denotes the projection of  $\theta$  onto the variables of L, and  $\models$  denotes that  $\theta'$  is a more general constraint than  $\theta$  (entailment). Intuitively, TS(L, P) is the set of constraints  $\theta$  for which the literal L succeeds without adding new constraints to  $\theta$  (i.e., without constraining it further). For example, given the following program P:

and L = list(X), both  $\theta_1 = \{X = [1, 2]\}$  and  $\theta_2 = \{X = [1, A]\}$  are in the trivial success set of L in P, since calling (X = [1, 2], list(X)) returns X = [1, 2] and calling (X = [1, A], list(X)) returns X = [1, A]. However,  $\theta_3 = \{X = [1|-]\}$  is not, since a call to (X = [1|Y], list(X)) will further constrain the term [1|Y], returning X = [1|Y], Y = []. We define abstract counterparts for Definition 3:

**Definition 4 (Abstract Trivial Success Subset of a Property Formula).** Under the same conditions of Definition 3, given an abstract domain  $D_{\alpha}$ ,  $\lambda_{TS(L,P)}^- \in D_{\alpha}$  is an abstract trivial success subset of L in P iff  $\gamma(\lambda_{TS(L,P)}^-) \subseteq TS(L,P)$ .

**Definition 5 (Abstract Trivial Success Superset of a Property Formula).** Under the same conditions of Definition 4, an abstract constraint  $\lambda_{TS(L,P)}^+$  is an abstract trivial success superset of L in P iff  $\gamma(\lambda_{TS(L,P)}^+) \supseteq TS(L,P)$ .

I.e.,  $\lambda_{TS(L,P)}^-$  and  $\lambda_{TS(L,P)}^+$  are, respectively, safe under- and over-approximations of TS(L,P). These abstractions come useful when the properties expressed in the assertions cannot be represented exactly in the abstract domain. Note that they are always computable by choosing the closest element in the abstract domain, and at the limit  $\perp$  is a trivial success subset of any property formula and  $\top$  is a trivial success superset of any property formula.

## 4.1 Including Guidance in the Fixpoint Algorithm

In Fig. 4 we present a version of PLAI-simp (from Fig. 1) that includes our proposed modifications to apply assertions during analysis. The additions to the algorithm are calls to functions apply\_succ and apply\_call, that guide analysis results with the information of the assertion conditions, and E, an analysis-like set of triples representing inferred states before applying the assertions that will be used to check whether the assertions provided by the user could be proved by the analyzer (see Sect. 5). Success conditions are applied (apply\_succ) after the body of the clause has been abstractly executed. It receives an atom G and  $\lambda^c$  as parameters to decide correctly which success conditions have to be applied. Call conditions are applied (apply\_call) before calling function solve. Otherwise, a less precise call pattern will be captured during the procedure (it adds new entries to the table). The last addition, E, collects tuples to be used later to check that the assertions were correct (see Sect. 5). We collect all success constraints before applying any success conditions (line 11 of Fig. 4) and all call constraints before applying any call condition (line 20 of Fig. 4).

```
ALGORITHM Guided_analyze(P, \mathcal{Q}_{\alpha})
input: P, \mathcal{Q}_{\alpha} global output: A \leftarrow \emptyset, E \leftarrow \emptyset
  1: a[G_i, \lambda_i] \leftarrow \bot for all G_i : \lambda_i \in \{G : \lambda^t | \lambda^t = apply\_call(G, \lambda), G : \lambda \in \mathcal{Q}_{\alpha}\}
 2: changes \leftarrow true
 3: while changes do
              changes \leftarrow false
 4:
              W \leftarrow \{ (G, \lambda^c, \mathsf{cl}) \mid a[G, \lambda^c] \text{ is defined } \land \mathsf{cl} \in P \land \exists \sigma \text{ s.t. } G = \sigma(\mathsf{cl.head}) \}
 5:
              for each (G, \lambda^c, cl) \in W do
 6:
                     \lambda^t \leftarrow \mathsf{abs\_call}(G, \lambda^c, \mathsf{cl.head})
 7:
                     \lambda^t \leftarrow \text{solve\_body}(\text{cl.body}, \lambda^t)
 8:
                     \lambda^{s_0} \leftarrow \texttt{abs\_proceed}(G, \mathsf{cl.head}, \lambda^t)
 9:
                     \lambda^{s_1} \leftarrow \texttt{abs\_generalize}(\lambda^{s_0}, \{a[G, \lambda^c]\})
10:
11:
                     E \leftarrow E \cup \{\langle G, \lambda^c, \lambda^{s_1} \rangle\}
                     \lambda^{s} \leftarrow \operatorname{apply\_succ} (G, \lambda^{c}, \lambda^{s_1})
12:
                     if \lambda^s \neq a[G, \lambda^c] then
13:
                            a[G, \lambda^c] \leftarrow \overline{\lambda^s}, changes \leftarrow true
14:
                                                                                                                     \triangleright Fixpoint not reached vet
15: function solve_body(B, \lambda^t)
               for each L \in B do
16:
17:
                     \lambda^c \leftarrow \mathsf{abs\_project}(L, \lambda^t)
                      Call = \{\lambda \mid a[H, \lambda'] \text{ is defined } \land \exists \sigma \text{ s.t. } \sigma(H) = L \land \lambda = \sigma(\lambda')\}
18:
                     \lambda^{c'} \leftarrow \mathsf{abs\_generalize}(\lambda^c, Call)
19:
                     E \leftarrow E \cup \{ \langle L, \lambda^{c'}, \_ \rangle \}
20:
                     \lambda^{c'} \leftarrow \operatorname{apply\_call}(L, \lambda^c)
21:
22:
                     \lambda^s \leftarrow \text{solve}(L, \lambda^{c'})
                     \lambda^t \leftarrow \mathsf{abs\_extend}(L, \lambda^s, \lambda^t)
23:
               return \lambda^t
24:
```

Fig. 4. Fixpoint analysis algorithm using assertion conditions.

```
global flag: speed-up
 1: function apply_call(L, \lambda^c)
 2:
           if \exists \sigma, \lambda^t = \lambda^+_{TS(\sigma(Pre),P)} s.t. calls(H, Pre) \in C, \sigma(H) = L then
                 if speed-up return \lambda^t else return \lambda^c \sqcap \lambda^t
 3:
           else return \lambda^c
 4:
 5: function apply_succ(G, \lambda^c, \lambda^{s_0})
           app = \{\lambda \mid \exists \sigma, \texttt{success}(H, Pre, Post) \in C, \sigma(H) = G, \}
 6:
                             \lambda = \lambda^+_{TS(\sigma(Post),P)}, \lambda^-_{TS(\sigma(Pre),P)} \sqsupseteq \lambda^c\}
 7:
           if app \neq \emptyset then
 8:
                 \lambda^t = \prod app
 9:
                 if speed-up return \lambda^t else return \lambda^t \sqcap \lambda^{s_0}
10:
           else return \lambda^{s_0}
11:
```

Fig. 5. Applying assertions.

Assuming that we are analyzing program P and the applicable assertion conditions are stored in C, the correct application of assertions is described in Fig. 5. Flag **speed-up** controls if assertions are used to recover accuracy or to (possibly) speed up fixpoint computation.

Applying Call Conditions. Given an atom G and an abstract call constraint  $\lambda^c$ , if there is a call assertion condition for G, if **speed-up** is true,  $\lambda^+_{TS(Pre,P)}$  is used directly, otherwise the operation  $\lambda^+_{TS(Pre,P)} \sqcap \lambda^c$  will prune from the analysis result the (abstracted) states that are outside the precondition. An over-approximation has to be made, otherwise we may remove calling states that the user did not specify.

Applying Success Conditions. Given an atom G, an abstract call constraint  $\lambda^c$  and its corresponding abstract success constraint  $\lambda^s$ , all success conditions whose precondition applies ( $\lambda^c \sqsubseteq \lambda_{TS(Pre,P)}^-$ ) are collected in *app*. Making an under-approximation of *Pre* is necessary to consider the application of the assertion condition only if it would be applied in the concrete executions of the program. An over-approximation of *Post* needs to be performed since otherwise success states that actually happen in the concrete execution of the program may be removed. If no conditions are applicable (i.e., *app* is empty), the result is kept as it was. Otherwise, if the flag **speed-up** is true  $\lambda_{TS(Post,P)}^+$  is used, as it is; otherwise, it is used to refine the value of the computed answer  $\lambda^s$ .

Applying assertion conditions bounds the extrapolation (widening) performed by **abs\_generalize**, avoiding unnecessary precision losses. Note that the existence of guidance assertions for a predicate does not save having to analyze the code of the corresponding predicate if it is available, since otherwise any calls generated within that predicate would be omitted and not analyzed for, resulting in an incorrect analysis result.

### 4.2 Fundamental Properties of Analysis Guided by Assertions

We claim the following properties for analysis of a program P applying assertions as described in the previous sections. The inferred abstract execution states are covered by the call and (applicable) success assertion conditions.

**Lemma 1.** Applied call conditions. Let calls(H, Pre) be an assertion condition from program P, and let  $\langle G, \lambda^c, \lambda^s \rangle$  be a triple derived for P and initial queries  $\mathcal{Q}_{\alpha}$  by GUIDED\_ANALYZE(P,  $Q_{\alpha}$ ). If  $G = \sigma(H)$  for some renaming  $\sigma$ then  $\lambda^c \sqsubseteq \lambda^+_{TS(\sigma(Pre),P)}$ .

*Proof.* Function **apply\_call** obtains in  $\lambda^t$  the trusted value for the call. It restricts the encountered call  $\lambda^c$  or uses it as is, in any case  $\lambda^c \sqsubseteq \lambda^t = \lambda^+_{TS(Pre,P)}$ . Hence if this function is applied whenever inferred call patterns are introduced in the analysis results, the lemma will hold.

The lemma holds after initialization, since the function is applied before inserting the tuples in A. Now we reason about how the algorithm changes the results. The two spots in which analysis results are updated are in function **solve** (line 7 of Fig. 2) and in the body of the loop of the algorithm (line 14 of Fig. 4). Function **solve** adds tuples to the analysis whenever new encountered call patterns are found, it is called right after **apply\_call**, therefore it only inserts call patterns taking into account calls conditions. The analysis updates made in

the body of the loop do not insert new call patterns, only the recomputed success abstractions for those already present (previously collected in W), therefore all call patterns encountered are added taking into account the call conditions and the lemma holds.

**Lemma 2.** Applied success conditions. Let  $\operatorname{success}(H, \operatorname{Pre}, \operatorname{Post})$  be an assertion condition from program P and let  $\langle G, \lambda^c, \lambda^s \rangle$  be a triple derived for P with  $\mathcal{Q}_{\alpha}$  initial queries by  $\operatorname{GUIDED}_{-}\operatorname{ANALYZE}(P, \mathcal{Q}_{\alpha})$ . If  $G = \sigma(H)$  for some renaming  $\sigma$  then  $\lambda^c \sqsubseteq \lambda_{TS(\sigma(\operatorname{Pre}), P)}^- \Rightarrow \lambda^s \sqsubseteq \lambda_{TS(\sigma(\operatorname{Post}), P)}^+$ .

*Proof.* Function **apply\_succ** computes the  $\sqcap$  of all applicable assertion conditions (checking  $\lambda^c \sqsubseteq \lambda_{TS(Pre,P)}^-$ ), if existing. Since we make the  $\sqcap$  of all applied conditions,  $\lambda^s \sqsubseteq \sqcap \lambda_{TS(Post_i,P)}^+ \sqsubseteq \lambda_{TS(Post_i,P)}^+$  for any *Post*. Hence if all results inserted in the analysis result have been previously processed by **apply\_succ** the lemma holds. The lemma holds for the initialized results, because  $\lambda^s = \bot \sqsubseteq \lambda_{TS(Post,P)}^+$  for any *Post*. Now we reason about how the algorithm changes the results. We have the same points in the algorithm that change the analysis result as in the proof of Lemma 1. The **solve** function initializes  $\lambda^s$  of the newly encountered calls with  $\bot$ , so it is the same situation as when initializing. In the body of the loop **apply\_succ** is always called before updating the value in the result and the lemma holds.

# 5 Checking Correctness in a Guided Analysis

We discuss how assertions may introduce errors in the analysis, depending on their status. **sample-check** assertions are not used by the analyzer. Any part of the execution stopped by them will conservatively be considered to continue, keeping the analysis safe. **check** assertions stop the execution of the program if the properties of the conditions are not met. Hence it is safe to narrow the analysis results using their information. Last, **trust** assertions are not considered during the concrete executions, so they may introduce errors. Such assertion conditions express correct properties if they comply with the following definitions:

**Definition 6 (Correct call condition).** Let P be a program with an assertion condition C = calls(H, Pre). C is correct for a query  $\mathcal{Q}$  to P if for any predicate descriptor G, s.t.  $G = \sigma(H)$  for some renaming  $\sigma$ ,  $\forall \theta^c \in calling\_context(G, P, \mathcal{Q}), \theta^c \in \gamma(\lambda_{TS(\sigma(Pre), P)}^+)$ .

**Definition 7 (Correct success condition).** Let P be a program with an assertion condition C = success(H, Pre, Post). C is correct for P if for any predicate descriptor G, s.t.  $G = \sigma(H)$  for some renaming  $\sigma$ ,  $\theta^c \in \gamma(\lambda_{TS(\sigma(Pre),P)}^-), \theta^s \in answers(P, \{G: \theta^c\}) \Rightarrow \theta^s \in \gamma(\lambda_{TS(\sigma(Post),P)}^+).$ 

**Theorem 2. Correctness modulo assertions.** Let P be a program with correct assertion conditions C and  $\mathcal{Q}_{\alpha}$  a set of initial abstract queries. Let  $\mathcal{Q}$  be the set of concrete queries:  $\mathcal{Q} = \{G: \theta \mid \theta \in \gamma(\lambda) \land G: \lambda \in \mathcal{Q}_{\alpha}\}$ . The analysis result  $A = \{\langle G_1, \lambda_1^c, \lambda_1^s \rangle, \dots, \langle G_n, \lambda_n^c, \lambda_n^s \rangle\}$  computed with GUIDED\_ANALYZE $(P, \mathcal{Q}_{\alpha})$  is correct (Definition 2) for  $P, \mathcal{Q}$ .

*Proof.* For conciseness in the proof we omit the renaming part. Fixed program P, given an abstract description d from an assertion (*Pre* or *Post*), let  $\lambda_d^- = \lambda_{TS(d,P)}^-, \lambda_d^+ = \lambda_{TS(d,P)}^+$ . If there are no assertion conditions, the theorem trivially holds (Theorem 1). If assertion conditions are used to generalize, the theorem also holds because  $\lambda^c = \lambda_{Pre}^+$  and  $\lambda^s = \lambda_{Post}^+$  are by definition (Definition 6, Definition 7, respectively) correct over-approximations. If assertion conditions are used to regain precision:

Call: We want to prove that

$$\forall G, \theta^c \in calling\_context(G, P, \mathcal{Q}) \exists \langle G, \lambda^c, \lambda^s \rangle \in A \text{ s.t. } \theta^c \in \gamma(\lambda^c) \text{ (Definition 2)}.$$

We applied: calls(G, Pre)

$$\begin{aligned} \theta^{c} &\in \gamma(\lambda_{Pre}^{+}) & \text{(by Definition 6)} \\ \text{In } E : \exists \langle G, \lambda_{E}^{c}, \lambda_{E}^{s} \rangle \in E, \theta^{c} \in \gamma(\lambda_{E}^{c}) & \text{(by algorithm (Fig. 4 line 20))} \\ \text{Then: } \theta^{c} &\in \gamma(\lambda_{E}^{c}) \cap \gamma(\lambda_{Pre}^{+}) \subseteq \gamma(\alpha(\gamma(\lambda_{E}^{c}) \cap \gamma(\lambda_{Pre}^{+}))) \subseteq \gamma(\lambda_{E}^{c} \sqcap \lambda_{Pre}^{+}) \\ \theta^{c} &\in \gamma(\lambda_{E}^{c} \sqcap \lambda_{Pre}^{+}) = \gamma(\lambda^{c}) & \text{(by algorithm (Fig. 5 line 3))} \end{aligned}$$

**Success:** We want to prove that  $\forall \langle G, \lambda^c, \lambda^s \rangle \in A, \forall \theta^c \in \gamma(\lambda^c) \text{ if } \theta^s \in answers(P, \{G : \theta^c\}) \text{ then } \theta^s \in \gamma(\lambda^s).$ 

We applied:  $success(G, Pre_i, Post_i)$ 

$$\theta^{s} \in \gamma(\lambda_{E}^{s}) \cap \gamma(\lambda^{p}) \subseteq \gamma(\alpha(\gamma(\lambda_{E}^{s}) \cap \gamma(\lambda^{p}))) \subseteq \gamma(\lambda_{E}^{s} \sqcap \lambda^{p})$$
  
$$\theta^{s} \in \gamma(\lambda_{E}^{s} \sqcap \lambda^{p}) = \gamma(\lambda^{s})$$

In other words, Theorem 2 and Lemmas 1 and 2 ensure that correct assertion conditions bound imprecision in the result, without affecting correctness. By applying the assertion conditions no actual concrete states are removed from the abstractions.

We can identify suspicious pruning during analysis. Let  $\lambda^a$  be the correct approximation of a condition and  $\lambda$  be an inferred abstract state, typically a value in the tuples of E. If  $\lambda \sqcap \lambda^a = \bot$  the inferred information is incompatible with that in the condition, therefore it is likely that the assertion is *erroneous*.  $\lambda \not\sqsubseteq \lambda^a$  indicates that the algorithm inferred more concrete constraint states than described in the assertion and the analysis results may be wrong. These checks can be performed while the algorithm is run or off-line, by comparing the properties of the assertion conditions against the triples stored in E, which, as mentioned earlier, stores partial analysis results with no assertions applied. A full description of this checking procedure is described in [25,27].

## 6 Related Work

The inference of arbitrary semantic properties of programs is known to be both undecidable and expensive, requiring user interaction in many realistic settings. Abstract interpreters allow the selection of different domains and parameters for such domains (e.g., polyhedra, octagons, regtypes with depth-k, etc.), as well as their widening operations (e.g., type shortening, structural widening, etc.). Other parameters include policies for partial evaluation and other transformations (loop unrolling, inlining, slicing, etc.). These parameters are orthogonal or complementary to the issues discussed in this paper. To the extent of our knowledge the use of **program-level annotations** (such as assertions) to guide abstract interpretation has not been widely studied in the literature, contrary to their (necessary) use in verification and theorem proving approaches. The **Cibai** [18] system includes *trust-style* annotations while sources are processed to encode some predefined runtime semantics. In [10] analysis is guided by modifying the analyzed program to restrict some of its behaviors. However, this guidance affects the order of program state exploration, rather the analysis results, as in our case. As mentioned in the introduction, the closest to our approach is **Astrée**, that allows *assert*-like statements, where correctness of the analysis is ensured by the presence of compulsory runtime checks, and trusted (known facts) asserts. These refine and guide analysis operations at program points. Like in CiaoPP, the analyzer shows errors if a known fact can be falsified statically. However, as with the corresponding Ciao assertions, while there has been some examples of use [8], there has been no detailed description of how such assertions are handled in the fixpoint algorithm. We argue that this paper contributes in this direction.

## 7 Conclusions

We have proposed a user-guided multivariant fixpoint algorithm that makes use of check and trust assertion information, and we have provided formal results on the influence of such assertions on correctness and efficiency. We have extended the semantics of the guidance (and all) assertions to deal with both the cases in which the program execution will and will not incorporate run-time tests for unverified assertions, as well as the cases in which the assertions are intended for refining the information or instead to lose precision in order to gain efficiency. We show that these annotations are not only useful when dealing with incomplete code but also provide the analyzer with recursion/loop invariants for speeding up global convergence.

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