Romeo: A Tool for Analyzing Time Petri Nets

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Abstract. In this paper, we present the features of ROMEO, a Time Petri Net (TPN) analyzer. The tool ROMEO allows state space computation of TPN and on-the-fly model-checking of reachability properties. It performs translations from TPNs to Timed Automata (TAs) that preserve the behavioural semantics (timed bisimilarity) of the TPNs. Besides, our tool also deals with an extension of Time Petri Nets (Scheduling-TPNs) for which the valuations of transitions may be stopped and resumed, thus allowing the modeling preemption.

Keywords: Time Petri nets, model-checking, state-space, DBM, polyhedron, scheduling, stopwatch.

1 Introduction

Time Petri Nets (TPNs) are a classical formalism, with Timed Automata (TAs), to design reactive systems. TPNs extend classical Petri nets with temporal intervals associated with transitions. They benefit from an easy representation of real-time systems features (synchronization, parallelism ...). State reachability and boundedness is proven to be undecidable for arbitrary TPNs. However, state reachability is decidable for bounded TPNs, which is sufficient for virtually all practical purposes.

In real-time applications, it is often necessary to memorize the progress status of an action when this one is suspended then resumed. In this class of models, some extensions of Time Petri Nets have been proposed to express the preemptive scheduling of tasks. Roux and Déplanche [1] propose an extension for Time Petri Nets (Scheduling-TPNs) that consists of mapping into the Petri net model the way the different schedulers of the system activate or suspend the tasks. For a fixed priority scheduling policy, Scheduling-TPNs introduce two new attributes associated to each place that respectively represent allocation (processor or resource) and priority (of the modeled task). Bucci et al. [2] propose a similar model: Preemptive Time Petri Net (Preemptive-TPN) by mapping the scheduling policies onto transitions.

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2 The Tool Romeo

The purposes of the tool ROMEO ¹ (available for Linux, MacOSX and Windows platforms) are the analysis and simulation of reactive systems modelled by *TPNs* or *Scheduling-TPNs*. It consists of: (1) a graphical user interface (GUI) (written in TCL/Tk) to edit and design *TPNs*, and (2) computation modules (GPN and MERCUTIO, written in C++).

2.1 System Design

In a system modelling activity, the ROMEO GUI allows to model reactive systems or preemptive reactive systems using *TPNs* or *Scheduling-TPNs*. Both benefit from an easy graphical representation and from an easy representation of common real-time features (parallelism, synchronization, resources management, watch-dogs...).

As a design helper, ROMEO implements on-line simulation (*TPNs*, *Scheduling-TPNs*) and reachability model-checking (*TPNs*). It allows the early detection of some modeling issues during the conception stage.

2.2 On-line Model-Checking

In addition to on-line simulation that makes scenarii testing possible, ROMEO provides an on-line model-checker for reachability. Properties over markings can be expressed and tested. It is then possible to test the reachability of a marking such that it verifies $M(P_1) = 1 \vee M(P_3) \geq 3$ where $M(P_i)$ is the number of tokens in the place P_i of the net. The tool returns a trace leading to such a marking if reachable.

Such a model-checker allows to verify more complex properties (quantitative properties for instance) expressed by observers (which translate a quantitative property into a reachability test), which is the main method used to study the behavior of a *TPN*.

2.3 Off-line Model-Checking

The modeling of a property using observers requires a good knowledge in *TPNs* and, as far as we know, no automatic observers generation is available to help a system designer.

Romeo implements different theoretical methods to translate the model analyzed into Automata, Timed Automata (TAs) or Stopwatch Automata (SWAs). The advantages of such translations is that several efficient model-checking tools are available for these models (Mec, Aldebaran, Uppaal, Kronos, Hytech). These translations also extend the class of properties that can be model-checked with observers to temporal logic (LTL, CTL) and quantitative temporal logic (TCTL).

 $^{^{1}\ \}mathrm{Download}\ \mathrm{at:}\ \mathrm{http://www.irccyn.ec-nantes.fr/irccyn/d/fr/equipes/TempsReel/logs}$

To our knowledge, the translations of *TPNs* to *TAs* implemented in ROMEO are currently the only existing methods allowing the verification of quantitative time properties (quantitative liveness and TCTL) on *TPNs*.

Structural Translation. ROMEO implements a structural transformation of a *TPN* into a timed-bisimilar synchronized product of *TAs* [3] that can be model-checked with the tool UPPAAL. The translation is optimized to take at its advantage the management of inactive clocks in UPPAAL. It follows that the algorithms implemented in this tool are used efficiently.

State Space Computation Based Translation. A first translation consists in the computation of the state class graphs (SCG) that provide finite representations for the behavior of bounded nets preserving their LTL properties [4]. For bounded *TPNs* the algorithm is based on DBM (Difference Bounds Matrix) data structure whereas, for *Scheduling-TPNs*, the semi-algorithm is based on polyhedra (using the *New Polka* library).

Two different methods are implemented for *TPNs* to generate a *TA* that preserves its semantics (in the sense of *timed bisimilarity*): the first one is derived from *TA* framework [5], the other one from the classical state class graph approach [6]. In the latter method, we reduce the number of clocks needed during the translation, so that the subsequent verification on the resulting *TA* is more efficient. In both methods, the *TAs* are generated in UPPAAL or KRONOS input format.

Concerning Scheduling-TPNs, the method introduced in [7] is implemented. It allows a fast translation into a Stopwatch Automaton (SWA) using an overapproximating semi-algorithm (DBM-based). Despite the overapproximation, it has been proven that the SWA is timed-bisimilar to the original Scheduling-TPN. The SWA is produced in the HYTECH input format and is computed with a low number of stopwatches. Since the number of stopwatches is critical for the complexity of the verification, the method increases the efficiency of the timed analysis of the system; moreover, in some cases, it may just make the analysis possible while it would be a dead-end to model the system directly with HYTECH.

2.4 Comparisons

The following tables are an overview of the ROMEO features compared to two others main tools used for the analysis of *TPNs* and *TPNs* extension dealing with preemption. They compare the capabilities of the tools in terms of the properties classes that can be handled.

TINA [8] is a tool for the analysis of *TPNs* mainly using state class graphs techniques. ORIS [9] is a tool that analyzes *Preemptive-TPNs* which are equivalent to *Scheduling-TPNs*.

Our major contribution is to bring to *TPNs* frameworks (*Scheduling-TPNs*) methods to efficiently model-check TCTL properties.

Quantitative Reachability CTLLiveness a TCTL LTLMarking Graph SCG^b Atomic SCG^b TINA $+ MC^{c}$ $+ MC^{c}$ Romeo On-the-fly checking $SCG + MC^c$ Translation to Timed Automata + Uppaal d of Kronos or Marking Graph or $ZFG^e + MC^c$

Table 1. Romeo capabilities on TPNs

Table 2. Romeo capabilities on Scheduling-TPNs

	State-space c	omputation	Timed analysis				
	Overapproximation	Exact abstraction	RTTL	TCTL			
Oris			DBM-SCG ^a				
	DBM	_	$+ MC^b$	_			
Romeo			Efficient translation to timed bisimilar				
	DBM	SCG^c	Stopwatch automata + Hytech				

^a Computation of a DBM over-approximation of the State Class Graph.

3 Case Study

3.1 Description

In this section, we work on a partial model for the control of an oscillation compensator (hydraulic shock absorber) and a differential blocking on a tractor with a sowing trailer. The partial system consists of processors running a real-time operating system, linked together with a CAN bus.

We used the translation of a Scheduling-TPN into a SWA whose state space is computed with HYTECH.

We compared the efficiency of our method with a generic direct modelling with HYTECH on this case study. We also tested several simpler and more complex related systems obtained by removing or adding tasks and/or processors. Table 3 gives the obtained results.

Columns 2 and 3 give the number of processors and tasks of the system. Columns 4, 5 and 6 describe the direct modelling in HYTECH results: the number

^a Includes response properties like $\forall \Box (\varphi \Longrightarrow \forall \Diamond \Psi)$ where φ or Ψ can contain clock constraints.

^b SCG = Computation of the State Class Graph.

 $^{^{}c}$ MC = requires the use of a Model-Checker on the SCG or the ZFG.

^d Subset of TCTL.

^e ZFG = Computation of the Zone-based Forward Graph.

b Oris supports exact timeliness analysis of traces (with respect to a linear-time variant of Real-Time Temporal Logic (RTTL).

^c As for TPNs, the SCG preserves LTL properties.

of SWA used to model the system, the number of stopwatches and the time taken by HYTECH to compute the state space. For this generic modelling, we basically used the product of one SWA per task and one SWA for each scheduler. Columns 7 to 10 give the results for our method: the number of locations/transitions, stopwatches of the SWA we generated, and the time taken for its generation. Finally, the last column gives the time used by HYTECH to compute the state space of the SWA generated by our method. Times are given in seconds and NA means that the HYTECH computation could not yield a result on the machine used.

	Description		Direct SWA Modelling			Our method ^a				
Ex.	Proc.	Tasks	SWA's	Sw.	$t_{ m HYTECH}$	Loc.	Trans.	Sw.	$t_{ m ROMEO}$	$t_{ m HYTECH}$
1	2	4	8	7	77.8	20	29	3	≤ 0.1	0.2
2	3	6	11	9	590.3	40	58	4	≤ 0.1	0.5
3	3	7	12	10	NA	52	84	4	≤ 0.1	0.7
4	3+CAN	7	13	11	NA	297	575	7	0.3	5.3
5	4+CAN	9	15	13	NA	761	1 677	8	0.9	29.8
6	5+CAN	11	17	15	NA	1 141	2 626	9	6	60.1
7	6+CAN	14			NA	4 587	12 777	10	59.7	438.8
8	7+CAN	18			NA	8 817	25 874	12	1 146.7	NA

Table 3. Experimental results

These computations have been performed on a PowerPC G4 1.25 GHz with 500MB of RAM.

We observe that the computation on a direct modelling as a product of SWA is quickly untractable (example 3). However, with our method, we are able to deal with systems of much greater size.

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 $[^]a$ Scheduling-TPN $\stackrel{\text{Romeo}}{\longrightarrow}$ SWA $\stackrel{\text{Hytech}}{\longrightarrow}$ state-space

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